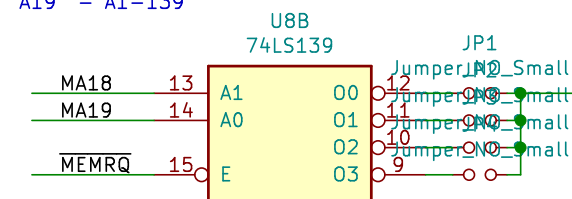
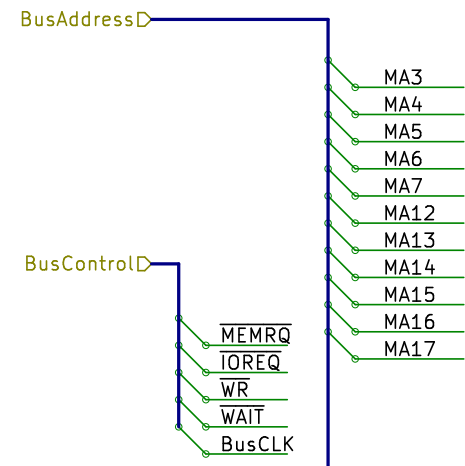


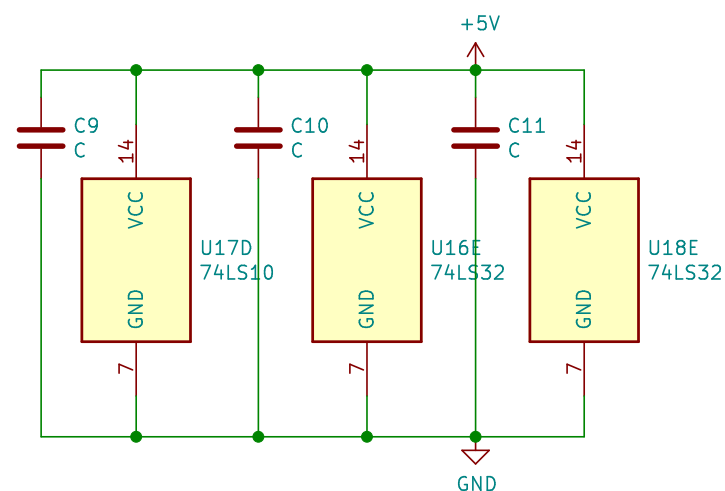
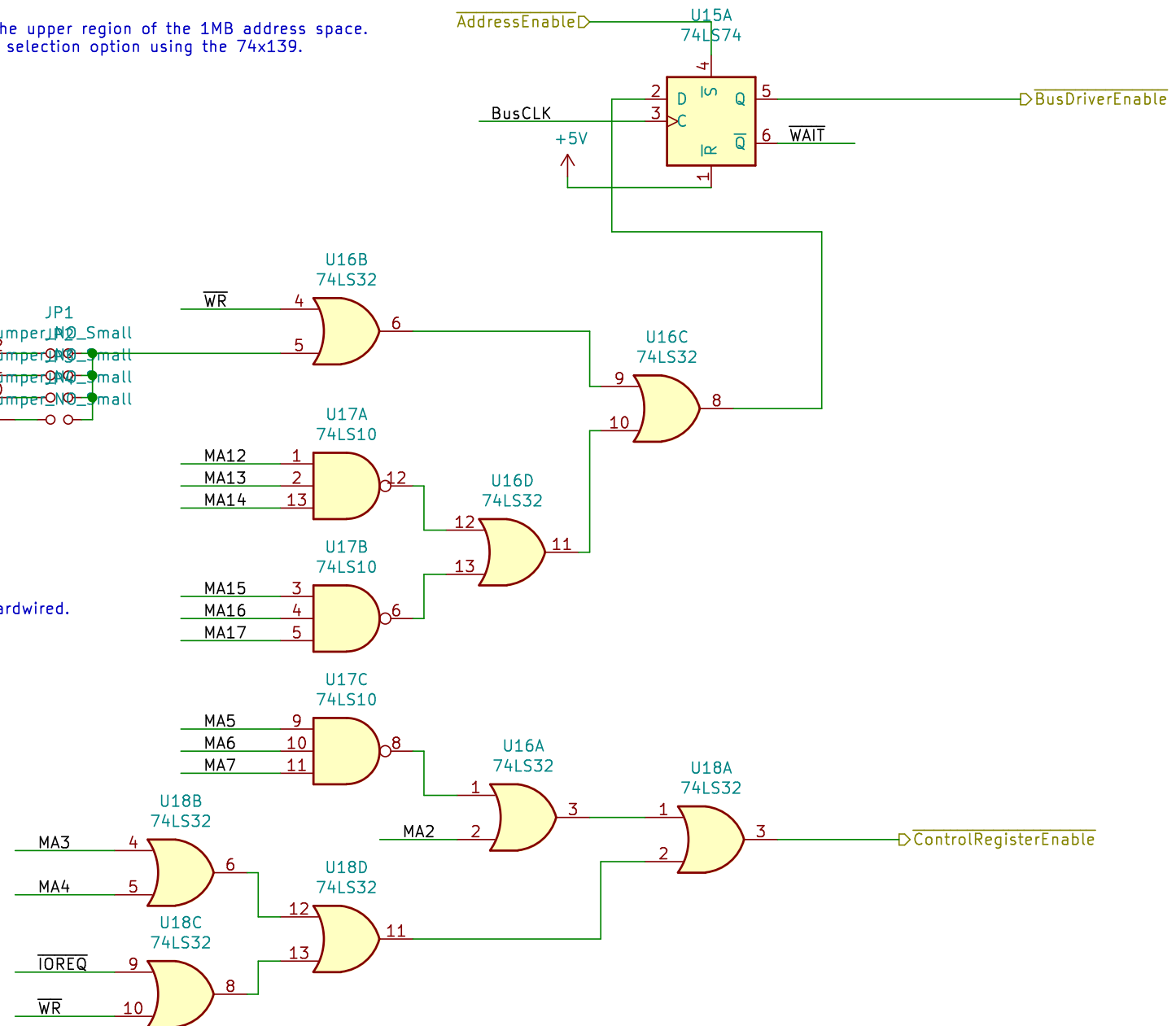


A12 = 1
A13 = 1
A14 = 1
A15 = 1
A16 = 1
A17 = 1
A18 - A0-139
A19 - A1-139



A0 = X
A1 = X
A2 = 0
A3 = 0
A4 = 0
A5 = 1
A6 = 1
A7 = 1

Control Register:
A17..A12 (6 bits)
Options (2 bits)



Id: 4/5



Pixel Output 512x384 + border 640x480 @60Hz Canned Bytes		
Sheet: /Pixel Output/ File: PixelOutput.sch		
Title: Zalt TTL VGA		
Size: A4	Date: 2020-02-09	Rev: 0.1
KiCad E.D.A. kicad (5.1.0)-1		Id: 5/5