Here are the items you should prepare as the report for the final projects:

**Project I:**

1- The synthesizable source code of the processor as well as the benchmark. Please note that your code must be fully commented.
2- A report on the evaluation of the processor using your testbench including.
3- A Report on the output of the synthesis processor including the timing parameters as well as occupation area. It can be synthesized either for FPGA or ASIC.

**Project II:**

4- An easy-to-follow and working how to install explanation for the simulator and corresponding software package for the OS you have worked with.
5- Explaining the simulator architecture as well as its different modules. You should clearly explain in detail how different modules work, what types of outputs they provide, and how to configure the simulator to provide different outputs.
6- Prepare the reports offered by the simulator for your selected two benchmark programs.
7- Create a VM in which the simulator has been installed so that one can follow your instructions and redo what you have done.

**Project III:**

1- The source code of the simulator written in a high-level programing language. Please note that your code must be fully commented.
2- Explaining your design simulator architecture as well as its different modules. You should clearly explain in detail how different modules work, what types of outputs they provide, and how to configure the simulator to provide different outputs.
3- Prepare the reports offered by the simulator for a typical assembly code.