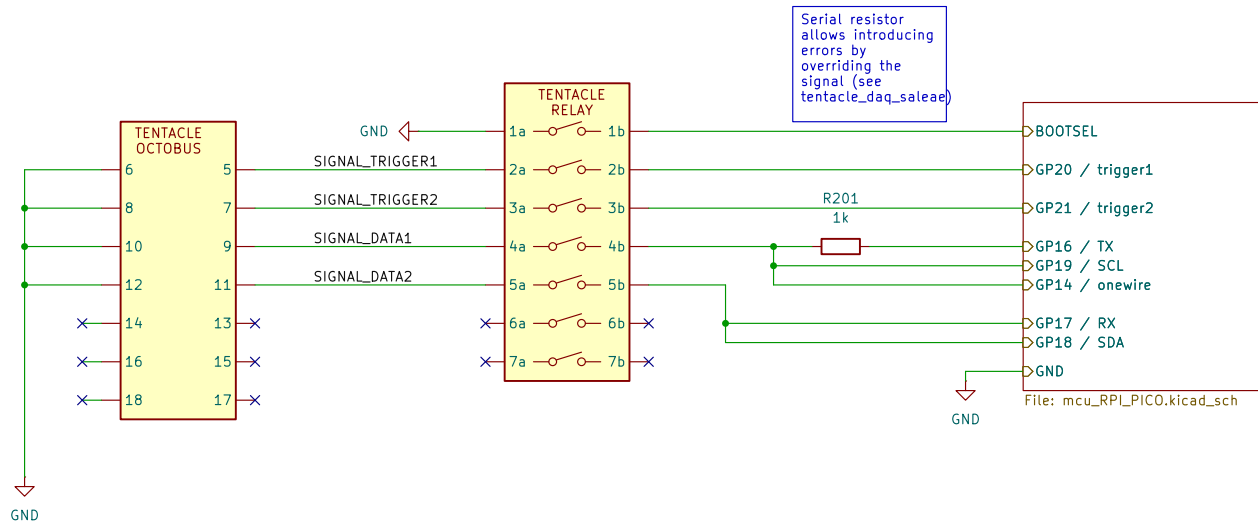


tentacle_MCU_RPI_PICO



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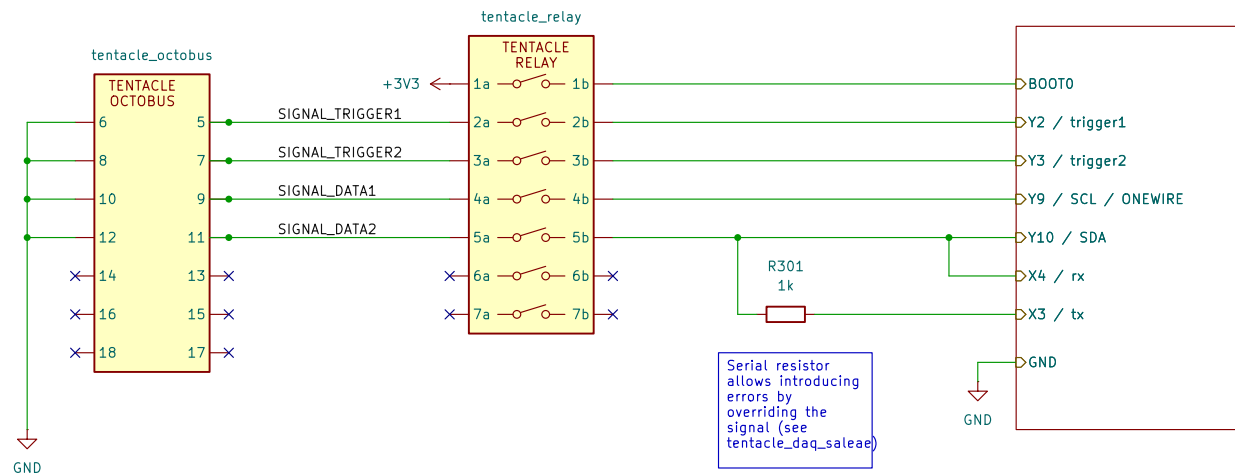
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tentacle_MCU_PYBV11



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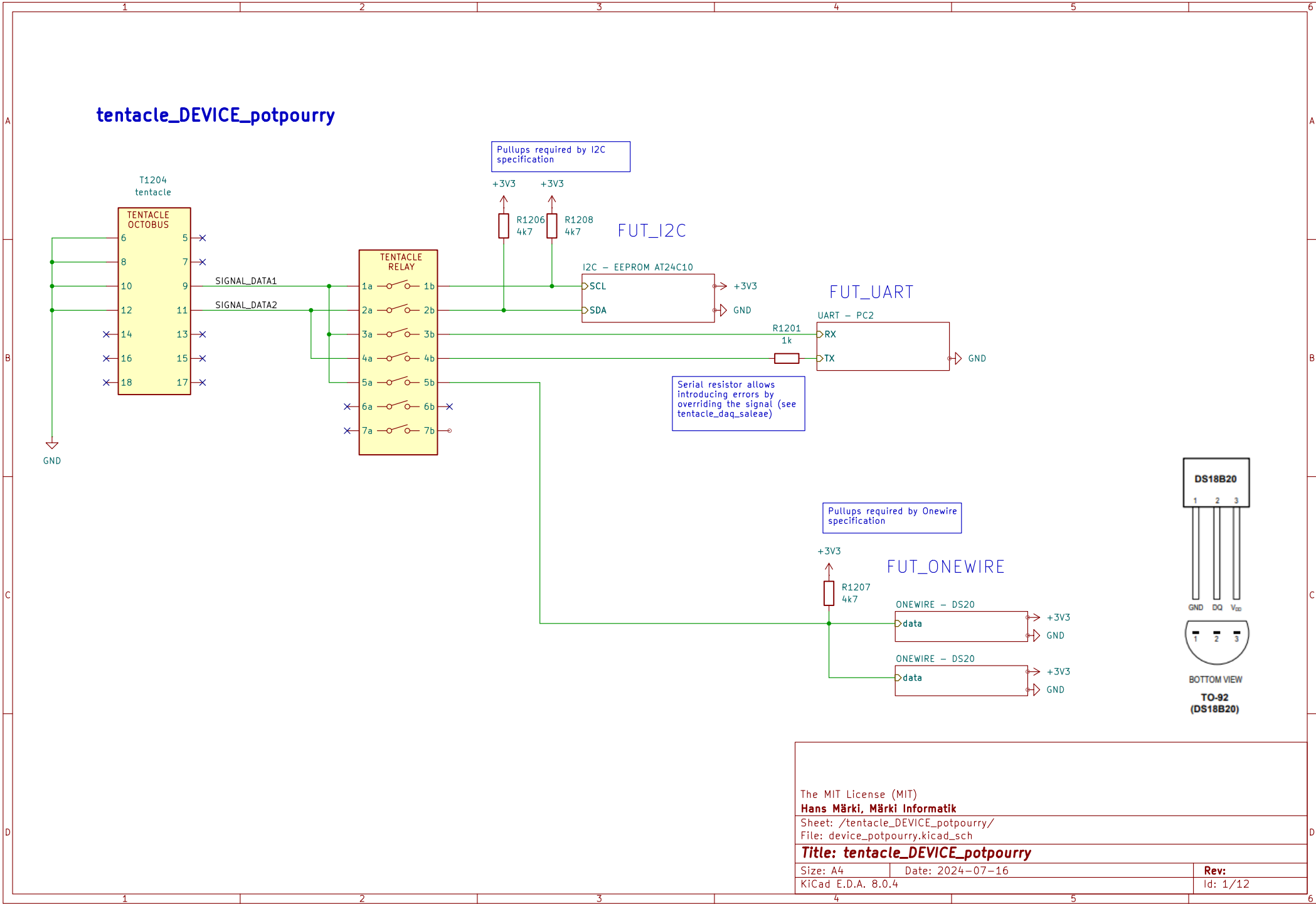
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tentacle_Daq_saleae

The schematic diagram illustrates the electrical connections for the **tentacle_Daq_saleae** circuit. It features three primary components: a **TENTACLE OCTOPUS**, a **TENTACLE RELAY**, and a **TENTACLE GPIO**.

Component Pinouts:

- TENTACLE OCTOPUS:** Pins 6, 8, 10, 12, 14, 16, 18 on the left; pins 5, 7, 9, 11, 13, 15, 17 on the right.
- TENTACLE RELAY:** Pins 1a, 2a, 3a, 4a, 5a, 6a, 7a on the left; pins 1b, 2b, 3b, 4b, 5b, 6b, 7b on the right.
- TENTACLE GPIO:** Pins GPIO9, GPIO10, GPIO11, GPIO12, GPIO13, GPIO14, GPIO15.

Signal Connections:

- SIGNAL_TRIGGER1:** Connects OCTOPUS pin 5 to RELAY pin 1a.
- SIGNAL_TRIGGER2:** Connects OCTOPUS pin 7 to RELAY pin 2a.
- SIGNAL_DATA1:** Connects OCTOPUS pin 9 to RELAY pin 3a.
- SIGNAL_DATA2:** Connects OCTOPUS pin 11 to RELAY pin 4a.
- trigger1:** Connects GPIO9 to RELAY pin 1b.
- trigger2:** Connects GPIO10 to RELAY pin 2b.
- error_data1:** Connects GPIO11 to RELAY pin 3b.
- error_data2:** Connects GPIO12 to RELAY pin 4b.

Power and Grounding:

- The OCTOPUS is connected to GND at pin 12.
- The RELAY is connected to GND at pin 1b.
- The GPIO is connected to GND at pin GPIO13.
- Pull-down resistors **R501 (10k)** and **R502 (10k)** are connected to GND at RELAY pins 1b and 2b, respectively.

Note: These pull-downs keep the signals at GND while the MCU board powercycles. Without these pull-downs, the signal jump to high (saleae seems to have pull ups).

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