ESPC2-12

Product Specification

-2.4GHz Wi-Fi and BLE5.0 Combo Module

Version: 1.0 Date: July.20, 2022

Features

General

- Chip: ESP32-C2, ESP8684H2/H4
- Module Size:16mm*24mm*3mm
- 2MByte embedded flash
- 576KB ROM
- 272KB SRAM

Wi-Fi Features

- IEEE 802.11 b/g/n-compliant
- Center frequency range of operating channel: 2412 ~ 2484 MHz
- Supports 20 MHz bandwidth in 2.4 GHz band
- 1T1R mode with data rate up to 72.2 Mbps
- TX/RX A-MPDU, TX/RX A-MSDU
- Immediate Block ACK
- Fragmentation and defragmentation
- Automatic Beacon monitoring (hardware TSF)
- 3 x virtual Wi-Fi interfaces
- Simultaneous support for Infrastructure BSS in Station mode, Soft-AP mode, Station + Soft-AP mode, and promiscuous mode

Bluetooth Features

- Bluetooth LE: Bluetooth 5
- Speed: 125 Kbps, 500 Kbps, 1 Mbps, 2 Mbps
- Advertising extensions
- Multiple advertisement sets
- Channel selection algorithm #2 JOSTONES OF THE

Peripheral Interfaces

- GPIO * 13;
- UART;
- IIC;

- SPI;
- EN;
- PWM x 6;
- ADC;
- Working Temperature: -40°C-105°C

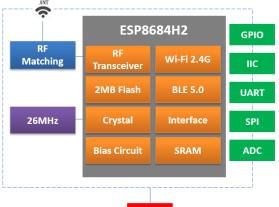
Applications

- Serial transparent transmission;
- Wi-Fi prober;
- Smart power plug/Smart LED light;
- Mesh networks:
- Sensor networks;
- Over-the-top (OTT) devices;
- Wireless location system beacon;
- Industrial field bus;

Module Type

	THE STATE OF THE S	Dr.				
	Name	Antenna Type				
	ESPC2-12	PCB ANT				
200	ESPC2-12E	U.F.L Ipex				

Module Structure



Update Record

Date Version		Update		
2022-07-20	V1.0	First released		

Table of Contents

1. Introduction	1
2. Interface Definition	3
3. Size and Layout	5
4. Electronica Characteristics	6
5. Power Consumption	7
6. Wi-Fi RF Characteristics	7
7. Bluetooth LE Radio	9
8. Recommended Sold Temperature Curve	11
9. Minimum User System	11
10. Recommended Layout Design	12
11. Peripheral Design Suggestion	13
12. Product Handling	14
13. U.F.L RF Connector	14
14. Packing Instruction	15
15. Module Schematic	16

1. Introduction

ESPC2-12 Wi-Fi and BLE coexistence Module is a highly integrated single-chip low power 802.11bgn Wireless LAN (WLAN) network controller. It combines a RISC CPU, WLAN MAC, a 1T1R capable WLAN baseband, RF, and Bluetooth in a single chip. It also provides a bunch of configurable GPIO, which are configured as digital peripherals for different applications and control usage.

ESPC2-12 Module use ESP8684 as Wi-Fi and BLE coexistence SOC chip.

ESPC2-12 Module integrates internal memories for complete Wi-Fi protocol functions. The embedded memory configuration also provides convenient application developments.

ESPC2-12 module supports the standard IEEE802.11 b/g/n/e/i protocol and the complete TCP/IP protocol stack. User can use it to add the Wi-Fi function for the installed devices, and also can be viewed as an independent network controller. Anyway, ESPC2-12 Wi-Fi module provides many probabilities with the best price.

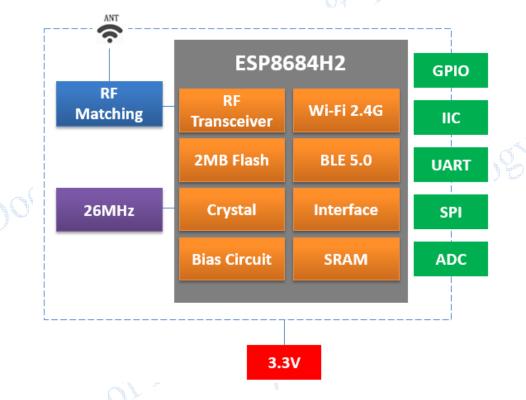


Fig.1.1 ESPC2-12 Module Structure

Technical parameters for ESPC2-12 are listed as follows.

Table.1.1 ESPC2-12 Parameters

Type	Item	Parameter		
	Frequency	2.4G~2.5G (2412M~2484M)		
		802.11b: +20.5 dBm		
	Transmit power	802.11g: +19 dBm		
		802.11n: +18 dBm		
		802.11b: -90 dBm (11Mbps)		
Wi-Fi	Receiver sensitivity	802.11g: -77 dBm (54Mbps)		
	Part of the second seco	802.11n: -74 dBm(MCS7)		
		-25dB @802.11b,11Mbps @21dBm		
	EVM	-27dB @802.11n,54Mbps @19dBm		
		-29dB @802.11n,HT20,MCS7 @18.5dBm		
	Antenna	PCB antenna		
BLE	RF power control range	-27~20dBm		
	CPU (1)	32-bit RISC CPU		
	Interface	UART/SDIO/SPI/I2C/GPIO/PWM		
TT 1	Working voltage	3.0V ~ 3.6V		
Hardware	Working temperature	-40°C ~ 105°C		
~(0)	Environment temperature	-40°C ~ 105°C		
	Shape	16mm x 24mm x 3mm		
	Wi-Fi working mode	STA, Soft-AP and sniffer modes		
	Security mode	WPS / WEP / WPA / WPA2 / WPA3		
Software	Update firmware	UART Download		
	Software develop	SDK		
	Network protocol	IPv4, TCP/UDP/HTTP/FTP/MQTT		
	CANAL STATE			
x (**)				
Docto				

2. Interface Definition

ESPC2-12 Wi-Fi & BLE module interface definition is shown as below $_{\circ}$

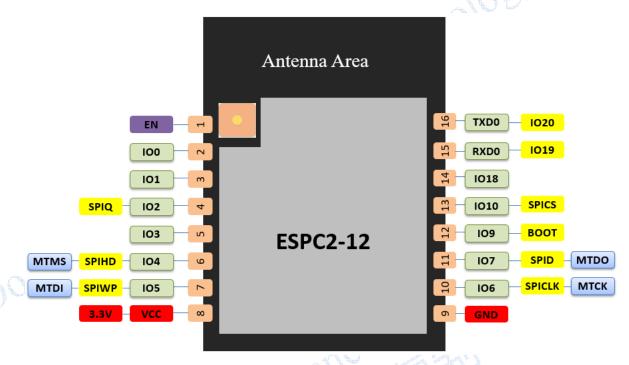


Fig.2.1 ESPC2-12 Pins Definition

Working modes and pins function is shown in Table 2.1.

Table.2.1Working Mode

	Table.2.1Wo	orking Mode	
	Mode	IO9 Voltage Level	
	UART Download Mode	LOW	
Berry Comments of the Comments	Flash Boot Mode	HIGH (Default)	

Table.2.2 Pins Function Definition

Num.	Pin Name	Type	Function
1	EN	I/O	Chip enable; Internal Pull-up. HIGH: enable the chip
2	IO0	I/O	GPIO0, ADC1_CH0
3	IO1	I/O	GPIO1, ADC1_CH1
4	IO2	I/O	GPIO2, ADC1_CH2, FSPIQ
5	IO3	I/O	GPIO3, ADC1_CH3
6	IO4	I/O	GPIO4, ADC1_CH4, MTMS, FSPIHD

	1		,
7	IO5	I/O	GPIO5,MTDI, FSPIWP
8	VCC	P	Power, 3.3V/500mA Recommended
9	GND	P	Power, 3.3V/500mA Recommended
10	IO6	I/O	GPIO6, MTCK, FSPICLK
11	IO7	I/O	GPIO7, MTDO, FSPID
12	IO9	I/O	GPIO9
13	IO10	I/O	GPIO10, FSPICS0
14	IO18	I/O	GPIO18
15	RXD0	I/O	GPIO19
16	TXD0	I/O	GPIO20
		and the same of th	MONEY TEAM

3. Size and Layout

Size for ESPC2-12 can be shown as follows.

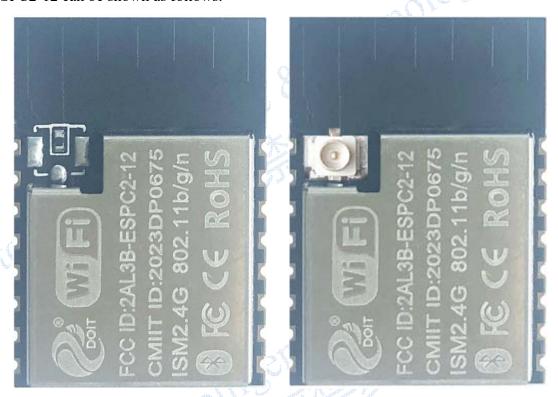
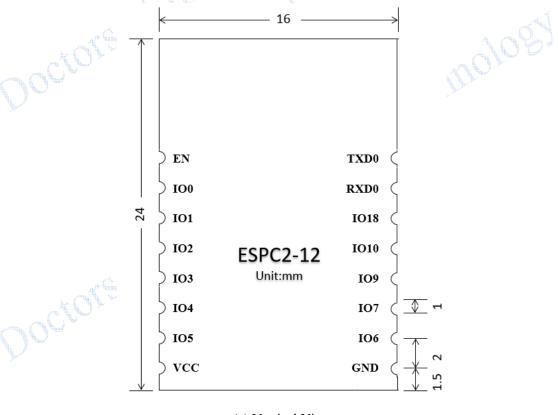
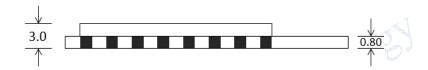


Fig.3.1 Shape for ESPC2-12 12E





(b) Side View

Fig.3.2 Size for ESPC2-12

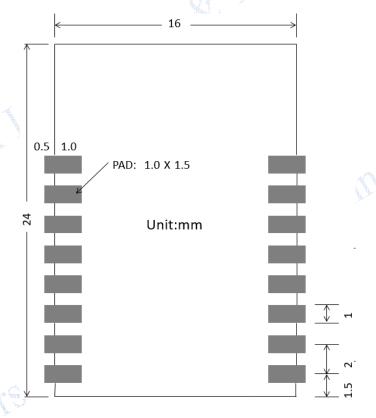


Fig. 3.3 PCB Layout for ESPC2-12

4. Electronica Characteristics

Table.4.1 Electronica Characteristics

		F F tar F					
Paramete	rs	Condition	Min Classical		Max	Unit	
Store Ten	nperature	<u>-</u>	-40	Normal	150	$^{\circ}$ C	
Sold Temperature		IPC/JEDEC J-STD-020			260	$^{\circ}$	
Working	Voltage		3.0	3.3	3.6	V	
	V _{IL}		-0.3	-	0.25*VDD		
I/O	V _{IH}	,r _	0.75*VDD	-	VDD+0.3	V	
1/0	V_{OL}	-	-	-	0.1*VDD	_	
	V _{OH}	-	0.8*VDD	-	-		
Electrostatic Release Quantity (Human model)		TAMB=25℃	-	-	2	KV	
Electrostatic Release Quantity (Machine model)		TAMB=25°C	-	-	0.5	KV	

5. Power Consumption

Table.5.1 Power Consumption

Parameters	Min	Classical	Max	Unit
RX 11b/g/n, HT20	- ()	<u> </u>	65	mA
TX 11b, 1Mbps @21dBm		-	370	
TX 11g, 54Mbps @19dBm		-	320	mA
TX 11n, HT20, MCS7, @18.5dBm		-	300	mA
Modem-sleep, CPU is powered on @80MHz	-	15	-	mA
Light-sleep	-	140	-	űA .
Deep-sleep, RTC timer + RTC memory	-	5		uA
Power off, CHIP_PU is set to low level	-		0	uA

The peak current consumption of ESPC2-12 exceed 400mA when the module start work (RF calibration work consumes maximum current). Therefore, the recommended power supply is no less than 500mA.

Note:

- 1. Active Mode: CPU and RF are all turned on.
- 2. Modem-sleep Mode: CPU is turned on. RF and baseband are turned off, but the communication is still connected.
- 3. Light-sleep Mode: CPU is turned off. RTC/external interrupt/MAC can wake up the chip. The communication is still connected.
- 4. Deep-sleep Mode: Only RTC is turned on.

6. Wi-Fi RF Characteristics

The data in the following table is gotten when voltage is 3.3V in the indoor temperature environment.

Table.6.1 Wi-Fi TX Characteristics

Parameters	Min	Classical	Max	Unit
Input frequency	2412	-	2484	MHz
802.11b @1Mbps,11Mbps	-	20.5	-	dBm
802.11g @6Mbps	-	20.0	-	dBm

802.11g @54Mbps	-	18.0	-	dBm
802.11n,HT20 MCS0	-	19.0		dBm
EVM @11b,1Mbps@20dBm	-	-25	39"	dBm
EVM @11g,54Mbps@19dBm	- (-26	-	dBm
EVM @11n,MCS7@18.5dBm		-29	-	dBm

Table.6.2 Wi-Fi RX Sensitivity

Parameters	Min	Classical	Max	Unit
802.11b,1Mbps	-	-98	-	dBm
802.11b,11Mbps	-	-90.	-	dBm
802.11g,6Mbps	-	-92	to	dBm
802.11g,54Mbps	-	-76		dBm
802.11n,HT20,MCS0	- 1	-92	-	dBm
802.11n,HT20,MCS3		-85		dBm
802.11n,HT20,MCS7	- 1	-74	-	dBm

Table.6.3 Wi-Fi RX Characteristics

Parameters	Min	Classical	Max	Unit
MAX RX Level @11b,1Mbps	-	5	- ^	dBm
MAX RX Level @11b,11Mbps	-	5		dBm
MAX RX Level @11g,6Mbps	-	5	## -	dBm
MAX RX Level @11g,54Mbps	a t	0	-	dBm
MAX RX Level @11n,HT20,MCS0	1	5		dBm
MAX RX Level @11n,HT20,MCS7			-	dBm
RX Adjacent Channel Rejection@11b,1Mbps	- 7	35	•	dB
RX Adjacent Channel Rejection@11b,11Mbps	-	35	-	dB
RX Adjacent Channel Rejection@11g,6Mbps	-	31	•	dB
RX Adjacent Channel Rejection@11g,54Mbps	-	20	•	dB
RX Adjacent Channel Rejection@11n,HT20,MCS0	-	16	-	dB
RX Adjacent Channel Rejection@11n,HT20,MCS7	-	25	-	dB

7. Bluetooth LE Radio

Table.7.1 TX Transmitter General Characteristics

Min	Classical	Max	Unit
-24	3	20	dBm
-	-38	•	dBm
	-41	•	dBm
	249	1	kHz
-	246		kHz
-	-43	-	dBm
-	-44		dBm
-	497		kHz
	589		kHz
1.0	-32		dBm
- bases	-38	-	dBm
	273	-	kHz
7_#	243	-	kHz
	4 / > \	-24 33841 - 249 - 2464344 - 497 - 5893238 - 273	-24 3 203841 249 24643 497 58932 38 273 -

Table.7.2 RX Transmitter General Characteristics

Par	ameters	Min	Classical	Max	Unit
10	Sensitivity @30.8% PER	-	-98	-	dBm
	Maximum received signal @30.8% PER	91	8	-	dBm
	Co-channel C/I	-	8	-	dB
	Image frequency		-33	-	dB
1	Adjacent channel to image frequency @F =F _{image} +1		-32	-	dB
M	Adjacent channel to image frequency @F =F _{image} -1	_	-34	-	dB
	Adjacent channel selectivity @ F =F0+1	-	-1	-	dB
	Adjacent channel selectivity @ F =F0-1	-	-3	-	dB
0	Adjacent channel selectivity @ F ≥F0+3	-	-33	-	dB
<i>70</i> **	Adjacent channel selectivity @ $F \leq F0-3$	-	-31	-	dB
	Sensitivity @30.8% PER	-	-95	-	dBm

	Maximum received signal @30.8% PER	-	8	- 1	dBm
•	Co-channel C/I	-	9		dB
·	Image frequency	- 1	-35	-	dB
2 M	Adjacent channel to image frequency @F =F _{image} +2	6	-35	-	dB
1V1	Adjacent channel to image frequency @F =F _{image} -2	_	-11	-	dB
·	Adjacent channel selectivity @ F =F0+2		-11	-	dB
·	Adjacent channel selectivity @ F =F0-2	# -	-7	-	dB
·	Adjacent channel selectivity @ F ≥F0+6	-	-39	-	dB
•	Adjacent channel selectivity @ F ≤F0-6	-	-33	-	dB
,mag	Sensitivity @30.8% PER	-	-106	- (1)	dBm
	Maximum received signal @30.8% PER	-	8		dBm
,	Co-channel C/I	<u></u>	3	-	dB
•	Image frequency	D	-41	-	dB
1 2	Adjacent channel to image frequency @F =F _{image} +1	- Justinist	-7	-	dB
5 K	Adjacent channel to image frequency @F =F _{image} -1		-5	-	dB
	Adjacent channel selectivity @ F =F0+2		-35	-	dB
*	Adjacent channel selectivity @ F = F0-2	-	-34	-	dB
*	Adjacent channel selectivity @ F ≥F0+3	-	-41		dB
	Adjacent channel selectivity @ F ≤F0-3	-	-45		dB
	Adjacent channel selectivity @ $F \ge F0+3$ Adjacent channel selectivity @ $F \le F0-3$				

Jechnolog 8. Recommended Sold Temperature Curve

(1) Reflow Times <= 2 times (Max.)

(2) Max Rising Slope: 3°C/sec

(3) Max Falling Slope: -3 ℃/sec

(4) Over 217°C Time: 60~120sec

(5) Peak Temp:240°C~250°C

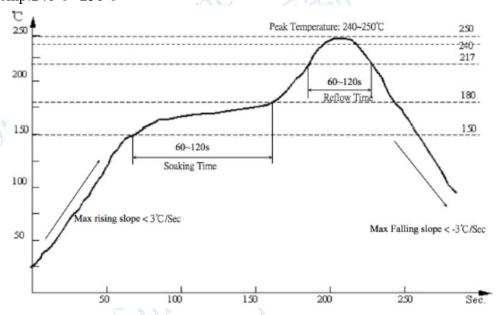


Fig.8.1 Recommended Reflow Profile

9. Minimum User System

This module can work just at 3.3V voltage condition:



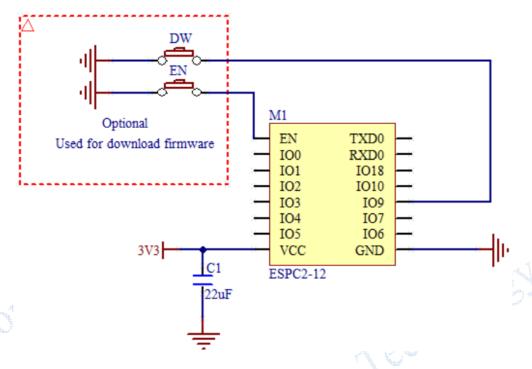


Fig.9.1 Minimum System

Note:

- (1) The working voltage for module is DC 3.3V;
- (2) The max current from IO of this module is 40mA;
- (3) Wi-Fi module is at download mode: IO9 is LOW level, then module reset to power;
- (4) Wi-Fi module is connected to RXD of the other MCU, and TXD is connected to RXD of the other MCU.

10. Recommended Layout Design

ESPC2-12 module can be sold on PCB board directly. For the high RF performance for the device, please notice the placement of the module. There are three ways to use the module for Wi-Fi Module with PCB antenna.

Solution 1: optical solution. The Wi-Fi module is placed on the side of the board, and the antennas are all exposed, and there is no metal material around the antenna, including wires, metal casings, weight plates, and the like.

Solution 2: sub-optical solution. The Wi-Fi module is placed on the side of the board, and the antenna below is hollowed out. There is a gap of not less than 5 mm reserved with the PCB, and there is no metal material around the antenna, including wires, metal casings, weight plates, and the like.

Solution 3: The Wi-Fi module is placed on the side of the board, and the PCB area under the antenna is empty, and copper cannot be laid.

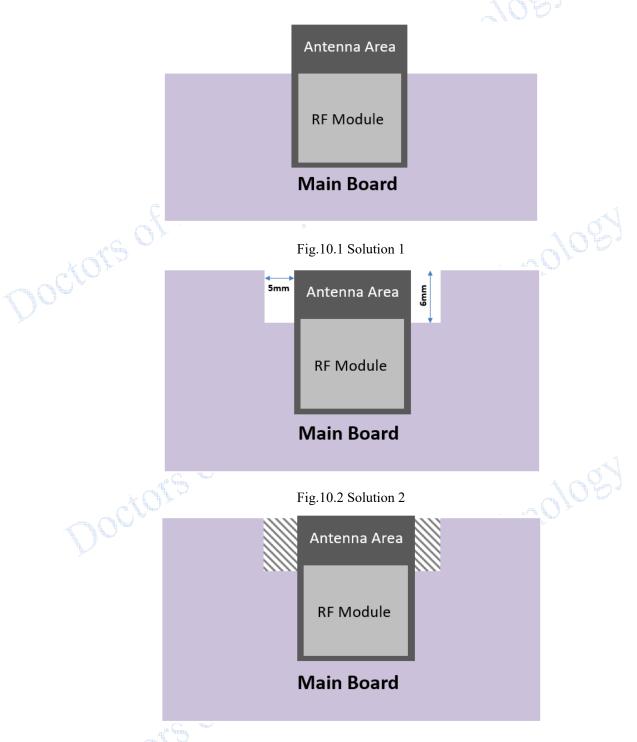


Fig.10.3 Solution 3

11. Peripheral Design Suggestion

Wi-Fi module is already integrated into high-speed GPIO and Peripheral interface, which may be generated the switch noise. If there is a high request for the power consumption and EMI characteristics,

it is suggested to connect a serial 10~100 ohm resistance, which can suppress overshoot when switching power supply, and can smooth signal. At the same time, it also can prevent electrostatic discharge (ESD).

12. Product Handling

12.1 Storage Conditions

The products sealed in moisture barrier bags (MBB) should be stored in a non-condensing atmospheric environment of < 40 °C and 90%RH. The module is rated at the moisture sensitivity level (MSL) of 3. After unpacking, the module must be soldered within 168 hours with the factory conditions 25 \pm 5 °C and 60%RH. If the above conditions are not met, the module needs to be baked.

12.2 Electrostatic Discharge (ESD)

- Human body model (HBM): ±2000 V
- Charged-device model (CDM): ±500 V

13. U.F.L RF Connector

ESPC2-12-E module use U.F.L type RF connector for external antenna connection. (IPEX V1.0).

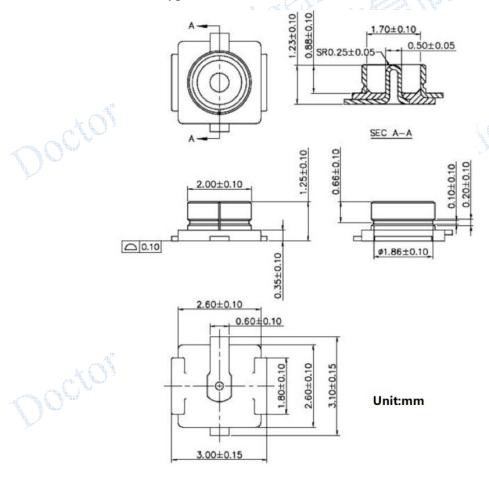


Fig.13.1 U.F.L RF Connector

14. Packing Instruction

The product is packed in a tray, as shown in the following figure.

The size of the single box is: 340 x 360 x 60mm, and 800 pieces module is in the box. And the outer box size is 355 x 375 x 325mm, including 5 single box which include 4000 pieces module.



15. Module Schematic

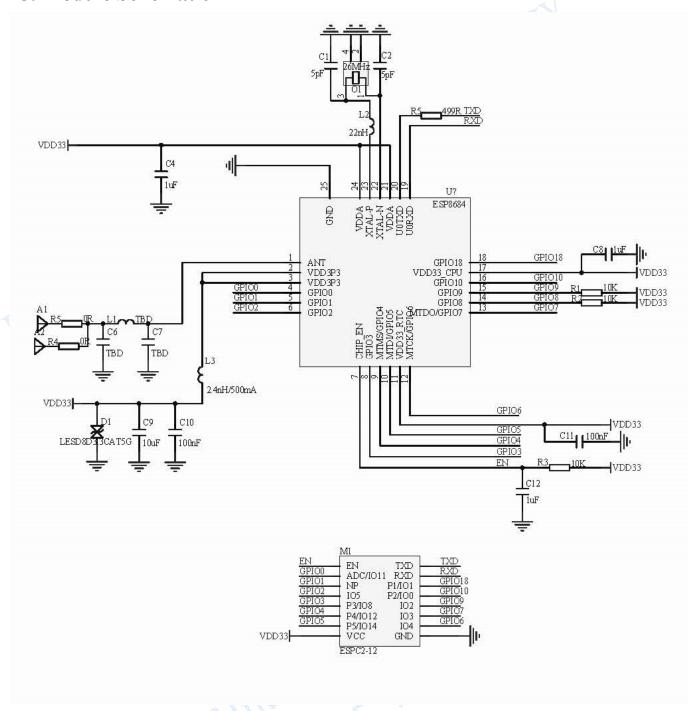


Fig.15.1 Module Schematic

FCC WARNING

FCC Caution: Any changes or modifications not expressly

Approved by the party responsible for compliance could void the user's authority to operate this equipment.

This device complies with Part 15 of the FCC Rules.

Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

This device and its antenna(s) must not be co-located or operating in conjunction with any other antenna or transmitter.

15.105 Information to the user.

(b) For a Class B digital device or peripheral, the instructions furnished the user shall include the following or similar statement, placed in a prominent location in the text of the manual:

Note: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- —Reorient or relocate the receiving antenna.
- —Increase the separation between the equipment and receiver.
- —Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- —Consult the dealer or an experienced radio/TV technician for help.

This equipment complies with FCC radiation exposure limits set forth for an uncontrolled environment. This equipment should be installed and operated with minimum distance 20 cm between the radiator and your body. Radiation Exposure Statement:

This equipment complies with FCC radiation exposure limits set forth for an uncontrolled environment.

This transmitter must not be co-located or operating in conjunction with any other antenna or transmitter.

The availability of some specific channels and/or operational frequency bands are country dependent and are firmware programmed at the factory to match the intended destination.

The firmware setting is not accessible by the end user.

The final end product must be labelled in a visible area with the following: "Contains Transmitter Module "2AL3B-ESPC2-12"