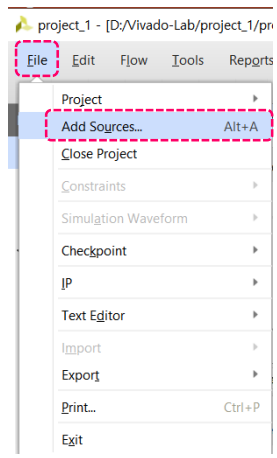
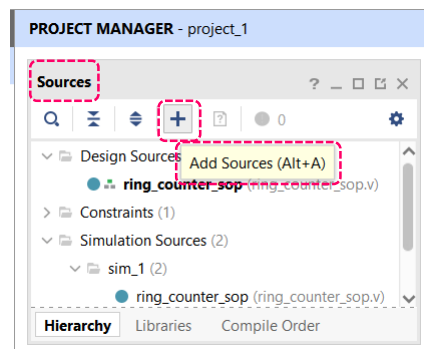
Xilinx Vivado Design Suite

4.1 설계입력 – Add Sources

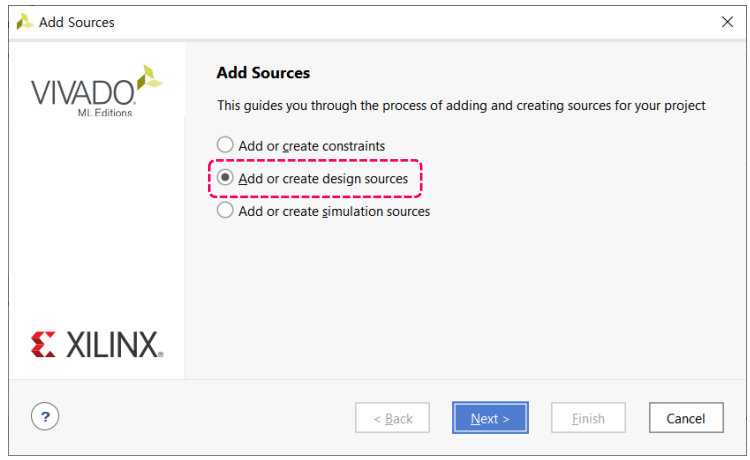
❑ **Add Sources**





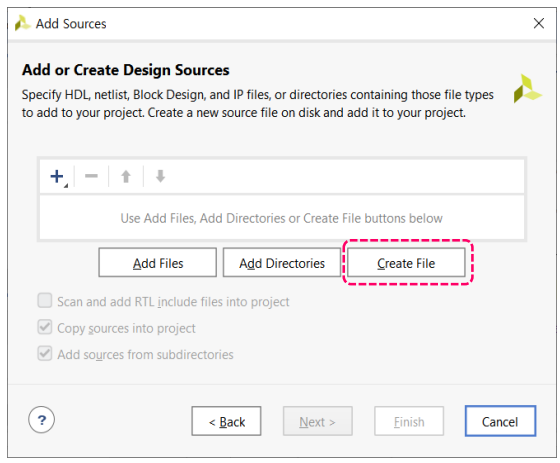
4.1 설계입력 – Add Sources

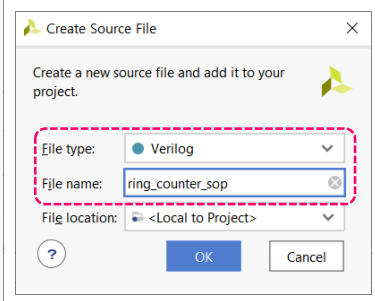
❑ Add or Create Design Sources



4.1 설계입력 – Add Sources

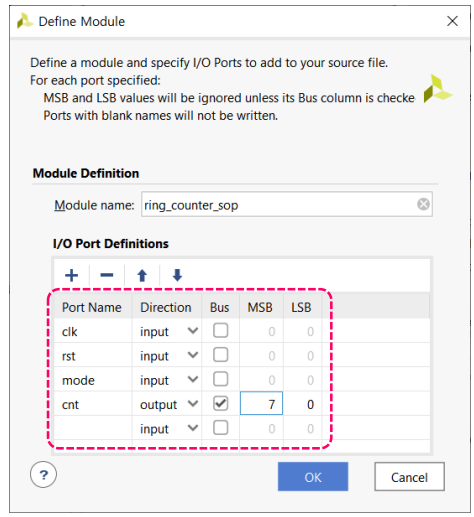
❑ Create Design Sources



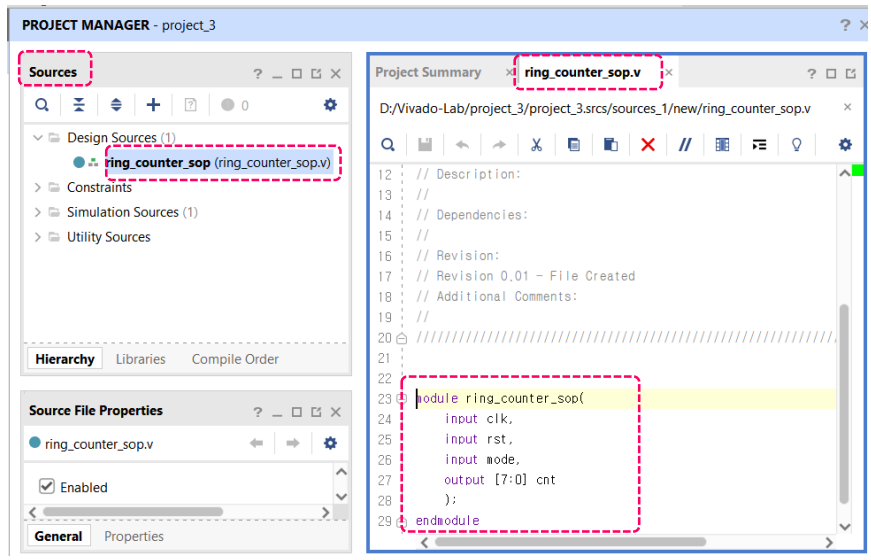


4.2 설계입력 – Define Module

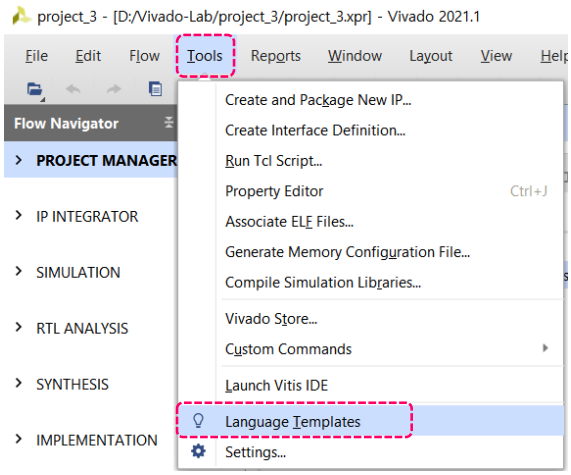
❑ **Define Module; 회로의 입력과 출력 포트 지정**



4.3 설계입력 – Text Editor



4.4 설계입력 – Language Templates



4.4 설계입력 – Language Templates