## Project Build Up - Error

[Reference]







➤ CustomIP 에 inout port 사용시

https://support.xilinx.com/s/question/0D54U000 05ZMzumSAD/how-to-correctly-connectpackaged-ip-to-a-bidirectional-inoutport?language=en\_US

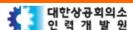
➤ Inout 포트가 output 포트로만 적용됨. (input x)

diagram. This approach did NOT work when synthesized with the default values of "Generate Output Products"... by default that selects Synthesis Option "Out of context per IP". Using these defaults the top-level INOUT port/pin worked as an output only, and always read 0 for input. I had to change the Synthesis Option from the default to "Global" to get things working. No clue why, but that worked.



▶ 오류 해결을 위하여 Synthesis 옵션 Global로 수정





## ※오류해결

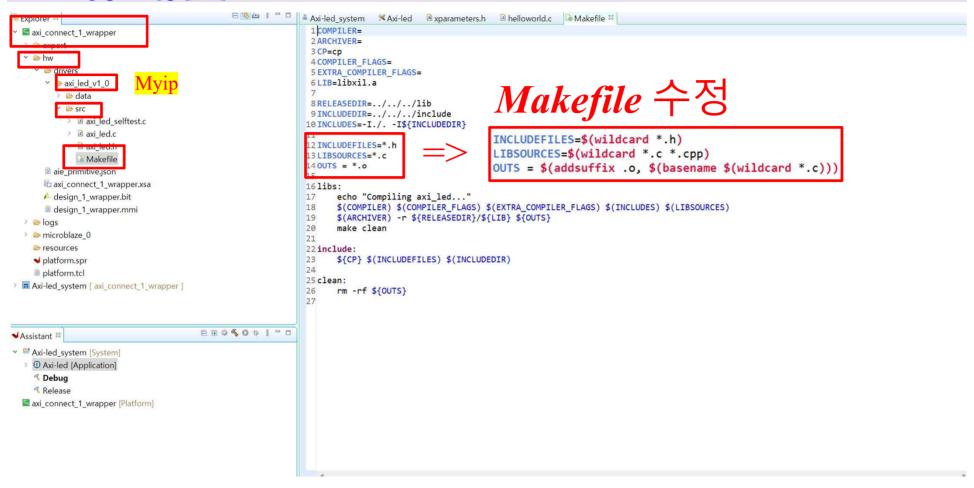
- ➤ Linux가 아닌 Window 사용자
- ➤ MyIp 포함 HW를 vitis에서 Project Build시 오류 발생

- ▶오류 해결을 위하여 MyIp Makefile 수정
- ➤ hw, microbalze\_0 모두 수정해야함.

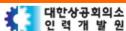




## ▶ Linux가 아닌 Window 사용자

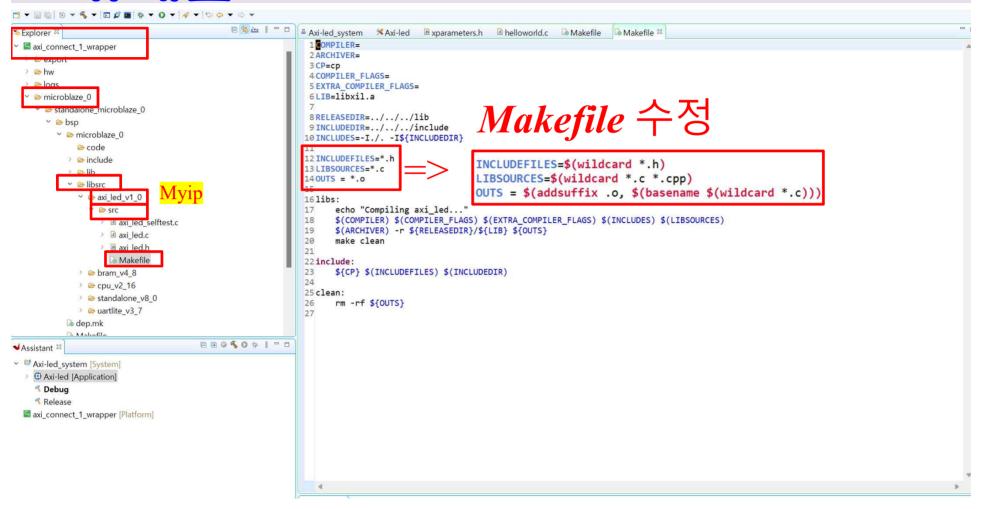


- ➤ INCLUDEFILES=\$(wildcard \*.h)
- ➤ LIBSOURCES=\$(wildcard \*.c \*.cpp)
- > OUTS = \$(addsuffix .o, \$(basename \$(wildcard \*.c)))





## ▶ Linux가 아닌 Window 사용자



- ➤ INCLUDEFILES=\$(wildcard \*.h)
- ➤ LIBSOURCES=\$(wildcard \*.c \*.cpp)
- $\triangleright$  OUTS = \$(addsuffix .o, \$(basename \$(wildcard \*.c)))

