



**leti**

# Leti-UTSOI 2.2.0 User's Manual

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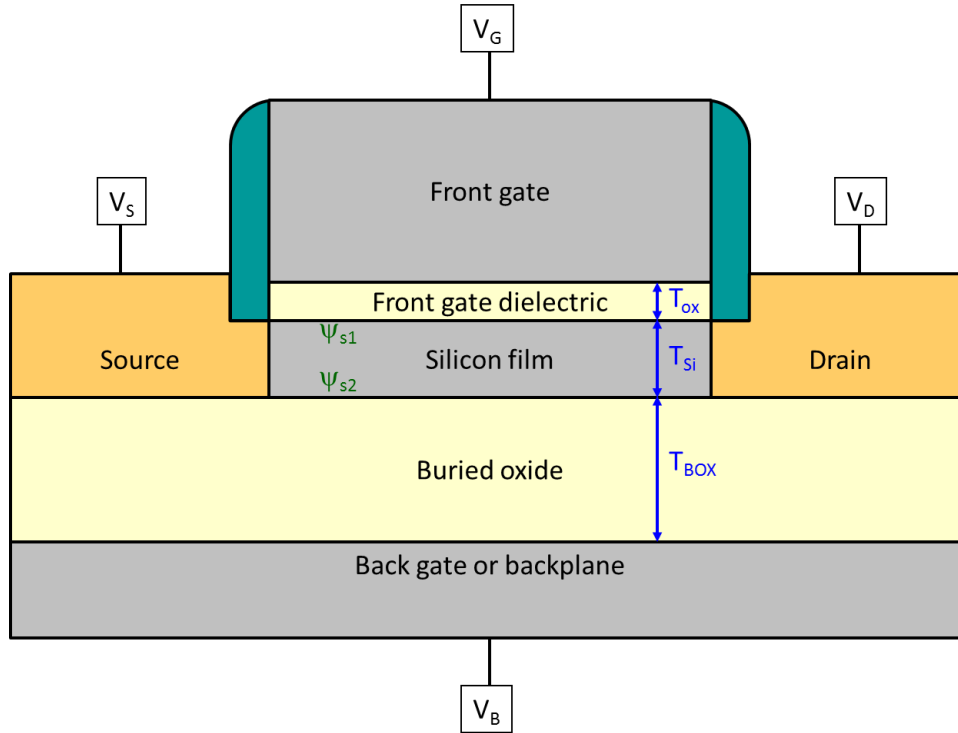
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# 1 Introduction

Leti-UTSOI2 is the second version of the Leti-UTSOI model, compact model dedicated to Fully-Depleted on Silicon-On-Insulator (FDSOI) technologies with low-doped channel, developed at CEA-LETI. The device architecture described by this model is illustrated in Figure 1.1.



*Figure 1.1- Schematic cross-section of a FDSOI transistor, describing the characteristic thicknesses and the surface potentials at the front ( $\psi_{s1}$ ) and back ( $\psi_{s2}$ ) interfaces.*

## 1.1 Model structure

As described in Figure 1.2, the model structure is the same as in the different versions of Leti-UTSOI1 [1] [2] and, thus, similar to PSP [3] [4]. It is based on a hierarchical construction featuring two levels of parameter set:

- A local mode, in which the knowledge of the device geometry (channel length and width) is not needed. In this mode, the local parameter values are directly obtained from the model cards.
- A global mode, in which the local parameters are computed from the global model card and the device geometry through scaling laws (see paragraph 3.1). The so-computed local parameters are then used to compute the model equations. In this mode, some local parameters can also be modified according to the stress model (see paragraph 3.2).

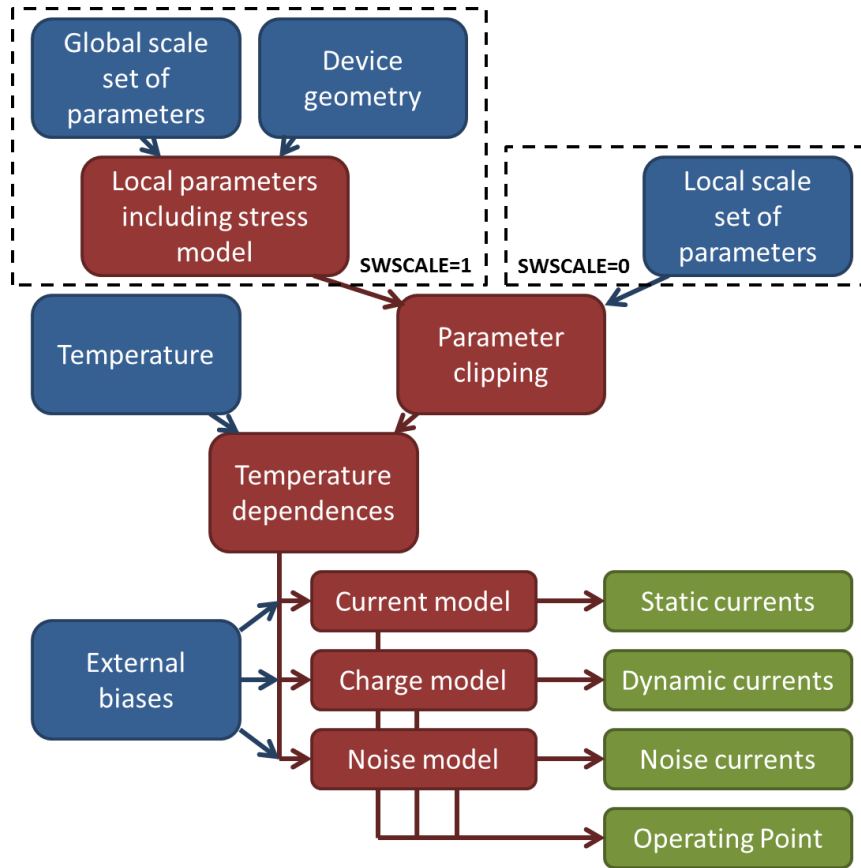


Figure 1.2- Model structure, showing the two scale levels of parameter set: local (SWSCALE=0) and global (SWSCALE=1).

## 1.2 Device physics

In terms of device physics, all the features included in Leti-UTSOI1 are also present in Leti-UTSOI2, but Leti-UTSOI2 includes some additional ingredients.

First, Leti-UTSOI2 has been developed with the aim of being able to describe device operations with a strongly inverted back interface. Thus, an original analytical procedure has been developed to calculate the exact values of the surface potentials at front and back interfaces in all operating conditions (see **Error! Reference source not found.**). Besides this surface potential calculation, new drain current (paragraph 4.4) and intrinsic charge (paragraph 4.8) models have also been developed. It should be noticed that this new model core is valid not only for FDSOI technologies, but more generally for all Independent Double Gate device architectures. An overview of Leti-UTSOI2 can be found in [5] and detailed description of the model core in [6] [7] [8].

Second, in Leti-UTSOI2 is introduced the effect of backplane depletion (paragraph 4.3), computed through a bulk MOSFET like surface potential calculation. This effect can be activated or not through the value of the SWSUBDEP flag. If the flag is set to 0, the backplane is assumed metallic, as in UTSOI1.

Third, Leti-UTSOI2 offers the possibility to define two different junctions at source and drain sides, as done in PSP. Junction related parameters are thus duplicated, the drain related parameters having the same name as their source side counterparts plus a final "D". This possible junction asymmetry can be activated or not thanks to the SWJUNASYM flag. When it is de-activated, drain related parameters are ignored (see paragraph 3.3).



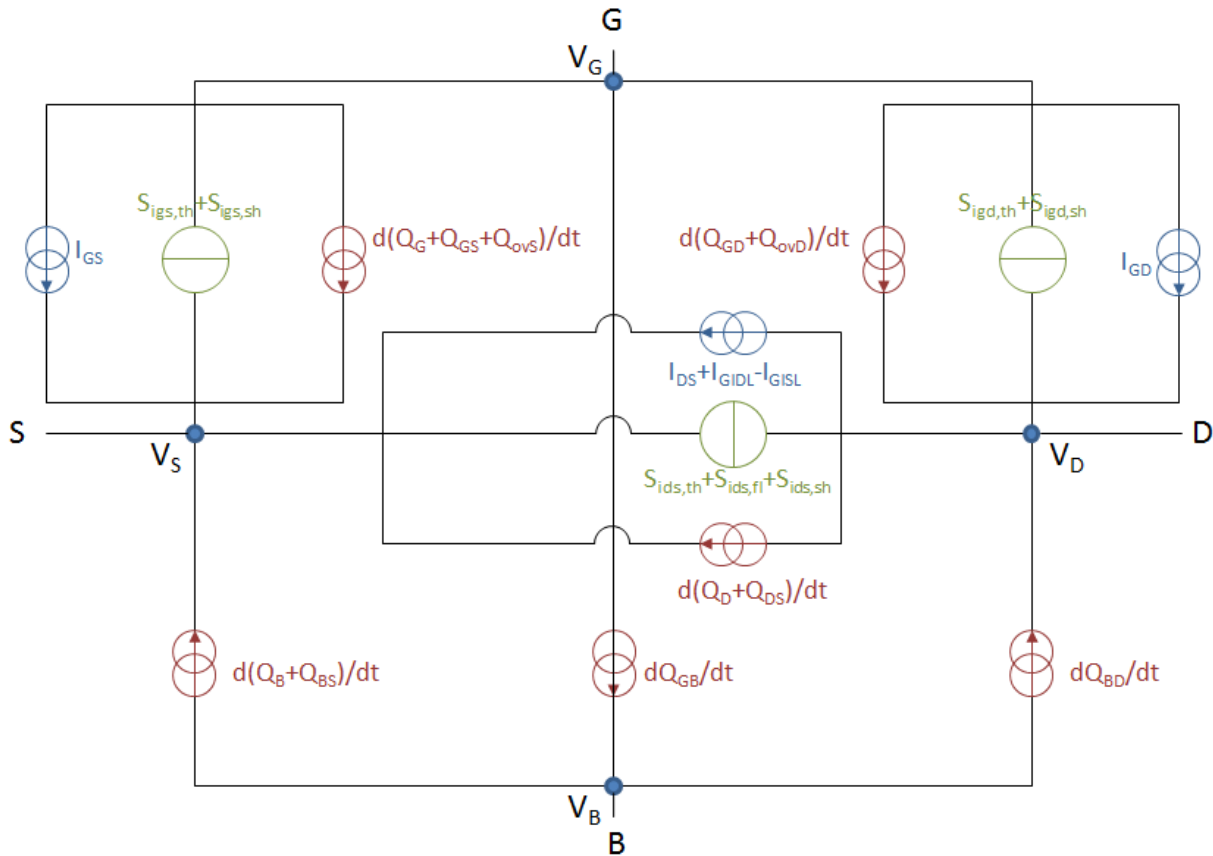
Finally, it should be noticed that the Leti-UTSOI1 possibility to switch from external to internal series resistance through the SWRSMOD flag is no longer included in Leti-UTSOI2.

Note that, from version 2.1.0, the temperature node of the transistor (Tnode), that gives channel temperature elevation induced by self-heating, is accessible from the circuit netlist (i.e. declared as “inout”). So, from version 2.1.0, transistor instantiation accepts 5 nodes: drain, gate, source, bulk and internal temperature.

Model evolutions from UTSOI 2.0.0 version are detailed in part 9.

### 1.3 Output currents

Figure 1.3 describes how the quantities calculated afterwards in this documentation are sent as output currents in the different branches. Static currents are in blue, dynamic currents in red and noise currents in green. It should be noticed that a multiplication parameter  $Mult_f = MULTxNF$  is applied to all the output quantities specified in Figure 1.3.



*Figure 1.3- Description of the output currents in the device branches. All quantities are multiplied by  $Mult_f$ .*

## 2 Constants and parameters

### 2.1 Constants

The following tables detail the signification and the values of the constants used in the model.

Symbol	Unit	Description	Value
$k_B$	J/K	Boltzmann constant	$1.3806488 \cdot 10^{-23}$
$\hbar$	J s	Reduced Planck constant	$1.054571726 \cdot 10^{-34}$
$q$	C	Elementary charge	$1.602176565 \cdot 10^{-19}$
$m_0$	kg	Electron intrinsic mass	$9.10938291 \cdot 10^{-31}$
$QM_N$	$V^{1/3}nm^{2/3}$	Constant for quantum confinement of electrons	1.27520989
$QM_P$	$V^{1/3}nm^{2/3}$	Constant for quantum confinement of holes	1.54120870

$QM_N$  and  $QM_P$  are equal to  $\left(\frac{9\pi\hbar}{4\sqrt{2qm_{conf}}}\right)^{2/3}$  with  $m_{conf}=0.918m_0$  for electrons and  $m_{conf}=0.52m_0$  for holes.

Symbol	Unit	Description	Value
$\epsilon_{ox}$	F/m	Permittivity of silicon dioxide (relative value is 3.9)	$3.45313 \cdot 10^{-11}$
$\epsilon_{Si}$	F/m	Permittivity of silicon (relative value is 11.8)	$1.04479 \cdot 10^{-10}$
$\epsilon_{Ge}$	F/m	Permittivity of germanium (relative value is 16.2)	$1.43438 \cdot 10^{-10}$
$E_{g0,Si}$	V	Bandgap voltage for silicon at 0K	1.17
$E_{g0,Ge}$	V	Bandgap voltage for germanium at 0K	0.744
$\alpha_{Si}$	V/K	First bandgap temperature dependence for silicon	$4.730 \cdot 10^{-4}$
$\alpha_{Ge}$	V/K	First bandgap temperature dependence for germanium	$4.774 \cdot 10^{-4}$
$\beta_{Si}$	K	Second bandgap temperature dependence for silicon	636
$\beta_{Ge}$	K	Second bandgap temperature dependence for germanium	235
$C_G$	---	Non linearity coefficient for SiGe bandgap	-0.4
$n_{i,fact,300}$	$m^{-3}$	Intrinsic concentration pre-factor for silicon at 300K	$4.05 \cdot 10^{25}$

### 2.2 Model selection, effect activation switches and reference temperature

The table below details the parameters used for model selection, the flags used to activate different physical effects, the selection of the transistor type and the reference temperature. Except for QMC, the value of the flags is 0 (effect de-activated) or 1 (effect activated). QMC parameter is used to adjust quantum confinement effects and can take other values than 0 or 1.

Name	Unit	Definition	Default	Min	Max
<b>SWSCALE</b>	---	Scale level: 0 = Local, 1 = Global	0	---	---
<b>VERSION</b>	---	Model version	2.20	---	---
<b>SWCLIPCHK</b>	---	Flag for warnings about parameter clipping	0	---	---
<b>SWSUBDEP</b>	---	Flag for backplane depletion effect	0	---	---
<b>SWIGATE</b>	---	Flag for gate current model	0	---	---
<b>SWGIDL</b>	---	Flag for gate induced source/drain leakage model	0	---	---
<b>SWSHE</b>	---	Flag for self-heating effect	0	---	---
<b>SWIGN</b>	---	Flag for induced gate noise model	1	---	---
<b>SWJUNASYM</b>	---	Flag for source/drain junction asymmetry	0	---	---
<b>QMC</b>	---	Quantum confinement coefficient	1.0	0.0	---
<b>TYPE</b>	---	Channel type: +1 = NMOS, -1 = PMOS	1	---	---
<b>TR</b>	°C	Temperature of parameter extraction	21.0	-273.0	---

**Warning:** Parameter **TYPE** defines the transistor type for the VerilogA version of the model. To define the transistor type when using an implemented version of Leti-UTSOI2 in a SPICE simulator, please refer to the corresponding simulator documentation.

## 2.3 Instance parameters

The following table describes the instance parameters.

Name	Unit	Definition	Default	Min	Max
<b>L</b>	m	Drawn channel length	$10^{-6}$	$10^{-9}$	---
<b>W</b>	m	Drawn channel width	$10^{-6}$	$10^{-9} \times \text{NF}$	---
<b>ASOURCE</b>	m <sup>2</sup>	Source region area	$10^{-12}$	0.0	---
<b>ADRAIN</b>	m <sup>2</sup>	Drain region area	$10^{-12}$	0.0	---
<b>PSOURCE</b>	m	Source region perimeter	$10^{-6}$	0.0	---
<b>PDRAIN</b>	m	Drain region perimeter	$10^{-6}$	0.0	---
<b>SA</b>	m	Distance between active edge and poly at source side	0.0	0.0	---
<b>SB</b>	m	Distance between active edge and poly at drain side	0.0	0.0	---
<b>SD</b>	m	Distance between neighbouring fingers	0.0	0.0	---
<b>NF</b>	---	Number of fingers	1	1	---
<b>MULT</b>	---	Number of devices in parallel	1	0	---
<b>DELVTO</b>	V	Threshold voltage shift parameter	0.0	---	---
<b>FACTUO</b>	---	Low field mobility pre-factor	1.0	0.0	---

## 2.4 Scaling parameters

In the following table are given the geometrical parameters that link drawn, physical and electrical transistor dimensions.

Name	Unit	Definition	Default	Min	Max
<b>LVARO</b>	m	Long channel difference between physical and drawn gate lengths	0.0	---	---
<b>LVARL</b>	---	Length dependence of physical to drawn gate length difference	0.0	---	---
<b>LVARW</b>	---	Width dependence of physical to drawn gate length difference	0.0	---	---
<b>LAP</b>	m	Effective channel length reduction per side	0.0	---	---
<b>WVARO</b>	m	Wide channel difference between physical and drawn active width	0.0	---	---
<b>WVARL</b>	---	Length dependence of physical to drawn active width difference	0.0	---	---
<b>WVARW</b>	---	Width dependence of physical to drawn active width difference	0.0	---	---
<b>WOT</b>	m	Effective channel width reduction per side	0.0	---	---
<b>DLQ</b>	m	Effective channel length additional offset for charge model	0.0	---	---
<b>DWQ</b>	m	Effective channel width additional offset for charge model	0.0	---	---

## 2.5 Stress model parameters

Besides the existing STI-based stress model, a new stress model dedicated to strained-FDSOI technologies has been introduced in Leti-UTSOI from version 2.2.0. Selection between the two models is done through **SWSTRESS** flag: 1 for STI-based model, 2 for strained-SOI model. Stress effect can be de-activated by setting the flag to 0.

Name	Unit	Definition	Default	Min	Max
<b>SWSTRESS</b>	---	Stress model selection flag	1	---	---
<b>SAREF</b>	m	Reference distance between active edge and poly from one side	$10^{-6}$	$10^{-9}$	---
<b>SBREF</b>	m	Reference distance between active edge and poly from other side	$10^{-6}$	$10^{-9}$	---

### 2.5.1 Parameters for SWSTRESS=1

The next table describes the parameters used for the STI-stress model. This model comes originally from BSIM4.4 and has been slightly modified in PSP.

Name	Unit	Definition	Default	Min	Max
<b>WLOD</b>	m	Width parameter	0.0	---	---
<b>KUO</b>	m	Mobility degradation/enhancement coefficient	0.0	---	---

<b>KVSAT</b>	---	Saturation velocity degradation/enhancement coefficient	0.0	-1.0	1.0
<b>TKUO</b>	---	Temperature dependence of KUO	0.0	---	---
<b>LKUO</b>	m <sup>LLODKUO</sup>	Length dependence of KUO	0.0	---	---
<b>WKUO</b>	m <sup>WLODKUO</sup>	Width dependence of KUO	0.0	---	---
<b>PKUO</b>	m <sup>sumLODKUO</sup>	Cross-term dependence of KUO	0.0	---	---
<b>LLODKUO</b>	---	Length parameter for mobility stress effect	0.0	0.0	---
<b>WLODKUO</b>	---	Width parameter for mobility stress effect	0.0	0.0	---
<b>KVTHO</b>	Vm	Threshold voltage shift parameter	0.0	---	---
<b>LKVTHO</b>	m <sup>LLODVTH</sup>	Length dependence of KVTHO	0.0	---	---
<b>WKVTHO</b>	m <sup>WLODVTH</sup>	Width dependence of KVTHO	0.0	---	---
<b>PKVTHO</b>	m <sup>sumLODVTH</sup>	Cross-term dependence of KVTHO	0.0	---	---
<b>LLODVTH</b>	---	Length parameter for threshold voltage stress effect	0.0	0.0	---
<b>WLODVTH</b>	---	Width parameter for threshold voltage stress effect	0.0	0.0	---
<b>STETAO</b>	m	ETAO shift factor related to threshold voltage change	0.0	---	---
<b>LODETAO</b>	---	ETAO shift modification factor	1.0	0.0	---

Note: sumLODKUO and sumLODVTH refer to LLODKUO+WLODKUO and LLODVTH+WLODVTH respectively.

## 2.5.2 Parameters for SWSTRESS=2

The next table describes the parameters used for the strained-SOI dedicated model.

Name	Unit	Definition	Default	Min	Max
<b>STRLAMBDA</b>	m	Strain relaxation characteristic length	10 <sup>-7</sup>	10 <sup>-9</sup>	10 <sup>-5</sup>
<b>STRALPHA</b>	---	Strain relaxation asymmetry parameter	3.0	0.5	---
<b>STRDVFBO</b>	V	Threshold shift parameter	0.0	---	---
<b>STRWDVFBO</b>	---	Width dependence of threshold shift parameter	0.0	---	---
<b>STRDCFL</b>	---	DIBL variation parameter	0.0	---	---
<b>STRRUO</b>	---	Mobility degradation/enhancement coefficient	0.0	---	---
<b>STRTRUO</b>	---	Temperature dependence of STRRUO	0.0	---	---
<b>STRRVSAT</b>	---	Saturation velocity degradation/enhancement coefficient	0.0	---	---

## 2.6 Process parameters

In the following tables are given all the local model parameters, in bold. Each local parameter is immediately followed by its related global scale parameters, in italic.

Name	Unit	Definition	Default	Min	Max
<b>TOXE</b>	m	Front gate equivalent oxide thickness (EOT)	2 10 <sup>-9</sup>	3 10 <sup>-10</sup>	10 <sup>-6</sup>
<i>TOXEO</i>	<i>m</i>	<i>Geometry independent global scale parameter for TOXE</i>	<i>2 10<sup>-9</sup></i>	<i>3 10<sup>-10</sup></i>	<i>10<sup>-6</sup></i>
<b>TSI</b>	m	Silicon or SiGe film thickness	10 <sup>-8</sup>	3 10 <sup>-9</sup>	2 10 <sup>-8</sup>
<i>TSIO</i>	<i>m</i>	<i>Geometry independent global scale parameter for TSI</i>	<i>10<sup>-8</sup></i>	<i>3 10<sup>-9</sup></i>	<i>2 10<sup>-8</sup></i>
<b>XGE</b>	---	Fraction of germanium content in the channel	0.0	0.0	1.0
<i>XGEO</i>	---	<i>Geometry independent global scale parameter for XGE</i>	<i>0.0</i>	<i>0.0</i>	<i>1.0</i>
<b>TBOX</b>	m	Back gate equivalent oxide thickness (EOT)	10 <sup>-7</sup>	3 10 <sup>-10</sup>	10 <sup>-6</sup>
<i>TBOXO</i>	<i>m</i>	<i>Geometry independent global scale parameter for TBOX</i>	<i>10<sup>-7</sup></i>	<i>3 10<sup>-10</sup></i>	<i>10<sup>-6</sup></i>
<b>NCH</b>	cm <sup>-3</sup>	Thin film doping: positive = p-type, negative = n-type	0.0	0.0	10 <sup>19</sup>
<i>NCHO</i>	<i>cm<sup>-3</sup></i>	<i>Geometry independent global scale parameter for NCH</i>	<i>0.0</i>	<i>0.0</i>	<i>10<sup>19</sup></i>
<b>NSUB</b>	cm <sup>-3</sup>	Backplane doping level: positive = p-type, negative = n-type	3 10 <sup>18</sup>	10 <sup>16</sup>	10 <sup>21</sup>
<i>NSUBO</i>	<i>cm<sup>-3</sup></i>	<i>Geometry independent global scale parameter for NSUB</i>	<i>3 10<sup>18</sup></i>	<i>10<sup>16</sup></i>	<i>10<sup>21</sup></i>
<b>CT</b>	---	Interface states factor	0.0	0.0	---
<i>CTO</i>	---	<i>Geometry independent global scale parameter for CT</i>	<i>0.0</i>	<i>0.0</i>	---
<b>TOXP</b>	m	Front gate physical oxide thickness	2 10 <sup>-9</sup>	3 10 <sup>-10</sup>	10 <sup>-6</sup>
<i>TOXP0</i>	<i>m</i>	<i>Geometry independent global scale parameter for TOXP</i>	<i>2 10<sup>-9</sup></i>	<i>3 10<sup>-10</sup></i>	<i>10<sup>-6</sup></i>
<b>NOV</b>	cm <sup>-3</sup>	Effective doping level of overlap-LDD regions	10 <sup>20</sup>	10 <sup>15</sup>	10 <sup>21</sup>
<i>NOVO</i>	<i>cm<sup>-3</sup></i>	<i>Geometry independent global scale parameter for NOV</i>	<i>10<sup>20</sup></i>	<i>10<sup>15</sup></i>	<i>10<sup>21</sup></i>
<b>NOVD</b>	cm <sup>-3</sup>	Effective doping level of overlap-LDD regions at drain side	10 <sup>20</sup>	10 <sup>15</sup>	10 <sup>21</sup>

NOVDO	cm <sup>-3</sup>	Geometry independent global scale parameter for NOVDO	10 <sup>20</sup>	10 <sup>15</sup>	10 <sup>21</sup>
VFB	V	Front gate workfunction referenced to Si midgap at TR	0.0	---	---
VFBO	V	Long and wide channel value of VFB	0.0	---	---
VFBL	V	Channel length scaling parameter of VFB	0.0	---	---
VFBLEXP	---	Channel length scaling exponent of VFB	2.0	---	---
VFBL2	---	Second order channel length dependence of VFB	0.0	0.0	---
VFBLEXP2	---	Second order channel length scaling exponent of VFB	2.0	---	---
VFBW	V	Channel width scaling parameter of VFB	0.0	---	---
VFBLW	---	Channel area scaling parameter of VFB	0.0	---	---
VFBB	V	Back gate workfunction offset at TR	0.0	---	---
VFBO	V	Long and wide channel value of VFBB	0.0	---	---
VFBLBO	---	Back to front interface asymmetry factor applied to VFBL	0.0	0.0	---
STVFB	V/K	Temperature dependence of VFB and VFBB	0.0	---	---
STVFBO	V/K	Long and wide channel value of STVFB	0.0	---	---
STVFBL	---	Channel length scaling parameter of STVFB	0.0	---	---
STVFBW	---	Channel width scaling parameter of STVFB	0.0	---	---
STVFBLW	---	Channel area scaling parameter of STVFB	0.0	---	---

## 2.7 Gate to interface coupling parameters

Name	Unit	Definition	Default	Min	Max
CICF	---	Long channel front interface coupling coefficient	1.0	0.1	10.0
CICFO	---	Geometry independent global scale parameter for CICF	1.0	0.1	10.0
CIC	---	Long channel back interface coupling coefficient	1.0	0.1	10.0
CICO	---	Geometry independent global scale parameter for CIC	1.0	0.1	10.0
PSCE	---	Short channel coupling attenuation parameter	0.0	0.0	5.0
PSCEL	---	Channel length scaling parameter of PSCE	0.0	---	---
PSCELEXP	---	Channel length scaling exponent of PSCE	2.0	---	---
PSCEW	---	Channel width scaling parameter of PSCE	0.0	---	---
PSCEB	---	Short channel back to front interface asymmetry factor	1.0	0.0	---
PSCEBO	---	Geometry independent global scale parameter for PSCEB	1.0	0.0	---
NSDDC	cm <sup>-3</sup>	Source/drain effective doping level for DC model	10 <sup>22</sup>	10 <sup>18</sup>	10 <sup>22</sup>
NSDDCO	cm <sup>-3</sup>	Geometry independent global scale parameter for NSDDC	10 <sup>22</sup>	10 <sup>18</sup>	10 <sup>22</sup>
PSCEDLB	---	Back bias dependence of short channel effect modulation	0.0	0.0	---
PSCEDLBO	---	Geometry independent global scale parameter for PSCEDLB	0.0	0.0	---
PNCE	---	Narrow channel effect on body factor	0.0	-1.0	1.0
PNCEW	---	Channel width scaling parameter of PNCE	0.0	---	---

## 2.8 Drain Induced Barrier Lowering parameters

Name	Unit	Definition	Default	Min	Max
CF	---	DIBL parameter at TR	0.0	0.0	---
CFL	---	Channel length scaling parameter of CF	0.0	---	---
CFLEXP	---	Channel length scaling exponent of CF	2.0	---	---
CFW	---	Channel width scaling parameter of CF	0.0	---	---
CFB	---	DIBL back to front interface asymmetry factor	1.0	0.0	---
CFBO	---	Geometry independent global scale parameter for CFB	1.0	0.0	---
STCF	K <sup>-1</sup>	Temperature dependence of CF	0.0	---	---
STCFL	K <sup>-1</sup>	Channel length scaling parameter for STCF	0.0	---	---
CFD	V	Drain voltage dependence parameter of DIBL	0.2	0.05	---
CFDO	V	Geometry independent global scale parameter for CFD	0.2	0.05	---
CFDL	---	DIBL modulation due to Leff dependence on biases	0.0	---	---
CFDLL	---	Channel length scaling parameter of CFDL	0.0	---	---
CFDLW	---	Channel width scaling parameter of CFDL	0.0	---	---
CFDLB	---	Back bias dependence of DIBL modulation	0.0	0.0	---
CFDLBO	---	Geometry independent global scale parameter for CFDLB	0.0	0.0	---

## 2.9 Mobility parameters

Name	Unit	Definition	Default	Min	Max
<b>BETN</b>	$\text{m}^2/\text{Vs}$	Front channel aspect ratio times low field mobility at TR	0.05	$10^{-10}$	---
UO	$\text{m}^2/\text{Vs}$	Front channel low field mobility at TR	0.05	---	---
FBET1	---	First length dependence modulation of BETN	0.0	---	---
FBET1W	---	Width dependence of FBET1	0.0	---	---
LP1	m	First characteristic length of BETN scaling	$10^{-8}$	$10^{-10}$	---
LP1W	---	Width dependence of LP1	0.0	---	---
FBET2	---	Second length dependence modulation of BETN	0.0	---	---
LP2	m	Second characteristic length of BETN scaling	$10^{-8}$	$10^{-10}$	---
BETW1	---	First width dependence modulation of BETN	0.0	---	---
BETW2	---	Second width dependence modulation of BETN	0.0	---	---
WBET	m	Characteristic width of BETN scaling	$10^{-8}$	$10^{-10}$	---
<b>BETNB</b>	---	Back channel over front channel low field mobility ratio	1.0	0.1	10.0
BETNBO	---	Geometry independent global scale parameter for BETNB	1.0	0.1	10.0
<b>STBET</b>	---	Temperature dependence exponent of BETN	1.5	---	---
STBETO	---	Long and wide channel value of STBET	1.5	---	---
STBETL	---	Channel length scaling parameter of STBET	0.0	---	---
STBETW	---	Channel width scaling parameter of STBET	0.0	---	---
STBETLW	---	Channel area scaling parameter of STBET	0.0	---	---
<b>CS</b>	---	Coulomb scattering parameter at TR	0.0	0.0	---
CSO	---	Long and wide channel value of CS	0.0	---	---
CSL	---	Channel length scaling parameter of CS	0.0	---	---
CSLEXP	---	Geometry independent global scale parameter for CS	1.0	---	---
CSW	---	Channel width scaling parameter of CS	0.0	---	---
CSLW	---	Channel area scaling parameter of CS	0.0	---	---
<b>CSFI</b>	---	Field dependence of Coulomb scattering at front interface	0.0	0.0	---
CSFIO	---	Geometry independent global scale parameter for CSFI	0.0	0.0	---
<b>CSBI</b>	---	Field dependence of Coulomb scattering at back interface	0.0	0.0	---
CSBIO	---	Geometry independent global scale parameter for CSBI	0.0	0.0	---
<b>STCS</b>	---	Temperature dependence exponent of CS	0.0	---	---
STCSO	---	Long and wide channel value of STCS	0.0	---	---
STCSL	---	Channel length scaling parameter of STCS	0.0	---	---
STCSW	---	Channel width scaling parameter of STCS	0.0	---	---
STCSLW	---	Channel area scaling parameter of STCS	0.0	---	---
<b>THECS</b>	---	Coulomb scattering exponent at TR	1.5	0.0	---
THECSO	---	Geometry independent global scale parameter for THECS	1.5	0.0	---
<b>STTHECS</b>	---	Temperature dependence exponent of THECS	0.0	---	---
STTHECSO	---	Geometry independent global scale parameter for STTHECS	0.0	---	---
<b>CSTHR</b>	---	Coulomb scattering threshold level	2.0	0.001	---
CSTHRO	---	Geometry independent global scale parameter for CSTHR	2.0	0.001	---
<b>CSTHRB</b>	---	Coulomb scattering threshold asymmetry parameter	1.0	0.1	---
CSTHRBO	---	Geometry independent global scale parameter for CSTHRB	1.0	0.1	---
<b>MUE</b>	cm/MV	High field mobility reduction coefficient at TR	0.0	0.0	---
MUEO	cm/MV	Geometry independent global scale parameter for MUE	0.0	0.0	---
<b>STMUE</b>	---	Temperature dependence exponent of MUE	0.0	---	---
STMUEO	---	Geometry independent global scale parameter for STMUE	0.0	---	---
<b>THEMU</b>	---	High field mobility reduction exponent at TR	1.5	0.0	---
THEMUO	---	Geometry independent global scale parameter for THEMU	1.5	0.0	---
<b>STTHEMU</b>	---	Temperature dependence exponent of THEMU	0.0	---	---
STTHEMUO	---	Geometry independent global scale parameter for STTHEMU	0.0	---	---
<b>XCOR</b>	$\text{V}^{-1}$	High field mobility non universality factor at TR	0.0	---	---
XCORO	$\text{V}^{-1}$	Long and wide channel value of XCOR	0.0	---	---
XCORL	---	Channel length scaling parameter of XCOR	0.0	---	---
XCORLEXP	---	Channel length scaling exponent of XCOR	1.0	---	---
XCORW	---	Channel width scaling parameter of XCOR	0.0	---	---

<i>XCORLW</i>	---	<i>Channel area scaling parameter of XCOR</i>	<i>0.0</i>	---	---
<b>XCORB</b>	---	Asymmetry term of non-universality factor	1.0	---	---
<i>XCORBO</i>	---	<i>Geometry independent global scale parameter for XCORB</i>	<i>1.0</i>	---	---
<b>STXCOR</b>	---	Temperature dependence exponent of XCOR	0.0	---	---
<i>STXCORO</i>	---	<i>Geometry independent global scale parameter for STXCOR</i>	<i>0.0</i>	---	---
<b>FETA</b>	---	Transverse effective field parameter	1.0	0.0	---
<i>FETAO</i>	---	<i>Geometry independent global scale parameter for FETA</i>	<i>1.0</i>	<i>0.0</i>	---

## 2.10 Series resistance parameters

Name	Unit	Definition	Default	Min	Max
<b>RS</b>	$\Omega$	Source/drain series resistance at TR	30.0	0.0	---
<i>RSW1</i>	$\Omega$	<i>Source/drain series resistance for a WEN width at TR</i>	<i>30.0</i>	---	---
<i>RSW2</i>	---	<i>Second order width scaling parameter of RS</i>	<i>0.0</i>	---	---
<b>RSIG</b>	---	Source/drain extension resistance coefficient	0.0	0.0	---
<i>RSIGO</i>	---	<i>Geometry independent global scale parameter for RSIG</i>	<i>0.0</i>	<i>0.0</i>	---
<b>STRS</b>	---	Temperature dependence exponent of RS	0.0	---	---
<i>STRSO</i>	---	<i>Geometry independent global scale parameter for STRS</i>	<i>0.0</i>	---	---
<b>RSG</b>	---	Transverse electric field dependence of RS	0.0	-0.5	---
<i>RSGO</i>	---	<i>Geometry independent global scale parameter for RSG</i>	<i>0.0</i>	<i>-0.5</i>	---
<b>RSB</b>	---	Back bias dependence of RS	0.0	---	---
<i>RSBO</i>	---	<i>Geometry independent global scale parameter for RSB</i>	<i>0.0</i>	---	---
<b>THERSG</b>	---	Transverse electric field exponent of RS	2.0	---	---
<i>THERSGO</i>	---	<i>Geometry independent global scale parameter for THERSG</i>	<i>2.0</i>	---	---

## 2.11 Velocity saturation parameters

Name	Unit	Definition	Default	Min	Max
<b>THESAT</b>	$V^{-1}$	Velocity saturation parameter at TR	0.0	0.0	---
<i>THESATO</i>	$V^{-1}$	<i>Long and wide channel parameter for THESAT</i>	<i>0.0</i>	<i>0.0</i>	---
<i>THESATL</i>	$s/m^2$	<i>Channel length scaling parameter of THESAT</i>	<i>0.0</i>	---	---
<i>THESATLEXP</i>	---	<i>Channel length scaling exponent of THESAT</i>	<i>1.0</i>	---	---
<i>THESATW</i>	---	<i>Channel width scaling parameter of THESAT</i>	<i>0.0</i>	---	---
<i>THESATLW</i>	---	<i>Channel area scaling parameter of THESAT</i>	<i>0.0</i>	---	---
<b>STTHESAT</b>	---	Temperature dependence exponent of THESAT	-0.1	---	---
<i>STTHESATO</i>	---	<i>Long and wide channel value of STTHESAT</i>	<i>-0.1</i>	---	---
<i>STTHESATL</i>	---	<i>Channel length scaling parameter of STTHESAT</i>	<i>0.0</i>	---	---
<i>STTHESATW</i>	---	<i>Channel width scaling parameter of STTHESAT</i>	<i>0.0</i>	---	---
<i>STTHESATLW</i>	---	<i>Channel area scaling parameter of STTHESAT</i>	<i>0.0</i>	---	---
<b>THESATG</b>	---	Front gate bias dependence of velocity saturation	0.0	-0.5	---
<i>THESATGO</i>	---	<i>Geometry independent global scale parameter for THESATG</i>	<i>0.0</i>	<i>-0.5</i>	---
<b>THESATB</b>	---	Back gate bias dependence of velocity saturation	0.0	-0.5	---
<i>THESATBO</i>	---	<i>Geometry independent global scale parameter for THESATB</i>	<i>0.0</i>	<i>-0.5</i>	---

## 2.12 Saturation and Channel Length Modulation parameters

Name	Unit	Definition	Default	Min	Max
<b>AX</b>	---	Linear/saturation transition exponent	8.0	1.0	16.0
<i>AXO</i>	---	<i>Long and wide channel value of AX</i>	<i>8.0</i>	---	---
<i>AXL</i>	---	<i>Channel length scaling parameter of AX</i>	<i>0.0</i>	<i>0.0</i>	---
<i>AXLEXP</i>	---	<i>Channel length scaling exponent of AX</i>	<i>1.0</i>	---	---
<b>ALP</b>	---	Channel length modulation pre-factor	0.0	0.0	---
<i>ALPL1</i>	---	<i>Channel length scaling parameter of ALP</i>	<i>0.0</i>	---	---
<i>ALPLEXP</i>	---	<i>Channel length scaling exponent of ALP</i>	<i>1.0</i>	---	---
<i>ALPL2</i>	---	<i>Second order channel length dependence of ALP</i>	<i>0.0</i>	<i>0.0</i>	---
<i>ALPLEXP2</i>	---	<i>Second order channel length scaling exponent of ALP</i>	<i>2.0</i>	---	---



ALPW	---	Channel width scaling parameter of ALP	0.0	---	---
<b>ALP1</b>	V	Channel length modulation enhancement above threshold	0.0	0.0	---
ALP1L1	---	Channel length scaling parameter of ALP1	0.0	---	---
ALP1LEXP	---	Channel length scaling exponent of ALP1	0.5	---	---
ALP1L2	---	Second order channel length dependence of ALP1	0.0	0.0	---
ALP1LEXP2	---	Second order channel length scaling exponent of ALP1	1.5	---	---
ALP1W	---	Channel width scaling parameter of ALP1	0.0	---	---
<b>ALPB</b>	---	Back bias dependence of channel length modulation	0.0	---	---
ALPBO	---	Geometry independent global scale parameter for ALPB	0.0	---	---
<b>VP</b>	V	Channel length modulation logarithm dependence factor	0.05	$10^{-10}$	---
VPO	V	Geometry independent global scale parameter for VP	0.05	$10^{-10}$	---
<b>VPG</b>	---	Transverse field dependence of CLM logarithm factor	0.0	0.0	---
VPGO	---	Geometry independent global scale parameter for VPG	0.0	0.0	---

## 2.13 Gate current parameters

Name	Unit	Definition	Default	Min	Max
<b>GCO</b>	---	Gate tunneling energy adjustment in inversion mode	0.0	-10.0	10.0
GCOO	---	Geometry independent global scale parameter for GCO	0.0	-10.0	10.0
<b>IGINV</b>	A	Gate to channel current pre-factor	0.0	0.0	---
IGINVLW	A	IGINV value for a LEN.WEN area	0.0	0.0	---
<b>IGOVINV</b>	A	Gate-overlap current pre-factor in inversion	0.0	0.0	---
IGOVINVW	A	IGOVINV value for a WEN width	0.0	0.0	---
<b>IGOVINVD</b>	A	Gate-overlap current pre-factor in inversion at drain side	0.0	0.0	---
IGOVINVDW	A	IGOVINVD for a WEN width	0.0	0.0	---
<b>IGOVACC</b>	A	Gate-overlap current pre-factor in accumulation	0.0	0.0	---
IGOVACCW	A	IGOVACC value for a WEN width	0.0	0.0	---
<b>IGOVACCD</b>	A	Gate-overlap current pre-factor in accumulation at drain side	0.0	0.0	---
IGOVACCDW	A	IGOVACCD value for a WEN width	0.0	0.0	---
<b>STIG</b>	---	Temperature dependence of all gate current pre-factors	0.0	---	---
STIGO	---	Geometry independent global scale parameter for STIG	0.0	---	---
<b>GC2CH</b>	---	Gate to channel current slope factor	0.375	0.0	10.0
GC2CHO	---	Geometry independent global scale parameter for GC2CH	0.375	0.0	10.0
<b>GC3CH</b>	---	Gate to channel current curvature factor	0.063	-2.0	2.0
GC3CHO	---	Geometry independent global scale parameter for GC3CH	0.063	-2.0	2.0
<b>GC2OVINV</b>	---	Gate-overlap current slope factor in inversion	0.375	0.0	10.0
GC2OVINVVO	---	Geometry independent global scale parameter for GC2OVINV	0.375	0.0	10.0
<b>GC3OVINV</b>	---	Gate-overlap current curvature factor in inversion	0.063	-2.0	2.0
GC3OVINVVO	---	Geometry independent global scale parameter for GC3OVINV	0.063	-2.0	2.0
<b>GC2OVACC</b>	---	Gate-overlap current slope factor in accumulation	0.375	0.0	10.0
GC2OVACCO	---	Geometry independent global scale parameter for GC2OVACC	0.375	0.0	10.0
<b>GC3OVACC</b>	---	Gate-overlap current curvature factor in accumulation	0.063	-2.0	2.0
GC3OVACCO	---	Geometry independent global scale parameter for GC3OVACC	0.063	-2.0	2.0
<b>GCDOV</b>	$V^{-1}$	High drain voltage dependence of overlap gate current	0.0	---	---
GCDOVL	$V^{-1}$	GCDOV value for a LEN length	0.0	---	---
<b>GCVDOV</b>	V	Threshold of high drain voltage effect on overlap gate current	1.0	---	---
GCVDOVO	V	Geometry independent global scale parameter for GCVDOV	1.0	---	---
<b>CHIB</b>	V	Tunneling barrier height	3.1	1.0	---
CHIBO	V	Geometry independent global scale parameter for CHIB	3.1	1.0	---
<b>NIGINV</b>	---	Gate tunneling slope adjustment in subthreshold regime	0.0	0.0	---
NIGINVO	---	Geometry independent global scale parameter for NIGINV	0.0	0.0	---

## 2.14 Gate Induced Drain/Source Leakage (GIDL/GISL) parameters

Name	Unit	Definition	Default	Min	Max
<b>AGIDL</b>	$A/V^3$	GIDL/GISL current pre-factor	0.0	0.0	---
AGIDLW	$A/V^3$	AGIDL value for a WEN width	0.0	0.0	---



<b>AGIDL</b>	$A/V^3$	GIDL current pre-factor at drain side	0.0	0.0	---
<i>AGIDLW</i>	$A/V^3$	<i>AGIDL value for a WEN width</i>	0.0	0.0	---
<b>BGIDL</b>	V	GIDL/GISL probability factor at TR	41.0	0.0	---
<i>BGIDLO</i>	V	<i>Geometry independent global scale parameter for BGIDL</i>	41.0	0.0	---
<b>BGIDLD</b>	V	GIDL probability factor at TR at drain side	41.0	0.0	---
<i>BGIDLDO</i>	V	<i>Geometry independent global scale parameter for BGIDLD</i>	41.0	0.0	---
<b>STBGIDL</b>	V/K	Temperature dependence of BGIDL	0.0	---	---
<i>STBGIDLO</i>	V/K	<i>Geometry independent global scale parameter for STBGIDL</i>	0.0	---	---
<b>STBGIDLD</b>	V/K	Temperature dependence of BGIDLD	0.0	---	---
<i>STBGIDLDO</i>	V/K	<i>Geometry independent global scale parameter for STBGIDLD</i>	0.0	---	---
<b>CGIDL</b>	$V^{-1}$	Substrate bias dependence of GIDL/GISL	0.0	---	---
<i>CGIDLO</i>	$V^{-1}$	<i>Geometry independent global scale parameter for CGIDL</i>	0.0	---	---
<b>CGIDLD</b>	$V^{-1}$	Substrate bias dependence of GIDL at drain side	0.0	---	---
<i>CGIDLDO</i>	$V^{-1}$	<i>Geometry independent global scale parameter for CGIDLD</i>	0.0	---	---
<b>DGIDL</b>	$V^{-1}$	High longitudinal field dependence of GIDL/GISL	0.0	---	---
<i>DGIDLL</i>	$V^{-1}$	<i>DGIDL value for a LEN length</i>	0.0	---	---
<b>DGIDLD</b>	$V^{-1}$	High longitudinal field dependence of GIDL at drain side	0.0	---	---
<i>DGIDLDL</i>	$V^{-1}$	<i>DGIDLD value for a LEN length</i>	0.0	---	---

## 2.15 Charge model parameters

Name	Unit	Definition	Default	Min	Max
<b>AREAQ</b>	$m^2$	Effective channel area for intrinsic charge model	$10^{-12}$	$10^{-18}$	---
<b>CGBOV</b>	F	Oxide capacitance for gate to substrate overlap	0.0	0.0	---
<i>CGBOVO</i>	F	<i>Geometry independent global scale parameter for CGBOV</i>	0.0	---	---
<i>CGBOVL</i>	F	<i>Gate length scaling parameter of CGBOV</i>	0.0	---	---
<b>NSDAC</b>	$cm^{-3}$	Source/drain effective doping level for AC model	$10^{22}$	$10^{18}$	$10^{22}$
<i>NSDACO</i>	$cm^{-3}$	<i>Geometry independent global scale parameter for NSDAC</i>	$10^{22}$	$10^{18}$	$10^{22}$
<b>FIF</b>	---	Inner fringe capacitance pre-factor	0.0	0.0	---
<i>FIFW</i>	---	<i>FIF value for a WEN width</i>	0.0	0.0	---
<b>FSCEAC</b>	---	Short channel effect adjustment factor for charge model	0.0	0.0	---
<i>FSCEACO</i>	---	<i>Geometry independent global scale parameter for FSCEAC</i>	0.0	0.0	---
<b>COV</b>	F	Overlap capacitance per side	0.0	0.0	---
<i>LOVO</i>	m	<i>Overlap length for gate/source-drain overlap capacitance</i>	0.0	0.0	---
<b>COVD</b>	F	Overlap capacitance at drain side	0.0	0.0	---
<i>LOVDO</i>	m	<i>Overlap length for gate/drain overlap capacitance</i>	0.0	0.0	---
<b>COVDL</b>	---	Overlap capacitance modulation due to Leff bias-dependence	0.0	---	---
<i>COVDLO</i>	---	<i>Wide channel parameter for COVDL</i>	0.0	---	---
<i>COVDLW</i>	---	<i>Channel width scaling parameter of COVDL</i>	0.0	---	---
<b>COVDLB</b>	---	Overlap capacitance modulation with back bias	0.0	---	---
<i>COVDLBO</i>	---	<i>Geometry independent global scale parameter for COVDLB</i>	0.0	---	---
<b>DVFBOV</b>	V	Overlap capacitance flat-band voltage adjustment	0.0	---	---
<i>DVFBOVO</i>	V	<i>Geometry independent global scale parameter for DVFBOV</i>	0.0	---	---
<b>CFR</b>	F	Outer fringe capacitance per side	0.0	0.0	---
<i>CFRO</i>	F	<i>Corner related outer fringe capacitance</i>	0.0	---	---
<i>CFRW</i>	F	<i>Outer fringe capacitance per side for a WEN width</i>	0.0	---	---
<b>CFRD</b>	F	Outer fringe capacitance at drain side	0.0	0.0	---
<i>CFRDO</i>	F	<i>Corner related outer fringe capacitance at drain side</i>	0.0	---	---
<i>CFRDW</i>	F	<i>Outer fringe capacitance at drain side for a WEN width</i>	0.0	---	---
<b>CSD</b>	F	Drain to source direct capacitance	$1.04 \cdot 10^{-18}$	0.0	---
<i>CSDO</i>	---	<i>Drain to source direct capacitance correction factor</i>	1.0	0.0	---
<b>CSDBP</b>	F/m	Drain/source to substrate perimeter capacitance	0.0	0.0	---
<i>CSDBPO</i>	F/m	<i>Geometry independent global scale parameter for CSDBP</i>	0.0	0.0	---

## 2.16 Self-heating parameters

Name	Unit	Definition	Default	Min	Max
------	------	------------	---------	-----	-----

<b>RTH</b>	K/W	Thermal resistance	$10^4$	$10^{-6}$	---
<i>RTHO</i>	K/W	<i>Geometry independent global scale parameter for RTH</i>	$10^5$	---	---
<i>RTHL</i>	---	<i>Channel length scaling parameter of RTH and CTH</i>	1.5	---	---
<i>RTHW</i>	---	<i>Channel width scaling parameter of RTH and CTH</i>	3.0	---	---
<i>RTHLW</i>	---	<i>Channel area scaling parameter of RTH and CTH</i>	4.5	---	---
<b>STRTH</b>	---	Temperature dependence of RTH	0.0	---	---
<i>STRTHO</i>	---	<i>Geometry independent global scale parameter for STRTH</i>	0.0	---	---
<b>CTH</b>	J/K	Thermal capacitance	$10^{-11}$	0.0	---
<i>CTHO</i>	J/K	<i>Geometry independent global scale parameter for CTH</i>	$10^{-12}$	---	---
<i>LAMBTHO</i>	<i>m</i>	<i>Characteristic length of thermal coupling for multifinger devices</i>	$10^{-7}$	$10^{-9}$	---
<i>FTHO</i>	---	<i>First neighbour thermal coupling factor for multifinger devices</i>	0.0	0.0	---

## 2.17 Noise model parameters

Name	Unit	Definition	Default	Min	Max
<b>FNT</b>	---	Thermal noise coefficient	1.0	0.0	---
<i>FNTO</i>	---	<i>Geometry independent global scale parameter for FNT</i>	1.0	0.0	---
<b>NFA</b>	$V^{-1}m^{-4}$	First coefficient of flicker noise	$8 \cdot 10^{22}$	0.0	---
<i>NFALW</i>	$V^{-1}m^{-4}$	<i>NFA value for a LEN.WEN area</i>	$8 \cdot 10^{22}$	0.0	---
<b>NFB</b>	$V^{-1}m^{-2}$	Second coefficient of flicker noise	$3 \cdot 10^7$	0.0	---
<i>NFBLW</i>	$V^{-1}m^{-2}$	<i>NFB value for a LEN.WEN area</i>	$3 \cdot 10^7$	0.0	---
<b>NFC</b>	$V^{-1}$	Third coefficient of flicker noise	0.0	0.0	---
<i>NFCLW</i>	$V^{-1}$	<i>NFC value for a LEN.WEN area</i>	0.0	0.0	---
<b>NFE</b>	---	Front interface transverse field effect coefficient	0.0	-1.0	1.0
<i>NFEO</i>	---	<i>Geometry independent global scale parameter for NFE</i>	0.0	-1.0	1.0
<b>NFEB</b>	---	Back interface transverse field effect coefficient	0.0	-1.0	1.0
<i>NFEBO</i>	---	<i>Geometry independent global scale parameter for NFEB</i>	0.0	-1.0	1.0
<b>EF</b>	---	Frequency dependence exponent of flicker noise	1.0	0.1	---
<i>EFO</i>	---	<i>Geometry independent global scale parameter for EF</i>	1.0	0.1	---

### 3 Geometrical dependences, stress effects and junction asymmetry

In this section, the global parameters are used to calculate local parameter values as a function of device geometry. These calculations includes geometrical scaling, modification of some parameters through the stress model and symmetrization of the source/drain junctions when the **SWJUNASYM** flag is set to 0.

#### 3.1 Scaling equations

In this part are detailed the calculation of local parameters from the global parameter set. These calculations are carried out when the scaling flag **SWSCALE** is set to 1.

##### 3.1.1 Effective dimensions

Introduction of the number of fingers

$$W_f = W/NF \quad (3.1)$$

$$A_{source,f} = ASOURCE/NF \quad (3.2)$$

$$A_{drain,f} = ADRAIN/NF \quad (3.3)$$

$$P_{source,f} = PSOURCE/NF \quad (3.4)$$

$$P_{drain,f} = PDRAIN/NF \quad (3.5)$$

$$Mult_f = MULT \times NF \quad (3.6)$$

Effective length and width for current model

$$L_{EN} = 10^{-6} \quad (3.7)$$

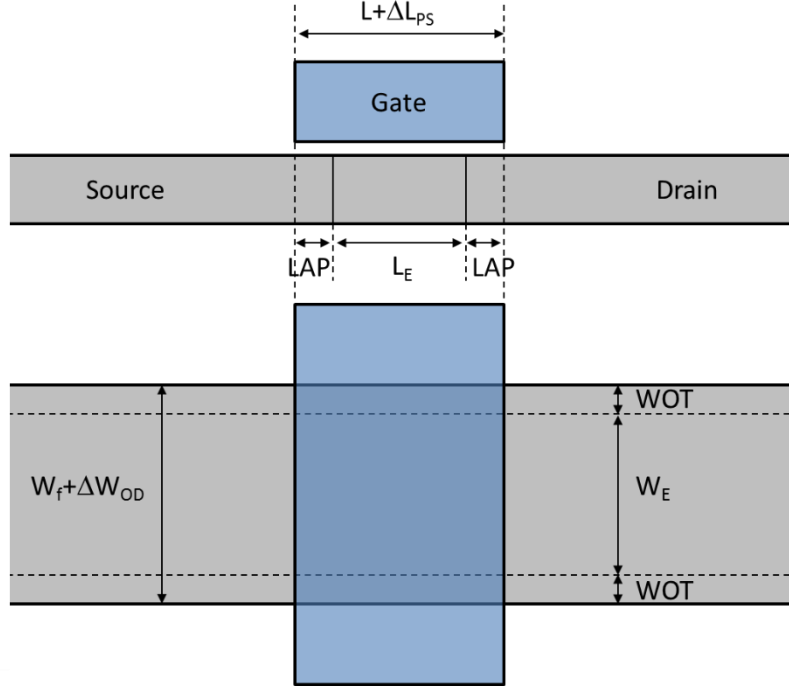
$$W_{EN} = 10^{-6} \quad (3.8)$$

$$\Delta L_{PS} = LVARO \left( 1 + LVARL \frac{L_{EN}}{L} \right) \left( 1 + LVARW \frac{W_{EN}}{W_f} \right) \quad (3.9)$$

$$\Delta W_{OD} = WVARO \left( 1 + WVARL \frac{L_{EN}}{L} \right) \left( 1 + WVARW \frac{W_{EN}}{W_f} \right) \quad (3.10)$$

$$L_E = L + \Delta L_{PS} - 2 \times LAP \quad (3.11)$$

$$W_E = W_f + \Delta W_{OD} - 2 \times WOT \quad (3.12)$$



*Figure 3.1- Description of transistor active and gate dimensions on a cross-section (top) and a top-view (bottom).*

Effective length and width for charge model

$$L_{E,CV} = L + \Delta L_{ps} - 2 \times \text{LAP} + \text{DLQ} \quad (3.13)$$

$$W_{E,CV} = W_f + \Delta W_{OD} - 2 \times \text{WOT} + \text{DWQ} \quad (3.14)$$

Physical length and width for parasitic charges and self-heating

$$L_{phy} = L + \Delta L_{ps} \quad (3.15)$$

$$W_{phy} = W_f + \Delta W_{OD} \quad (3.16)$$

### 3.1.2 Process parameters

$$\text{TOXE} = \text{TOXEO} \quad (3.17)$$

$$\text{TSI} = \text{TSIO} \quad (3.18)$$

$$\text{XGE} = \text{XGEO} \quad (3.19)$$

$$\text{TBOX} = \text{TBOXO} \quad (3.20)$$

$$\text{NCH} = \text{NCHO} \quad (3.21)$$

$$\text{NSUB} = \text{NSUBO} \quad (3.22)$$

$$\text{CT} = \text{CTO} \quad (3.23)$$

$$\text{TOXP} = \text{TOXPO} \quad (3.24)$$

$$\text{NOV} = \text{NOVO} \quad (3.25)$$

$$\text{NOVD} = \text{NOVDO} \quad (3.26)$$

$$\text{VFB} = \text{VFBO} + \text{VFBL} \left( \frac{L_{EN}}{L_E} \right)^{\text{VFBLEXP}} \left/ \left( 1 + \text{VFBL2} \left( \frac{L_{EN}}{L_E} \right)^{\text{VFBLEXP2}} \right) \right. + \text{VFBW} \frac{W_{EN}}{W_E} + \text{VFBLW} \frac{L_{EN}}{L_E} \frac{W_{EN}}{W_E} \quad (3.27)$$

$$\mathbf{VFBB} = \mathbf{VFBB0} + \mathbf{VFBLBO} \times \frac{\mathbf{TBOX}}{\mathbf{TOXE}} \times \mathbf{VFBL} \left( \frac{L_{EN}}{L_E} \right)^{\mathbf{VFBLEXP}} \left/ \left( 1 + \mathbf{VFBL2} \left( \frac{L_{EN}}{L_E} \right)^{\mathbf{VFBLEXP2}} \right) \right. \quad (3.28)$$

$$\mathbf{STVFB} = \mathbf{STVFB0} \left( 1 + \mathbf{STVFBL} \frac{L_{EN}}{L_E} \right) \left( 1 + \mathbf{STVFBW} \frac{W_{EN}}{W_E} \right) \left( 1 + \mathbf{STVFBLW} \frac{L_{EN}}{L_E} \frac{W_{EN}}{W_E} \right) \quad (3.29)$$

### 3.1.3 Gate to interface coupling parameters

$$\mathbf{CICF} = \mathbf{CICFO} \quad (3.30)$$

$$\mathbf{CIC} = \mathbf{CICO} \quad (3.31)$$

$$\lambda_{2D} = \sqrt{\frac{\epsilon_{Si}(1 - \mathbf{XGE}) + \epsilon_{Ge}\mathbf{XGE}}{\epsilon_{ox}}} \times \mathbf{TSI} \times (\mathbf{TOXE} + 4 \times 10^{-10}) \quad (3.32)$$

$$\mathbf{PSCE} = 2 \times \mathbf{PSCEL} \left( \frac{\lambda_{2D}}{L_E} \right)^{\mathbf{PSCELEXP}} \left( 1 + \mathbf{PSCEW} \frac{W_{EN}}{W_E} \right) \quad (3.33)$$

$$\mathbf{PSCEB} = \mathbf{PSCEBO} \quad (3.34)$$

$$\mathbf{NSDDC} = \mathbf{NSDDCO} \quad (3.35)$$

$$\mathbf{PSCEDLB} = \mathbf{PSCEDLBO} \quad (3.36)$$

$$\mathbf{PNCE} = \mathbf{PNCEW} \frac{W_{EN}}{W_E} \quad (3.37)$$

### 3.1.4 Drain Induced Barrier Lowering parameters

$$\mathbf{CF} = \mathbf{CFL} \left( \frac{\lambda_{2D}}{L_E} \right)^{\mathbf{CFLEXP}} \left( 1 + \mathbf{CFW} \frac{W_{EN}}{W_E} \right) \quad (3.38)$$

$$\mathbf{CFB} = \mathbf{CFBO} \quad (3.39)$$

$$\mathbf{STCF} = \mathbf{STCFL} \left( \frac{\lambda_{2D}}{L_E} \right)^{\mathbf{CFLEXP}} \left( 1 + \mathbf{CFW} \frac{W_{EN}}{W_E} \right) \quad (3.40)$$

$$\mathbf{CFD} = \mathbf{CFDO} \quad (3.41)$$

$$\mathbf{CFDL} = \mathbf{CFDLL} \frac{L_{EN}}{L_E} \left/ \max \left( 1 + \mathbf{CFDLW} \frac{W_{EN}}{W_E}, 10^{-3} \right) \right. \quad (3.42)$$

$$\mathbf{CFDLB} = \mathbf{CFDLBO} \quad (3.43)$$

### 3.1.5 Mobility parameters

$$L_{p1eff} = \mathbf{LP1} \times \max \left( 1 + \mathbf{LP1W} \frac{W_{EN}}{W_E}, 10^{-3} \right) \quad (3.44)$$

$$G_{PE} = \max \left( 1 + \mathbf{FBET1} \left( 1 + \mathbf{FBET1W} \frac{W_{EN}}{W_E} \right) \frac{1 - \exp(-L_E/L_{p1eff})}{L_E/L_{p1eff}} + \mathbf{FBET2} \frac{1 - \exp(-L_E/\mathbf{LP2})}{L_E/\mathbf{LP2}}, 10^{-6} \right) \quad (3.45)$$

$$G_{WE} = \max \left( 1 + \mathbf{BETW1} \frac{W_{EN}}{W_E} + \mathbf{BETW2} \frac{W_{EN}}{W_E} \ln \left( 1 + \frac{W_E}{\mathbf{WBET}} \right), 10^{-6} \right) \quad (3.46)$$

$$G_E = \mathbf{UO} \frac{G_{WE}}{G_{PE}} \quad (3.47)$$

Note that, unlike in UTSOI1, **UO** is integrated into  $G_E$ . This changes the signification of the **THESAT** related global parameters, as detailed in the velocity saturation parameters part.

$$\text{BETN} = G_E \frac{W_E}{L_E} \quad (3.48)$$

$$\text{BETNB} = \text{BETNBO} \quad (3.49)$$

$$\text{STBET} = \text{STBETO} \left( 1 + \text{STBETL} \frac{L_{EN}}{L_E} \right) \left( 1 + \text{STBETW} \frac{W_{EN}}{W_E} \right) \left( 1 + \text{STBETLW} \frac{L_{EN}}{L_E} \frac{W_{EN}}{W_E} \right) \quad (3.50)$$

$$\text{CS} = \left( \text{CSO} + \text{CSL} \left( \frac{L_{EN}}{L_E} \right)^{\text{CSLEXP}} \right) \left( 1 + \text{CSW} \frac{W_{EN}}{W_E} \right) \left( 1 + \text{CSLW} \frac{L_{EN}}{L_E} \frac{W_{EN}}{W_E} \right) \quad (3.51)$$

$$\text{CSFI} = \text{CSFIO} \quad (3.52)$$

$$\text{CSBI} = \text{CSBIO} \quad (3.53)$$

$$\text{STCS} = \text{STCSO} \left( 1 + \text{STCSL} \frac{L_{EN}}{L_E} \right) \left( 1 + \text{STCSW} \frac{W_{EN}}{W_E} \right) \left( 1 + \text{STCSLW} \frac{L_{EN}}{L_E} \frac{W_{EN}}{W_E} \right) \quad (3.54)$$

$$\text{THECS} = \text{THECSO} \quad (3.55)$$

$$\text{STTHECS} = \text{STTHECSO} \quad (3.56)$$

$$\text{CSTHR} = \text{CSTHRO} \quad (3.57)$$

$$\text{CSTHRB} = \text{CSTHRBO} \quad (3.58)$$

$$\text{MUE} = \text{MUEO} \quad (3.59)$$

$$\text{STMUE} = \text{STMUEO} \quad (3.60)$$

$$\text{THEMU} = \text{THEMUO} \quad (3.61)$$

$$\text{STTHEMU} = \text{STTHEMUO} \quad (3.62)$$

$$\text{XCOR} = \left( \text{XCORO} + \text{XCORL} \left( \frac{L_{EN}}{L_E} \right)^{\text{XCORLEXP}} \right) \left( 1 + \text{XCORW} \frac{W_{EN}}{W_E} \right) \left( 1 + \text{XCORLW} \frac{L_{EN}}{L_E} \frac{W_{EN}}{W_E} \right) \quad (3.63)$$

$$\text{XCORB} = \text{XCORBO} \quad (3.64)$$

$$\text{STXCOR} = \text{STXCORO} \quad (3.65)$$

$$\text{FETA} = \text{FETAO} \quad (3.66)$$

### 3.1.6 Series resistance parameters

$$\text{RS} = \text{RSW1} \frac{W_{EN}}{W_E} \left( 1 + \text{RSW2} \frac{W_{EN}}{W_E} \right) \quad (3.67)$$

$$\text{RSIG} = \text{RSIGO} \quad (3.68)$$

$$\text{STRS} = \text{STRSO} \quad (3.69)$$

$$\text{RSG} = \text{RSGO} \quad (3.70)$$

$$\text{THERSG} = \text{THERSGO} \quad (3.71)$$

$$\text{RSB} = \text{RSBO} \quad (3.72)$$

### 3.1.7 Velocity saturation parameters

$$\text{THESAT} = G_E \left( \text{THESATO} + \text{THESATL} \left( \frac{L_{EN}}{L_E} \right)^{\text{THESATLEXP}} \right) \left( 1 + \text{THESATW} \frac{W_{EN}}{W_E} \right) \left( 1 + \text{THESATLW} \frac{L_{EN}}{L_E} \frac{W_{EN}}{W_E} \right) \quad (3.73)$$

Since **UO** has been included in  $G_E$  (see (3.47)), **THESATL** is, in Leti-UTSOI2, closely related to the saturation velocity itself.

$$\text{STTHESAT} = \text{STTHESATO} \left( 1 + \text{STTHESATL} \frac{L_{EN}}{L_E} \right) \left( 1 + \text{STTHESATW} \frac{W_{EN}}{W_E} \right) \left( 1 + \text{STTHESATLW} \frac{L_{EN}}{L_E} \frac{W_{EN}}{W_E} \right) \quad (3.74)$$

$$\text{THESATG} = \text{THESATGO} \quad (3.75)$$

$$\text{THESATB} = \text{THESATBO} \quad (3.76)$$

### 3.1.8 Saturation and Channel Length Modulation parameters

$$\text{AX} = \frac{\text{AXO}}{1 + \text{AXL} \left( \frac{L_{EN}}{L_E} \right)^{\text{AXLEXP}}} \quad (3.77)$$

$$\text{ALP} = \frac{\text{ALPL1} \left( \frac{L_{EN}}{L_E} \right)^{\text{ALPLEXP}}}{1 + \text{ALPL2} \left( \frac{L_{EN}}{L_E} \right)^{\text{ALPLEXP2}}} \left( 1 + \text{ALPW} \frac{W_{EN}}{W_E} \right) \quad (3.78)$$

$$\text{ALP1} = \frac{\text{ALP1L1} \left( \frac{L_{EN}}{L_E} \right)^{\text{ALP1LEXP}}}{1 + \text{ALP1L2} \left( \frac{L_{EN}}{L_E} \right)^{\text{ALP1LEXP2}}} \left( 1 + \text{ALP1W} \frac{W_{EN}}{W_E} \right) \quad (3.79)$$

$$\text{ALPB} = \text{ALPBO} \quad (3.80)$$

$$\text{VP} = \text{VPO} \quad (3.81)$$

$$\text{VPG} = \text{VPGO} \quad (3.82)$$

### 3.1.9 Gate current parameters

$$\text{GCO} = \text{GCOO} \quad (3.83)$$

$$\text{IGINV} = \text{IGINVLW} \frac{L_E}{L_{EN}} \frac{W_E}{W_{EN}} \quad (3.84)$$

$$\text{IGOVINV} = \text{IGOVINW} \frac{W_E}{W_{EN}} \quad (3.85)$$

$$\text{IGOVINVD} = \text{IGOVINVDW} \frac{W_E}{W_{EN}} \quad (3.86)$$

$$\text{IGOVACC} = \text{IGOVACW} \frac{W_E}{W_{EN}} \quad (3.87)$$

$$\text{IGOVACCD} = \text{IGOVACCDW} \frac{W_E}{W_{EN}} \quad (3.88)$$

Notice that, unlike in Leti-UTSOI1, gate currents in overlap regions are not linked with **LOVO** in Leti-UTSOI2.

$$\text{STIG} = \text{STIGO} \quad (3.89)$$

$$\text{GC2CH} = \text{GC2CHO} \quad (3.90)$$

$$\text{GC3CH} = \text{GC3CHO} \quad (3.91)$$

$$\text{GC2OVINV} = \text{GC2OVINVO} \quad (3.92)$$

$$\text{GC3OVINV} = \text{GC3OVINVO} \quad (3.93)$$

$$\text{GC2OVACC} = \text{GC2OVACCO} \quad (3.94)$$

$$\text{GC3OVACC} = \text{GC3OVACCO} \quad (3.95)$$

$$\text{GCDOV} = \text{GCDOVL} \frac{L_E}{L_{EN}} \quad (3.96)$$

$$\text{GCVDOV} = \text{GCVDOVO} \quad (3.97)$$

$$\text{CHIB} = \text{CHIBO} \quad (3.98)$$

$$\text{NIGINV} = \text{NIGINVO} \quad (3.99)$$

### 3.1.10 Gate Induced Drain/Source Leakage (GIDL/GISL) parameters

$$\text{AGIDL} = \text{AGIDLW} \frac{W_E}{W_{EN}} \quad (3.100)$$

$$\text{AGIDLD} = \text{AGIDLW} \frac{W_E}{W_{EN}} \quad (3.101)$$

Notice that, as for gate currents, GIDL/GISL currents are not linked with **LOVO** in Leti-UTSOI2.

$$\text{BGIDL} = \text{BGIDLO} \quad (3.102)$$

$$\text{BGIDLD} = \text{BGIDLDO} \quad (3.103)$$

$$\text{STBGIDL} = \text{STBGIDLO} \quad (3.104)$$

$$\text{STBGIDLD} = \text{STBGIDLDO} \quad (3.105)$$

$$\text{CGIDL} = \text{CGIDLO} \quad (3.106)$$

$$\text{CGIDLD} = \text{CGIDLDO} \quad (3.107)$$

$$\text{DGIDL} = \text{DGIDLL} \frac{L_E}{L_{EN}} \quad (3.108)$$

$$\text{DGIDLD} = \text{DGIDLDL} \frac{L_E}{L_{EN}} \quad (3.109)$$

### 3.1.11 Charge model parameters

$$\text{AREAQ} = L_{E,CV} W_{E,CV} \quad (3.110)$$

$$\text{CGBOV} = \text{CGBOVO} + \text{CGBOVL} \frac{L_{phy}}{L_{EN}} \quad (3.111)$$

$$\text{NSDAC} = \text{NSDACO} \quad (3.112)$$

$$\text{FIF} = \text{FIFW} \frac{W_{E,CV}}{W_{EN}} \quad (3.113)$$

$$\text{FSCEAC} = \text{FSCEACO} \quad (3.114)$$

$$\text{COV} = \varepsilon_{ox} \frac{W_{E,CV}}{\text{TOXE}} \text{LOVO} \quad (3.115)$$

$$\text{COVD} = \varepsilon_{ox} \frac{W_{E,CV}}{\text{TOXE}} \text{LOVDO} \quad (3.116)$$

$$\text{COVDL} = \text{COVDLO} / \max \left( 1 + \text{COVDLW} \frac{W_{EN}}{W_{E,CV}}, 10^{-3} \right) \quad (3.117)$$

$$\text{COVDLB} = \text{COVDLBO} \quad (3.118)$$

$$\text{DVFBOV} = \text{DVFBOVO} \quad (3.119)$$



$$\text{CFR} = \text{CFRO} + \text{CFRW} \frac{W_{phy}}{W_{EN}} \quad (3.120)$$

$$\text{CFRD} = \text{CFRDO} + \text{CFRDW} \frac{W_{phy}}{W_{EN}} \quad (3.121)$$

$$\text{CSD} = \text{CSDO}(\varepsilon_{Si}(1 - \text{XGE}) + \varepsilon_{Ge} \text{XGE}) \text{TSI} \frac{W_E}{L_E} \quad (3.122)$$

$$\text{CSDBP} = \text{CSDBPO} \quad (3.123)$$

### 3.1.12 Self-heating parameters

$$z_{th} = \exp\left(-\frac{\text{SD} + L}{\text{LAMBTHO}}\right) \quad (3.124)$$

$$n_{th} = 1 + 2 \times \text{FTHO} \times z_{th} \frac{1 - z_{th} - (1 - z_{th}^{\text{NF}})/\text{NF}}{(1 - z_{th})^2} \quad (3.125)$$

$$\text{RTH} = \frac{\text{RTHO}}{1 + \text{RTHL} \frac{L_{G,TH}}{L_{EN}} + \text{RTHW} \frac{W_{G,TH}}{W_{EN}} + \text{RTHLW} \frac{L_{G,TH}}{L_{EN}} \frac{W_{G,TH}}{W_{EN}}} \times n_{th} \quad (3.126)$$

$$\text{STRTH} = \text{STRTHO} \quad (3.127)$$

$$\text{CTH} = \text{CTHO} \left( 1 + \text{RTHL} \frac{L_{G,TH}}{L_{EN}} + \text{RTHW} \frac{W_{G,TH}}{W_{EN}} + \text{RTHLW} \frac{L_{G,TH}}{L_{EN}} \frac{W_{G,TH}}{W_{EN}} \right) / n_{th} \quad (3.128)$$

### 3.1.13 Noise model parameters

$$\text{FNT} = \text{FNT0} \quad (3.129)$$

$$\text{NFA} = \text{NFALW} \frac{L_{EN}}{L_E} \frac{W_{EN}}{W_E} \quad (3.130)$$

$$\text{NFB} = \text{NFBWL} \frac{L_{EN}}{L_E} \frac{W_{EN}}{W_E} \quad (3.131)$$

$$\text{NFC} = \text{NFCLW} \frac{L_{EN}}{L_E} \frac{W_{EN}}{W_E} \quad (3.132)$$

$$\text{EF} = \text{EFO} \quad (3.133)$$

$$\text{NFE} = \text{NFEO} \quad (3.134)$$

$$\text{NFEB} = \text{NFEB0} \quad (3.135)$$

## 3.2 Stress model for SWSTRESS=1

In this paragraph are reported the modifications brought to mobility, saturation velocity and threshold voltage parameters when the STI-stress model is selected (SWSTRESS=1).

### 3.2.1 Effective SA/SB related parameters

$$R_A = \frac{1}{\text{NF}} \sum_{i=0}^{\text{NF}-1} \frac{1}{\text{SA} + L/2 + i(\text{SD} + L)} \quad (3.136)$$

$$R_B = \frac{1}{\text{NF}} \sum_{i=0}^{\text{NF}-1} \frac{1}{\text{SB} + L/2 + i(\text{SD} + L)} \quad (3.137)$$

$$R_{A,ref} = \frac{1}{\text{SAREF} + L/2} \quad (3.138)$$

$$R_{B,ref} = \frac{1}{\mathbf{SBREF} + L/2} \quad (3.139)$$

### 3.2.2 Modification of mobility related parameters

$$K_{u0} = \left( 1 + \frac{\mathbf{LKUO}}{(L + \Delta L_{PS})^{\mathbf{LLODKUO}}} + \frac{\mathbf{WKUO}}{(W_f + \Delta W_{OD} + \mathbf{WLOD})^{\mathbf{WLODKUO}}} \right. \\ \left. + \frac{\mathbf{PKUO}}{(L + \Delta L_{PS})^{\mathbf{LLODKUO}}(W_f + \Delta W_{OD} + \mathbf{WLOD})^{\mathbf{WLODKUO}}} \right) \left( 1 + \mathbf{TKUO} \left( \frac{T_{KD}}{T_{KR}} - 1 \right) \right) \quad (3.140)$$

$$\rho_\beta = \frac{\mathbf{KUO}}{K_{u0}} (R_A + R_B) \quad (3.141)$$

$$\rho_{\beta,ref} = \frac{\mathbf{KUO}}{K_{u0}} (R_{A,ref} + R_{B,ref}) \quad (3.142)$$

$$\mathbf{BETN} = \frac{1 + \rho_\beta}{1 + \rho_{\beta,ref}} \mathbf{BETN}_{ref} \quad (3.143)$$

$$\mathbf{THESAT} = \frac{1 + \rho_\beta}{1 + \rho_{\beta,ref}} \frac{1 + \mathbf{KVSAT} \rho_{\beta,ref}}{1 + \mathbf{KVSAT} \rho_\beta} \mathbf{THESAT}_{ref} \quad (3.144)$$

### 3.2.3 Modification of threshold voltage related parameters

$$K_{vth0} = 1 + \frac{\mathbf{LKVTHO}}{(L + \Delta L_{PS})^{\mathbf{LLODVTH}}} + \frac{\mathbf{WKVTHO}}{(W_f + \Delta W_{OD} + \mathbf{WLOD})^{\mathbf{WLODVTH}}} \\ + \frac{\mathbf{PKVTHO}}{(L + \Delta L_{PS})^{\mathbf{LLODVTH}}(W_f + \Delta W_{OD} + \mathbf{WLOD})^{\mathbf{WLODVTH}}} \quad (3.145)$$

$$\Delta R = R_A + R_B - R_{A,ref} - R_{B,ref} \quad (3.146)$$

$$\mathbf{VFB} = \mathbf{VFB}_{ref} + \frac{\mathbf{KVTHO}}{K_{vth0}} \Delta R \quad (3.147)$$

$$\mathbf{VFB} = \mathbf{VFB}_{ref} + \frac{\mathbf{KVTHO}}{K_{vth0}} \Delta R \quad (3.148)$$

$$\mathbf{CF} = \mathbf{CF}_{ref} + \frac{\mathbf{STETAO}}{K_{vth0} \mathbf{LOETAO}} \Delta R \quad (3.149)$$

## 3.3 Stress model for SWSTRESS=2

In this paragraph are reported the modifications brought to mobility, saturation velocity and threshold voltage parameters when the strained-SOI dedicated stress model is selected (SWSTRESS=2).

### 3.3.1 Effective SA/SB related parameters

$$R_A(i) = \left( 1 - \exp \left( - \frac{\mathbf{SA} + L/2 + i(\mathbf{SD} + L)}{\mathbf{STRLAMBDA}} \right) \right)^{-\mathbf{STRALPHA}} \quad (3.150)$$

$$R_B(i) = \left( 1 - \exp \left( - \frac{\mathbf{SB} + L/2 + (\mathbf{NF} - 1 - i)(\mathbf{SD} + L)}{\mathbf{STRLAMBDA}} \right) \right)^{-\mathbf{STRALPHA}} \quad (3.151)$$

$$g = 1 - \frac{1}{\mathbf{NF}} \sum_{i=0}^{\mathbf{NF}-1} \left[ \frac{2}{R_A(i) + R_B(i)} \right]^{1/\mathbf{STRALPHA}} \quad (3.152)$$

$$R_{A,ref} = \left( 1 - \exp\left( -\frac{\mathbf{SAREF} + L/2}{\mathbf{STRLAMBDA}} \right) \right)^{-\mathbf{STRALPHA}} \quad (3.153)$$

$$R_{B,ref} = \left( 1 - \exp\left( -\frac{\mathbf{SBREF} + L/2}{\mathbf{STRLAMBDA}} \right) \right)^{-\mathbf{STRALPHA}} \quad (3.154)$$

$$g_{ref} = 1 - \left[ \frac{2}{R_{A,ref}(i) + R_{B,ref}(i)} \right]^{1/\mathbf{STRALPHA}} \quad (3.155)$$

### 3.3.2 Modification of mobility related parameters

$$r_{uo} = \frac{\mathbf{STRRUO}}{1 + \mathbf{STRTRUO} \left( \frac{T_{KD}}{T_{KR}} - 1 \right)} \quad (3.156)$$

$$\rho_{\beta} = r_{uo} \times g \quad (3.157)$$

$$\rho_{\beta,ref} = r_{uo} \times g_{ref} \quad (3.158)$$

$$\mathbf{BETN} = \frac{1 + \rho_{\beta}}{1 + \rho_{\beta,ref}} \mathbf{BETN}_{ref} \quad (3.159)$$

$$\mathbf{THESAT} = \frac{1 + \rho_{\beta}}{1 + \rho_{\beta,ref}} \frac{1 + \mathbf{STRRVSAT} \rho_{\beta,ref}}{1 + \mathbf{STRRVSAT} \rho_{\beta}} \mathbf{THESAT}_{ref} \quad (3.160)$$

### 3.3.3 Modification of threshold voltage related parameters

$$K_{vth0} = 1 + \mathbf{STRWDVFBO} \frac{W_f + \Delta W_{OD} + \mathbf{WLOD}}{W_{EN}} \quad (3.161)$$

$$\mathbf{VFB} = \mathbf{VFB}_{ref} + \frac{\mathbf{STRDVFBO}}{K_{vth0}} (g - g_{ref}) \quad (3.162)$$

$$\mathbf{VFBB} = \mathbf{VFBB}_{ref} + \frac{\mathbf{STRDVFBO}}{K_{vth0}} (g - g_{ref}) \quad (3.163)$$

$$\mathbf{CF} = \mathbf{CF}_{ref} + \mathbf{STRDCFL} \left( \frac{\lambda_{2D}}{L_E} \right)^{\mathbf{CFLEXP}} \left( 1 + \mathbf{CFW} \frac{W_{EN}}{W_E} \right) (g - g_{ref}) \quad (3.164)$$

## 3.4 Asymmetric junctions

After the calculations described in paragraphs 3.1 and 3.2, local parameters are clipped according to the min/max values given in the parameters tables.

Then, if the switch parameter **SWJUNASYM** is equal to 0, drain and source junctions are assumed symmetrical and all parameters related to the drain junction are overwritten by their source side counterparts.

if **SWJUNASYM** = 0:

$$\mathbf{NOVD} = \mathbf{NOV} \quad (3.165)$$

$$\mathbf{IGOVINVD} = \mathbf{IGOVINV} \quad (3.166)$$

$$\mathbf{IGOVACCD} = \mathbf{IGOVACC} \quad (3.167)$$

$$\mathbf{AGIDL} = \mathbf{AGIDL} \quad (3.168)$$

$$\mathbf{BGIDL} = \mathbf{BGIDL} \quad (3.169)$$

$$\mathbf{STBGIDL} = \mathbf{STBGIDL} \quad (3.170)$$

<b>CGIDLD = CGIDL</b>	(3.171)
<b>DGIDLD = DGIDL</b>	(3.172)
<b>COVD = COV</b>	(3.173)
<b>CFRD = CFR</b>	(3.174)

If **SWJUNASYM** = 1, the drain side related parameters are kept unchanged. Notice that, if some of the drain side parameters are not specified in the model card, they take their default value and *not* their source side counterpart value.

## 4 Model equations

In this part are detailed all the equations of the model. The complete calculation of the surface potentials is given in **Error! Reference source not found.** and some useful functions are described in **Error! Reference source not found.**.

### 4.1 Internal parameters including temperature dependences

In this section are calculated bias independent internal parameters. These calculations include temperature dependences and are carried out from local scale parameters. Local scale parameters are in capital characters and bold font.

#### 4.1.1 Transistor temperature

$$T_{KR} = 273.15 + \mathbf{TR} \quad (4.1)$$

$$T_{KD} = 273.15 + T_{Ambient} \quad (4.2)$$

As in UTSOI1, a temperature node is used to compute the elevation of the channel temperature with respect to the circuit temperature due to self-heating when the **SWSHE** flag is set to 1 (see paragraph 4.9 for details). This temperature elevation is given by  $\Delta T_C$ . The channel temperature  $T_{KC}$  is thus given by:

$$\begin{cases} T_{KC} = T_{KD} + \Delta T_C & \text{if } \mathbf{SWSHE} = 1 \\ T_{KC} = T_{KD} & \text{else} \end{cases} \quad (4.3)$$

$$\Delta T = T_{KC} - T_{KR} \quad (4.4)$$

$$\phi_{T0} = \frac{k_B T_{KC}}{q} \quad (4.5)$$

#### 4.1.2 Local process parameters

$$\varepsilon_{ch} = \varepsilon_{Si}(1 - \mathbf{XGE}) + \varepsilon_{Ge} \mathbf{XGE} \quad (4.6)$$

$$E_{g,Si} = E_{g0,Si} - \frac{\alpha_{Si} T_{KC}^2}{\beta_{Si} + T_{KC}} \quad (4.7)$$

$$E_{g,Ge} = E_{g0,Ge} - \frac{\alpha_{Ge} T_{KC}^2}{\beta_{Ge} + T_{KC}} \quad (4.8)$$

$$\delta E_g = (E_{g,Ge} - E_{g,Si} + C_G(1 - \mathbf{XGE})) \mathbf{XGE} \quad (4.9)$$

$$E_g = E_{g,Si} + \delta E_g \quad (4.10)$$

$$n_{eff} = \frac{n_{i,fact,300}}{1 + \sqrt{10 \mathbf{XGE}}} \left( \frac{T_{KC}}{300} \right)^{3/2} \exp \left( - \frac{E_g}{2\phi_{T0}} \right) \quad (4.11)$$

$$\delta V_{FB,ch} = 0.05 \mathbf{XGE} - \delta E_g / 2 \quad (4.12)$$

$$\delta V_{FB1,NCH} = \frac{q \times \mathbf{NCH} \times 10^6 \times \mathbf{TSI}}{2\varepsilon_{ox}} (\mathbf{TOXE} + 4 \times 10^{-10} \mathbf{QMC}) \quad (4.13)$$

$$\delta V_{FB2,NCH} = \frac{q \times \mathbf{NCH} \times 10^6 \times \mathbf{TSI}}{2\varepsilon_{ox}} (\mathbf{TBOX} + 4 \times 10^{-10} \mathbf{QMC}) \quad (4.14)$$

#### 4.1.3 Interface coupling internal parameters

$$\begin{cases} C_{ox1}' = \frac{\varepsilon_{ox}}{\text{TOXE}} & \text{if } \text{PNCE} \leq 0 \\ C_{ox1}' = \frac{\varepsilon_{ox}}{\text{TOXE}} (1 + \text{PNCE}) & \text{else} \end{cases} \quad (4.15)$$

$$\begin{cases} C_{ox2}' = \frac{\varepsilon_{ox}}{\text{TBOX}} (1 - \text{PNCE}) & \text{if } \text{PNCE} \leq 0 \\ C_{ox2}' = \frac{\varepsilon_{ox}}{\text{TBOX}} & \text{else} \end{cases} \quad (4.16)$$

$$C_{SiO}' = \frac{\varepsilon_{ch}}{\text{TSI}} \quad (4.17)$$

$$\phi_T = \phi_{T0} \left( 1 + \text{CT} \frac{T_{KR}}{T_{KC}} \right) \quad (4.18)$$

$$k_{1,1D} = \frac{C_{ox1}'}{C_{SiO}'} \quad (4.19)$$

$$k_{2,1D} = \frac{C_{ox2}'}{C_{SiO}'} \quad (4.20)$$

$$k_{eq,1D} = \frac{1}{1 + 1/k_{1,1D} + 1/k_{2,1D}} \quad (4.21)$$

$$C_{eq,1D} = C_{SiO}' k_{eq,1D} \quad (4.22)$$

$$\text{PSCE}_1 = \text{PSCE} \quad (4.23)$$

$$\text{PSCE}_2 = \text{PSCEB} \times \text{PSCE} \times \text{TBOX} / \text{TOXE} \quad (4.24)$$

$$f_{A0} = \frac{2qn_{eff}\varepsilon_{ch}}{\phi_T} \quad (4.25)$$

$$x_{th,1D} = \ln \left( \frac{C_{SiO}'^2}{2f_{A0}} \right) \quad (4.26)$$

$$x_{SD,dep} = \frac{q \times \text{NSDDC} \times 10^6 \times \text{TSI}}{2(C_{ox1}' + C_{ox2}')\phi_T} \quad (4.27)$$

#### 4.1.4 Drain Induced Barrier Lowering internal parameters

$$CF_1 = \text{CF} + \text{STCF} \times \Delta T \quad (4.28)$$

$$CF_2 = \text{CF} \times \text{CFB} \times \frac{\text{TBOX}}{\text{TOXE}} + \text{STCF} \times \Delta T \quad (4.29)$$

$$x_{d0} = \frac{\text{CFD}}{\phi_T} \quad (4.30)$$

#### 4.1.5 Backplane internal parameters

Since the backplane is located under the buried oxide of the device, it is assumed that its temperature is that of the environment. Thus, the self-heating effect is not included for the corresponding parameters and temperature  $T_{KD}$  is considered.

$$n_{eff,sub} = n_{i,fact,300} \left( \frac{T_{KD}}{300} \right)^{3/2} \exp \left( -\frac{E_{g,Si}}{2(k_B T_{KD}/q)} \right) \quad (4.31)$$

$$G_{f,sub} = \frac{\sqrt{2q\epsilon_{si}NSUB \times 10^6}}{C_{eq0} \sqrt{k_B T_{KD}/q}} \quad (4.32)$$

$$\xi_{sub} = 1 + \frac{G_{f,sub}}{\sqrt{2}} \quad (4.33)$$

$$\xi_{mrg,sub} = 10^{-5} \xi_{sub} \quad (4.34)$$

$$x_{b,sub} = \ln\left(\frac{NSUB \times 10^6}{n_{eff,sub}}\right) \quad (4.35)$$

$$x_{n,sub} = 2x_{b,sub} \quad (4.36)$$

$$\begin{cases} type_{sub} = 1 & \text{if } NSUB \geq 0 \\ type_{sub} = -1 & \text{else} \end{cases} \quad (4.37)$$

#### 4.1.6 Quantum mechanical correction internal parameters

$$\begin{cases} \delta V_{FB,qm} = 0 & \text{if } QMC = 0 \\ \delta V_{FB,qm} = \frac{0.409618895}{10^{18} TSI^2} & \text{if } QMC > 0 \text{ and } TYPE = +1 \text{ (NMOS)} \\ \delta V_{FB,qm} = \frac{0.723134895}{10^{18} TSI^2} & \text{if } QMC > 0 \text{ and } TYPE = -1 \text{ (PMOS)} \end{cases} \quad (4.38)$$

$$\begin{cases} qq = 0 & \text{if } QMC = 0 \\ qq = \frac{0.4QM_N QMC}{(10^{18} TSI^2 \phi_T)^{1/3}} & \text{if } QMC > 0 \text{ and } TYPE = +1 \text{ (NMOS)} \\ qq = \frac{0.4QM_P QMC}{(10^{18} TSI^2 \phi_T)^{1/3}} & \text{if } QMC > 0 \text{ and } TYPE = -1 \text{ (PMOS)} \end{cases} \quad (4.39)$$

$$V_{FB1} = TYPE(VFB + STVFB \times \Delta T + \delta V_{FB,ch} + \delta V_{FB1,NCH}) + \delta V_{FB,qm} + DELVTO \quad (4.40)$$

$$\begin{cases} V_{FB2} = TYPE(VFBB + STVFB \times \Delta T + \delta V_{FB,ch} + \delta V_{FB2,NCH}) + \delta V_{FB,qm} & \text{if } SWSUBDEP = 0 \\ V_{FB2} = TYPE(VFBB + STVFB \times \Delta T + \delta V_{FB,ch} + \delta V_{FB2,NCH} + type_{sub} x_{b,sub} (k_B T_{KD}/q)) + \delta V_{FB,qm} & \text{else} \end{cases} \quad (4.41)$$

#### 4.1.7 Mobility internal parameters

$$\beta_{N1} = FACTUO \times BETN \times \left(\frac{T_{KR}}{T_{KC}}\right)^{STBET} \quad (4.42)$$

$$\beta_{N2} = FACTUO \times BETN \times BETNB \times \left(\frac{T_{KR}}{T_{KC}}\right)^{STBET} \quad (4.43)$$

$$\mu_E = MUE \times \left(\frac{T_{KR}}{T_{KC}}\right)^{STMUE} \quad (4.44)$$

$$\theta_\mu = THEMU \times \left(\frac{T_{KR}}{T_{KC}}\right)^{STTHEMU} \quad (4.45)$$

$$C_S = \mathbf{CS} \times \left( \frac{T_{KR}}{T_{KC}} \right)^{\mathbf{STCS}} \quad (4.46)$$

$$\theta_{CS} = \mathbf{THECS} \times \left( \frac{T_{KR}}{T_{KC}} \right)^{\mathbf{STTHECS}} \quad (4.47)$$

$$X_{cor} = \mathbf{XCOR} \times \left( \frac{T_{KR}}{T_{KC}} \right)^{\mathbf{STXCOR}} \quad (4.48)$$

$$f_{\mu E} = 10^{-8} \frac{\phi_T}{\mathbf{TSI}} \mu_E \quad (4.49)$$

$$q_{i1th,CS} = 0.5 \times \mathbf{CSTHR} \quad (4.50)$$

$$q_{i2th,CS} = q_{i1th,CS} \times \mathbf{CSTHRB} \quad (4.51)$$

$$\begin{cases} \eta_{\mu} = 1/2 \times \mathbf{FETA} & \text{if } \mathbf{TYPE} = +1 \text{ (NMOS)} \\ \eta_{\mu} = 1/3 \times \mathbf{FETA} & \text{if } \mathbf{TYPE} = -1 \text{ (PMOS)} \end{cases} \quad (4.52)$$

#### 4.1.8 Series resistance internal parameters

$$R_S = \mathbf{RS} \times \left( \frac{T_{KR}}{T_{KC}} \right)^{\mathbf{STRS}} \quad (4.53)$$

$$f_{RS} = 2\phi_T R_S \quad (4.54)$$

#### 4.1.9 Velocity saturation internal parameters

$$\theta_{sat} = \mathbf{FACTUO} \times \mathbf{THESAT} \times \left( \frac{T_{KR}}{T_{KC}} \right)^{\mathbf{STTHESAT} + \mathbf{STBET}} \quad (4.55)$$

Notice the presence of the **FACTUO** pre-factor and of **STBET** in the temperature exponent. This contributes to decouple velocity saturation parameters from low longitudinal field mobility ones, and makes in particular **STTHESAT** the temperature exponent of the saturation velocity itself.

$$f_{vsat} = \phi_T \theta_{sat} \quad (4.56)$$

#### 4.1.10 Channel Length Modulation internal parameters

$$\gamma_{AX} = \left( 2^{16/\mathbf{AX}} - 1 \right)^{3/8} - 1 \quad (4.57)$$

$$f_{alp1} = \frac{\mathbf{ALP1}}{\phi_T} \quad (4.58)$$

#### 4.1.11 Gate current internal parameters

$$I_{G,inv} = \mathbf{IGINV} \times \left( \frac{T_{KC}}{T_{KR}} \right)^{\mathbf{STIG}} \quad (4.59)$$

$$I_{G,ovinV} = \mathbf{IGOVINV} \times \left( \frac{T_{KC}}{T_{KR}} \right)^{\mathbf{STIG}} \quad (4.60)$$

$$I_{G,ovinVD} = \mathbf{IGOVINVD} \times \left( \frac{T_{KC}}{T_{KR}} \right)^{\mathbf{STIG}} \quad (4.61)$$



$$I_{G,ovacc} = \mathbf{IGOVACC} \times \left( \frac{T_{KC}}{T_{KR}} \right)^{\mathbf{STIG}} \quad (4.62)$$

$$I_{G,ovaccD} = \mathbf{IGOVACCD} \times \left( \frac{T_{KC}}{T_{KR}} \right)^{\mathbf{STIG}} \quad (4.63)$$

$$B_{ch} = \frac{4}{3} \frac{\sqrt{2qm_0\mathbf{CHIB}}}{\hbar} \mathbf{TOXP} \quad (4.64)$$

$$B_{ov} = \frac{4}{3} \frac{\sqrt{2qm_0\mathbf{CHIB}}}{\hbar} \mathbf{TOXP} \quad (4.65)$$

Note that,  $B_{ch}$  and  $B_{ov}$  are equal in Leti-UTSOI2, since the Leti-UTSOI1 parameter **TOXOV** has been removed.

$$\begin{cases} G_{CQ,ch} = 0 & \text{if } \mathbf{GC3CH} \geq 0 \\ G_{CQ,ch} = -0.495\mathbf{GC2CH}/\mathbf{GC3CH} & \text{else} \end{cases} \quad (4.66)$$

$$\begin{cases} G_{CQ,ovin} = 0 & \text{if } \mathbf{GC3OVINV} \geq 0 \\ G_{CQ,ovin} = -0.495\mathbf{GC2OVINV}/\mathbf{GC3OVINV} & \text{else} \end{cases} \quad (4.67)$$

$$\begin{cases} G_{CQ,ovacc} = 0 & \text{if } \mathbf{GC3OVACC} \geq 0 \\ G_{CQ,ovacc} = -0.495\mathbf{GC2OVACC}/\mathbf{GC3OVACC} & \text{else} \end{cases} \quad (4.68)$$

$$\alpha_b = \frac{E_g}{2} \quad (4.69)$$

$$D_{ch} = \phi_T \mathbf{GCO} \quad (4.70)$$

$$D_{ov} = \phi_{T0} \mathbf{GCO} \quad (4.71)$$

$$n_{iginv} = \frac{1}{1 + \mathbf{NIGINV} \times E_g / (2\phi_T)} \quad (4.72)$$

#### 4.1.12 Gate Induced Drain/Source Leakage (GIDL/GISL) internal parameters

$$A_{GIDL} = \frac{4 \times 10^{-18}}{\mathbf{TOXP}^2} \mathbf{AGIDL} \quad (4.73)$$

$$A_{GIDL D} = \frac{4 \times 10^{-18}}{\mathbf{TOXP}^2} \mathbf{AGIDL D} \quad (4.74)$$

$$B_{GIDL} = 5 \times 10^8 \mathbf{TOXP} (1 + \mathbf{STBGIDL} \times \Delta T) \mathbf{BGIDL} \quad (4.75)$$

$$B_{GIDL D} = 5 \times 10^8 \mathbf{TOXP} (1 + \mathbf{STBGIDL D} \times \Delta T) \mathbf{BGIDL D} \quad (4.76)$$

#### 4.1.13 Charge model internal parameters

$$f_{area} = \phi_T \mathbf{AREAQ} \quad (4.77)$$

$$f_{SD,inner} = \frac{q\mathbf{NSDAC} \times 10^6}{4\epsilon_{ch}\phi_T} \quad (4.78)$$

$$x_{SD} = \ln \left( \frac{\mathbf{NSDAC} \times 10^6}{n_{eff}} \right) \quad (4.79)$$

$$f_{if} = 1.25 \times 10^{-6} \phi_T \mathbf{FIF} \quad (4.80)$$

#### 4.1.14 Self-heating internal parameters

$$R_{th} = \mathbf{RTH} \times \left( \frac{T_{KR}}{T_{KC}} \right)^{\mathbf{STRTH}} \quad (4.81)$$

#### 4.1.15 Noise model internal parameters

$$n_T = 4k_B T_{KC} \text{FNT} \quad (4.82)$$

### 4.2 Terminal voltage conditioning

The input voltages used in the model are  $V_{GS}$ ,  $V_{DS}$  and  $V_{SB}$  in nMOSFET and positive  $V_{DS}$  configuration (i.e. sign of external voltages is reversed for pMOSFET, and source/drain are interchanged in case of negative  $V_{DS}$ ), from which  $V_{SD}$ ,  $V_{GD}$ ,  $V_{DB}$  and  $V_{GB}$  are computed. Then, dimensionless quantities are defined from these voltages.

#### 4.2.1 Voltages for channel current and intrinsic charge models

$$x_d = \frac{V_{DS}}{\phi_T} \quad (4.83)$$

$$x_{dsx} = \frac{\sqrt{V_{DS}^2 + 0.01} - 0.1}{\phi_T} \quad (4.84)$$

$$x_{g10} = \frac{V_{GS} - V_{FB1}}{\phi_T} - \frac{x_d - x_{dsx}}{2} \quad (4.85)$$

$$x_{g20} = \frac{-V_{SB} - V_{FB2}}{\phi_T} - \frac{x_d - x_{dsx}}{2} \quad (4.86)$$

#### 4.2.2 Voltages for overlap currents and charges

$$x_{gs,ov} = -\frac{V_{GS}}{\phi_{T0}} \quad (4.87)$$

$$x_{gd,ov} = -\frac{V_{GD}}{\phi_{T0}} \quad (4.88)$$

$$x_{gs,ovcv} = -\frac{V_{GS} - \text{TYPE} \times \text{DVFBOV} - E_g/2}{\phi_{T0}} \quad (4.89)$$

$$x_{gd,ovcv} = -\frac{V_{GD} - \text{TYPE} \times \text{DVFBOV} - E_g/2}{\phi_{T0}} \quad (4.90)$$

### 4.3 Backplane depletion

Depletion of the backplane is accounted for through the calculation of an effective backplane bias  $x_{g2eff}$ .

If backplane depletion is not activated (i.e. **SWSUBDEP**=0), then  $x_{g2eff}$  is simply given by:

$$x_{g2eff} = x_{g20} \quad (4.91)$$

On the contrary, if backplane depletion is activated (i.e. **SWSUBDEP**=1), then the computation of  $x_{g2eff}$  uses a PSP-like surface potential calculation sequence. This computation is based on the following analogy: the backplane is considered as the channel of a bulk MOSFET, which gate electrode is the front gate and which gate oxide capacitance is equal to the front gate oxide capacitance in series with the channel film capacitance and the buried oxide capacitance. The detailed calculation of  $x_{g2eff}$  is:

$$x_{g1int} = \text{TYPE} \times \text{type}_{sub} \times x_{g10} \quad (4.92)$$

$$x_{g2int} = \text{TYPE} \times \text{type}_{sub} \times x_{g20} \quad (4.93)$$

$$x_{gbint} = x_{g1int} - x_{g2int} \quad (4.94)$$

if  $|x_{gbint}| \leq \xi_{mrg,sub}$ :

$$\delta x_{g2,sub} = \frac{x_{gbint}}{\xi_{sub}} \left( 1 + \frac{x_{gbint} G_{f,sub} (1 - \exp(-x_{n,sub}))}{6\sqrt{2}\xi_{sub}^2} \right) \quad (4.95)$$

else if  $x_{gbint} < -\xi_{mrg,sub}$ :

$$y_g = -x_{gbint} \quad (4.96)$$

$$y_{sub} = 1.25 y_g / \xi_{sub} \quad (4.97)$$

$$\eta = \left( y_{sub} + 10 - \sqrt{(y_{sub} - 6)^2 + 64} \right) / 2 \quad (4.98)$$

$$a = (y_g - \eta)^2 + G_{f,sub}^2 (\eta + 1) \quad (4.99)$$

$$c = 2(y_g - \eta) - G_{f,sub}^2 \quad (4.100)$$

$$\tau = -\eta + \ln(a / G_{f,sub}^2) \quad (4.101)$$

$$y_0 = \sigma_3(a, c, \tau, \eta) \quad (4.102)$$

$$\Delta_0 = \exp(y_0) \quad (4.103)$$

$$\chi_0 = y_0^2 / (2 + y_0^2) \quad (4.104)$$

$$\chi_1 = 4y_0 / (2 + y_0^2)^2 \quad (4.105)$$

$$\chi_2 = (8 - 12y_0^2) / (2 + y_0^2)^3 \quad (4.106)$$

$$p_c = 2(y_g - y_0) + G_{f,sub}^2 (\Delta_0 - 1 + \exp(-x_{n,sub}) (1 - \chi_1 - 1/\Delta_0)) \quad (4.107)$$

$$q_c = (y_g - y_0)^2 + G_{f,sub}^2 (y_0 - \Delta_0 + 1 + \exp(-x_{n,sub}) (1 + \chi_0 - 1/\Delta_0 - y_0)) \quad (4.108)$$

$$\delta x_{g2,sub} = -y_0 - \frac{2q_c}{p_c + \sqrt{p_c^2 - 2q_c (2 - G_{f,sub}^2 (\Delta_0 + \exp(-x_{n,sub}) (1/\Delta_0 - \chi_2)))}} \quad (4.109)$$

else:

$$x_{g1} = 1.25 + 0.732464877 \xi_{f,sub} \quad (4.110)$$

$$\frac{1}{\tilde{x}_{g1}} = \frac{1.25 \xi_{sub} / x_{g1} - 1}{x_{g1}} \quad (4.111)$$

$$\bar{x} = \frac{x_{gbint}}{\xi_{sub}} \left( 1 + \frac{x_{gbint}}{\tilde{x}_{g1}} \right) \quad (4.112)$$

$$w = 1 - \exp(-\bar{x}) \quad (4.113)$$

$$x_1 = x_{gbint} + G_{f,sub}^2 / 2 - G_{f,sub} \sqrt{x_{gbint}^2 + G_{f,sub}^2 / 4 - w} \quad (4.114)$$

$$b_x = x_{n,sub} + 3 \quad (4.115)$$

$$\eta = \text{MIN\_FUNC}(x_1, b_x, 5) - (b_x - \sqrt{b_x^2 + 5}) / 2 \quad (4.116)$$

$$\chi_0 = \eta^2 / (2 + \eta^2) \quad (4.117)$$

$$\chi_1 = 4\eta / (2 + \eta^2)^2 \quad (4.118)$$

$$\chi_2 = (8 - 12\eta^2) / (2 + \eta^2)^3 \quad (4.119)$$

$$a = (x_{gbint} - \eta)^2 - G_{f,sub}^2 (\exp(-\eta) + \eta - 1 - \exp(-x_{n,sub}) (\eta + 1 + \chi_0)) \quad (4.120)$$

$$b = 1 - G_{f,sub}^2 (\exp(-\eta) - \chi_2 \exp(-x_{n,sub})) / 2 \quad (4.121)$$

$$c = 2(x_{gbint} - \eta) + G_{f,sub}^2 (1 - \exp(-\eta) - \exp(-x_{n,sub})(1 + \chi_1)) \quad (4.122)$$

$$\tau = x_{n,sub} - \eta + \ln(a/G_{f,sub}^2) \quad (4.123)$$

$$x_0 = \sigma_2(a, b, c, \tau, \eta) \quad (4.124)$$

$$\Delta_0 = \exp(x_0) \quad (4.125)$$

$$\chi_0' = x_0^2 / (2 + x_0^2) \quad (4.126)$$

$$\chi_1' = 4x_0 / (2 + x_0^2)^2 \quad (4.127)$$

$$\chi_2' = (8 - 12x_0^2) / (2 + x_0^2)^3 \quad (4.128)$$

$$p_c = 2(x_{gbint} - x_0) + G_{f,sub}^2 (1 - 1/\Delta_0 + \exp(-x_{n,sub})(\Delta_0 - 1 - \chi_1')) \quad (4.129)$$

$$q_c = (x_{gbint} - x_0)^2 - G_{f,sub}^2 (x_0 - 1 + 1/\Delta_0 + \exp(-x_{n,sub})(\Delta_0 - 1 - \chi_0' - x_0)) \quad (4.130)$$

$$\delta x_{g2,sub} = x_0 + \frac{2q_c}{p_c + \sqrt{p_c^2 - 2q_c(2 - G_{f,sub}^2(1/\Delta_0 + \exp(-x_{n,sub})(\Delta_0 - \chi_2'))}} \quad (4.131)$$

Finally:

$$x_{g2eff} = \text{TYPE} \times \text{type}_{sub} \times (x_{g2int} + \delta x_{g2,sub}) \quad (4.132)$$

## 4.4 Channel current

This part is dedicated to the calculation of the MOSFET channel current. Since this calculation is completely symmetrical between front and back interfaces, it is valid not only for Ultra-Thin Body and Buried oxide FDSOI transistors, but also for Independent Double Gate MOSFETs.

### 4.4.1 Quantum mechanical correction in subthreshold regime

To account properly for quantum confinement when the effect is activated (**QMC**>0), the effective geometry of the device is modified. In the first correction detailed here, it is assumed that there's no charge in the channel. Thus, strictly speaking, this correction is valid only in the subthreshold regime. A second correction will be brought afterwards to account properly for quantum confinement also in the strong inversion regime.

if **QMC** > 0:

$$e_1 = \text{MAX\_FUNC}(k_{eq,1D}(x_{g10} - x_{g2eff}), 15, 225) \quad (4.133)$$

$$e_2 = \text{MAX\_FUNC}(-k_{eq,1D}(x_{g10} - x_{g2eff}), 15, 225) \quad (4.134)$$

$$C_{si}' = \frac{C_{si0}'}{1 - qq(e_1^{-1/3} + e_2^{-1/3})} \quad (4.135)$$

$$k_{1,1D,QM} = k_{1,1D} \frac{1 - qq(e_1^{-1/3} + e_2^{-1/3})}{1 + k_{1,1D}e_1^{-1/3}qq} \quad (4.136)$$

$$k_{2,1D,QM} = k_{2,1D} \frac{1 - qq(e_1^{-1/3} + e_2^{-1/3})}{1 + k_{2,1D}e_2^{-1/3}qq} \quad (4.137)$$

$$k_{eq,1D,QM} = \frac{1}{1 + 1/k_{1,1D,QM} + 1/k_{2,1D,QM}} \quad (4.138)$$

$$t_{ox1fact} = 1 + k_{1,1D,QM}e_1^{-1/3}qq \quad (4.139)$$

$$t_{ox2fact} = 1 + k_{2,1D,QM}e_2^{-1/3}qq \quad (4.140)$$

else:

$$C_{Si}' = C_{Si0}' \quad (4.141)$$

$$k_{1,1D,QM} = k_{1,1D} \quad (4.142)$$

$$k_{2,1D,QM} = k_{2,1D} \quad (4.143)$$

$$k_{eq,1D,QM} = k_{eq,1D} \quad (4.144)$$

$$t_{ox1fact} = 1 \quad (4.145)$$

$$t_{ox2fact} = 1 \quad (4.146)$$

#### 4.4.2 Short channel effects

$$\delta x_{W1,1D} = k_{eq,1D,QM} (x_{g10} - x_{g2eff}) \quad (4.147)$$

$$\begin{cases} x_{W1,1D} = x_{g10} - \frac{\delta x_{W1,1D}}{k_{1,1D,QM}} + \ln\left(\frac{1 + e^{-\delta x_{W1,1D}}}{2}\right) & \text{if } \delta x_{W1,1D} > 0 \\ x_{W1,1D} = x_{g2eff} + \frac{\delta x_{W1,1D}}{k_{2,1D,QM}} + \ln\left(\frac{1 + e^{\delta x_{W1,1D}}}{2}\right) & \text{else} \end{cases} \quad (4.148)$$

$$x_{1D} = \text{MIN\_FUNC}(x_{W1,1D}, x_{th,1D}, 4.0) \quad (4.149)$$

$$\delta l_{eff} = \sqrt{1 + 2 \frac{x_{th,1D} - x_{1D}}{x_{SD,dep}}} - 1 \quad (4.150)$$

$$x_{edge} = x_{1D} + x_{SD,dep} \delta l_{eff} \quad (4.151)$$

$$c_{sce1} = \frac{1}{1 + PSCE_1 \times \text{MAX\_FUNC}(1 + \text{PSCEDLB} \times x_{g20}, 0.5, 0.01)} \quad (4.152)$$

$$c_{sce2} = \frac{1}{1 + PSCE_2 \times \text{MAX\_FUNC}(1 + \text{PSCEDLB} \times x_{g20}, 0.5, 0.01)} \quad (4.153)$$

$$\delta x_{g1,DIBL} = 2CF_1 x_{d0} \left( \sqrt{1 + x_{dsx}/x_{d0}} - 1 \right) (1 + \text{CFDL} \times \delta l_{eff}) (1 + \text{CFDLB} \times x_{g20}) \quad (4.154)$$

$$\delta x_{g2,DIBL} = 2CF_2 x_{d0} \left( \sqrt{1 + x_{dsx}/x_{d0}} - 1 \right) (1 + \text{CFDL} \times \delta l_{eff}) (1 + \text{CFDLB} \times x_{g20}) \quad (4.155)$$

$$x_{g1} = (x_{g10} - x_{edge} + \delta x_{g1,DIBL}) c_{sce1} + x_{edge} + \frac{x_d - x_{dsx}}{2} \quad (4.156)$$

$$x_{g2} = (x_{g2eff} - x_{edge} + \delta x_{g2,DIBL}) c_{sce2} + x_{edge} + \frac{x_d - x_{dsx}}{2} \quad (4.157)$$

$$x_{g1x} = \text{MIN\_FUNC}(x_{g2} + \text{CICF}(x_{g1} - x_{g2}), 600, 0.01) \quad (4.158)$$

$$x_{g2x} = \text{MIN\_FUNC}(x_{g1} + \text{CIC}(x_{g2} - x_{g1}), 600, 0.01) \quad (4.159)$$

#### 4.4.3 Interface coupling in subthreshold regime

$$k_1 = \frac{k_{1,1D,QM}}{c_{sce1}} \quad (4.160)$$

$$k_2 = \frac{k_{2,1D,QM}}{c_{sce2}} \quad (4.161)$$

$$k_{eq} = \frac{1}{1 + 1/k_1 + 1/k_2} \quad (4.162)$$

$$A_0 = \frac{f_{A0}}{C_{Si}'^2} \quad (4.163)$$

$$\delta x_{th} = \ln\left(\frac{1+k_1}{1+k_2}\right) \quad (4.164)$$

$$diff_{min} = 2\delta x_{th} \frac{\exp(\delta x_{th}) + 1}{\exp(\delta x_{th}) - 1} \quad (4.165)$$

$$\delta x_{WI} = k_{eq}(x_{g1x} - x_{g2x}) \quad (4.166)$$

$$x_{1,WI0} = x_{g1x} - \delta x_{WI}/k_1 \quad (4.167)$$

$$x_{2,WI0} = x_{g2x} + \delta x_{WI}/k_2 \quad (4.168)$$

#### 4.4.4 Inversion charge and related quantities at source side

First, the gate charge density at the source side  $q_{1S}$ , normalized to  $k_1 C_{Si}' \phi_T$ , is computed by a call to the "CHARGE\_DENSITY" function described in **Error! Reference source not found.**:

$$q_{1S} = \text{CHARGE\_DENSITY}(x_{g1x}, x_{g2x}, 0) \quad (4.169)$$

Then, inversion and back gate charge densities, normalized to  $C_{Si}' \phi_T$  and  $k_2 C_{Si}' \phi_T$  respectively, are calculated:

$$A_{e1S} = A_0 \exp(x_{g1x} - q_{1S}) \quad (4.170)$$

$$f_{qsqs} = k_1^2 q_{1S}^2 - A_{e1S} \quad (4.171)$$

if  $f_{qsqs} < -0.005$ :

$$f_{qctS} = \sqrt{|f_{qsqs}|} \cot\left(\sqrt{|f_{qsqs}|}/2\right) \quad (4.172)$$

$$f_{shS} = -\frac{f_{qsqs}}{\sin\left(\sqrt{|f_{qsqs}|}/2\right)^2} \quad (4.173)$$

$$f_{lnS} = \ln(f_{shS}) \quad (4.174)$$

else if  $f_{qsqs} > 0.005$ :

$$\zeta_S = \exp\left(-\sqrt{|f_{qsqs}|}\right) \quad (4.175)$$

$$f_{qctS} = \sqrt{|f_{qsqs}|} \frac{1 + \zeta_S}{1 - \zeta_S} \quad (4.176)$$

$$f_{shS} = \frac{4f_{qsqs}}{1 - \zeta_S(2 - \zeta_S)} \zeta_S \quad (4.177)$$

$$f_{lnS} = \ln\left(\frac{4f_{qsqs}}{1 - \zeta_S(2 - \zeta_S)}\right) - \sqrt{|f_{qsqs}|} \quad (4.178)$$

else:

$$f_{qctS} = 2 + \frac{f_{qsqs}}{6} \left(1 - \frac{f_{qsqs}}{60} \left(1 - \frac{f_{qsqs}}{42}\right)\right) \quad (4.179)$$

$$f_{shS} = 4 - \frac{f_{qsqs}}{3} \left(1 - \frac{f_{qsqs}}{20} \left(1 - \frac{5f_{qsqs}}{126}\right)\right) \quad (4.180)$$

$$f_{lnS} = \ln(f_{shS}) \quad (4.181)$$

if  $1.01k_1q_{1S} + f_{qctS} \leq 0$ :

$$q_{iS} = \frac{k_1q_{1S} - f_{qctS}}{1 - f_{shS}/A_{e1S}} \quad (4.182)$$

$$q_{2S} = \frac{q_{iS} - k_1q_{1S}}{k_2} \quad (4.183)$$

else if  $A_{e1S}k_1q_{1S} < 0.9k_1^2q_{1S}^2(k_1q_{1S} + f_{qctS})$ :

$$q_{iS} = \frac{A_{e1S}}{k_1q_{1S} + f_{qctS}} \quad (4.184)$$

$$q_{2S} = \frac{q_{iS} - k_1q_{1S}}{k_2} \quad (4.185)$$

else:

$$q_{2S} = x_{g2x} - x_{g1x} + q_{1S} + 2\ln(k_1q_{1S} + f_{qctS}) - f_{lnS} \quad (4.186)$$

$$q_{iS} = k_1q_{1S} + k_2q_{2S} \quad (4.187)$$

Finally, some required quantities, including the drift electrostatic potential, are computed:

$$A_{e2S} = A_0 \exp(x_{g2x} - q_{2S}) \quad (4.188)$$

if  $q_{iS} > 10^{-6}$ :

$$b_{1S} = A_{e1S}/k_1 \quad (4.189)$$

$$b_{2S} = A_{e2S}/k_2 \quad (4.190)$$

$$a_{1S} = b_{1S} + 2k_1q_{1S} \quad (4.191)$$

$$a_{2S} = b_{2S} + 2k_2q_{2S} \quad (4.192)$$

$$\Sigma_S = 2q_{iS} + b_{1S} + b_{2S} \quad (4.193)$$

$$\left\{ \begin{array}{ll} Z_S = \frac{-4f_{qsqs}\Sigma_S}{q_{iS}(a_{1S}a_{2S} + 2a_{1S}(q_{2S} + 2) + 2a_{2S}(q_{1S} + 2))} & \text{if } |f_{qsqs}| > 0.005 \\ Z_S = \frac{A_{e1S}A_{e2S}\Sigma_S}{q_{iS}\left(a_{1S}A_{e1S} + a_{2S}A_{e2S} + a_{1S}a_{2S}q_{iS}\left(1 + \frac{q_{iS}}{6}\left(1 - \frac{f_{qsqs}}{30}\left(1 - \frac{f_{qsqs}}{28}\left(1 - \frac{f_{qsqs}}{30}\right)\right)\right)\right)\right)} & \text{else} \end{array} \right. \quad (4.194)$$

Finally:

$$x_{driftS} = \ln(q_{iS}) \quad (4.195)$$

#### 4.4.5 Mobility attenuation and series resistance at source side

Front and back transverse effective fields, normalized to  $C_{Si}'\phi_T/\epsilon_{ch}$ .

$$e_{surf1S} = 2\ln(1 + \exp(k_1q_{1S}/2)) \quad (4.196)$$

$$e_{surf2S} = 2\ln(1 + \exp(k_2q_{2S}/2)) \quad (4.197)$$

$$e_{cpl1S} = e_{surf2S} - k_2q_{2S} \quad (4.198)$$

$$e_{cpl2S} = e_{surf1S} - k_1q_{1S} \quad (4.199)$$

$$e_{eff1S} = \eta_{\mu}e_{surf1S} + (1 - \eta_{\mu})e_{cpl1S} \quad (4.200)$$

$$e_{eff2S} = \eta_{\mu}e_{surf2S} + (1 - \eta_{\mu})e_{cpl2S} \quad (4.201)$$

Non-universality correction factor:

$$f_{corS} = \frac{\text{MAX\_FUNC}(1 + X_{cor}(e_{cp1S} + \mathbf{XCORB} \times e_{cp2S}), 0, 0.01)}{\text{MAX\_FUNC}(1 + 0.2 \times X_{cor}(e_{cp1S} + \mathbf{XCORB} \times e_{cp2S}), 0, 0.01)} \quad (4.202)$$

Coulomb scattering term:

$$q_{i1S} = \frac{e_{surf1S}}{e_{surf1S} + e_{surf2S}} q_{iS} \quad (4.203)$$

$$q_{i2S} = \frac{e_{surf2S}}{e_{surf1S} + e_{surf2S}} q_{iS} \quad (4.204)$$

$$G_{CSS} = C_S (1 + \mathbf{CSFI} \times e_{cp1S} + \mathbf{CSBI} \times e_{cp2S}) (1 + q_{i1S}/q_{i1th,CS} + q_{i2S}/q_{i2th,CS})^{-\theta_{cs}} \quad (4.205)$$

Series resistance term:

$$\begin{cases} f_{RSGS} = 1 - \mathbf{RSG} \times q_{iS}^{\text{THERSG}} & \text{if } \mathbf{RSG} < 0 \\ f_{RSGS} = 1 / (1 + \mathbf{RSG} \times q_{iS}^{\text{THERSG}}) & \text{else} \end{cases} \quad (4.206)$$

$$G_{RSS} = f_{RS} C_{Si} (q_{iS} f_{RSGS} + \mathbf{RSIG}) \times \text{MAX\_FUNC}(1 - \mathbf{RSB} x_{g20}, 0, 0.01) \quad (4.207)$$

Total mobility degradation term, including high field mobility effect:

$$G_{mob1S} = 1 + (f_{\mu E} e_{eff1S})^{\theta_{\mu}} + G_{CSS} + \beta_{N1} G_{RSS} \quad (4.208)$$

$$G_{mob2S} = 1 + (f_{\mu E} e_{eff2S})^{\theta_{\mu}} + G_{CSS} + \beta_{N2} G_{RSS} \quad (4.209)$$

$$c_{1S} = e_{surf1S} \beta_{N1} \quad (4.210)$$

$$c_{2S} = e_{surf2S} \beta_{N2} \quad (4.211)$$

$$G_{mobS} = f_{corS} \frac{c_{1S} + c_{2S}}{c_{1S}/G_{mob1S} + c_{2S}/G_{mob2S}} \quad (4.212)$$

#### 4.4.6 Drain saturation voltage, including velocity saturation effect

Derivative of inversion charge versus drift potential at the onset of saturation

if  $\delta x_{WI} > 0.007$ :

$$s_1 = \frac{\delta x_{WI}}{1 - \exp(-\delta x_{WI})} \quad (4.213)$$

$$s_2 = \exp(-\delta x_{WI}) s_1 \quad (4.214)$$

$$\delta x_{\infty} = \ln\left(\frac{A_0}{2q_{iS} s_1}\right) + x_{1,WI0} \quad (4.215)$$

$$\hat{q}_{1\infty} = \frac{-\delta x_{WI}}{k_{eq}(1 - s_1 - \delta x_{WI}/k_2)} \quad (4.216)$$

$$\hat{q}_{2\infty} = \frac{\delta x_{WI}}{k_{eq}(1 - s_2 - \delta x_{WI}/k_1)} \quad (4.217)$$

$$d_{\infty} = \frac{\delta x_{WI}}{(s_2/k_2 + 1/2)/\hat{q}_{2\infty} - (s_1/k_1 + 1/2)/\hat{q}_{1\infty}} \quad (4.218)$$

else if  $\delta x_{WI} < -0.007$ :



$$s_2 = \frac{\delta x_{WI}}{\exp(\delta x_{WI}) - 1} \quad (4.219)$$

$$s_1 = \exp(\delta x_{WI}) s_2 \quad (4.220)$$

$$\delta x_{\infty} = \ln\left(\frac{A_0}{2q_{IS} s_2}\right) + x_{2,WI0} \quad (4.221)$$

$$\hat{q}_{1\infty} = \frac{-\delta x_{WI}}{k_{eq}(1 - s_1 - \delta x_{WI}/k_2)} \quad (4.222)$$

$$\hat{q}_{2\infty} = \frac{\delta x_{WI}}{k_{eq}(1 - s_2 - \delta x_{WI}/k_1)} \quad (4.223)$$

$$d_{\infty} = \frac{\delta x_{WI}}{(s_2/k_2 + 1/2)/\hat{q}_{2\infty} - (s_1/k_1 + 1/2)/\hat{q}_{1\infty}} \quad (4.224)$$

else:

$$s_1 = 1 + \frac{\delta x_{WI}}{2} + \frac{\delta x_{WI}^2}{12} \quad (4.225)$$

$$s_2 = 1 - \frac{\delta x_{WI}}{2} + \frac{\delta x_{WI}^2}{12} \quad (4.226)$$

$$\hat{q}_{1\infty} = \frac{1}{k_{eq}(1/2 + 1/k_2 + \delta x_{WI}/12)} \quad (4.227)$$

$$\hat{q}_{2\infty} = \frac{1}{k_{eq}(1/2 + 1/k_1 - \delta x_{WI}/12)} \quad (4.228)$$

$$\delta x_{\infty} = \ln\left(\frac{A_0}{2q_{IS}(1 - \delta x_{WI}^2/24)}\right) + \frac{x_{1,WI0} + x_{2,WI0}}{2} \quad (4.229)$$

$$d_{\infty} = \frac{-12}{4 - 3k_{eq} + 12k_{eq}/(k_1 k_2) + (k_{eq}/k_1 - k_{eq}/k_2)\delta x_{WI} + (1/15 - k_{eq}/12)\delta x_{WI}^2} \quad (4.230)$$

Calculation of  $\delta x_{sat}$  and  $q_{iDsats}$

if  $q_{IS} > 10^{-6}$ :

$$w_{sat1S} = \frac{100e_{surf1S}}{100 + e_{surf1S}} \quad (4.231)$$

$$\begin{cases} sat_{fact1S} = 1/(1 - w_{sat1S} \text{THESATG}) & \text{if THESATG} < 0 \\ sat_{fact1S} = 1 + w_{sat1S} \text{THESATG} & \text{else} \end{cases} \quad (4.232)$$

$$w_{sat2S} = \frac{100e_{surf2S}}{100 + e_{surf2S}} \quad (4.233)$$

$$\begin{cases} sat_{fact2S} = 1/(1 - w_{sat2S} \text{THESATB}) & \text{if THESATB} < 0 \\ sat_{fact2S} = 1 + w_{sat2S} \text{THESATB} & \text{else} \end{cases} \quad (4.234)$$

$$Z_{IS} = \frac{Z_S \Sigma_S}{a_{1S} a_{2S}} - \frac{1}{q_{IS}} \left( \frac{A_{e1S}}{a_{1S}} + \frac{A_{e2S}}{a_{2S}} \right) \quad (4.235)$$

$$d_S = \frac{Z_{IS} q_{IS}}{Z_{IS} + 1} \quad (4.236)$$

$$\delta x_i = \text{MAX\_FUNC}\left(\frac{q_{iS} + d_\infty \delta x_\infty}{d_\infty - d_S}, 0, 10^{-6}\right) \quad (4.237)$$

$$\gamma_{sat} = \frac{f_{vsat}}{G_{mobS}} \frac{sat_{fact1S} + sat_{fact2S}}{2} \quad (4.238)$$

$$v_S = 1 - q_{iS}/d_S \quad (4.239)$$

$$v_D = 1 + \delta x_\infty \quad (4.240)$$

$$w_D = ((2d_S - q_{iS})/d_\infty - 2 - \delta x_\infty) \delta x_i \quad (4.241)$$

$$p_S = 2/\gamma_{sat}^2 \quad (4.242)$$

$$q_S = p_S v_S \quad (4.243)$$

$$p_D = p_S + w_D \quad (4.244)$$

$$q_D = p_S v_D \quad (4.245)$$

$$r_{cS} = \sqrt{q_S^2 + 4p_S^3/27} \quad (4.246)$$

$$r_{cD} = \sqrt{q_D^2 + 4p_D^3/27} \quad (4.247)$$

$$\delta x_{satS} = \left(\frac{r_{cS} + q_S}{2}\right)^{1/3} - \left(\frac{r_{cS} - q_S}{2}\right)^{1/3} \quad (4.248)$$

$$\delta x_{satD} = \left(\frac{r_{cD} + q_D}{2}\right)^{1/3} - \left(\frac{r_{cD} - q_D}{2}\right)^{1/3} \quad (4.249)$$

$$\delta x_{sat} = 0.94 \times \text{MAX\_FUNC}(\delta x_{satS}, \delta x_{satD}, 10(d_S - d_\infty)^2) \quad (4.250)$$

$$q_{iDsats} = q_{iS} + d_S \delta x_{sat} \quad (4.251)$$

$$q_{iDsatsD} = d_\infty (\delta x_{sat} - \delta x_\infty) \quad (4.252)$$

$$q_{iDsats} = \text{MAX\_FUNC}(q_{iDsats}, q_{iDsatsD}, 36(d_S - d_\infty)^2) \quad (4.253)$$

else:

$$d_S = d_\infty \quad (4.254)$$

$$\delta x_{sat} = 0.94 \times (1 + \delta x_\infty) \quad (4.255)$$

$$q_{iDsats} = q_{iS}/2 + d_\infty (\delta x_{sat} - \delta x_\infty/2) \quad (4.256)$$

Normalized saturation and effective drain voltages

$$x_{nDS,0} = \delta x_{sat} + \ln\left(\frac{q_{iS}}{0.5 + \ln(1 + \exp(q_{iDsats} - 0.5))}\right) \quad (4.257)$$

$$x_{nDS,1} = 6 + \ln(1 + \exp(x_{nDS,0} - 6)) \quad (4.258)$$

$$x_{nDS,sat} = 600 - \ln(1 + \exp(600 - x_{nDS,1})) \quad (4.259)$$

$$x_{Deff} = \frac{x_D}{\left(\left(1 + \gamma_{AX}(x_D/x_{nDS,sat})^4\right)^{8/3} + (x_D/x_{nDS,sat})^{16}\right)^{1/16}} \quad (4.260)$$

#### 4.4.7 Inversion charge and related quantities at drain side

Gate charge density at drain side:

$$q_{1D} = \text{CHARGE\_DENSITY}(x_{g1x}, x_{g2x}, x_{Deff}) \quad (4.261)$$

Then, inversion and back gate charge densities, normalized to  $C_{Si}'\phi_T$  and  $k_2C_{Si}'\phi_T$  respectively, are calculated:

$$A_{e1D} = A_0 \exp(x_{g1x} - q_{1D} - x_{Deff}) \quad (4.262)$$

$$f_{qsqD} = k_1^2 q_{1D}^2 - A_{e1D} \quad (4.263)$$

if  $f_{qsqD} < -0.005$ :

$$f_{qctD} = \sqrt{|f_{qsqD}|} \cot\left(\sqrt{|f_{qsqD}|}/2\right) \quad (4.264)$$

$$f_{shD} = -\frac{f_{qsqD}}{\sin\left(\sqrt{|f_{qsqD}|}/2\right)^2} \quad (4.265)$$

$$f_{lnD} = \ln(f_{shD}) \quad (4.266)$$

else if  $f_{qsqD} > 0.005$ :

$$\zeta_D = \exp\left(-\sqrt{|f_{qsqD}|}\right) \quad (4.267)$$

$$f_{qctD} = \sqrt{|f_{qsqD}|} \frac{1 + \zeta_D}{1 - \zeta_D} \quad (4.268)$$

$$f_{shD} = \frac{4f_{qsqD}}{1 - \zeta_D(2 - \zeta_D)} \zeta_D \quad (4.269)$$

$$f_{lnD} = \ln\left(\frac{4f_{qsqD}}{1 - \zeta_D(2 - \zeta_D)}\right) - \sqrt{|f_{qsqD}|} \quad (4.270)$$

else:

$$f_{qctD} = 2 + \frac{f_{qsqD}}{6} \left(1 - \frac{f_{qsqD}}{60} \left(1 - \frac{f_{qsqD}}{42}\right)\right) \quad (4.271)$$

$$f_{shD} = 4 - \frac{f_{qsqD}}{3} \left(1 - \frac{f_{qsqD}}{20} \left(1 - \frac{5f_{qsqD}}{126}\right)\right) \quad (4.272)$$

$$f_{lnD} = \ln(f_{shD}) \quad (4.273)$$

if  $1.01k_1q_{1D} + f_{qctD} \leq 0$ :

$$q_{iD} = \frac{k_1q_{1D} - f_{qctD}}{1 - f_{shD}/A_{e1D}} \quad (4.274)$$

$$q_{2D} = \frac{q_{iD} - k_1q_{1D}}{k_2} \quad (4.275)$$

else if  $A_{e1D}k_1q_{1D} < 0.9k_1^2q_{1D}^2(k_1q_{1D} + f_{qctD})$ :

$$q_{iD} = \frac{A_{e1D}}{k_1q_{1D} + f_{qctD}} \quad (4.276)$$

$$q_{2D} = \frac{q_{iD} - k_1q_{1D}}{k_2} \quad (4.277)$$

else:

$$q_{2D} = x_{g2x} - x_{g1x} + q_{1D} + 2\ln(k_1q_{1D} + f_{qctD}) - f_{lnD} \quad (4.278)$$

$$q_{iD} = k_1q_{1D} + k_2q_{2D} \quad (4.279)$$

Finally, some required quantities, including the drift electrostatic potential, are computed:

$$A_{e2D} = A_0 \exp(x_{g2x} - q_{2D} - x_{Deff}) \quad (4.280)$$

if  $q_{iS} > 10^{-6}$ :

$$b_{1D} = A_{e1D} / k_1 \quad (4.281)$$

$$b_{2D} = A_{e2D} / k_2 \quad (4.282)$$

$$a_{1D} = b_{1D} + 2k_1 q_{1D} \quad (4.283)$$

$$a_{2D} = b_{2D} + 2k_2 q_{2D} \quad (4.284)$$

$$\Sigma_D = 2q_{iD} + b_{1D} + b_{2D} \quad (4.285)$$

$$\left\{ \begin{array}{ll} Z_D = \frac{-4f_{qsqD}\Sigma_D}{q_{iD}(a_{1D}a_{2D} + 2a_{1D}(q_{2D} + 2) + 2a_{2D}(q_{1D} + 2))} & \text{if } |f_{qsqD}| > 0.005 \\ Z_D = \frac{A_{e1D}A_{e2D}\Sigma_D}{q_{iD}\left(a_{1D}A_{e1D} + a_{2D}A_{e2D} + a_{1D}a_{2D}q_{iD}\left(1 + \frac{q_{iD}}{6}\left(1 - \frac{f_{qsqD}}{30}\left(1 - \frac{f_{qsqD}}{28}\left(1 - \frac{f_{qsqD}}{30}\right)\right)\right)\right)\right)} & \text{else} \end{array} \right. \quad (4.286)$$

Finally:

$$x_{driftD} = x_{Deff} + \ln(q_{iD}) \quad (4.287)$$

#### 4.4.8 Mid-point inversion charge

$$q_{im} = \frac{q_{iS} + q_{iD}}{2} \quad (4.288)$$

$$\delta x_{drift} = x_{driftD} - x_{driftS} \quad (4.289)$$

#### 4.4.9 Mobility attenuation and series resistance

Front and back transverse effective fields at drain side, normalized to  $C_{Si}'\phi_T/\epsilon_{ch}$

$$e_{surf1D} = 2\ln(1 + \exp(k_1 q_{1D}/2)) \quad (4.290)$$

$$e_{surf2D} = 2\ln(1 + \exp(k_2 q_{2D}/2)) \quad (4.291)$$

$$e_{cpl1D} = e_{surf2D} - k_2 q_{2D} \quad (4.292)$$

$$e_{cpl2D} = e_{surf1D} - k_1 q_{1D} \quad (4.293)$$

$$e_{eff1D} = \eta_\mu e_{surf1D} + (1 - \eta_\mu) e_{cpl1D} \quad (4.294)$$

$$e_{eff2D} = \eta_\mu e_{surf2D} + (1 - \eta_\mu) e_{cpl2D} \quad (4.295)$$

Mid-values of surface and effective fields

$$e_{surf1} = \frac{e_{surf1S} + e_{surf1D}}{2} \quad (4.296)$$

$$e_{surf2} = \frac{e_{surf2S} + e_{surf2D}}{2} \quad (4.297)$$

$$e_{cpl1} = \frac{e_{cpl1S} + e_{cpl1D}}{2} \quad (4.298)$$

$$e_{cpl2} = \frac{e_{cpl2S} + e_{cpl2D}}{2} \quad (4.299)$$

$$e_{eff1} = \frac{e_{eff1S} + e_{eff1D}}{2} \quad (4.300)$$

$$e_{eff2} = \frac{e_{eff2S} + e_{eff2D}}{2} \quad (4.301)$$

Non-universality correction factor:

$$f_{cor} = \frac{\text{MAX\_FUNC}(1 + X_{cor}(e_{cpl1} + \mathbf{XCORB} \times e_{cpl2}), 0, 0.01)}{\text{MAX\_FUNC}(1 + 0.2 \times X_{cor}(e_{cpl1} + \mathbf{XCORB} \times e_{cpl2}), 0, 0.01)} \quad (4.302)$$

Coulomb scattering term:

$$q_{i1m} = \frac{e_{surf1}}{e_{surf1} + e_{surf2}} q_{im} \quad (4.303)$$

$$q_{i2m} = \frac{e_{surf2}}{e_{surf1} + e_{surf2}} q_{im} \quad (4.304)$$

$$G_{CS} = C_S (1 + \mathbf{CSFI} \times e_{cpl1} + \mathbf{CSBI} \times e_{cpl2}) (1 + q_{i1m}/q_{i1th,CS} + q_{i2m}/q_{i2th,CS})^{-\theta_{CS}} \quad (4.305)$$

Series resistance term:

$$\begin{cases} f_{RSG} = 1 - \mathbf{RSG} \times q_{im}^{\text{THERSG}} & \text{if } \mathbf{RSG} < 0 \\ f_{RSG} = 1 / (1 + \mathbf{RSG} \times q_{im}^{\text{THERSG}}) & \text{else} \end{cases} \quad (4.306)$$

$$G_{RS} = f_{RS} C_{Si} (q_{im} f_{RSG} + \mathbf{RSIG}) \times \text{MAX\_FUNC}(1 - \mathbf{RSB} x_{g20}, 0, 0.01) \quad (4.307)$$

Total mobility degradation term, including high field mobility effect:

$$G_{mob1} = 1 + (f_{\mu E} e_{eff1})^{\theta_{\mu}} + G_{CS} + \beta_{N1} G_{RS} \quad (4.308)$$

$$G_{mob2} = 1 + (f_{\mu E} e_{eff2})^{\theta_{\mu}} + G_{CS} + \beta_{N2} G_{RS} \quad (4.309)$$

$$c_1 = e_{surf1} \beta_{N1} \quad (4.310)$$

$$c_2 = e_{surf2} \beta_{N2} \quad (4.311)$$

$$G_{mob} = f_{cor} \frac{c_1 + c_2}{c_1/G_{mob1} + c_2/G_{mob2}} \quad (4.312)$$

$$\beta_{Neff} = \frac{c_1 + c_2}{e_{surf1} + e_{surf2}} \quad (4.313)$$

#### 4.4.10 Channel length modulation

$$q_{im1}^* = q_{im} + 4 \quad (4.314)$$

$$\begin{cases} r_1 = q_{im}/q_{im1}^* \times 1/(1 + \mathbf{ALPB} \times q_{im2}) & \text{if } \mathbf{ALPB} > 0 \\ r_1 = q_{im}/q_{im1}^* \times (1 - \mathbf{ALPB} \times q_{im2}) & \text{else} \end{cases} \quad (4.315)$$

$$\Delta L/L = \mathbf{ALP} \times \ln \left( 1 + \frac{x_D - x_{Deff}}{\mathbf{VP}/\phi_t + \mathbf{VPG} \times q_{im}^2} \right) \times r_1 \quad (4.316)$$

$$G_{\Delta L} = \frac{1}{1 + \Delta L/L (1 + \Delta L/L)} \quad (4.317)$$

$$\Delta L_1/L = \left( \mathbf{ALP} + \frac{f_{alp1}}{q_{im1}^*} \right) \times \ln \left( 1 + \frac{x_D - x_{Deff}}{\mathbf{VP}/\phi_t + \mathbf{VPG} \times q_{im}^2} \right) \times r_1 \quad (4.318)$$

$$F_{\Delta L} = \frac{1 + \Delta L_1/L(1 + \Delta L_1/L)}{1 + \Delta L/L(1 + \Delta L/L)} \quad (4.319)$$

#### 4.4.11 Velocity saturation

$$w_{sat1} = \frac{100e_{surf1}}{100 + e_{surf1}} \quad (4.320)$$

$$\begin{cases} sat_{fact1} = 1/(1 - w_{sat1} \text{THESATG}) & \text{if } \text{THESATG} < 0 \\ sat_{fact1} = 1 + w_{sat1} \text{THESATG} & \text{else} \end{cases} \quad (4.321)$$

$$w_{sat2} = \frac{100e_{surf2}}{100 + e_{surf2}} \quad (4.322)$$

$$\begin{cases} sat_{fact2} = 1/(1 - w_{sat2} \text{THESATB}) & \text{if } \text{THESATB} < 0 \\ sat_{fact2} = 1 + w_{sat2} \text{THESATB} & \text{else} \end{cases} \quad (4.323)$$

$$G_\gamma = f_{vsat} \delta x_{drift} \frac{sat_{fact1} + sat_{fact2}}{2} \quad (4.324)$$

$$z_{sat} = \left( \frac{G_\gamma}{G_{mob} G_{\Delta L}} \right)^2 \quad (4.325)$$

$$G_{vsat} = G_{mob} G_{\Delta L} \sqrt{1 + z_{sat}} \quad (4.326)$$

#### 4.4.12 Quantum confinement

Here is detailed the second quantum confinement correction that complements the correction described in paragraph 4.4.1.

if **QMC** > 0:

$$qm_{fact1} = \frac{1 + k_1 qq 0.6 / (e_{surf1}^2 + 60)^{1/6}}{t_{ox1fact}} \quad (4.327)$$

$$qm_{fact2} = \frac{1 + k_2 qq 0.6 / (e_{surf2}^2 + 60)^{1/6}}{t_{ox2fact}} \quad (4.328)$$

$$qm_{fact} = \frac{e_{surf1} + e_{surf2}}{e_{surf1}/qm_{fact1} + e_{surf2}/qm_{fact2}} \quad (4.329)$$

else:

$$qm_{fact1} = 1 \quad (4.330)$$

$$qm_{fact2} = 1 \quad (4.331)$$

$$qm_{fact} = 1 \quad (4.332)$$

#### 4.4.13 Channel current

if  $q_{IS} > 10^{-6}$ :

$$Z_{iD} = \frac{Z_D \Sigma_D}{a_{1D} a_{2D}} - \frac{1}{q_{iD}} \left( \frac{A_{e1D}}{a_{1D}} + \frac{A_{e2D}}{a_{2D}} \right) \quad (4.333)$$

$$\begin{cases} d_D = \frac{Z_{iD} q_{iD}}{Z_{iD} + 1} & \text{if } q_{iD} > 10^{-6} \\ d_D = d_\infty & \text{else} \end{cases} \quad (4.334)$$

if  $|d_D - d_S| > 10^{-3}$ :

$$\begin{aligned} L_S &= q_{iD} - q_{IS} - d_D \delta x_{drift} \\ L_D &= q_{iD} - q_{IS} - d_S \delta x_{drift} \\ U_S &= \sqrt{L_S^2 + 1 + 36(d_D - d_S)^2} \\ U_D &= \sqrt{L_D^2 + 1 + 36(d_D - d_S)^2} \\ \delta i_{drift} &= \frac{L_S U_D - L_D U_S + (1 + 36(d_D - d_S)^2) \ln((L_D + U_D)/(L_S + U_S))}{4(d_D - d_S)} \end{aligned} \quad (4.335)$$

else:

$$\delta i_{drift} = - \frac{\delta x_{drift}^3 (d_D - d_S)^2}{24 \sqrt{1 + 36(d_D - d_S)^2}} \quad (4.336)$$

else:

$$d_D = d_\infty \quad (4.337)$$

$$\delta i_{drift} = 0 \quad (4.338)$$

$$i_{DS,norm} = q_{im} \delta x_{drift} + \delta i_{drift} + q_{IS} - q_{iD} \quad (4.339)$$

$$I_{DS} = \frac{F_{\Delta L}}{G_{vsat} q m_{fact}} \beta_{Neff} \phi_T^2 C_{Si} i_{DS,norm} \quad (4.340)$$

### 4.5 Gate current, intrinsic charges and overlap related variables

In this paragraph are defined some variables that will be used for gate current, GIDL/GISL current and intrinsic charge models.

#### 4.5.1 Effective gate charge at front and back interfaces

if  $q_{IS} > 10^{-6}$ :

$$\hat{q}_{1S} = \frac{a_{1S}}{A_{e1S}/q_{IS} - Z_S} \quad (4.341)$$

$$\hat{q}_{1D} = \frac{a_{1D}}{A_{e1D}/q_{iD} - Z_D} \quad (4.342)$$

$$k_1 h_{10} = \frac{i_{DS,norm}}{\hat{q}_{1S} - \hat{q}_{1D}} \quad (4.343)$$

$$\hat{q}_{2S} = \frac{a_{2S}}{A_{e2S}/q_{iS} - Z_S} \quad (4.344)$$

$$\hat{q}_{2D} = \frac{a_{2D}}{A_{e2D}/q_{iD} - Z_D} \quad (4.345)$$

$$k_2 h_{20} = \frac{i_{DS,norm}}{\hat{q}_{2S} - \hat{q}_{2D}} \quad (4.346)$$

else:

$$\zeta_1 = -2s_1 \left( \frac{1}{k_1 \hat{q}_{1\infty}} + \frac{1}{d_\infty} \right) \quad (4.347)$$

$$\zeta_2 = -2s_2 \left( \frac{1}{k_2 \hat{q}_{2\infty}} + \frac{1}{d_\infty} \right) \quad (4.348)$$

$$\xi_1 = \frac{\zeta_2/k_2 + (\zeta_2 - \zeta_1)/d_\infty - (\zeta_1/k_1 + \zeta_2/k_2)/\hat{q}_{1\infty}}{3 + 2(s_1/k_1 + s_2/k_2)} \quad (4.349)$$

$$\xi_2 = \frac{\zeta_1/k_1 + (\zeta_1 - \zeta_2)/d_\infty - (\zeta_2/k_2 + \zeta_1/k_1)/\hat{q}_{2\infty}}{3 + 2(s_2/k_2 + s_1/k_1)} \quad (4.350)$$

$$k_1 h_{10} = -\frac{1}{\hat{q}_{1\infty}(\xi_1 \hat{q}_{1\infty} + 1/d_\infty)} \quad (4.351)$$

$$k_2 h_{20} = -\frac{1}{\hat{q}_{2\infty}(\xi_2 \hat{q}_{2\infty} + 1/d_\infty)} \quad (4.352)$$

$$k_1 h_1 = k_1 h_{10} \frac{\sqrt{1 + z_{sat}}}{1 + 3z_{sat}/2} \quad (4.353)$$

$$k_2 h_2 = k_2 h_{20} \frac{\sqrt{1 + z_{sat}}}{1 + 3z_{sat}/2} \quad (4.354)$$

$$\Delta_{k1q1} = \frac{k_1 q_{1D} - k_1 q_{1S}}{2} \quad (4.355)$$

$$\Delta_{k2q2} = \frac{k_2 q_{2D} - k_2 q_{2S}}{2} \quad (4.356)$$

$$P_1 = \frac{\Delta_{k1q1}}{k_1 h_1} \quad (4.357)$$

$$P_2 = \frac{\Delta_{k2q2}}{k_2 h_2} \quad (4.358)$$

#### 4.5.2 Surface potential and gate dielectric voltage drop in gate-source overlap region

$$G_{ov} = \frac{\sqrt{2q\epsilon_{ch} \text{NOV} \times 10^6}}{C_{ox1} \sqrt{\phi_{T0}}} \quad (4.359)$$

$$\xi_{ov} = 1 + \frac{G_{ov}}{\sqrt{2}} \quad (4.360)$$

$$\xi_{mrg,ov} = 10^{-5} \xi_{ov} \quad (4.361)$$

$$x_{g1,ov} = 1.25 + 0.732464877 \xi_{ov} \quad (4.362)$$



$$x_{S,ov} = \text{SP\_FDSOI\_OV}(x_{gs,ov}) \quad (4.363)$$

$$x_{S,ovcv} = \text{SP\_FDSOI\_OV}(x_{gs,ovcv}) \quad (4.364)$$

$$V_{ovS} = -\phi_{T0}(x_{gs,ov} + x_{S,ov}) \quad (4.365)$$

$$V_{ovS,cv} = -\phi_{T0}(x_{gs,ovcv} + x_{S,ovcv}) \quad (4.366)$$

#### 4.5.3 Surface potential and gate dielectric voltage drop in gate-drain overlap region

$$G_{ov} = \frac{\sqrt{2q\varepsilon_{ch}\text{NOVD} \times 10^6}}{C_{ox1} \sqrt{\phi_{T0}}} \quad (4.367)$$

$$\xi_{ov} = 1 + \frac{G_{ov}}{\sqrt{2}} \quad (4.368)$$

$$\xi_{mrg,ov} = 10^{-5} \xi_{ov} \quad (4.369)$$

$$x_{g1,ov} = 1.25 + 0.732464877 \xi_{ov} \quad (4.370)$$

$$x_{D,ov} = \text{SP\_FDSOI\_OV}(x_{gd,ov}) \quad (4.371)$$

$$x_{D,ovcv} = \text{SP\_FDSOI\_OV}(x_{gd,ovcv}) \quad (4.372)$$

$$V_{ovD} = -\phi_{T0}(x_{gd,ov} + x_{D,ov}) \quad (4.373)$$

$$V_{ovD,cv} = -\phi_{T0}(x_{gd,ovcv} + x_{D,ovcv}) \quad (4.374)$$

### 4.6 Gate current

In this section is detailed the calculation of gate current components. This calculation is not carried out when the flag **SWIGATE** is set to 0.

#### 4.6.1 Gate to source overlap component

$$\psi_t = \text{MIN\_FUNC}(V_{ovS} + D_{ov}, 0, 0.01) \quad (4.375)$$

$$G_{C2,oveff} = \frac{\text{GC2OVACC} + \text{GC2OVINV} \exp(x_{gs,ov}/2)}{1 + \exp(x_{gs,ov}/2)} \quad (4.376)$$

$$G_{C3,oveff} = \frac{\text{GC3OVACC} + \text{GC3OVINV} \exp(x_{gs,ov}/2)}{1 + \exp(x_{gs,ov}/2)} \quad (4.377)$$

$$G_{CQ,oveff} = \frac{G_{CQ,ovacc} + G_{CQ,ovin} \exp(x_{gs,ov}/2)}{1 + \exp(x_{gs,ov}/2)} \quad (4.378)$$

$$I_{G,oveff} = \frac{I_{G,ovacc} + I_{G,ovin} \exp(x_{gs,ov}/2)}{1 + \exp(x_{gs,ov}/2)} \quad (4.379)$$

$$\begin{cases} z_g = \text{MIN\_FUNC}\left(\frac{\sqrt{V_{ovS}^2 + 10^{-4}}}{\text{CHIB}}, G_{CQ,oveff}, 10^{-6}\right) & \text{if } G_{C3,oveff} < 0 \\ z_g = \frac{\sqrt{V_{ovS}^2 + 10^{-4}}}{\text{CHIB}} & \text{else} \end{cases} \quad (4.380)$$

$$\Delta_{Si} = \exp(3 + x_{S,ov} + \psi_t / \phi_{T0}) \quad (4.381)$$

$$\Delta_{gate} = \exp(3 + x_{S,ov} + (\psi_t - V_{GS}) / \phi_{T0}) \quad (4.382)$$

$$f_{GS,ov} = \frac{1 + \exp(\text{GCDOV}(V_{GD} - \text{GCVD OV}))}{1 + \exp(\text{GCDOV}(V_{GD} - \text{GCVD OV})) \exp(\text{GCDOV} \times V_{SD})} \quad (4.383)$$

$$I_{GS,ov} = I_{G,oveff} \ln\left(\frac{1 + \Delta_{Si}}{1 + \Delta_{gate}}\right) \exp(B_{ov}(z_g(G_{C2,oveff} + z_g G_{C3,oveff}) - 3/2)) \times f_{GS,ov} \quad (4.384)$$

#### 4.6.2 Gate to drain overlap component

$$\psi_t = \text{MIN\_FUNC}(V_{ovD} + D_{ov}, 0, 0.01) \quad (4.385)$$

$$G_{C2,oveff} = \frac{\text{GC2OVACC} + \text{GC2OVINV} \exp(x_{gd,ov}/2)}{1 + \exp(x_{gd,ov}/2)} \quad (4.386)$$

$$G_{C3,oveff} = \frac{\text{GC3OVACC} + \text{GC3OVINV} \exp(x_{gd,ov}/2)}{1 + \exp(x_{gd,ov}/2)} \quad (4.387)$$

$$G_{CQ,oveff} = \frac{G_{CQ,ovacc} + G_{CQ,ovin} \exp(x_{gd,ov}/2)}{1 + \exp(x_{gd,ov}/2)} \quad (4.388)$$

$$I_{G,oveff} = \frac{I_{G,ovaccD} + I_{G,ovinD} \exp(x_{gd,ov}/2)}{1 + \exp(x_{gd,ov}/2)} \quad (4.389)$$

$$\begin{cases} z_g = \text{MIN\_FUNC}\left(\frac{\sqrt{V_{ovD}^2 + 10^{-4}}}{\text{CHIB}}, G_{CQ,oveff}, 10^{-6}\right) & \text{if } G_{C3,oveff} < 0 \\ z_g = \frac{\sqrt{V_{ovD}^2 + 10^{-4}}}{\text{CHIB}} & \text{else} \end{cases} \quad (4.390)$$

$$\Delta_{Si} = \exp(3 + x_{D,ov} + \psi_t / \phi_{T0}) \quad (4.391)$$

$$\Delta_{gate} = \exp(3 + x_{D,ov} + (\psi_t - V_{GD}) / \phi_{T0}) \quad (4.392)$$

$$f_{GD,ov} = \frac{1 + \exp(\text{GCDOV}(V_{GS} - \text{GCVD OV}))}{1 + \exp(\text{GCDOV}(V_{GS} - \text{GCVD OV})) \exp(\text{GCDOV} \times V_{DS})} \quad (4.393)$$

$$I_{GD,ov} = I_{G,oveff} \ln\left(\frac{1 + \Delta_{Si}}{1 + \Delta_{gate}}\right) \exp(B_{ov}(z_g(G_{C2,oveff} + z_g G_{C3,oveff}) - 3/2)) \times f_{GD,ov} \quad (4.394)$$

#### 4.6.3 Gate to channel component

$$x_{DS} = -2\Delta_{k1q1}/k_1 \quad (4.395)$$

$$V_m = \phi_T \left( \frac{x_{DS}}{2} - \ln\left(\frac{1 + \exp(x_{DS} - x_{Deff})}{2}\right) \right) \quad (4.396)$$

$$q_{1m} = \frac{q_{1S} + q_{1D}}{2} \quad (4.397)$$

$$V_{oxm} = \phi_T q_{1m} \quad (4.398)$$

$$\psi_t = \text{MIN\_FUNC}(V_{oxm} + D_{ch}, 0, 0.01) \quad (4.399)$$

$$\begin{cases} z_g = \text{MIN\_FUNC}\left(\frac{\sqrt{V_{oxm}^2 + 10^{-4}}}{\text{CHIB}}, G_{CQ,ch}, 10^{-6}\right) & \text{if } \text{GC3CH} < 0 \\ z_g = \frac{\sqrt{V_{oxm}^2 + 10^{-4}}}{\text{CHIB}} & \text{else} \end{cases} \quad (4.400)$$

$$\Delta_{Si} = \exp((x_{g1x} - q_{1m} + (\psi_t - \alpha_b - V_m) / \phi_T) \times n_{iginv}) \quad (4.401)$$

$$\Delta_{gate} = \Delta_{Si} \exp(-(V_{GS} - V_m) / \phi_T \times n_{iginv}) \quad (4.402)$$

$$I_{GC0} = I_{G,inv} \ln\left(\frac{1 + \Delta_{Si}}{1 + \Delta_{gate}}\right) \exp(B_{ch}(z_g(\text{GC2CH} + z_g \text{GC3CH}) - 3/2)) \quad (4.403)$$

#### 4.6.4 Source/drain partitioning of gate to channel current

if  $x_{g1x} > 0$ :

$$u_0 = \frac{\text{CHIB}/\phi_T}{B_{ch}(\text{GC2CH} + 2Z_g \text{GC3CH})} \quad (4.404)$$

$$x = \frac{x_{DS}}{2u_0} \quad (4.405)$$

$$b = \frac{u_0}{k_1 h_1 / k_1} \quad (4.406)$$

$$B_g = b(1 - b)/2 \quad (4.407)$$

$$A_g = 1/2 - 3B_g \quad (4.408)$$

$$p_{GC} = (1 - b) \frac{\sinh(x)}{x} + b \cosh(x) \quad (4.409)$$

$$p_{GD} = \frac{p_{GC}}{2} - B_g \sinh(x) - \frac{A_g}{x} \left( \cosh(x) - \frac{\sinh(x)}{x} \right) \quad (4.410)$$

else:

$$p_{GC} = 1 \quad (4.411)$$

$$p_{GD} = 1/2 \quad (4.412)$$

$$I_{GC} = I_{GC0} p_{GC} \quad (4.413)$$

$$I_{GCD} = I_{GC0} p_{GD} \quad (4.414)$$

$$I_{GCS} = I_{GC} - I_{GCD} \quad (4.415)$$

#### 4.6.5 Gate to source and gate to drain total currents

$$I_{GS} = I_{GCS} + I_{GS,ov} \quad (4.416)$$

$$I_{GD} = I_{GCD} + I_{GD,ov} \quad (4.417)$$

### 4.7 Gate Induced Drain/Source Leakage (GIDL/GISL)

This paragraph details the calculation of GIDL and GISL currents. Notice that the model, coming from UTISOI1, has been simplified. In particular, GIDL/GISL currents are no longer proportional to **LOV**.

#### 4.7.1 Gate induced source leakage

$$V_{tovS} = \sqrt{V_{ovS}^2 + \text{CGIDL}^2 V_{SB}^2 + 10^{-6}} \quad (4.418)$$

$$I_{GISL} = -A_{GIDL} V_{SD} V_{ovS} V_{tovS} \exp\left(-\frac{B_{GIDL}}{V_{tovS}}\right) \frac{1 + \exp(\text{DGIDL} \times V_{SD})}{2} \quad (4.419)$$

#### 4.7.2 Gate induced drain leakage

$$V_{tovD} = \sqrt{V_{ovD}^2 + \text{CGIDL}^2 V_{DB}^2 + 10^{-6}} \quad (4.420)$$

$$I_{GIDL} = -A_{GIDL} V_{DS} V_{ovD} V_{tovD} \exp\left(-\frac{B_{GIDL}}{V_{tovD}}\right) \frac{1 + \exp(\text{DGIDL} \times V_{DS})}{2} \quad (4.421)$$

## 4.8 Charge model

This part is dedicated to the calculations of intrinsic and parasitic charges in the different electrodes. The intrinsic charge model is derived from [9].

### 4.8.1 Quantum mechanical corrections

$$k_1 q_{1m} = \frac{k_1 q_{1S} + k_1 q_{1D}}{2} + \text{FSCEAC} \frac{\delta x_{Wl,1D} - \delta x_{Wl}}{1 + q_{im}/4} \quad (4.422)$$

$$k_2 q_{2m} = \frac{k_2 q_{2S} + k_2 q_{2D}}{2} - \text{FSCEAC} \frac{\delta x_{Wl,1D} - \delta x_{Wl}}{1 + q_{im}/4} \quad (4.423)$$

$$\begin{cases} k_1 q_{1eff} = k_1 q_{1m} - \frac{qm_{fact1} - 1}{qm_{fact1}} q_{i1m} & \text{if } \mathbf{QMC} > 0 \\ k_1 q_{1eff} = k_1 q_{1m} & \text{else} \end{cases} \quad (4.424)$$

$$\begin{cases} k_2 q_{2eff} = k_2 q_{2m} - \frac{qm_{fact2} - 1}{qm_{fact2}} q_{i2m} & \text{if } \mathbf{QMC} > 0 \\ k_2 q_{2eff} = k_2 q_{2m} & \text{else} \end{cases} \quad (4.425)$$

### 4.8.2 Intrinsic charge model

$$Q_G = C_{Si} ' f_{area} \left( k_1 q_{1eff} + \frac{\Delta_{k1q1}}{3} P_1 \right) \quad (4.426)$$

$$Q_B = C_{Si} ' f_{area} \left( k_2 q_{2eff} + \frac{\Delta_{k2q2}}{3} P_2 \right) \quad (4.427)$$

$$Q_D = -\frac{C_{Si} ' f_{area}}{2} \left[ k_1 q_{1eff} + \frac{\Delta_{k1q1}}{3} \left( 1 + P_1 - \frac{P_1^2}{5} \right) + k_2 q_{2eff} + \frac{\Delta_{k2q2}}{3} \left( 1 + P_2 - \frac{P_2^2}{5} \right) \right] \quad (4.428)$$

### 4.8.3 Parasitic charges

Inner fringe charges, computed if **FIF** > 0

$$x_{effS} = x_{driftS} + x_{th,1D} + 2 \ln(2) \quad (4.429)$$

$$x_{effD} = x_{driftD} + x_{th,1D} + 2 \ln(2) \quad (4.430)$$

$$x_S^* = \text{MIN\_FUNC}(x_{effS}, x_{th,1D}, 9.0) \quad (4.431)$$

$$x_D^* = \text{MIN\_FUNC}(x_{effD}, x_{th,1D} + x_D, 9.0) \quad (4.432)$$

$$\lambda_f = \lambda_{2D} \sqrt{k_{eq} (1/2 + 1/k_2)} \quad (4.433)$$

$$\lambda_b = \lambda_{2D} \sqrt{k_{eq} (k_1/k_2) (1/2 + 1/k_1)} \quad (4.434)$$

$$x_{\alpha f} = \lambda_f^2 f_{SD,inner} \quad (4.435)$$

$$x_{\alpha b} = \lambda_b^2 f_{SD,inner} \quad (4.436)$$

$$x_{edge,fs} = x_S^* + 2x_{\alpha f} \left( \sqrt{1 + (x_{SD} - x_S^*)/x_{\alpha f}} - 1 \right) \quad (4.437)$$

$$x_{edge,fd} = x_D^* + 2x_{\alpha f} \left( \sqrt{1 + (x_{SD} + x_D - x_D^*)/x_{\alpha f}} - 1 \right) \quad (4.438)$$

$$x_{edge,bs} = x_S^* + 2x_{\alpha b} \left( \sqrt{1 + (x_{SD} - x_S^*)/x_{\alpha b}} - 1 \right) \quad (4.439)$$

$$x_{edge,bD} = x_D^* + 2x_{\alpha b} \left( \sqrt{1 + (x_{SD} + x_D - x_D^*)/x_{\alpha b} - 1} \right) \quad (4.440)$$

$$Q_{GS,if} = -f_{if} C_{Si} \lambda_f k_1 c_{sce1} \frac{\text{MAX\_FUNC}(x_{edge,fs} - x_{effs}, 0.0, 1.0)^2}{x_{edge,fs} - x_S^*} \quad (4.441)$$

$$Q_{GD,if} = -f_{if} C_{Si} \lambda_f k_1 c_{sce1} \frac{\text{MAX\_FUNC}(x_{edge,fD} - x_{effD}, 0.0, 1.0)^2}{x_{edge,fD} - x_D^*} \quad (4.442)$$

$$Q_{BS,if} = -f_{if} C_{Si} \lambda_b k_2 c_{sce2} \frac{\text{MAX\_FUNC}(x_{edge,bS} - x_{effs}, 0.0, 1.0)^2}{x_{edge,bS} - x_S^*} \quad (4.443)$$

$$Q_{BD,if} = -f_{if} C_{Si} \lambda_b k_2 c_{sce2} \frac{\text{MAX\_FUNC}(x_{edge,bD} - x_{effD}, 0.0, 1.0)^2}{x_{edge,bD} - x_D^*} \quad (4.444)$$

Outer fringe and overlap charges

$$Q_{GS} = \text{CFR} \times V_{GS} \quad (4.445)$$

$$Q_{GD} = \text{CFRD} \times V_{GD} \quad (4.446)$$

$$Q_{ovS} = \text{COV} \times V_{ovS,cv} \times \text{MAX\_FUNC}(1 - \text{COVDL} \times \delta l_{eff} (1 - \text{COVDLB} \times x_{g20}), 0.0, 0.01) \quad (4.447)$$

$$Q_{ovD} = \text{COVD} \times V_{ovD,cv} \times \text{MAX\_FUNC}(1 - \text{COVDL} \times \delta l_{eff} (1 - \text{COVDLB} \times x_{g20}), 0.0, 0.01) \quad (4.448)$$

$$Q_{GB} = \text{CGBOV} \times V_{GB} \quad (4.449)$$

Drain to source direct coupling

$$Q_{DS} = \text{CSD} \times V_{DS} \quad (4.450)$$

Substrate extrinsic charge model

$$Q_{BS} = -(C_{ox2} \times A_{source,f} + \text{CSDBP} \times P_{source,f}) V_{SB} \quad (4.451)$$

$$Q_{BD} = -(C_{ox2} \times A_{drain,f} + \text{CSDBP} \times P_{drain,f}) V_{DB} \quad (4.452)$$

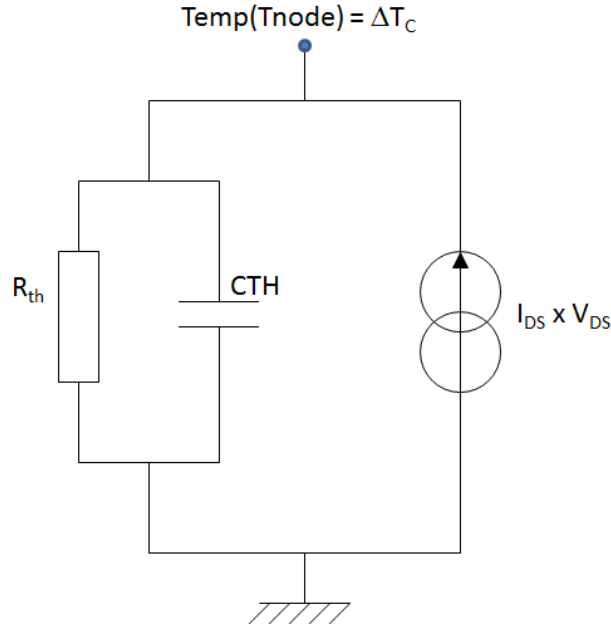
## 4.9 Self-heating

As in Leti-UTSOI1, a temperature node named “Tnode” is used to compute the channel temperature elevation induced by the self-heating effect.

Note that, from version Leti-UTSOI 2.1.0, this node is accessible.

While this node voltage represented one hundredth of the temperature elevation in Leti-UTSOI 2.0.0, it corresponds, from Leti-UTSOI 2.1.0, to the actual temperature elevation (i.e. there is no more 1/100 factor).

This node is linked to the ground node through a simple parallel RC circuit, as illustrated in Figure 4.1.



**Figure 4.1- Description of the self-heating equivalent circuit used to compute channel temperature elevation.**

$$\Delta T_C = \text{Temp}(\text{Tnode}) \quad (4.453)$$

$$I_{th} = \frac{\Delta T_C}{R_{th}} - I_{DS} V_{DS} \quad (4.454)$$

$$Q_{th} = \text{CTH} \times \Delta T_C \quad (4.455)$$

## 4.10 Noise model

In this section are described the noise sources and the calculation of their power spectral densities. The way thermal induced gate noise is implemented in the VerilogA code is also indicated.

### 4.10.1 Channel thermal noise

$$d_m = -\frac{d_s + d_D}{2} \quad (4.456)$$

$$q_{im}^* = q_{im} + d_m \quad (4.457)$$

$$t_1 = \frac{q_{im}}{q_{im}^*} \quad (4.458)$$

$$t_2 = \left( -\frac{\Delta_{k1q1}}{6k_1 h_{10}} \right)^2 \quad (4.459)$$

$$R = \frac{1 + 3z_{sat}/2}{\sqrt{1 + z_{sat}}} - 1 \quad (4.460)$$

$$I_C = 1 - 12t_2 R \quad (4.461)$$

$$g_{ideal} = \beta_{Neff} C_{Si} \phi_T q_{im}^* \frac{F_{\Delta L}}{G_{vsat} qm_{fact}} \quad (4.462)$$

$$g_{Sid} = \frac{g_{ideal}}{I_C^2} (t_1 + 12t_2 - 24(1 + t_1)t_2 R) \quad (4.463)$$

$$S_{ids,th} = n_T g_{Sid} \quad (4.464)$$

#### 4.10.2 Induced gate noise

Induced gate noise and its correlation with drain thermal noise are computed only when **SWIGN** flag is set to 1.

$$C_{Geff} = (1 + z_{sat}) \frac{k_1 C_{si}'}{q m_{fact1}} \text{AREAQ} \quad (4.465)$$

$$g_{Sig} = \frac{g_{ideal} I_C^2}{t_1/12 - (1/5 + t_1 - 12t_2)t_2 - 8/5(1 + t_1 - 12t_2)t_2 R} \quad (4.466)$$

$$S_{ig,th} = n_T g_{Sig} \frac{(2\pi f_{op} C_{Geff} / g_{Sig})^2}{1 + (2\pi f_{op} C_{Geff} / g_{Sig})^2} \quad (4.467)$$

The gate induced noise current is finally partitioned between the gate-source and the gate-drain branches, with a  $V_{DS}$  dependent fraction equal to  $1/2 + \sqrt{t_2}/4$  and  $1/2 - \sqrt{t_2}/4$ , respectively. The noise currents in these two branches are thus perfectly correlated and correspond to the following spectral densities:

$$S_{igs,th} = \left( \frac{1}{2} + \frac{\sqrt{t_2}}{4} \right)^2 S_{ig,th} \quad (4.468)$$

$$S_{igd,th} = \left( \frac{1}{2} - \frac{\sqrt{t_2}}{4} \right)^2 S_{ig,th} \quad (4.469)$$

#### 4.10.3 Drain and gate thermal noise correlation

$$m_{igid} = \frac{\sqrt{t_2}}{I_C^2} (1 - 12t_2 - (t_1 + 96t_2/5 - 12t_1 t_2)t_2 R) \quad (4.470)$$

$$S_{igid,th} = n_T \frac{2j\pi f_{op} C_{Geff} m_{igid}}{1 + 2j\pi f_{op} C_{Geff} / g_{Sig}} \quad (4.471)$$

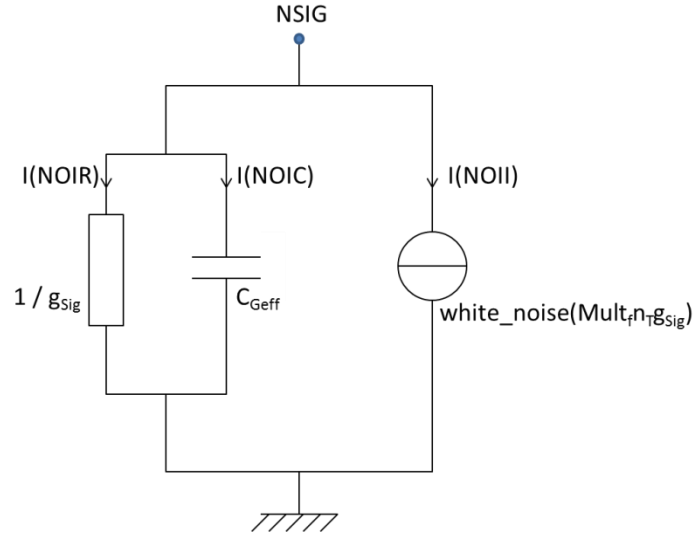
#### 4.10.4 VerilogA implementation of induced gate noise and correlation

Since there is no noise function with frequency dependence suitable for the induced gate noise in VerilogA, an internal node NSIG linked to the ground through a parallel RC circuit is used. The equivalent circuit is described in Figure 4.2 and the currents in the different branches are given by:

$$I(NOIR) = g_{Sig} V(NSIG) \quad (4.472)$$

$$I(NOIC) = \frac{d}{dt} (C_{Geff} V(NSIG)) \quad (4.473)$$

$$I(NOII) = \text{white\_noise}(Mult_f n_T g_{Sig}) \quad (4.474)$$



**Figure 4.2- Description of the equivalent circuit used to define induced gate noise.**

The noise current in the gate-source and gate-drain branches is obtained by:

$$I_{gs,th} = \frac{d}{dt} \left( - \left( \frac{1}{2} + \frac{\sqrt{t_2}}{4} \right) C_{Geff} V(NSIG) \right) \quad (4.475)$$

$$I_{gd,th} = \frac{d}{dt} \left( - \left( \frac{1}{2} - \frac{\sqrt{t_2}}{4} \right) C_{Geff} V(NSIG) \right) \quad (4.476)$$

A variable  $c_{igid}$  is then defined to obtain the correct correlation between the drain thermal noise current and the gate induced one:

$$c_{igid} = m_{igid} \sqrt{\frac{g_{Sig}}{g_{Sid}}} \quad (4.477)$$

The channel thermal noise current is finally given as the sum of an uncorrelated part and a correlated one:

$$I_{ds,th} = \text{white\_noise}(Mult_f S_{ids,th} (1 - c_{igid}^2)) + m_{igid} I(NOII) \quad (4.478)$$

#### 4.10.5 Channel flicker noise

$$N_{unit} = \frac{C_{Si} \phi_T}{q} \quad (4.479)$$

$$N^* = N_{unit} d_m \quad (4.480)$$

$$N_m^* = N_{unit} (q_{im} + 1) \quad (4.481)$$

$$\Delta N = N_{unit} (q_{is} - q_{id}^*) \quad (4.482)$$

$$f_{NFE} = \text{MAX\_FUNC} \left( 1 + \frac{e_{surf1} \mathbf{NFE} + e_{surf2} \mathbf{NFEB}}{q_{im} + 1}, 0.01, 0.0001 \right) \quad (4.483)$$

$$S_{ids,fl} = \frac{q \beta_{Neff} \phi_T^2 I_{DS}}{f_{op}^{EF} G_{vsat} N^*} \left[ \left( \mathbf{NFA} - N^* \mathbf{NFB} + N^{*2} \mathbf{NFC} \right) n \left( \frac{N_m^* + \Delta N / 2}{N_m^* - \Delta N / 2} \right) + \left( \mathbf{NFB} + \mathbf{NFC} (N_m^* - 2N^*) \right) \Delta N \right] f_{NFE} \quad (4.484)$$



#### 4.10.6 Shot noises

Gate current shot noise

$$S_{igs,sh} = 2q|I_{GS}| \quad (4.485)$$

$$S_{igd,sh} = 2q|I_{GD}| \quad (4.486)$$

GIDL/GISL current shot noise

$$S_{ids,sh} = 2q|I_{GIDL} - I_{GISL}| \quad (4.487)$$

### 4.11 Total currents and charges

According to Figure 1.3, the total currents in the branches and the total node charges are obtained as follows.

#### 4.11.1 Static currents

$$I_{DS,dc} = Mult_f \times TYPE \times (I_{DS} + I_{GIDL} - I_{GISL}) \quad (4.488)$$

$$I_{GS,dc} = Mult_f \times TYPE \times I_{GS} \quad (4.489)$$

$$I_{GD,dc} = Mult_f \times TYPE \times I_{GD} \quad (4.490)$$

$$I_{BS,dc} = 0 \quad (4.491)$$

$$I_{BD,dc} = 0 \quad (4.492)$$

$$I_{GB,dc} = 0 \quad (4.493)$$

#### 4.11.2 Total charges

$$Q_{G,tot} = Mult_f \times TYPE \times (Q_G + Q_{GS,if} + Q_{GD,if} + Q_{GS} + Q_{GD} + Q_{ovs} + Q_{ovD} + Q_{GB}) \quad (4.494)$$

$$Q_{D,tot} = Mult_f \times TYPE \times (Q_D + Q_{DS} - Q_{GD,if} - Q_{GD} - Q_{ovD} - Q_{BD,if} - Q_{BD}) \quad (4.495)$$

$$Q_{B,tot} = Mult_f \times TYPE \times (Q_B + Q_{BS,if} + Q_{BD,if} + Q_{BS} + Q_{BD} - Q_{GB}) \quad (4.496)$$

$$Q_{S,tot} = -Q_{G,tot} - Q_{D,tot} - Q_{B,tot} \quad (4.497)$$

#### 4.11.3 Dynamic currents

$$I_{DS,ac} = Mult_f \times TYPE \times d(Q_D + Q_{DS})/dt \quad (4.498)$$

$$I_{GS,ac} = Mult_f \times TYPE \times d(Q_G + Q_{GS,if} + Q_{GS} + Q_{ovs})/dt \quad (4.499)$$

$$I_{GD,ac} = Mult_f \times TYPE \times d(Q_{GD,if} + Q_{GD} + Q_{ovD})/dt \quad (4.500)$$

$$I_{BS,ac} = Mult_f \times TYPE \times d(Q_B + Q_{BS,if} + Q_{BS})/dt \quad (4.501)$$

$$I_{BD,ac} = Mult_f \times TYPE \times d(Q_{BD,if} + Q_{BD})/dt \quad (4.502)$$

$$I_{GB,ac} = Mult_f \times TYPE \times dQ_{GB}/dt \quad (4.503)$$

## 5 Operating Point output

In this section are described the quantities of DC operating point output, defined with **nMOSFET sign and positive  $V_{DS}$  convention**. These values give information on the device state at its current operation point.

### 5.1 Voltages

First, device threshold voltage is calculated. This requires re-computing of several quantities in order to get device state (temperature, effective channel length, effective back gate depletion effect, quantum confinement correction) at gate to source voltage equal to threshold voltage. Therefore, an initial value of threshold voltage is computed from (5.1) to (5.8). Then, required expressions of sections 4.1 to 4.4.2 are computed with TKD instead of TKC, and with  $V_{GS} = v_{th}$  as given by (5.8) as input gate to source voltage, to get updated values of  $k_1$ ,  $k_2$ ,  $diff_{min}$ ,... Finally, equation sequence (5.1) to (5.8) is re-computed to obtain final threshold voltage value.

$$r_{1,op} = \frac{k_2}{k_1(1 + k_2)} \quad (5.1)$$

$$r_{2,op} = \frac{k_1}{k_2(1 + k_1)} \quad (5.2)$$

$$x_{1sat,op} = \ln\left(k_1(1 + r_{1,op})\frac{diff_{min}}{A_0}\right) + 2 \quad (5.3)$$

$$x_{2sat,op} = \ln\left(k_2(1 + r_{2,op})\frac{diff_{min}}{A_0}\right) + 2 \quad (5.4)$$

$$x_{th1,op} = x_{1sat,op}(1 + r_{1,op}) - x_{g2x}r_{1,op} \quad (5.5)$$

$$x_{th2,op} = x_{2sat,op}(1 + 1/r_{2,op}) - x_{g2x}/r_{2,op} \quad (5.6)$$

$$x_{g1th,op} = \frac{MIN\_FUNC(x_{th1,op}, x_{th2,op}, 38) - x_{g2}}{CICF} + x_{g2} \quad (5.7)$$

$$v_{th} = \phi_T \left( \frac{x_{g1th,op} - x_{edge}}{C_{sce1}} - \delta x_{g1,DIBL} + x_{edge} \right) + V_{FB1} \quad (5.8)$$

We compute also the drain saturation voltage:

$$v_{Dsat} = \phi_T x_{nDS,sat} \quad (5.9)$$

Name	Unit	Definition	Value
<b>Vds</b>	V	Internal drain-source DC voltage	$V_{DS}$
<b>Vsb</b>	V	Internal source-bulk DC voltage	$V_{SB}$
<b>Vgs</b>	V	Internal gate-source DC voltage	$V_{GS}$
<b>Vth</b>	V	Threshold voltage	$v_{th}$
<b>Vth_drive</b>	V	Effective gate drive voltage	$V_{GS} - v_{th}$
<b>Vdsat</b>	V	Drain saturation voltage at the given bias	$V_{Dsat}$
<b>Vdsat_marg</b>	V	$V_{DS}$ voltage margin	$V_{DS} - V_{Dsat}$

### 5.2 Current components

Name	Unit	Definition	Value
<b>Id</b>	A	Total DC drain current flowing into drain terminal	<b>TYPE</b> $x$ ( $I_{DS,dc} - I_{GD,dc}$ )
<b>Ig</b>	A	Total DC gate current flowing into gate terminal	<b>TYPE</b> $x$ ( $I_{GS,dc} + I_{GD,dc}$ )
<b>Is</b>	A	Total DC source current flowing into source terminal	<b>TYPE</b> $x$ ( $-I_{DS,dc} - I_{GS,dc}$ )
<b>Ib</b>	A	Total DC bulk current flowing into bulk terminal	0
<b>Ids</b>	A	DC channel current excl. tunnel, GISL and GIDL currents	$Multi \times I_{DS}$

<b>Igidl</b>	A	DC Gate Induced Drain Leakage current	$Mult_f \times I_{GIDL}$
<b>Igisl</b>	A	DC Gate Induced Source Leakage current	$Mult_f \times I_{GISL}$
<b>Igs</b>	A	DC gate-source leakage current	$Mult_f \times I_{GS}$
<b>Igd</b>	A	DC gate-drain leakage current	$Mult_f \times I_{GD}$
<b>Isb</b>	A	DC source-bulk current	0
<b>Idb</b>	A	DC drain-bulk current	0

### 5.3 Conductances and transconductances

Here,  $V_G$ ,  $V_B$  and  $V_D$  refer to the electrode potentials with the nMOSFET sign convention already applied.

Name	Unit	Definition	Value
<b>Gm</b>	S	Internal DC transconductance	$\partial Id / \partial V_G$
<b>Gmb</b>	S	Internal DC bulk transconductance	$\partial Id / \partial V_B$
<b>Gds</b>	S	Internal DC output conductance	$\partial Id / \partial V_D$

### 5.4 Capacitances and transcapacitances

Here,  $V_G$ ,  $V_B$  and  $V_D$  refer to the electrode potentials with the nMOSFET sign convention already applied.

Name	Unit	Definition	Value
<b>Cgg</b>	F	Internal AC gate capacitance	$TYPE \times \partial Q_{G,tot} / \partial V_G$
<b>Cgd</b>	F	Internal AC gate-drain transcapacitance	$-TYPE \times \partial Q_{G,tot} / \partial V_D$
<b>Cgb</b>	F	Internal AC gate-bulk transcapacitance	$-TYPE \times \partial Q_{G,tot} / \partial V_B$
<b>Cgs</b>	F	Internal AC gate-source transcapacitance	$C_{gg} - C_{gd} - C_{gb}$
<b>Cdd</b>	F	Internal AC drain capacitance	$TYPE \times \partial Q_{D,tot} / \partial V_D$
<b>Cdg</b>	F	Internal AC drain-gate transcapacitance	$-TYPE \times \partial Q_{D,tot} / \partial V_G$
<b>Cdb</b>	F	Internal AC drain-bulk transcapacitance	$-TYPE \times \partial Q_{D,tot} / \partial V_B$
<b>Cds</b>	F	Internal AC drain-source transcapacitance	$C_{dd} - C_{dg} - C_{db}$
<b>Cbb</b>	F	Internal AC bulk capacitance	$TYPE \times \partial Q_{B,tot} / \partial V_B$
<b>Cbg</b>	F	Internal AC bulk-gate transcapacitance	$-TYPE \times \partial Q_{B,tot} / \partial V_G$
<b>Cbd</b>	F	Internal AC bulk-drain transcapacitance	$-TYPE \times \partial Q_{B,tot} / \partial V_D$
<b>Cbs</b>	F	Internal AC bulk-source transcapacitance	$C_{bb} - C_{bg} - C_{bd}$
<b>Csg</b>	F	Internal AC source-gate transcapacitance	$C_{gg} - C_{dg} - C_{bg}$
<b>Csb</b>	F	Internal AC source-bulk transcapacitance	$C_{bb} - C_{gb} - C_{db}$
<b>Csd</b>	F	Internal AC source-drain transcapacitance	$C_{dd} - C_{gd} - C_{bd}$
<b>Css</b>	F	Internal AC source capacitance	$C_{sg} + C_{sd} + C_{sb}$

### 5.5 Miscellaneous

Name	Unit	Definition	Value
<b>TYPE</b>	---	MOSFET type	<b>TYPE</b>
<b>Tk</b>	K	MOSFET device temperature	$T_{KC}$
<b>Dtsh</b>	K	MOSFET device temperature increase due to self-heating	$\Delta T_C$
<b>Self_gain</b>	---	Internal UTSOI model self-gain	<b>Gm / Gds</b>
<b>Rout</b>	$\Omega$	AC output resistance	$1 / G_{ds}$
<b>Beff</b>	A/V <sup>2</sup>	Gain factor in saturation	$2Id / V_{gt}^2$
<b>Ft</b>	Hz	Unity gain frequency	$Gm / (2\pi C_{gg})$
<b>Rgate</b>	$\Omega$	MOS gate resistance (not included in this version)	0
<b>Gmoverid</b>	1/V	Transconductance over drain current ratio	<b>Gm / Id</b>
<b>Vearly</b>	V	Equivalent Early voltage	<b>Id / Gds</b>

## 6 Parameters extraction

The following tables summarize the main steps of a local parameter extraction sequence, first for a long channel transistor, and then for short channel devices. Note that accounting for self heating requires temperature dependence extraction at each step of the flow.

### 6.1 Long channel device

Step	Physical effect	Curves	Parameters
1	Self heating	DC self heating measurement	RTH
2	Process	Cgc(Vg) at various Vb	TOXE, VFB, (TSI)
3	Process	Cgb(Vg) in subthreshold regime	TBOX, NSUB, (CGBOV)
4	Process	Log(Id)(Vg) at low Vd and various Vb	VFB, CT, (NSUB)
5	Process	Log(Id)(Vg) at low Vd and various temperatures	STVFB
6	Mobility	Id(Vg), Gm(Vg), Gm2(Vg) at low Vd and Vb=0V	BETN, CS, THECS, MUE, THEMU
7	Mobility	Id(Vg), Gm(Vg), Gm2(Vg) at low Vd and various Vb	BETN, CS, THECS, MUE, THEMU, XCOR, BETNB
8	Mobility	Id(Vg), Gm(Vg), Gm2(Vg) at low Vd and various temp.	STBET, STCS, STTHECS, STMUE, STTHEMU, STXCOR
9	Saturation velocity	Id(Vd), Log(Gd)(Vd) at high Vg	AX, THESAT
10	Gate current	Ig(Vg)	IGINV, IGOVINV, IGOVACC, GC2CH, GC3CH, GC2OV, GC3OV, CHIB
11	Gate current	Ig(Vg) at various temperatures	STIG
12	GIDL	Log(Id)(Vg) at high Vd and various Vb	AGIDL, BGIDL, CGIDL
13	GIDL	Log(Id)(Vg) at high Vd and various temperatures	STBGIDL

### 6.2 Short channel devices

Step	Physical effect	Curves	Parameters
1	Self heating	DC self heating measurement	RTH
4	Process	Log(Id)(Vg) at low Vd and various Vb	VFB, VFBB, PSCE, PSCEB
5	Process	Log(Id)(Vg) at low Vd and various temperatures	STVFB
6	Mobility, Rseries	Id(Vg), Gm(Vg), Gm2(Vg) at low Vd and Vb=0V	BETN, CS, RS, RSG, THERSG
7	Mobility, Rseries	Id(Vg), Gm(Vg), Gm2(Vg) at low Vd and various Vb	BETN, CS, RS, RSG, THERSG, XCOR
8	Mobility, Rseries	Id(Vg), Gm(Vg), Gm2(Vg) at low Vd and various temp.	STBET, STRS, STXCOR
9	DIBL	Log(Id)(Vg) at high Vd and various Vb	CF, CFB
10	DIBL	Log(Id)(Vg) at high Vd and various temperatures	STCF
11	DIBL	Log(Id)(Vd), Log(Gd)(Vd) at low Vg	CF, CFD
12	Sat. velocity, CLM	Id(Vd), Log(Gd)(Vd) at various Vg Gm(Vg) at various Vd	AX, ALP, ALP1, VP, THESAT, THESATG
13	Sat. velocity, CLM	Id(Vd) at high Vg and various Vb	THESAT, THESATB
14	Sat. velocity, CLM	Id(Vd), Log(Gd)(Vd) at high Vg and various temp.	STTHESAT
15	Capacitance	Cgb(Vg)	CGBOV
16	Capacitance	Cgc(Vg)	AREAQ, COV, NOV, CFR
17	Gate current	Ig(Vg)	IGINV, IGOVINV, IGOVACC
18	GIDL	Log(Id)(Vg) at high Vd	AGIDL

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## 8 References

- [1] O. Rozeau, M. A. Jaud, T. Poiroux and M. Benosman, "Surface potential based model of ultra-thin fully depleted SOI MOSFET for IC simulations," in *IEEE International SOI Conference*, 2011.
- [2] O. Rozeau, "UTSOI model 1.1.4 - Model description," 2012.
- [3] G. Gildenblat, X. Li, W. Wu, H. Wang, A. Jha, R. Van Langevelde, G. D. J. Smit, A. J. Scholten and D. B. M. Klaassen, "PSP: An advanced surface-potential-based MOSFET model for circuit simulation," *IEEE Transactions on Electron Devices*, vol. 53, no. 9, pp. 1979-1993, 2006.
- [4] X. Li, W. Wu, G. Gildenblat, G. D. J. Smit, A. J. Scholten, D. B. M. Klaassen and R. Van Langevelde, "PSP 103.1," Technical note NXP-R-TN-2008/00299, NXP Semiconductors, 2009.
- [5] T. Poiroux, O. Rozeau, S. Martinie, P. Scheer, S. Puget, M. A. Jaud, S. El Ghoul, J. C. Barbé, A. Juge and O. Faynot, "UTSOI2: a complete physical compact model for UTBB and independent double gate MOSFETs," in *IEEE International Electron Device Meeting*, 2013.
- [6] T. Poiroux, O. Rozeau, S. Martinie and M. A. Jaud, "UTSOI 2 - Physical background," CEA-LETI, Minatec Campus, 2013.
- [7] T. Poiroux, O. Rozeau, P. Scheer, S. Martinie, M. Jaud, M. Minondo, A. Juge, J. Barbé and M. Vinet, "Leti-UTSOI2.1: A compact model for UTBB-FDSOI technologies - Part I: Interface potentials analytical model," *IEEE Transactions on Electron Devices*, vol. 62, no. 9, pp. 2751-2759, 2015.
- [8] T. Poiroux, O. Rozeau, P. Scheer, S. Martinie, M. Jaud, M. Minondo, A. Juge, J. Barbé and M. Vinet, "Leti-UTSOI2.1: A compact model for UTBB-FDSOI technologies - Part II: DC and AC model description," *IEEE Transactions on Electron Devices*, vol. 62, no. 9, pp. 2760-2768, 2015.
- [9] G. Dessai, W. Wu and G. Gildenblat, "Compact charge model for independent-gate asymmetric DGFET," *IEEE Transactions on Electron Devices*, vol. 57, no. 9, pp. 2106-2115, 2010.

## 9 Model history

This section details all modifications brought to the code from Leti-UTSOI2.0.0.

### 9.1 Leti-UTSOI2.0.0 to Leti-UTSOI2.1.0

**Important change:** Channel temperature elevation node is now accessible from the circuit netlist and can be declared in transistor instantiation. Thus, with Leti-UTSOI2.1.0, transistor instantiation accepts 5 nodes (D, G, S, B, Tnode), while 4 nodes were available with previous versions.

#### 9.1.1 Bug fixes

- **BF1:** Modification of **CSD** default value for consistency with default global parameter values.
- **BF2:** Correction of a bug concerning temperature dependence of DIBL. **CF** scaling law is now applied to **STCF**, which suppresses apparition of undesirable DIBL on long channel devices at low and/or high temperature when **STCF** is non-null. Hard clamp previously applied to **CF** has been removed to avoid discontinuities on derivatives that could appear with some parameter configurations and activated self-heating.
- **BF3:** Correction of a bug concerning the conditionnal clipping of **SWSHE** flag. In previous version, condition was defined for local scale mode only.
- **BF4:** Correction of **CFB** clipping. In previous version, clipping was done on **CF\*CFB**, which could lead to uncorrect clipping in some cases (**CF** < 0 and **CFB** < 0).
- **BF5:** Correction of a bug concerning the recalculation of DIBL related variable “xd0” when self-heating is activated. Line was missing.
- **BF6:** Modification of **CIC** and **CICF** implementation to avoid negative transcapacitances that could be observed for **CIC** and/or **CICF** different from 1.
- **BF7:** Improvement of numerical robustness in the subthreshold regime: corrects a bug in thermal noise calculation and avoids divisions by zero that could occur in some extreme bias/temperature conditions.
- **BF8:** Adding of a protection in the calculation of effective  $V_{DS}$ .
- **BF9:** Correction of GIDL/GISL component output.
- **BF10:** Modification of the operating point section to account properly for source/drain interchange.

#### 9.1.2 Accuracy and predictability improvements

- **AP1:** The effect of effective channel length dependence on front and back gate biases has been introduced in the 2D electrostatic part of the model (subthreshold slope, DIBL) through local parameters **PSCEDL** and **CFDL**, and in overlap capacitance model through parameter **COVDL**.
- **AP2:** The impact of narrow channel effect on front/back gate to interface couplings has been introduced through parameter **PNCE**.
- **AP3:** To improve the description of gate to source/drain overlap tunnelling currents, **GC2OV** and **GC3OV** parameters have been split into **GC2OVINV**, **GC3OVINV**, **GC2OVACC** and **GC3OVACC**.
- **AP4:** Gate to channel tunnelling current model has been improved for a better description in the subthreshold regime, with introduction of local parameter **NIGINV**.
- **AP5:** Predictability of the 2D electrostatic part of the model has been improved, by linking subthreshold slope and DIBL related parameters (**PSCE**, **CF**) to process parameters (**TSI**, **TOXE**, **TBOX**) in scaling laws. In addition, introduction of subthreshold slope degradation parameter in model equations has been modified, so that threshold voltage roll-off predictability is also improved.
- **AP6:** Scaling law of **BETN** has been changed for better accuracy. PSP-like scaling law has been adopted.
- **AP7:** Introduction of narrow-short channel effect in **VFB** scaling law has been modified for better description.

- **AP8**: The impact of 2D electrostatic effect on back gate depletion has been introduced through calculation of an effective back gate doping level.
- **AP9**: Adding of a flat-band voltage adjustment parameter for overlap capacitances (**DVFBVOV**).

### 9.1.3 Changes in model inputs and outputs

- **IO1**: Warnings about clipping of instance, global and local parameters, as well as channel temperature elevation, have been added. Display is controlled through **SWCLIPCHK** flag.
- **IO2**: Device temperature node (Tnode) was internal in previous version, and was corresponding to  $1/100^{\text{th}}$  of channel temperature elevation due to self-heating. This node is now accessible (declared as "inout") and represents the actual channel temperature elevation. Device instantiation requires now 5 nodes.
- **IO3**: Modification of some parameter clipping: **MULT** min value from 1 to 0, **PSCE** min value from -0.5 to 0 and **AX** max value from 12 to 16. Change of **RTHL**, **RTHW** and **RTHLW** default values.
- **IO4**: Update of description and notation of operating point outputs. Adding of MOSFET type and device temperature as new outputs.

## 9.2 Leti-UTSOI2.1.0 to Leti-UTSOI2.1.1

### 9.2.1 Bug fixes

- **BF1**: Modification of smoothing in drain saturation voltage calculation to improve smoothness of third order derivatives.
- **BF2**: Modification of smoothing in gate current model to improve smoothness of third order derivatives.

### 9.2.2 Accuracy and predictability improvements

- **AP1**: Improvement of threshold voltage calculation in Operating Point section.

## 9.3 Leti-UTSOI2.1.1 to Leti-UTSOI2.2.0

### 9.3.1 Bug fixes

- **BF1**: Parameters **DLQ** and **DWQ** were involved in device effective length and width, respectively, for computation of external parasitic capacitances, such as external fringe capacitance or gate to substrate one. This is no longer the case, and these capacitances are now calculated with the physical length and width of the device.
- **BF2**: In the scaling law of velocity saturation parameter **THESAT**, **THESATO** and **THESATL** were not treated similarly. This has been modified for more consistent description of this scaling law.

### 9.3.2 Accuracy and predictability improvements

- **AP1**: Additional flexibility has been introduced in channel length modulation model for more accurate description of current and conductances over applied biases.
- **AP2**: Model has been extended to the case of doped thin film transistors, with introduction of channel doping level local parameter **NCH**.
- **AP3**: Channel length dependence over front and back bias in subthreshold and moderate inversion regimes has been introduced in Leti-UTSOI 2.1. This part of the model has been improved for better accuracy, with a physical description of source/drain depletion effect.



- **AP4:** Mobility model accuracy has been improved, in particular with the introduction of transverse field dependence of Coulomb scattering component.
- **AP5:** Series resistance model flexibility has been increased, with introduction of source/drain extension component and explicit back bias dependence.
- **AP6:** Gate-overlap tunnelling current description at high drain voltage has been improved.
- **AP7:** Introduction high longitudinal field dependence of GISL/GIDL currents.
- **AP8:** Introduction of a physical inner fringe capacitance model, including source/drain depletion effect (see AP3), for better description of short channel capacitances.
- **AP9:** Overlap capacitance model flexibility has been increased to account for source/drain depletion effect introduced in DC model (see AP3).
- **AP10:** Global scale parameters **VFBL2** and **VFBLEXP2** have been added for more flexibility in **VFB** scaling description.
- **AP11:** Global scale parameters **ALPLEXP2** and **ALP1LEXP2** have been added for more flexibility in **ALP** and **ALP1** scaling description, respectively.
- **AP12:** Description of edge effect in gate to substrate parasitic capacitance has been introduced through global scale parameter **CGBOVO**.
- **AP13:** Geometrical dependences of thermal resistance and thermal capacitance have been extended to multifinger transistors, by accounting for thermal coupling between fingers.
- **AP14:** A new strain relaxation model, dedicated to strained-SOI technologies, has been introduced besides existing STI-based stress model.
- **AP15:** High order derivability of the model around null source-drain voltage has been improved.

### 9.3.3 Changes in model inputs and outputs

- **IO1:** Warnings about clipping of instance, global and local parameters, as well as channel temperature elevation, have been updated. Display is still controlled through **SWCLIPCHK** flag.