

1. Release Notes

1.1 Product Release Information

Table 1. Product Identification

Parameter	Description
Library Name	C28SOI_IO_BUMP_6U1X2T8XLB
Library Version	7.0
Library Type	IO Cells
Technology	CMOS028_FDSOI
DK Version	DK_cmos28FDSOI_AMS_6U1x_2T8x_LB_2.7.a-31

1.2 Impact of Product Release

For latest information, please refer to LYS (<http://col2.cro.st.com/libyield>).

1.3 Related Documentation

- C28SOI_IO_BUMP_6U1X2T8XLB_6.0 User Manual
- C28SOI_IO_BUMP_6U1X2T8XLB_6.0 Release Notes and Known Problems and Solutions

2. Changes with respect to all Previous Versions

2.1 Version 7.0

A. This is the Final release

B. Major content of packages are

(1) Physical Views (SIP, CADENCE LEF, SIGNOFF LEF, SIP ,Abstract, GDS, FRAM, layout)

3. Known Problems and Solutions of this release



This document is compatible for viewing with acroread 7.0 and later versions. If opened with a lower version of acroread, there might be some color display problem.



DRC errors :

- For all BUMP_FC* top cells and ALLCELLS_BUMP_CLASS_FC* allcells:

ERROR : Rule EMET.DEN.1
 ERROR : Rule EMET.DEN.2
 ERROR : Rule EMET.DEN.3
 ERROR : Rule EMET.DEN.4
 ERROR : Rule EMET.DEN.5
 ERROR : Rule EMET.DEN.6
 ERROR : Rule EMET.DEN.7
 ERROR : Rule EMET.DEN.8
 ERROR : Rule EMET.DEN.9
 ERROR : Rule EMET.DEN.10
 ERROR : Rule EMET.DEN.11
 ERROR : Rule EMET.DEN.12
 ERROR : Rule RX.DEN.1
 ERROR : Rule PC.DEN.1
 ERROR : Rule M1.DEN.1
 ERROR : Rule M2.DEN.1
 ERROR : Rule M3.DEN.1
 ERROR : Rule M4.DEN.1
 ERROR : Rule M5.DEN.1
 ERROR : Rule IA.DEN.1
 ERROR : Rule IB.DEN.1
 ERROR : Rule FC.R.2.V1
 ERROR : Rule FC.R.2.V2
 ERROR : Rule FC.R.2.V3
 ERROR : Rule FC.R.2.V4
 ERROR : Rule FC.R.3
 ERROR : Rule B1.DEN.1
 ERROR : Rule B1.DEN.8
 ERROR : Rule B2.DEN.1
 ERROR : Rule B2.DEN.8

ERROR : Rule IA.DEN.8
ERROR : Rule IB.DEN.8
ERROR : Rule M1.DEN.8
ERROR : Rule M2.DEN.8
ERROR : Rule M3.DEN.8
ERROR : Rule M4.DEN.8
ERROR : Rule M5.DEN.8
ERROR : Rule M6.DEN.1
ERROR : Rule M6.DEN.8
ERROR : Rule PC.DEN.8
ERROR : Rule RX.DEN.8

→ Will be corrected at Chip level

- For all BUMP.FC61A* and BUMP.FC52A* top:
FC52A.EN.2 FC61A.EN.2

→ Corrected in Allcells or at Chip level

4. Contact Information

For more information about this product/IP/Library or any problems or suggestions, please contact **HELPDESK** (<http://col2.cro.st.com/helpdesk>).

Non-ST users, please contact the respective Customer Support.



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