

# C28SOI\_IO\_EXT\_CSF\_COMPENSATION1V8\_LR\_EG Databook

September 2016

# **COMPENSATION\_EXT\_1V8**

## **Cell Description**

#### COMPENSATION\_EXT\_1V8

- The cell has "dont\_use" attribute set in the Synopsys STF.
- The cell has "dont\_touch" attribute set in the Synopsys STF.

## **Physical Dimensions**

Area(um2): 30872.000

## Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge

TOWERON CSOP NASROCK SSOP RASROCK SSOP RASROCK SSOP RASROCK SSOP NASROCK SSOP RASROCK SSOP NASROCK SSOP NASRO
SSR A A REG
ASRCN1VBCORE<3:Ø>
ASRCP1V8CORE<3:0> ■

## **Cell Capacitance**

Parameter	Value(pF)	
i arameter	best 1.10 125	worst 0.90 -40
ACCURATE Input Cap.	0.0083	0.0083
ANAREXT Input Cap.	0.0100	0.0100
ANAREXT Max Load	10000.000	10000.000
ASRCN1V8CORE[0] Input Cap.	0.0100	0.0100

ASRCN1V8CORE[0] Max Load	10000.000	10000.000
ASRCN1V8CORE[1] Input Cap.	0.0100	0.0100
ASRCN1V8CORE[1] Max Load	10000.000	10000.000
ASRCN1V8CORE[2] Input Cap.	0.0100	0.0100
ASRCN1V8CORE[2] Max Load	10000.000	10000.000
ASRCN1V8CORE[3] Input Cap.	0.0100	0.0100
ASRCN1V8CORE[3] Max Load	10000.000	10000.000
ASRCP1V8CORE[0] Input Cap.	0.0100	0.0100
ASRCP1V8CORE[0] Max Load	10000.000	10000.000
ASRCP1V8CORE[1] Input Cap.	0.0100	0.0100
ASRCP1V8CORE[1] Max Load	10000.000	10000.000
ASRCP1V8CORE[2] Input Cap.	0.0100	0.0100
ASRCP1V8CORE[2] Max Load	10000.000	10000.000
ASRCP1V8CORE[3] Input Cap.	0.0100	0.0100
ASRCP1V8CORE[3] Max Load	10000.000	10000.000
COMPEN Input Cap.	0.0095	0.0095
COMPOK Max Load	0.200	0.200
COMPTQ Input Cap.	0.0120	0.0120
FASTFRZ Input Cap.	0.0680	0.0680
FREEZE Input Cap.	0.0076	0.0076
NASRCN[0] Input Cap.	0.0000	0.0000
NASRCN[0] Max Load	0.200	0.200
NASRCN[1] Input Cap.	0.0000	0.0000
NASRCN[1] Max Load	0.200	0.200
NASRCN[2] Input Cap.	0.0000	0.0000
NASRCN[2] Max Load	0.200	0.200
NASRCN[3] Input Cap.	0.0000	0.0000
NASRCN[3] Max Load	0.200	0.200
NASRCP[0] Input Cap.	0.0000	0.0000
NASRCP[0] Max Load	0.200	0.200
NASRCP[1] Input Cap.	0.0000	0.0000
NASRCP[1] Max Load	0.200	0.200
NASRCP[2] Input Cap.	0.0000	0.0000
NASRCP[2] Max Load	0.200	0.200
NASRCP[3] Input Cap.	0.0000	0.0000
NASRCP[3] Max Load	0.200	0.200
RASRCN[0] Input Cap.	0.0100	0.0100
RASRCN[0] Max Load	-	-
RASRCN[1] Input Cap.	0.0100	0.0100
RASRCN[1] Max Load	-	-
RASRCN[2] Input Cap.	0.0100	0.0100
RASRCN[2] Max Load	-	-
RASRCN[3] Input Cap.	0.0100	0.0100
RASRCN[3] Max Load	-	-
RASRCP[0] Input Cap.	0.0100	0.0100
RASRCP[0] Max Load	-	-
RASRCP[1] Input Cap.	0.0100	0.0100
RASRCP[1] Max Load	-	-
RASRCP[2] Input Cap.	0.0100	0.0100
RASRCP[2] Max Load	-	-
RASRCP[3] Input Cap.	0.0100	0.0100
RASRCP[3] Max Load	-	-



## **Default Leakage Power**

Default Leakage Power (mW)	vdd	vdde1v8
best 1.10 125	1.634e-03	4.211e-02
worst 0.90 -40	1.340e-06	9.785e-06



# COMPENSATION\_EXT\_CSF\_1V8\_FC\_LIN

## **Cell Description**

## COMPENSATION\_EXT\_CSF\_1V8\_FC\_LIN

- The cell has "dont\_use" attribute set in the Synopsys STF.
- The cell has "dont\_touch" attribute set in the Synopsys STF.

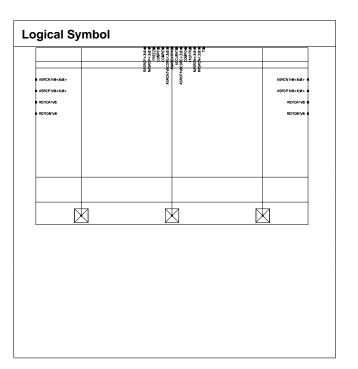
## **Physical Dimensions**

Area(um2): 41768.000

## Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



## **Cell Capacitance**

Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ACCURATE Input Cap.	0.0083	0.0083
ANAREXTPAD Input Cap.	1.4000	1.4000
ANAREXTPAD Max Load	10000.000	10000.000
ASRCN1V8[0] Input Cap.	0.0100	0.0100
ASRCN1V8[0] Max Load	10000.000	10000.000
ASRCN1V8[1] Input Cap.	0.0100	0.0100
ASRCN1V8[1] Max Load	10000.000	10000.000
ASRCN1V8[2] Input Cap.	0.0100	0.0100
ASRCN1V8[2] Max Load	10000.000	10000.000
ASRCN1V8[3] Input Cap.	0.0100	0.0100
ASRCN1V8[3] Max Load	10000.000	10000.000
ASRCN1V8[4] Input Cap.	0.0100	0.0100
ASRCN1V8[4] Max Load	10000.000	10000.000
ASRCN1V8[5] Input Cap.	0.0100	0.0100
ASRCN1V8[5] Max Load	10000.000	10000.000
ASRCN1V8[6] Input Cap.	0.0100	0.0100
ASRCN1V8[6] Max Load	10000.000	10000.000
ASRCN1V8CORE[0] Input Cap.	0.0100	0.0100
ASRCN1V8CORE[0] Max Load	10000.000	10000.000
ASRCN1V8CORE[1] Input Cap.	0.0100	0.0100
ASRCN1V8CORE[1] Max Load	10000.000	10000.000
ASRCN1V8CORE[2] Input Cap.	0.0100	0.0100



ASRCN1V8CORE[2] Max Load	10000.000	10000.000
ASRCN1V8CORE[3] Input Cap.	0.0100	0.0100
ASRCN1V8CORE[3] Max Load	10000.000	10000.000
ASRCP1V8[0] Input Cap.	0.0100	0.0100
ASRCP1V8[0] Max Load	10000.000	10000.000
ASRCP1V8[1] Input Cap.	0.0100	0.0100
ASRCP1V8[1] Max Load	10000.000	10000.000
ASRCP1V8[2] Input Cap.	0.0100	0.0100
ASRCP1V8[2] Max Load	10000.000	10000.000
ASRCP1V8[3] Input Cap.	0.0100	0.0100
ASRCP1V8[3] Max Load	10000.000	10000.000
ASRCP1V8[4] Input Cap.	0.0100	0.0100
ASRCP1V8[4] Max Load	10000.000	10000.000
ASRCP1V8[5] Input Cap.	0.0100	0.0100
ASRCP1V8[5] Max Load	10000.000	10000.000
ASRCP1V8[6] Input Cap.	0.0100	0.0100
ASRCP1V8[6] Max Load	10000.000	10000.000
ASRCP1V8CORE[0] Input Cap.	0.0100	0.0100
ASRCP1V8CORE[0] Max Load	10000.000	10000.000
ASRCP1V8CORE[1] Input Cap.	0.0100	0.0100
ASRCP1V8CORE[1] Max Load	10000.000	10000.000
ASRCP1V8CORE[2] Input Cap.	0.0100	0.0100
ASRCP1V8CORE[2] Max Load	10000.000	10000.000
ASRCP1V8CORE[3] Input Cap.	0.0100	0.0100
ASRCP1V8CORE[3] Max Load	10000.000	10000.000
COMPEN Input Cap.	0.0095	0.0095
COMPOK Max Load	0.200	0.200
COMPTQ Input Cap.	0.0120	0.0120
FASTFRZ Input Cap.	0.0069	0.0069
FREEZE Input Cap.	0.0076	0.0076
NASRCN[0] Input Cap.	0.0000	0.0000
NASRCN[0] Max Load	0.200	0.200
NASRCN[1] Input Cap.	0.0000	0.0000
NASRCN[1] Max Load	0.200	0.200
NASRCN[2] Input Cap.	0.0000	0.0000
NASRCN[2] Max Load	0.200	0.200
NASRCN[3] Input Cap.	0.0000	0.0000
NASRCN[3] Max Load	0.200	0.200
NASRCP[0] Input Cap.	0.0000	0.0000
NASRCP[0] Max Load	0.200	0.200
NASRCP[1] Input Cap.	0.0000	0.0000
NASRCP[1] Max Load	0.200	0.200
NASRCP[2] Input Cap.	0.0000	0.0000
NASRCP[2] Max Load	0.200	0.200
NASRCP[2] Max Load NASRCP[3] Input Cap.	0.200	0.0000
NASRCP[3] Max Load	0.200	0.200
RASRCN[0] Input Cap.	0.200	0.200
RASRCN[0] Max Load	- 0.0100	
RASRCN[0] Max Load  RASRCN[1] Input Cap.	0.0100	0.0100
RASRON[1] Input Cap.	0.0100	0.0100
RASRON[1] Max Load  RASRON[2] Input Cap.	0.0100	0.0100
RASRCN[2] Max Load	- 0.0100	0.0100
KASKCIN[2] IVIAX LOAG	-	-



RASRCN[3] Input Cap.	0.0100	0.0100
	0.0100	0.0100
RASRCN[3] Max Load	-	-
RASRCP[0] Input Cap.	0.0100	0.0100
RASRCP[0] Max Load	-	-
RASRCP[1] Input Cap.	0.0100	0.0100
RASRCP[1] Max Load	-	-
RASRCP[2] Input Cap.	0.0100	0.0100
RASRCP[2] Max Load	-	-
RASRCP[3] Input Cap.	0.0100	0.0100
RASRCP[3] Max Load	-	-
REFIOA1V8 Input Cap.	0.2360	0.2360
REFIOA1V8 Max Load	10000.000	10000.000
REFIOB1V8 Input Cap.	0.2360	0.2360
REFIOB1V8 Max Load	10000.000	10000.000
TQ Input Cap.	0.0085	0.0085

## **Default Leakage Power**

Default Leakage Power (mW)	vdd	vdde1v8
best 1.10 125	1.634e-03	4.211e-02
worst 0.90 -40	1.340e-06	9.785e-06





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