

February 2016

CORECELL_B2B_EXT

Cell Description

CORECELL_B2B_EXT

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

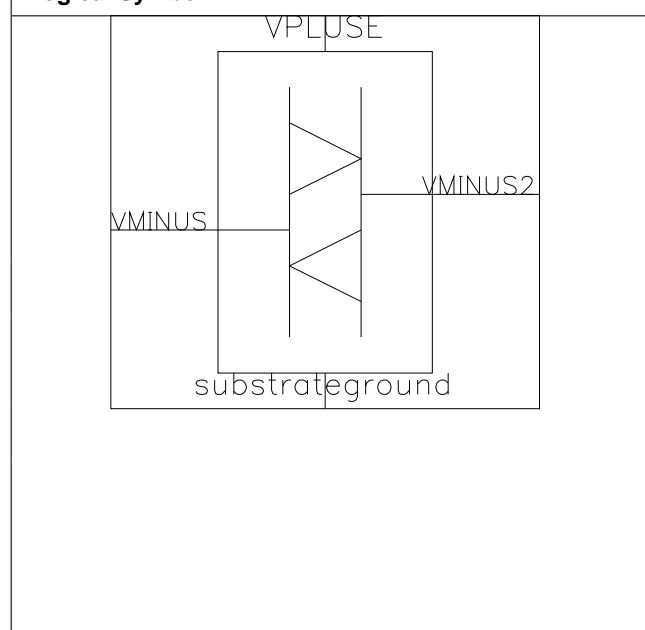
Physical Dimensions

Area(um2) : 415.670

Glossary

Tr : Input Transition time
 C : Output (capacitive) load
 R : Rising edge
 F : Falling edge

Logical Symbol



Default Leakage Power

Default Leakage Power (mW)	VPLUS
best 1.10 125	2.141e-05
worst 0.90 -40	9.200e-08

ESDCLAMP_1V0_45U_130U_EXT

Cell Description

ESDCLAMP_1V0_45U_130U_EXT

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

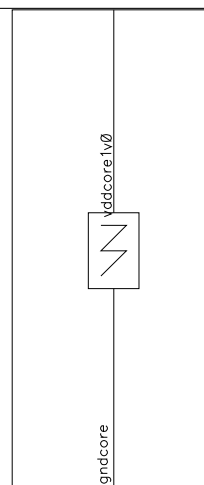
Physical Dimensions

Area(um2) : 5850.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Default Leakage Power

Default Leakage Power (mW)	vddcore1v0
best 1.10 125	1.101e-02
worst 0.90 -40	1.196e-06

ESDCLAMP_1V0_EXT

Cell Description

ESDCLAMP_1V0_EXT

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

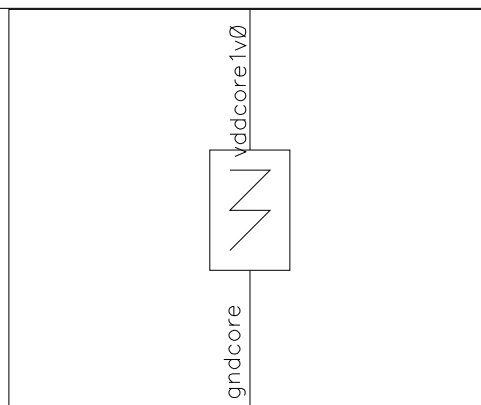
Physical Dimensions

Area(um2) : 5767.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Default Leakage Power

Default Leakage Power (mW)	vddcore1v0
best 1.10 125	1.071e-02
worst 0.90 -40	1.104e-06

ESDCLAMP_1V8_EXT

Cell Description

ESDCLAMP_1V8_EXT

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

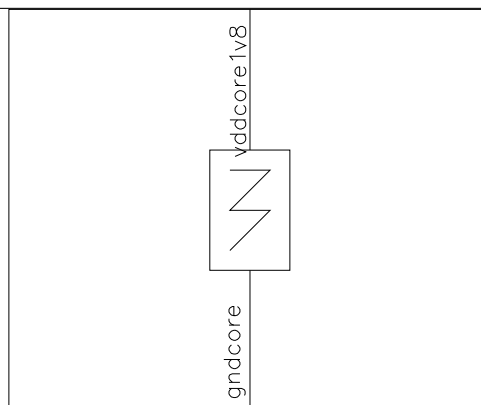
Physical Dimensions

Area(um2) : 5767.000

Glossary

Tr : Input Transition time
 C : Output (capacitive) load
 R : Rising edge
 F : Falling edge

Logical Symbol



Default Leakage Power

Default Leakage Power (mW)	vddcore1v8
best 1.10 125	7.547e-03
worst 0.90 -40	5.524e-06

ESDCLAMP_3V3_EXT

Cell Description

ESDCLAMP_3V3_EXT

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

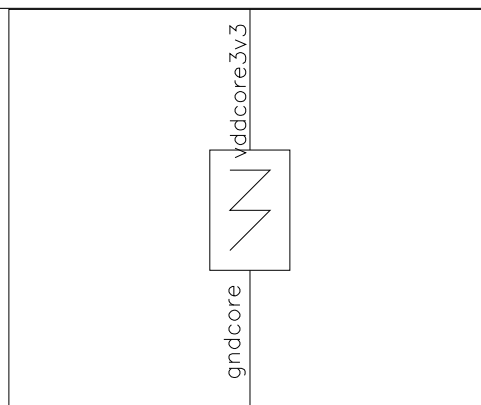
Physical Dimensions

Area(um2) : 8034.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Default Leakage Power

Default Leakage Power (mW)	vddcore3v3
best 1.10 125	9.296e-03
worst 0.90 -40	4.168e-06

GNDCORE_EXT_3V3SF_CL_LIN

Cell Description

GNDCORE_EXT_3V3SF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

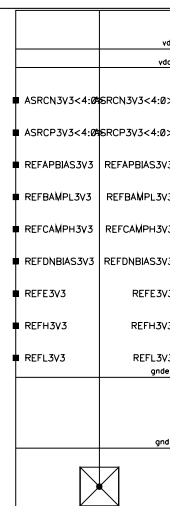
Physical Dimensions

Area(um2) : 2126.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	1.098e-05
worst 0.90 -40	0.000e+00	6.145e-08

GNDCORE_EXT_3V3SF_FC_INNER

Cell Description

GNDCORE_EXT_3V3SF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

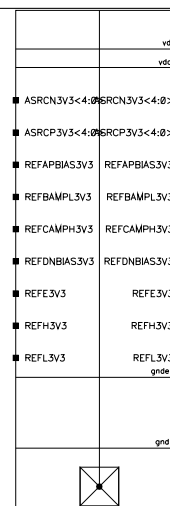
Physical Dimensions

Area(um2) : 1816.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	1.098e-05
worst 0.90 -40	0.000e+00	6.145e-08

GNDCORE_EXT_3V3SF_FC_LIN

Cell Description

GNDCORE_EXT_3V3SF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

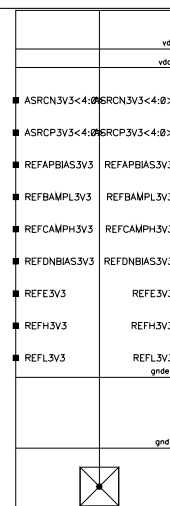
Physical Dimensions

Area(um2) : 1816.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	1.098e-05
worst 0.90 -40	0.000e+00	6.145e-08

GNDCORE_EXT_CSF_CL_LIN

Cell Description

GNDCORE_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

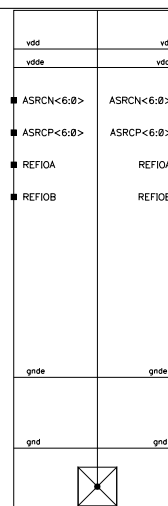
Physical Dimensions

Area(um2) : 2126.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	1.098e-05
worst 0.90 -40	0.000e+00	6.145e-08

GNDCORE_EXT_CSF_FC_INNER

Cell Description

GNDCORE_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

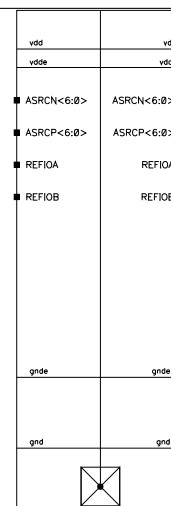
Physical Dimensions

Area(um2) : 3632.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	1.098e-05
worst 0.90 -40	0.000e+00	6.145e-08

GNDCORE_EXT_CSF_FC_LIN

Cell Description

GNDCORE_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

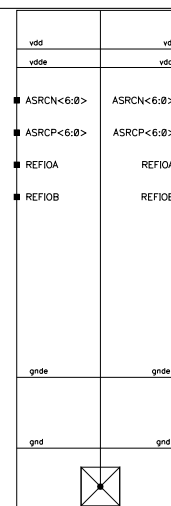
Physical Dimensions

Area(um2) : 1816.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	1.098e-05
worst 0.90 -40	0.000e+00	6.145e-08

VDDCORE_1V0_GND_EXT_3V3SF_CL_LIN

Cell Description

VDDCORE_1V0_GND_EXT_3V3SF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

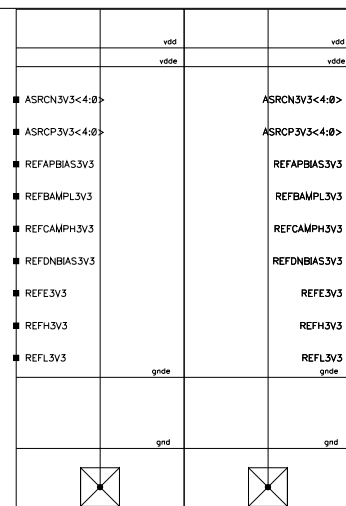
Physical Dimensions

Area(um2) : 7441.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	1.071e-02	0.000e+00
worst 0.90 -40	1.110e-06	0.000e+00

VDDCORE_1V0_GND_EXT_3V3SF_FC_INNER

Cell Description

VDDCORE_1V0_GND_EXT_3V3SF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

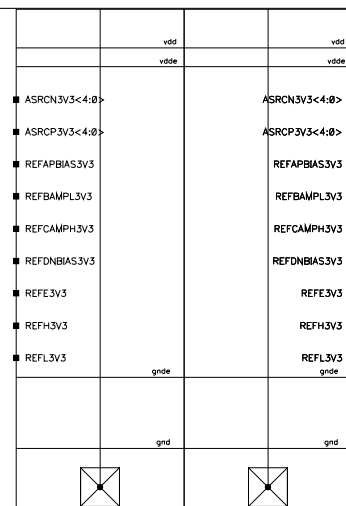
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	1.071e-02	0.000e+00
worst 0.90 -40	1.110e-06	0.000e+00

VDDCORE_1V0_GND_EXT_3V3SF_FC_LIN

Cell Description

VDDCORE_1V0_GND_EXT_3V3SF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

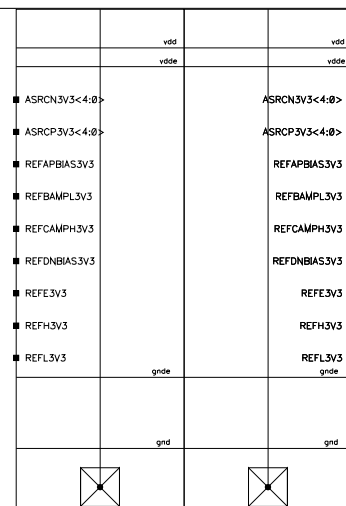
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v0	vdde
best 1.10 125	1.071e-02	1.071e-02	0.000e+00
worst 0.90 -40	1.110e-06	1.110e-06	0.000e+00

VDDCORE_1V0_GND_EXT_CSF_CL_LIN

Cell Description

VDDCORE_1V0_GND_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

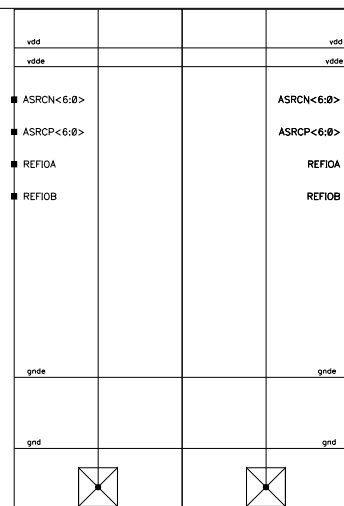
Physical Dimensions

Area(um2) : 7441.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	1.071e-02	0.000e+00
worst 0.90 -40	1.111e-06	0.000e+00

VDDCORE_1V0_GND_EXT_CSF_FC_INNER

Cell Description

VDDCORE_1V0_GND_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

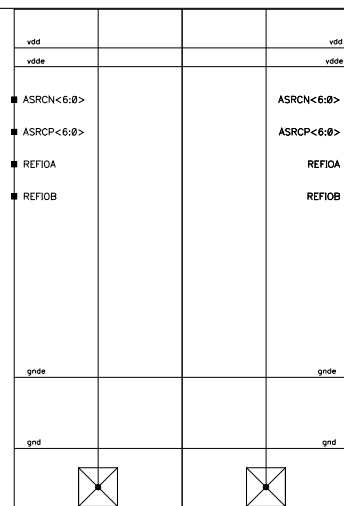
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	1.071e-02	0.000e+00
worst 0.90 -40	1.111e-06	0.000e+00

VDDCORE_1V0_GND_EXT_CSF_FC_LIN

Cell Description

VDDCORE_1V0_GND_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

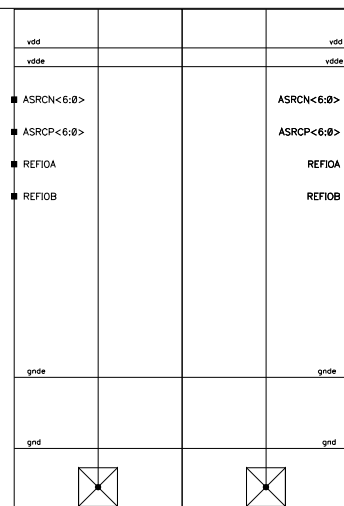
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v0	vdde
best 1.10 125	1.071e-02	1.071e-02	0.000e+00
worst 0.90 -40	1.111e-06	1.111e-06	0.000e+00

VDDCORE_1V0_GNDCORE_EXT_3V3SF_CL_LIN

Cell Description

VDDCORE_1V0_GNDCORE_EXT_3V3SF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

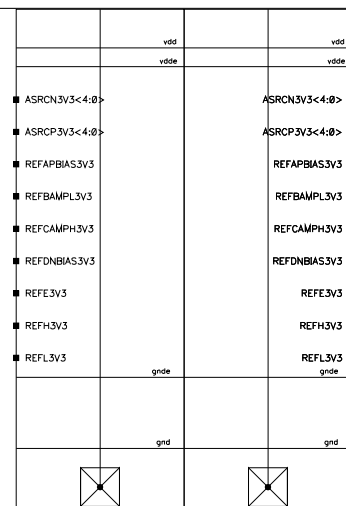
Physical Dimensions

Area(um2) : 7441.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	1.071e-02	0.000e+00
worst 0.90 -40	1.109e-06	0.000e+00

VDDCORE_1V0_GNDCORE_EXT_3V3SF_FC_INNER

Cell Description

VDDCORE_1V0_GNDCORE_EXT_3V3SF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

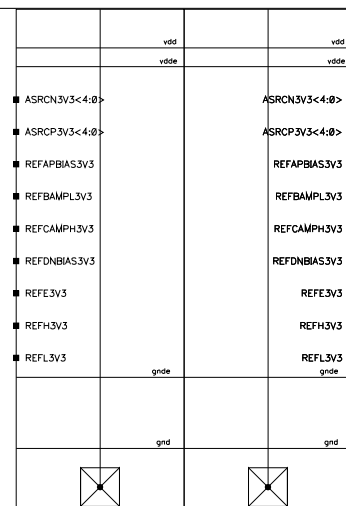
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	1.071e-02	0.000e+00
worst 0.90 -40	1.109e-06	0.000e+00

VDDCORE_1V0_GNDCORE_EXT_3V3SF_FC_LIN

Cell Description

VDDCORE_1V0_GNDCORE_EXT_3V3SF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

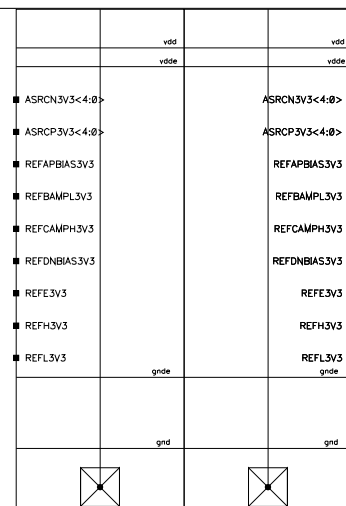
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v0	vdde
best 1.10 125	1.071e-02	1.071e-02	0.000e+00
worst 0.90 -40	1.109e-06	1.109e-06	0.000e+00

VDDCORE_1V0_GNDCORE_EXT_CSF_CL_LIN

Cell Description

VDDCORE_1V0_GNDCORE_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

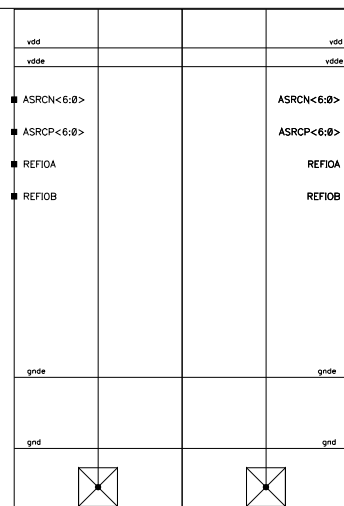
Physical Dimensions

Area(um2) : 7441.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	1.071e-02	0.000e+00
worst 0.90 -40	1.109e-06	0.000e+00

VDDCORE_1V0_GNDCORE_EXT_CSF_FC_INNER

Cell Description

VDDCORE_1V0_GNDCORE_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

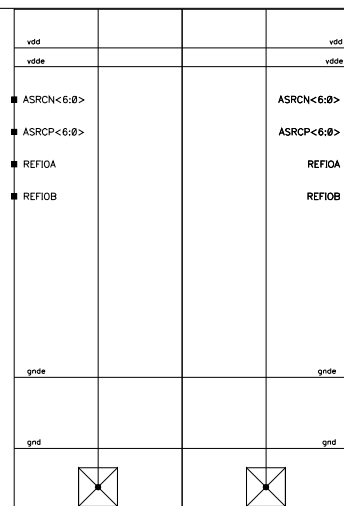
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	1.071e-02	0.000e+00
worst 0.90 -40	1.109e-06	0.000e+00

VDDCORE_1V0_GNDCORE_EXT_CSF_FC_LIN

Cell Description

VDDCORE_1V0_GNDCORE_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

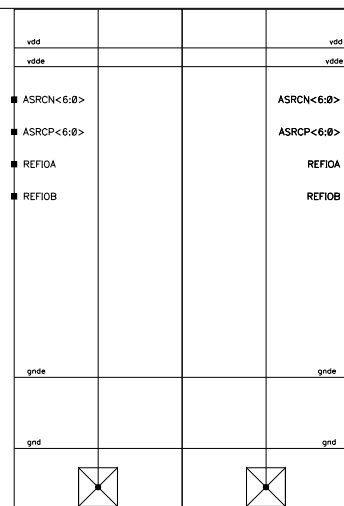
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v0	vdde
best 1.10 125	1.071e-02	1.071e-02	0.000e+00
worst 0.90 -40	1.109e-06	1.109e-06	0.000e+00

VDDCORE_1V0_GNDE_EXT_3V3SF_CL_LIN

Cell Description

VDDCORE_1V0_GNDE_EXT_3V3SF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

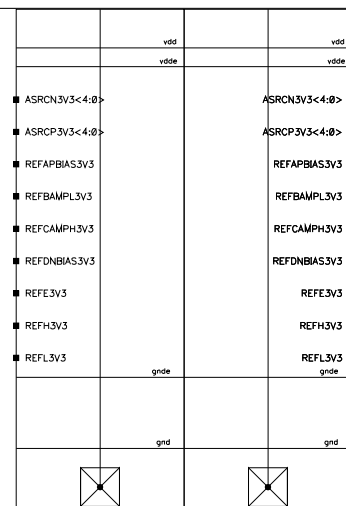
Physical Dimensions

Area(um2) : 7441.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	1.071e-02	0.000e+00
worst 0.90 -40	1.109e-06	0.000e+00

VDDCORE_1V0_GNDE_EXT_3V3SF_FC_INNER

Cell Description

VDDCORE_1V0_GNDE_EXT_3V3SF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

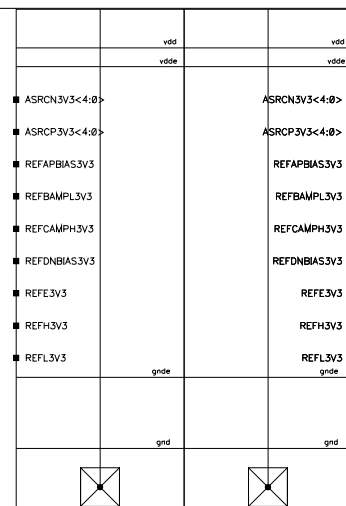
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	1.071e-02	0.000e+00
worst 0.90 -40	1.109e-06	0.000e+00

VDDCORE_1V0_GNDE_EXT_3V3SF_FC_LIN

Cell Description

VDDCORE_1V0_GNDE_EXT_3V3SF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

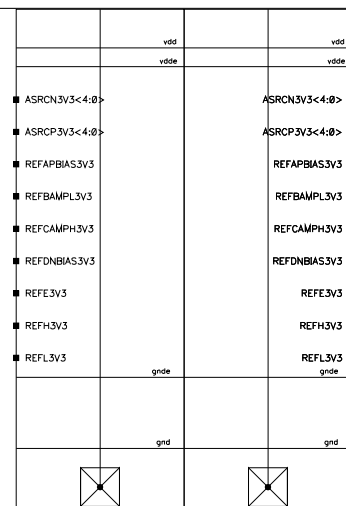
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v0	vdde
best 1.10 125	1.071e-02	1.071e-02	0.000e+00
worst 0.90 -40	1.109e-06	1.109e-06	0.000e+00

VDDCORE_1V0_GNDE_EXT_CSF_CL_LIN

Cell Description

VDDCORE_1V0_GNDE_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

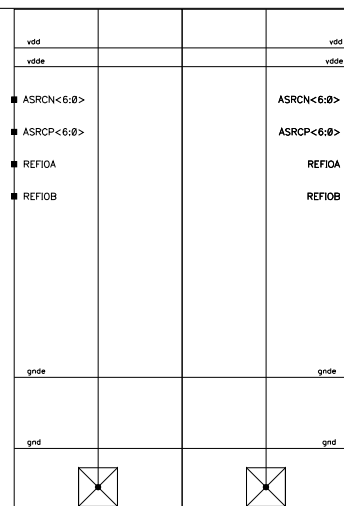
Physical Dimensions

Area(um2) : 7441.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	1.071e-02	0.000e+00
worst 0.90 -40	1.109e-06	0.000e+00

VDDCORE_1V0_GNDE_EXT_CSF_FC_INNER

Cell Description

VDDCORE_1V0_GNDE_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

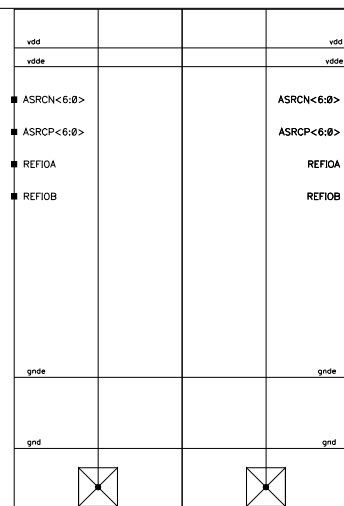
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	1.071e-02	0.000e+00
worst 0.90 -40	1.109e-06	0.000e+00

VDDCORE_1V0_GNDE_EXT_CSF_FC_LIN

Cell Description

VDDCORE_1V0_GNDE_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

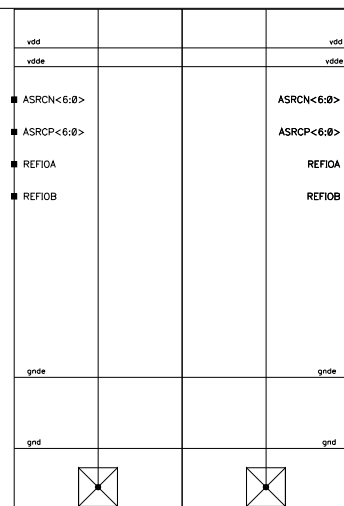
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v0	vdde
best 1.10 125	1.071e-02	1.071e-02	0.000e+00
worst 0.90 -40	1.109e-06	1.109e-06	0.000e+00

VDDCORE_1V8_GND_EXT_3V3SF_CL_LIN

Cell Description

VDDCORE_1V8_GND_EXT_3V3SF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

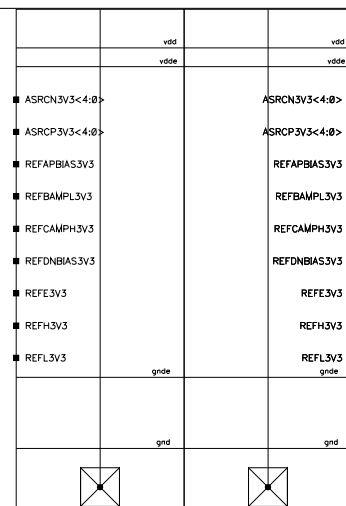
Physical Dimensions

Area(um2) : 7441.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v8	vdde
best 1.10 125	0.000e+00	7.551e-03	0.000e+00
worst 0.90 -40	0.000e+00	5.543e-06	0.000e+00

VDDCORE_1V8_GND_EXT_3V3SF_FC_INNER

Cell Description

VDDCORE_1V8_GND_EXT_3V3SF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

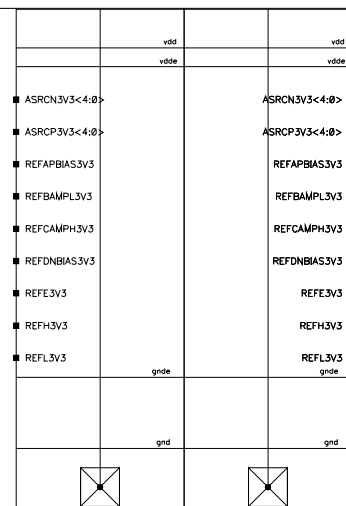
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v8	vdde
best 1.10 125	0.000e+00	7.551e-03	0.000e+00
worst 0.90 -40	0.000e+00	5.543e-06	0.000e+00

VDDCORE_1V8_GND_EXT_3V3SF_FC_LIN

Cell Description

VDDCORE_1V8_GND_EXT_3V3SF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

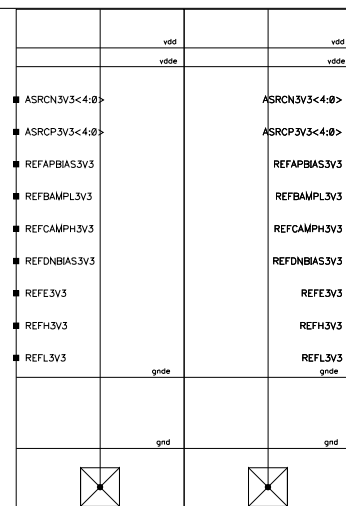
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v8	vdde
best 1.10 125	0.000e+00	7.551e-03	0.000e+00
worst 0.90 -40	0.000e+00	5.543e-06	0.000e+00

VDDCORE_1V8_GND_EXT_CSF_CL_LIN

Cell Description

VDDCORE_1V8_GND_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

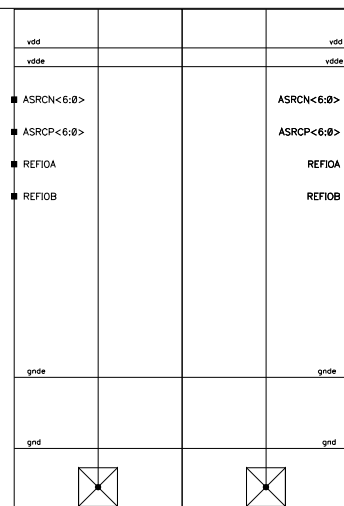
Physical Dimensions

Area(um2) : 7441.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v8	vdde
best 1.10 125	0.000e+00	7.551e-03	0.000e+00
worst 0.90 -40	0.000e+00	5.546e-06	0.000e+00

VDDCORE_1V8_GND_EXT_CSF_FC_INNER

Cell Description

VDDCORE_1V8_GND_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

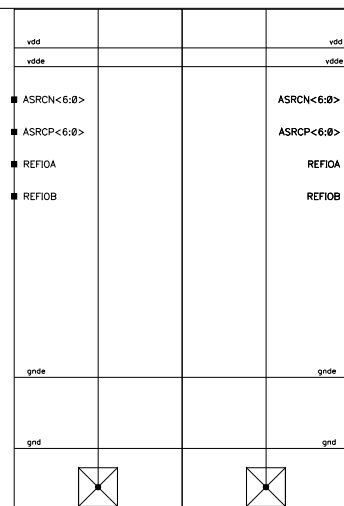
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v8	vdde
best 1.10 125	0.000e+00	7.551e-03	0.000e+00
worst 0.90 -40	0.000e+00	5.546e-06	0.000e+00

VDDCORE_1V8_GND_EXT_CSF_FC_LIN

Cell Description

VDDCORE_1V8_GND_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

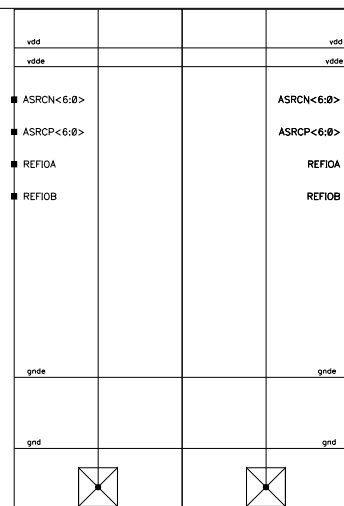
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v8	vdde
best 1.10 125	0.000e+00	7.551e-03	0.000e+00
worst 0.90 -40	0.000e+00	5.546e-06	0.000e+00

VDDCORE_1V8_GNDCORE_EXT_3V3SF_CL_LIN

Cell Description

VDDCORE_1V8_GNDCORE_EXT_3V3SF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

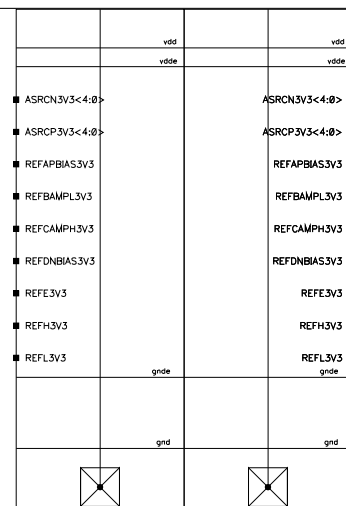
Physical Dimensions

Area(um2) : 7441.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v8	vdde
best 1.10 125	0.000e+00	7.551e-03	0.000e+00
worst 0.90 -40	0.000e+00	5.538e-06	0.000e+00

VDDCORE_1V8_GNDCORE_EXT_3V3SF_FC_INNER

Cell Description

VDDCORE_1V8_GNDCORE_EXT_3V3SF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

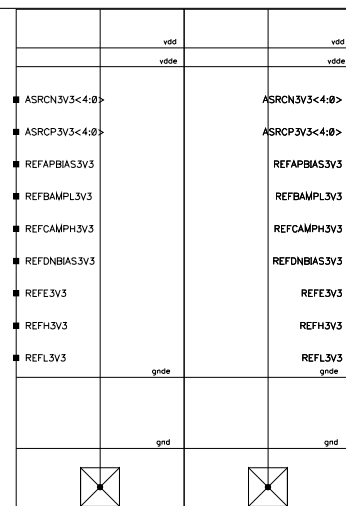
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v8	vdde
best 1.10 125	0.000e+00	7.551e-03	0.000e+00
worst 0.90 -40	0.000e+00	5.538e-06	0.000e+00

VDDCORE_1V8_GNDCORE_EXT_3V3SF_FC_LIN

Cell Description

VDDCORE_1V8_GNDCORE_EXT_3V3SF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

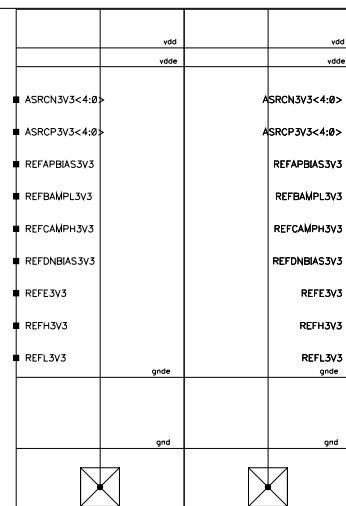
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v8	vdde
best 1.10 125	0.000e+00	7.551e-03	0.000e+00
worst 0.90 -40	0.000e+00	5.538e-06	0.000e+00

VDDCORE_1V8_GNDCORE_EXT_CSF_CL_LIN

Cell Description

VDDCORE_1V8_GNDCORE_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

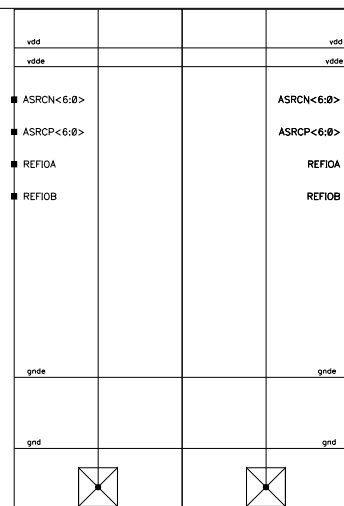
Physical Dimensions

Area(um2) : 7441.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v8	vdde
best 1.10 125	0.000e+00	7.551e-03	0.000e+00
worst 0.90 -40	0.000e+00	5.538e-06	0.000e+00

VDDCORE_1V8_GNDCORE_EXT_CSF_FC_INNER

Cell Description

VDDCORE_1V8_GNDCORE_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

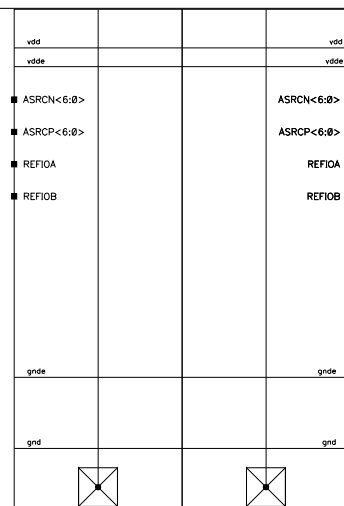
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v8	vdde
best 1.10 125	0.000e+00	7.551e-03	0.000e+00
worst 0.90 -40	0.000e+00	5.538e-06	0.000e+00

VDDCORE_1V8_GNDCORE_EXT_CSF_FC_LIN

Cell Description

VDDCORE_1V8_GNDCORE_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

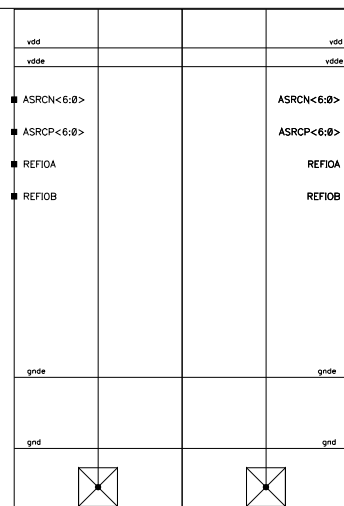
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v8	vdde
best 1.10 125	0.000e+00	7.551e-03	0.000e+00
worst 0.90 -40	0.000e+00	5.538e-06	0.000e+00

VDDCORE_1V8_GNDE_EXT_3V3SF_CL_LIN

Cell Description

VDDCORE_1V8_GNDE_EXT_3V3SF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

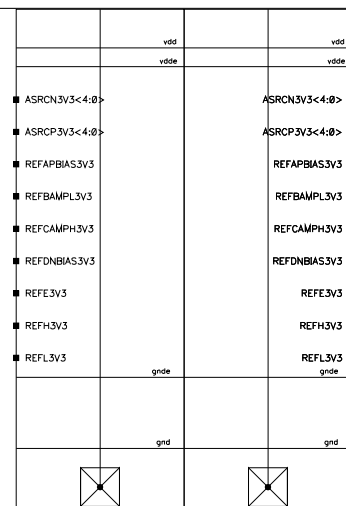
Physical Dimensions

Area(um2) : 7441.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v8	vdde
best 1.10 125	0.000e+00	7.551e-03	0.000e+00
worst 0.90 -40	0.000e+00	5.538e-06	0.000e+00

VDDCORE_1V8_GNDE_EXT_3V3SF_FC_INNER

Cell Description

VDDCORE_1V8_GNDE_EXT_3V3SF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

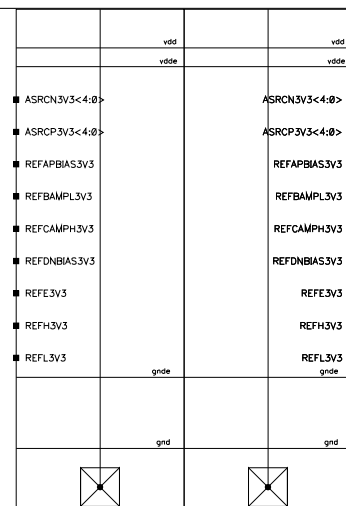
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v8	vdde
best 1.10 125	0.000e+00	7.551e-03	0.000e+00
worst 0.90 -40	0.000e+00	5.538e-06	0.000e+00

VDDCORE_1V8_GNDE_EXT_3V3SF_FC_LIN

Cell Description

VDDCORE_1V8_GNDE_EXT_3V3SF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

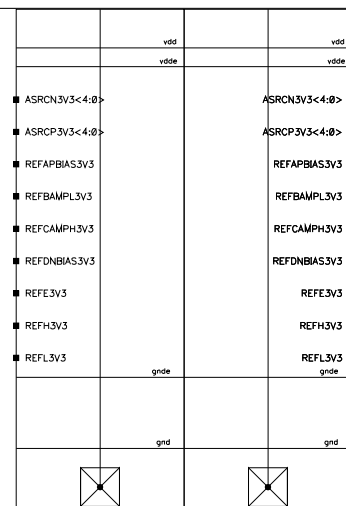
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v8	vdde
best 1.10 125	0.000e+00	7.551e-03	0.000e+00
worst 0.90 -40	0.000e+00	5.538e-06	0.000e+00

VDDCORE_1V8_GNDE_EXT_CSF_CL_LIN

Cell Description

VDDCORE_1V8_GNDE_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

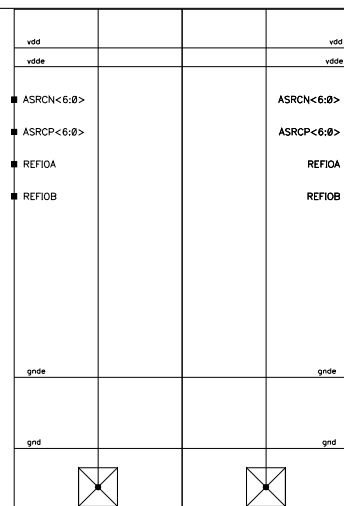
Physical Dimensions

Area(um2) : 7441.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v8	vdde
best 1.10 125	0.000e+00	7.551e-03	0.000e+00
worst 0.90 -40	0.000e+00	5.541e-06	0.000e+00

VDDCORE_1V8_GNDE_EXT_CSF_FC_INNER

Cell Description

VDDCORE_1V8_GNDE_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

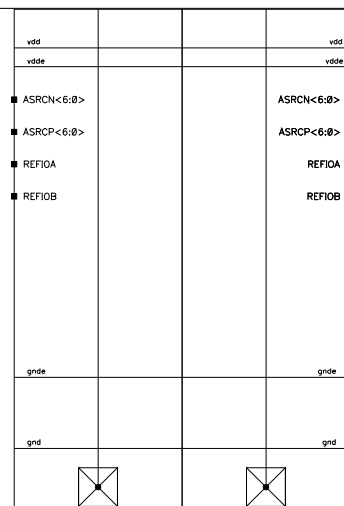
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v8	vdde
best 1.10 125	0.000e+00	7.551e-03	0.000e+00
worst 0.90 -40	0.000e+00	5.541e-06	0.000e+00

VDDCORE_1V8_GNDE_EXT_CSF_FC_LIN

Cell Description

VDDCORE_1V8_GNDE_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

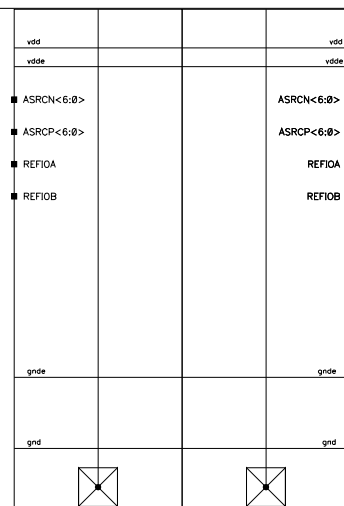
Physical Dimensions

Area(um2) : 6356.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore1v8	vdde
best 1.10 125	0.000e+00	7.551e-03	0.000e+00
worst 0.90 -40	0.000e+00	5.541e-06	0.000e+00

VDDCORE_3V3_GND_EXT_3V3SF_CL_LIN

Cell Description

VDDCORE_3V3_GND_EXT_3V3SF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

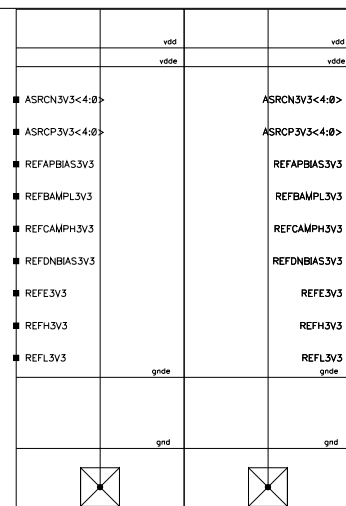
Physical Dimensions

Area(um2) : 10630.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore3v3	vdde
best 1.10 125	0.000e+00	9.320e-03	0.000e+00
worst 0.90 -40	0.000e+00	4.233e-06	0.000e+00

VDDCORE_3V3_GND_EXT_3V3SF_FC_INNER

Cell Description

VDDCORE_3V3_GND_EXT_3V3SF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

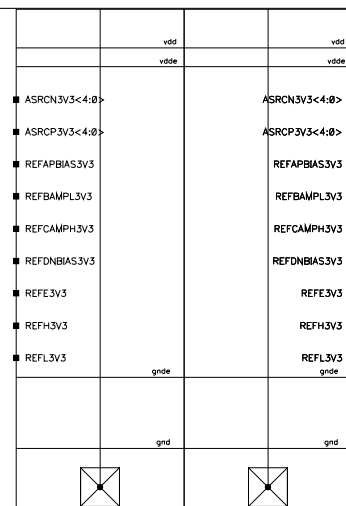
Physical Dimensions

Area(um2) : 9080.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore3v3	vdde
best 1.10 125	0.000e+00	9.320e-03	0.000e+00
worst 0.90 -40	0.000e+00	4.233e-06	0.000e+00

VDDCORE_3V3_GND_EXT_3V3SF_FC_LIN

Cell Description

VDDCORE_3V3_GND_EXT_3V3SF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

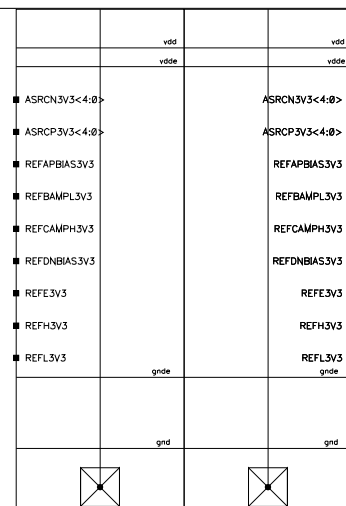
Physical Dimensions

Area(um2) : 9080.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore3v3	vdde
best 1.10 125	0.000e+00	9.320e-03	0.000e+00
worst 0.90 -40	0.000e+00	4.233e-06	0.000e+00

VDDCORE_3V3_GND_EXT_CSF_CL_LIN

Cell Description

VDDCORE_3V3_GND_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

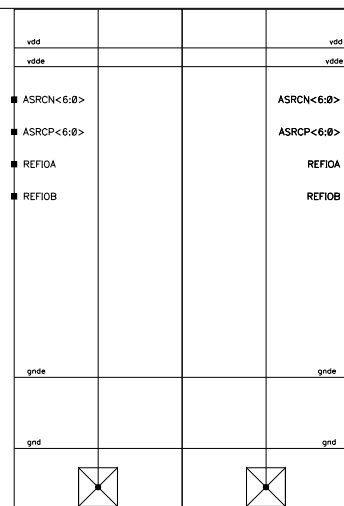
Physical Dimensions

Area(um2) : 10630.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore3v3	vdde
best 1.10 125	0.000e+00	9.320e-03	0.000e+00
worst 0.90 -40	0.000e+00	4.229e-06	4.326e-24

VDDCORE_3V3_GND_EXT_CSF_FC_INNER

Cell Description

VDDCORE_3V3_GND_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

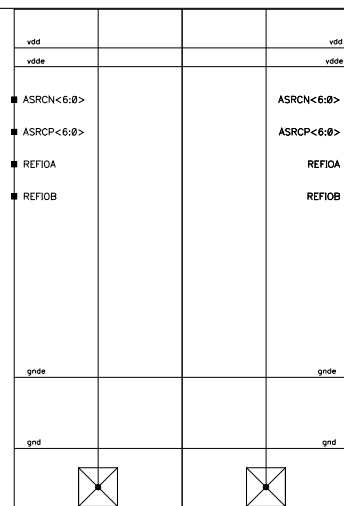
Physical Dimensions

Area(um2) : 9080.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore3v3	vdde
best 1.10 125	0.000e+00	9.320e-03	0.000e+00
worst 0.90 -40	0.000e+00	4.229e-06	4.326e-24

VDDCORE_3V3_GND_EXT_CSF_FC_LIN

Cell Description

VDDCORE_3V3_GND_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

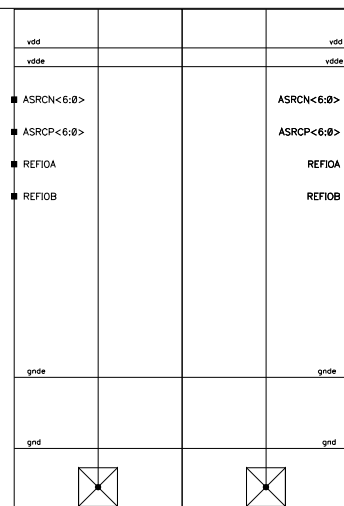
Physical Dimensions

Area(um2) : 9080.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore3v3	vdde
best 1.10 125	0.000e+00	9.320e-03	0.000e+00
worst 0.90 -40	0.000e+00	4.229e-06	4.326e-24

VDDCORE_3V3_GNDCORE_EXT_3V3SF_CL_LIN

Cell Description

VDDCORE_3V3_GNDCORE_EXT_3V3SF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

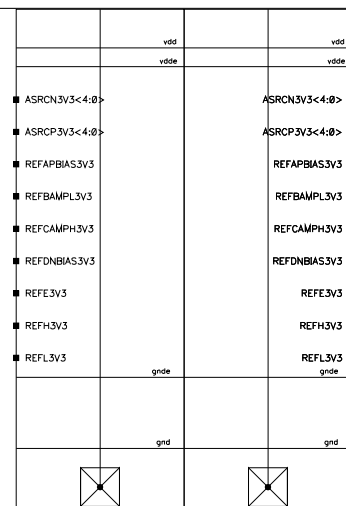
Physical Dimensions

Area(um2) : 10630.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore3v3	vdde
best 1.10 125	0.000e+00	9.320e-03	6.366e-26
worst 0.90 -40	0.000e+00	4.233e-06	0.000e+00

VDDCORE_3V3_GNDCORE_EXT_3V3SF_FC_INNER

Cell Description

VDDCORE_3V3_GNDCORE_EXT_3V3SF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

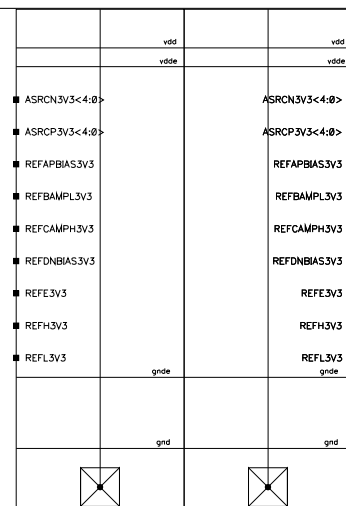
Physical Dimensions

Area(um2) : 9080.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore3v3	vdde
best 1.10 125	0.000e+00	9.320e-03	6.366e-26
worst 0.90 -40	0.000e+00	4.233e-06	0.000e+00

VDDCORE_3V3_GNDCORE_EXT_3V3SF_FC_LIN

Cell Description

VDDCORE_3V3_GNDCORE_EXT_3V3SF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

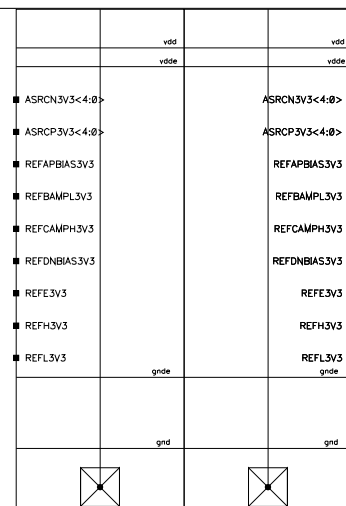
Physical Dimensions

Area(um2) : 9080.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore3v3	vdde
best 1.10 125	0.000e+00	9.320e-03	6.366e-26
worst 0.90 -40	0.000e+00	4.233e-06	0.000e+00

VDDCORE_3V3_GNDCORE_EXT_CSF_CL_LIN

Cell Description

VDDCORE_3V3_GNDCORE_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

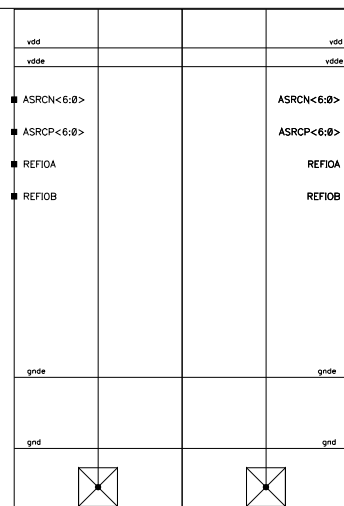
Physical Dimensions

Area(um2) : 10630.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore3v3	vdde
best 1.10 125	0.000e+00	9.320e-03	0.000e+00
worst 0.90 -40	0.000e+00	4.233e-06	0.000e+00

VDDCORE_3V3_GNDCORE_EXT_CSF_FC_INNER

Cell Description

VDDCORE_3V3_GNDCORE_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

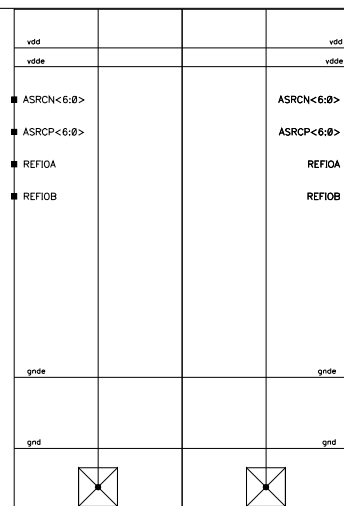
Physical Dimensions

Area(um2) : 9080.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore3v3	vdde
best 1.10 125	0.000e+00	9.320e-03	0.000e+00
worst 0.90 -40	0.000e+00	4.233e-06	0.000e+00

VDDCORE_3V3_GNDCORE_EXT_CSF_FC_LIN

Cell Description

VDDCORE_3V3_GNDCORE_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

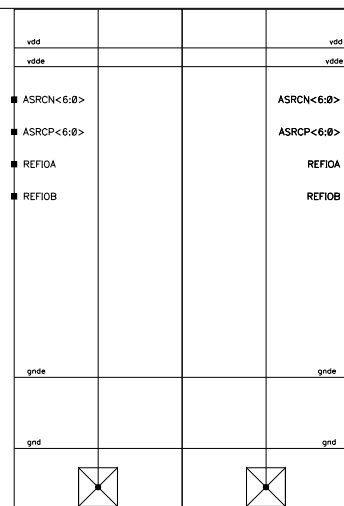
Physical Dimensions

Area(um2) : 9080.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore3v3	vdde
best 1.10 125	0.000e+00	9.320e-03	0.000e+00
worst 0.90 -40	0.000e+00	4.233e-06	0.000e+00

VDDCORE_3V3_GNDE_EXT_3V3SF_CL_LIN

Cell Description

VDDCORE_3V3_GNDE_EXT_3V3SF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

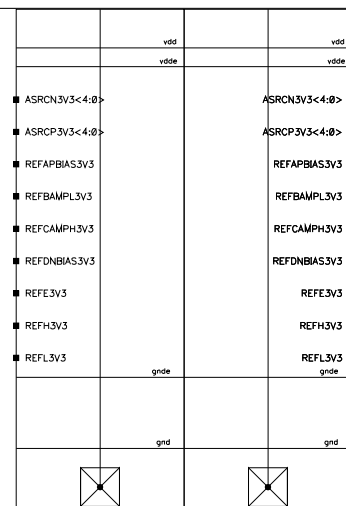
Physical Dimensions

Area(um2) : 10630.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore3v3	vdde
best 1.10 125	0.000e+00	9.320e-03	4.543e-26
worst 0.90 -40	0.000e+00	4.233e-06	5.238e-25

VDDCORE_3V3_GNDE_EXT_3V3SF_FC_INNER

Cell Description

VDDCORE_3V3_GNDE_EXT_3V3SF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

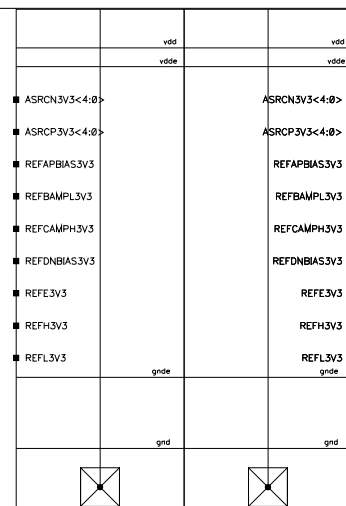
Physical Dimensions

Area(um2) : 9080.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore3v3	vdde
best 1.10 125	0.000e+00	9.320e-03	4.543e-26
worst 0.90 -40	0.000e+00	4.233e-06	5.238e-25

VDDCORE_3V3_GNDE_EXT_3V3SF_FC_LIN

Cell Description

VDDCORE_3V3_GNDE_EXT_3V3SF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

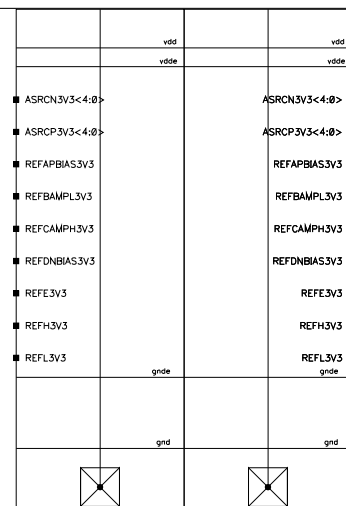
Physical Dimensions

Area(um2) : 9080.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN3V3[0] Input Cap.	0.0100	0.0100
ASRCN3V3[0] Max Load	10000.000	10000.000
ASRCN3V3[1] Input Cap.	0.0100	0.0100
ASRCN3V3[1] Max Load	10000.000	10000.000
ASRCN3V3[2] Input Cap.	0.0100	0.0100
ASRCN3V3[2] Max Load	10000.000	10000.000
ASRCN3V3[3] Input Cap.	0.0100	0.0100
ASRCN3V3[3] Max Load	10000.000	10000.000
ASRCN3V3[4] Input Cap.	0.0100	0.0100
ASRCN3V3[4] Max Load	10000.000	10000.000
ASRCP3V3[0] Input Cap.	0.0100	0.0100
ASRCP3V3[0] Max Load	10000.000	10000.000
ASRCP3V3[1] Input Cap.	0.0100	0.0100
ASRCP3V3[1] Max Load	10000.000	10000.000
ASRCP3V3[2] Input Cap.	0.0100	0.0100
ASRCP3V3[2] Max Load	10000.000	10000.000
ASRCP3V3[3] Input Cap.	0.0100	0.0100
ASRCP3V3[3] Max Load	10000.000	10000.000
ASRCP3V3[4] Input Cap.	0.0100	0.0100
ASRCP3V3[4] Max Load	10000.000	10000.000
REFAPBIAS3V3 Input Cap.	0.0100	0.0100
REFAPBIAS3V3 Max Load	10000.000	10000.000

REFBAMPL3V3 Input Cap.	0.0100	0.0100
REFBAMPL3V3 Max Load	10000.000	10000.000
REFCAMP3V3 Input Cap.	0.0100	0.0100
REFCAMP3V3 Max Load	10000.000	10000.000
REFDNBIAS3V3 Input Cap.	0.0100	0.0100
REFDNBIAS3V3 Max Load	10000.000	10000.000
REFE3V3 Input Cap.	0.0100	0.0100
REFE3V3 Max Load	10000.000	10000.000
REFH3V3 Input Cap.	0.0100	0.0100
REFH3V3 Max Load	10000.000	10000.000
REFL3V3 Input Cap.	0.0100	0.0100
REFL3V3 Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore3v3	vdde
best 1.10 125	0.000e+00	9.320e-03	4.543e-26
worst 0.90 -40	0.000e+00	4.233e-06	5.238e-25

VDDCORE_3V3_GNDE_EXT_CSF_CL_LIN

Cell Description

VDDCORE_3V3_GNDE_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

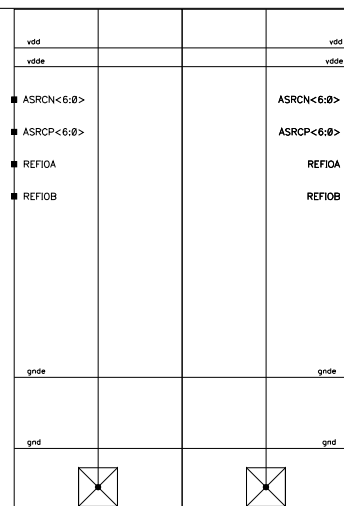
Physical Dimensions

Area(um2) : 10630.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore3v3	vdde
best 1.10 125	0.000e+00	9.320e-03	0.000e+00
worst 0.90 -40	0.000e+00	4.233e-06	3.101e-25

VDDCORE_3V3_GNDE_EXT_CSF_FC_INNER

Cell Description

VDDCORE_3V3_GNDE_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

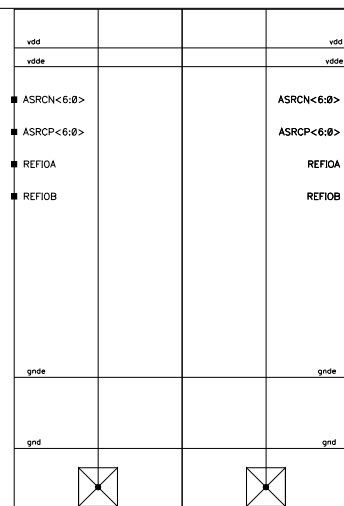
Physical Dimensions

Area(um2) : 9080.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore3v3	vdde
best 1.10 125	0.000e+00	9.320e-03	0.000e+00
worst 0.90 -40	0.000e+00	4.233e-06	3.101e-25

VDDCORE_3V3_GNDE_EXT_CSF_FC_LIN

Cell Description

VDDCORE_3V3_GNDE_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

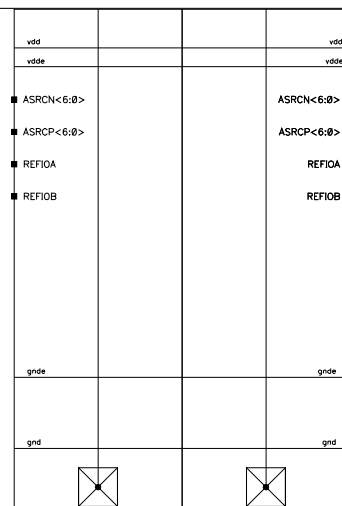
Physical Dimensions

Area(um2) : 9080.000

Glossary

Tr : Input Transition time
C : Output (capacitive) load
R : Rising edge
F : Falling edge

Logical Symbol



Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power

Default Leakage Power (mW)	vdd	vddcore3v3	vdde
best 1.10 125	0.000e+00	9.320e-03	0.000e+00
worst 0.90 -40	0.000e+00	4.233e-06	3.101e-25



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