



cmos028fdsoi Technology

SG LLE LOD models

DK1.2_RF_mmW

Comparison with DK1.1_RF_mmW model(s)

LLE - SG LOD

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Sep 24, 2018

Technology R&D Crolles Site – TDP/TDS/SPICE Modeling

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General information on SG LLE LOD models

- Maximum supply voltage is - V.
- Validity domain is defined as follows:
 - ✓ Drawn gate length varies from 30nm to 10um.
 - ✓ Drawn transistor width varies from 80nm to 10um.
 - ✓ Device temperature varies from -40 °C to 125 °C.

Output parameters definitions

- Model(s): lvtmfet_acc, lvtpfet_acc, nfet_acc, pfet_acc
 - ✓ V_{t_lin} : Threshold voltage defined as V_{gs} value for which drain current is $i_{vt} \cdot M \cdot 1 \cdot W / (1 \cdot L + 0 + 1 \cdot p_la)$ at $V_{ds} = 0.05V$.
 - ✓ I_{sat} : Drain current at $V_{gs} = 1V$, $V_{ds} = V_{dd}V$.
 - ✓ V_{t_sat} : Threshold voltage defined as V_{gs} value for which drain current is $i_{vt} \cdot M \cdot 1 \cdot W / (1 \cdot L + 0 + 1 \cdot p_la)$ at $V_{ds} = v_{ds_sat}V$.
 - ✓ I_{lin} : Drain current at $V_{gs} = 1V$, $V_{ds} = 0.05V$.
 - ✓ $Logioff$: $\log_{10}(I_{offsat})$.

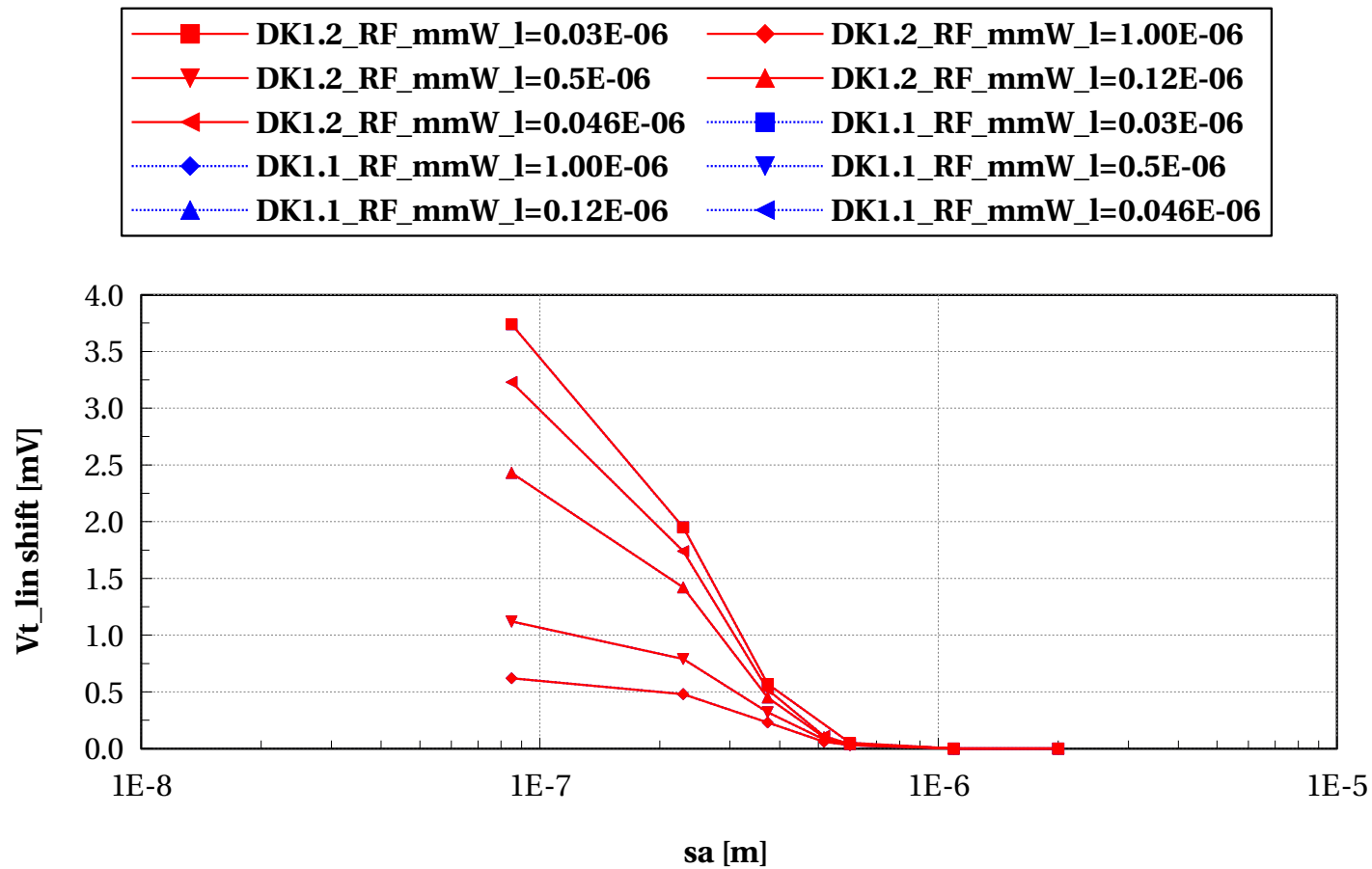
lvtnfet_acc

Electrical characteristics scaling

LOD effect (sa=sb) - Lscaling at $W=1e-6$

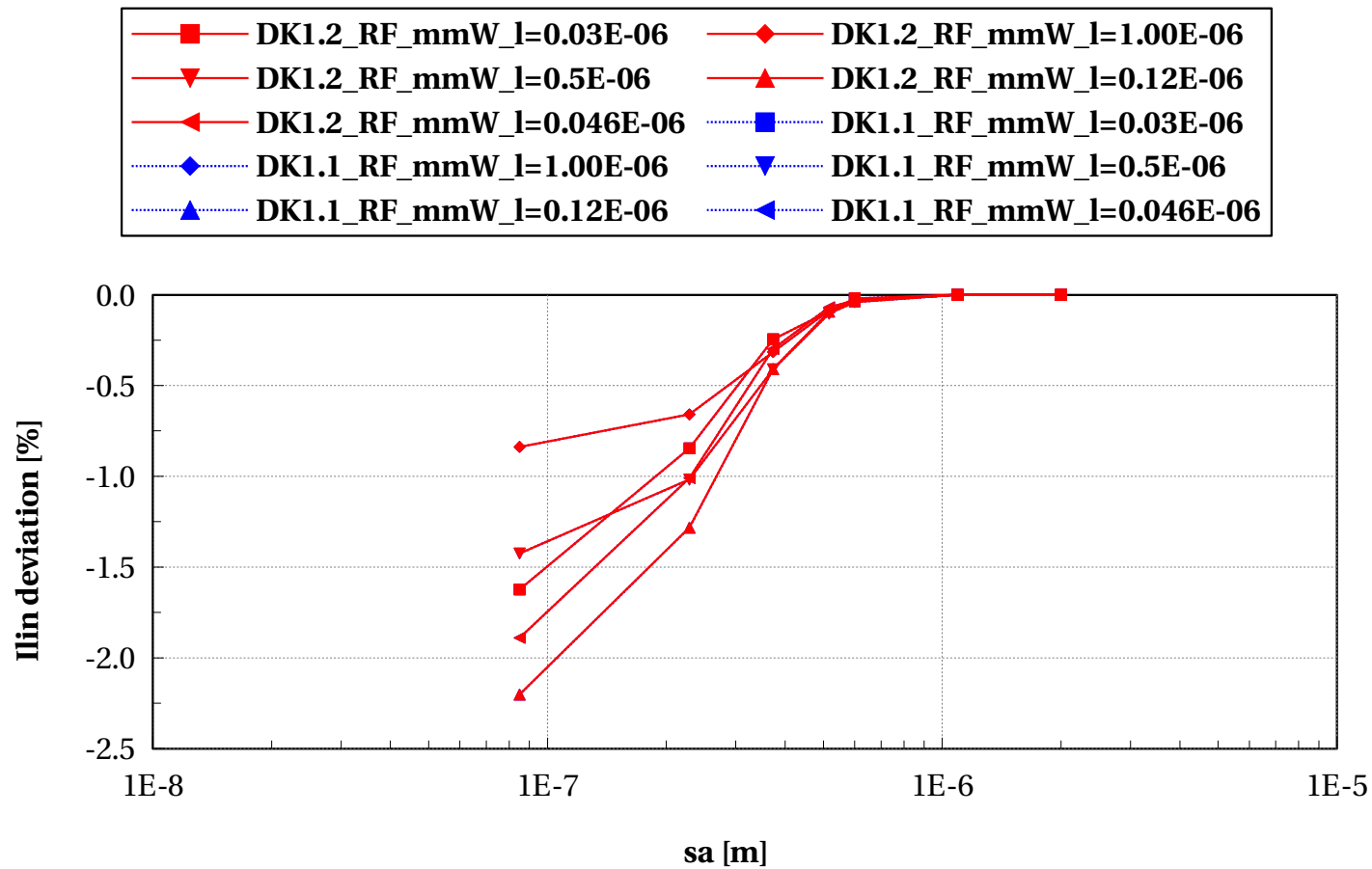
lvtnfet_acc, Vt_lin shift [mV] vs sa [m]

ip==25 and w==1e-6 and p_la==0 and (L==0.030e-6 or L==0.046e-6 or L==0.12e-6 or L==0.5e-6 or L==1e-6)



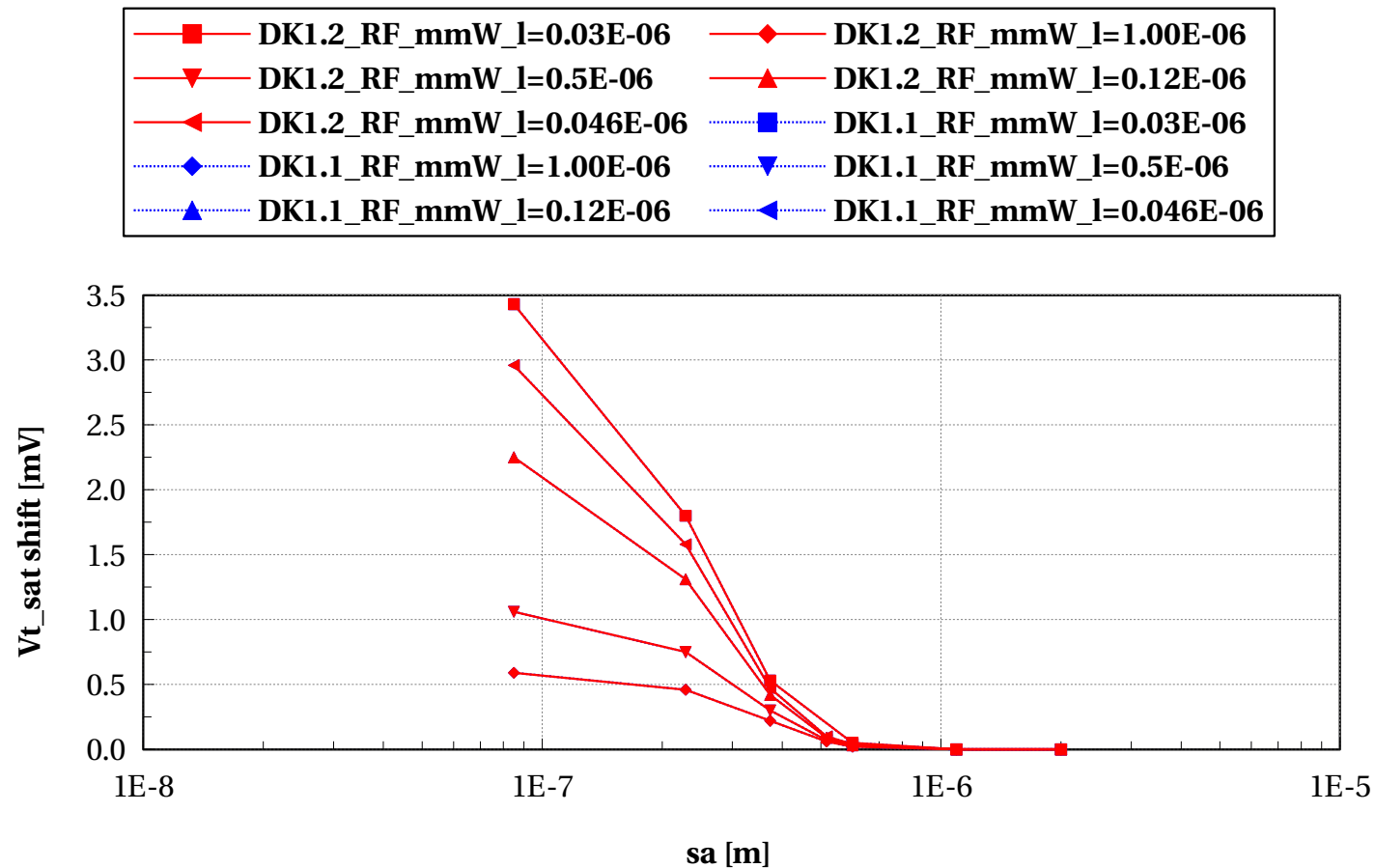
lvtnfet_acc, Ilin deviation [%] vs sa [m]

ip==25 and w==1e-6 and p_la==0 and (L==0.030e-6 or L==0.046e-6 or L==0.12e-6 or L==0.5e-6 or L==1e-6)



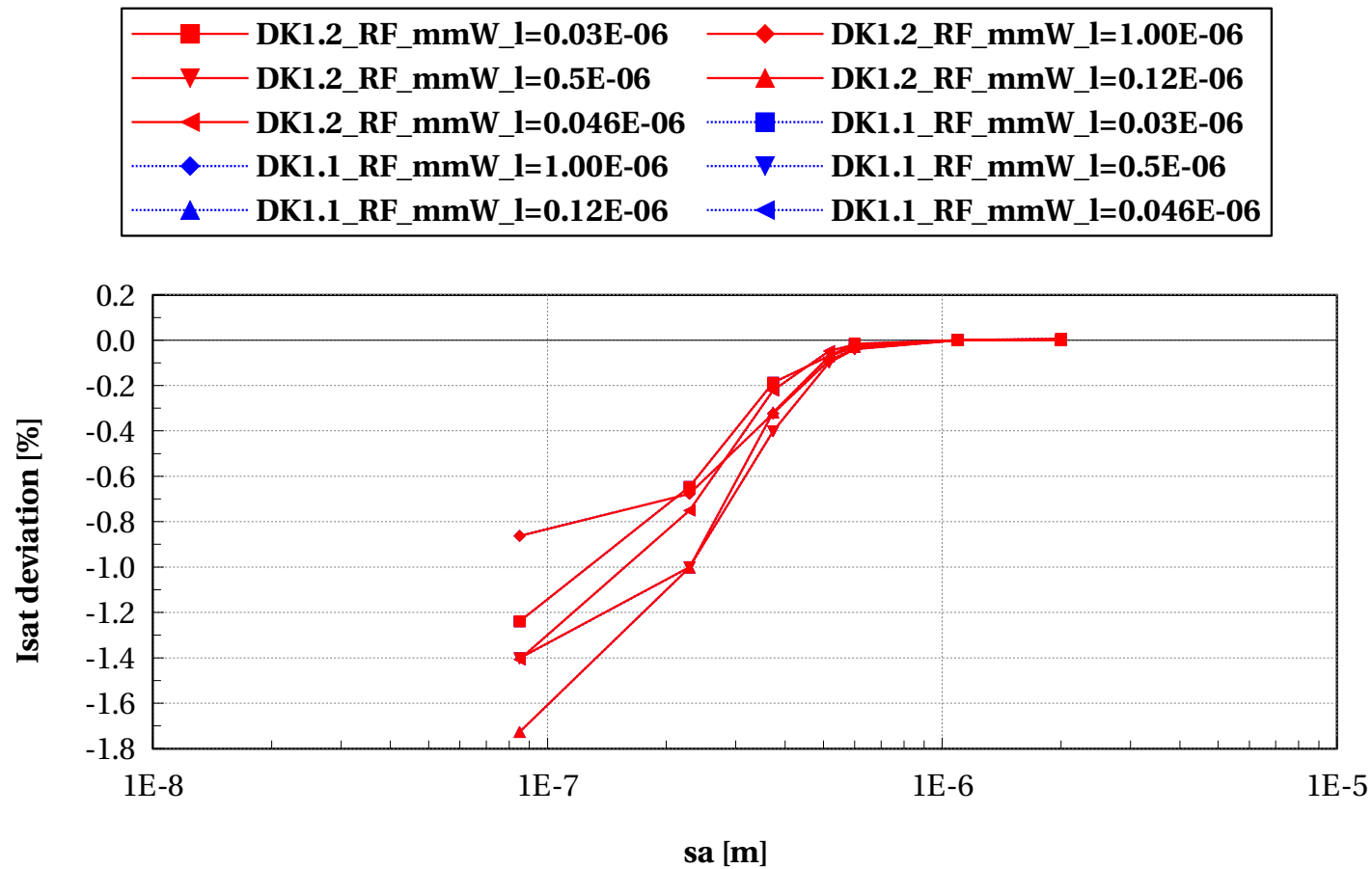
lvtnfet_acc, Vt_sat shift [mV] vs sa [m]

$i_p=25$ and $w=1e-6$ and $p_{la}=0$ and ($L=0.030e-6$ or $L=0.046e-6$ or $L=0.12e-6$ or $L=0.5e-6$ or $L=1e-6$)



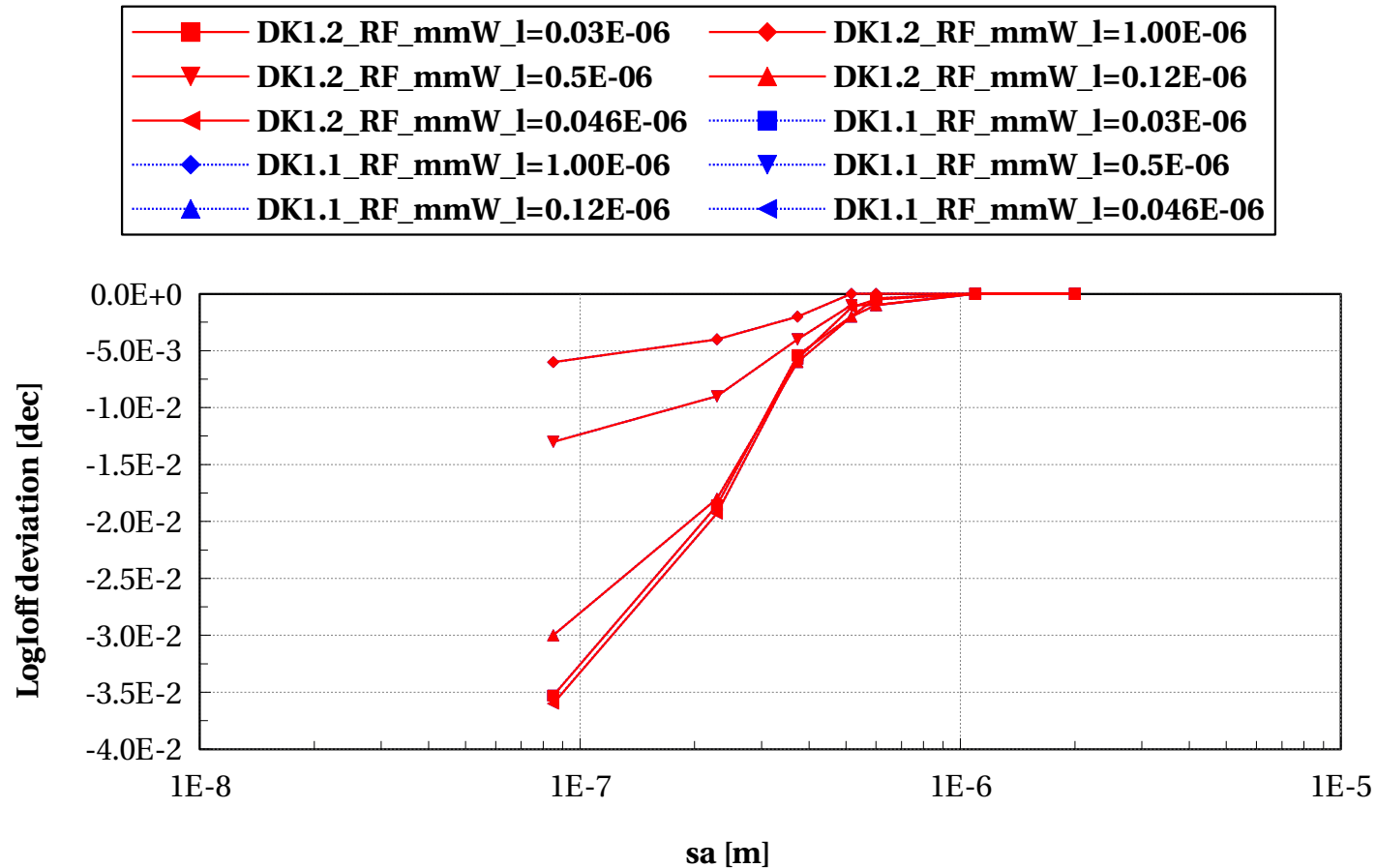
lvtnfet_acc, Isat deviation [%] vs sa [m]

$i_p=25$ and $w=1e-6$ and $p_{la}=0$ and ($L=0.030e-6$ or $L=0.046e-6$ or $L=0.12e-6$ or $L=0.5e-6$ or $L=1e-6$)



lvtnfet_acc, LogIoff deviation [dec] vs sa [m]

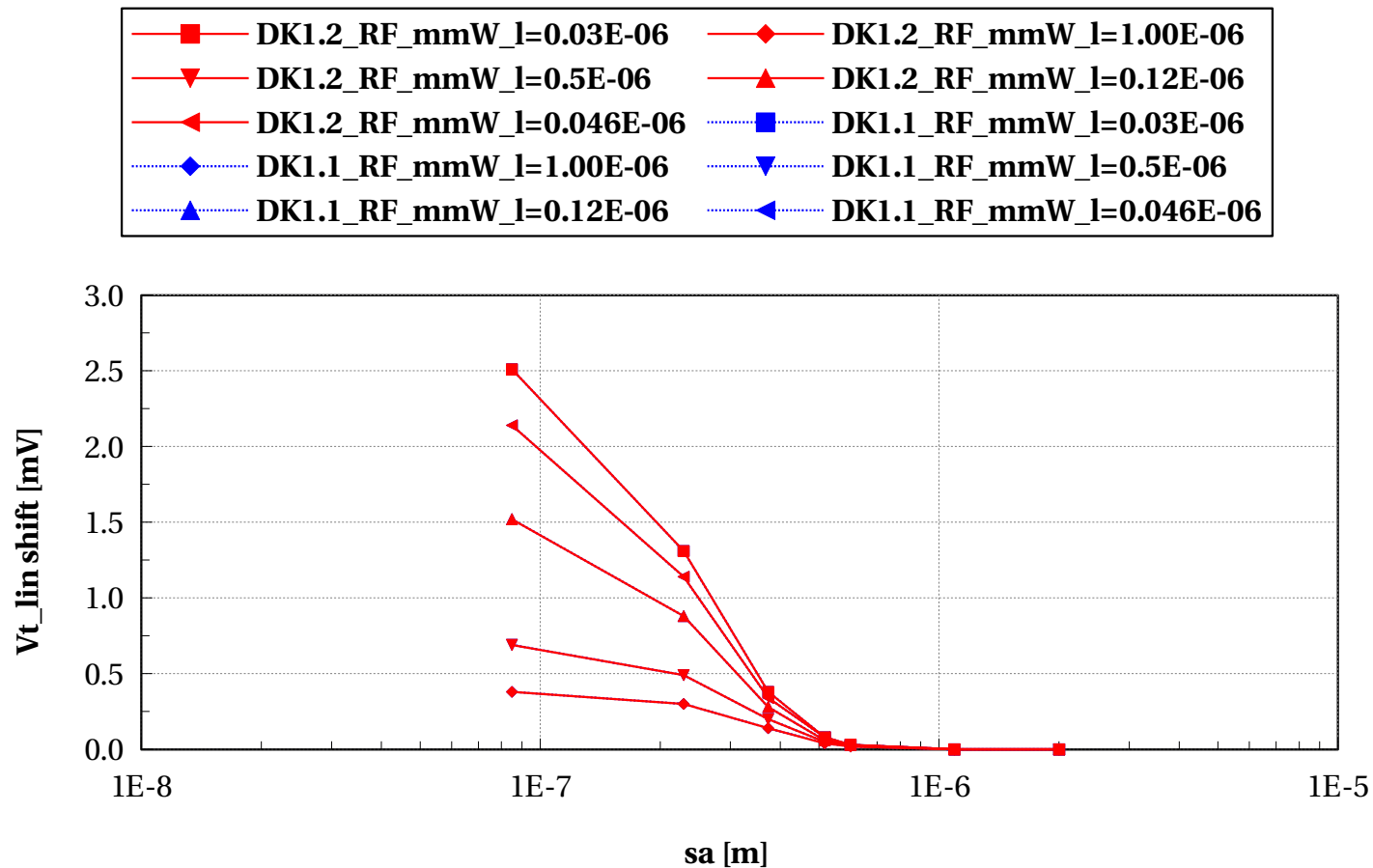
$i_p=25$ and $w=1e-6$ and $p_{la}=0$ and ($L=0.030e-6$ or $L=0.046e-6$ or $L=0.12e-6$ or $L=0.5e-6$ or $L=1e-6$)



LOD effect (sa=sb) - Lscaling at $W=0.3e-6$

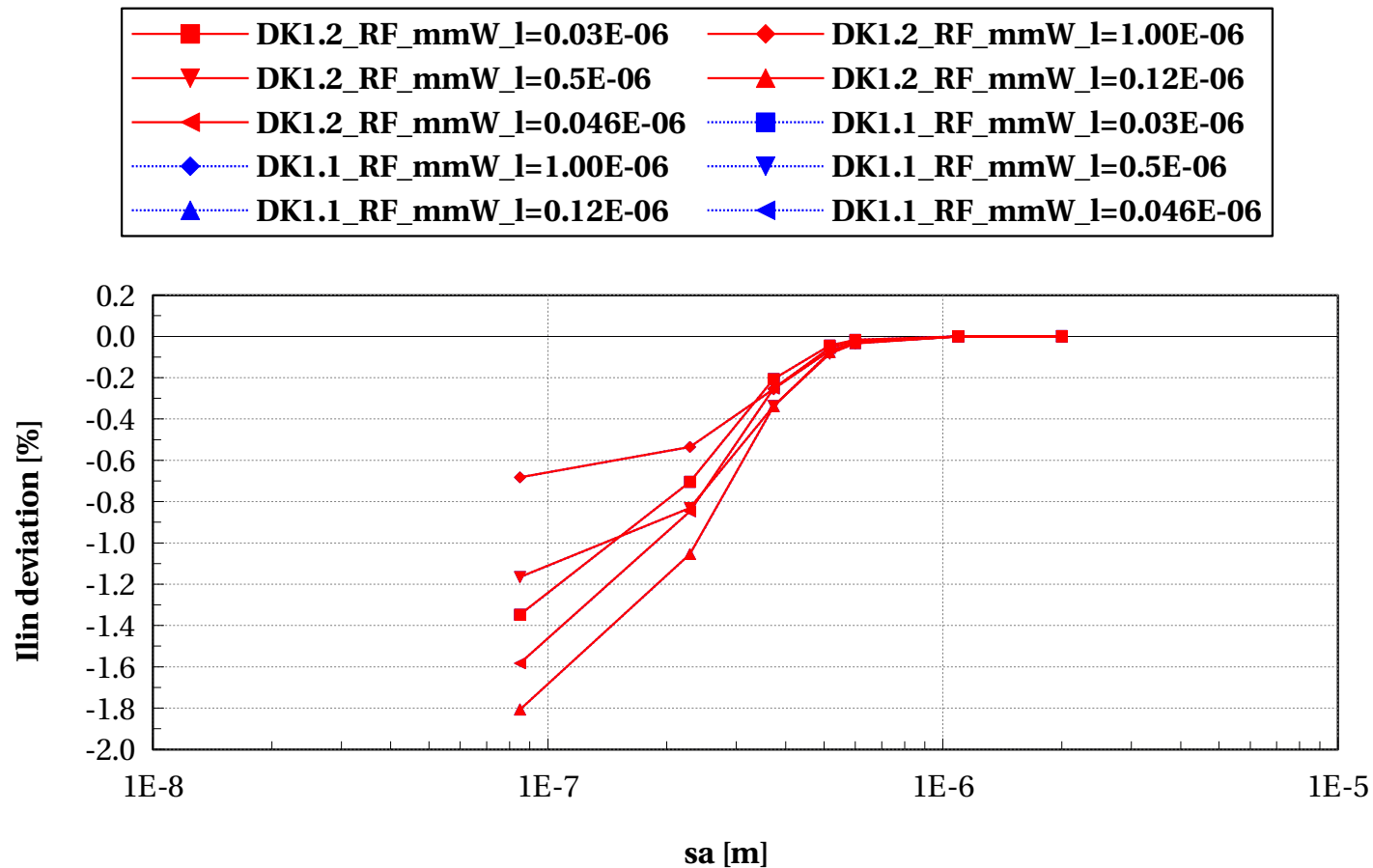
lvtnfet_acc, Vt_lin shift [mV] vs sa [m]

$\rho==25$ and $w==0.3e-6$ and $p_la==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



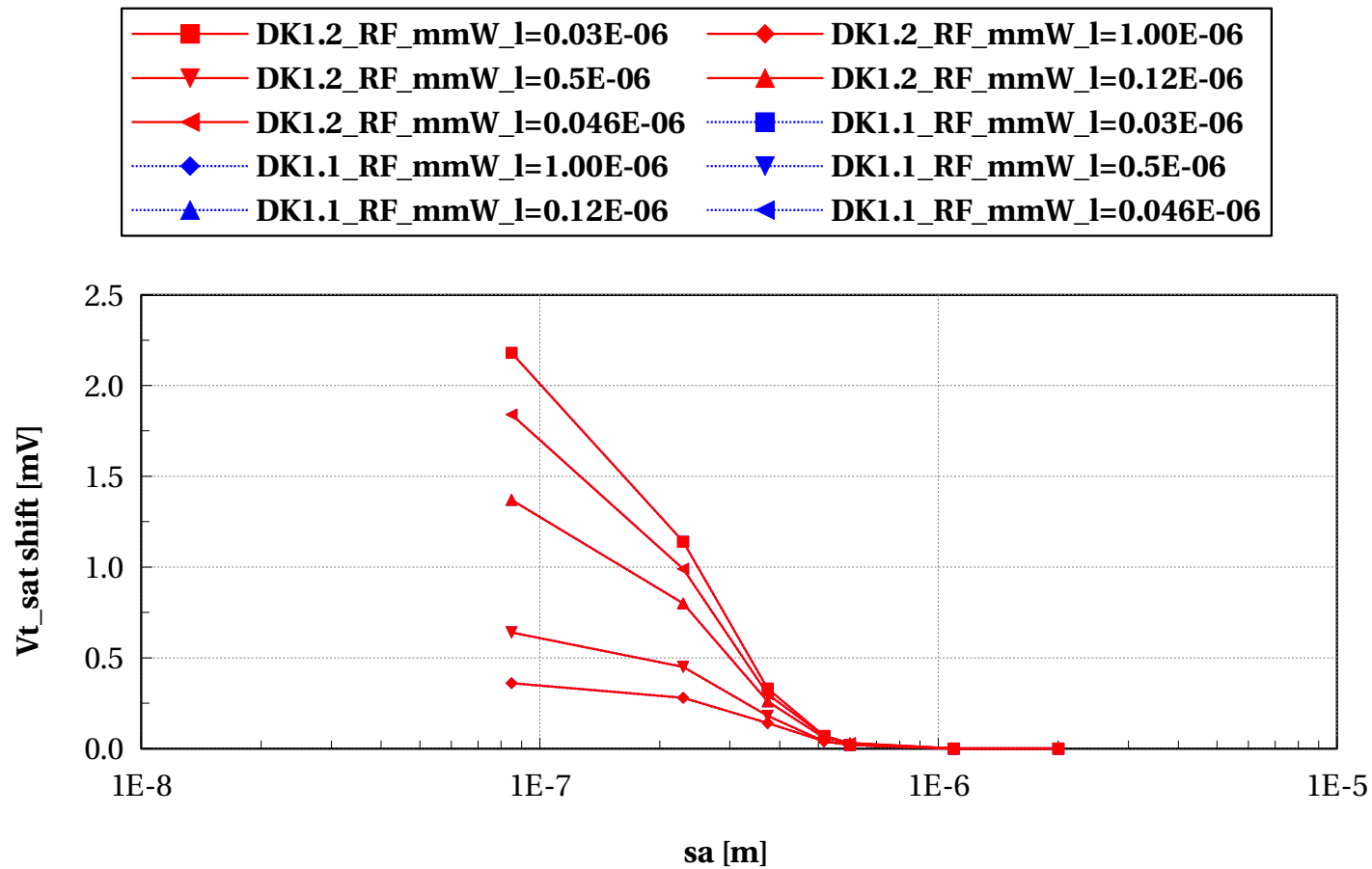
lvtnfet_acc, Ilin deviation [%] vs sa [m]

$\rho==25$ and $w==0.3e-6$ and $p_{la}==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



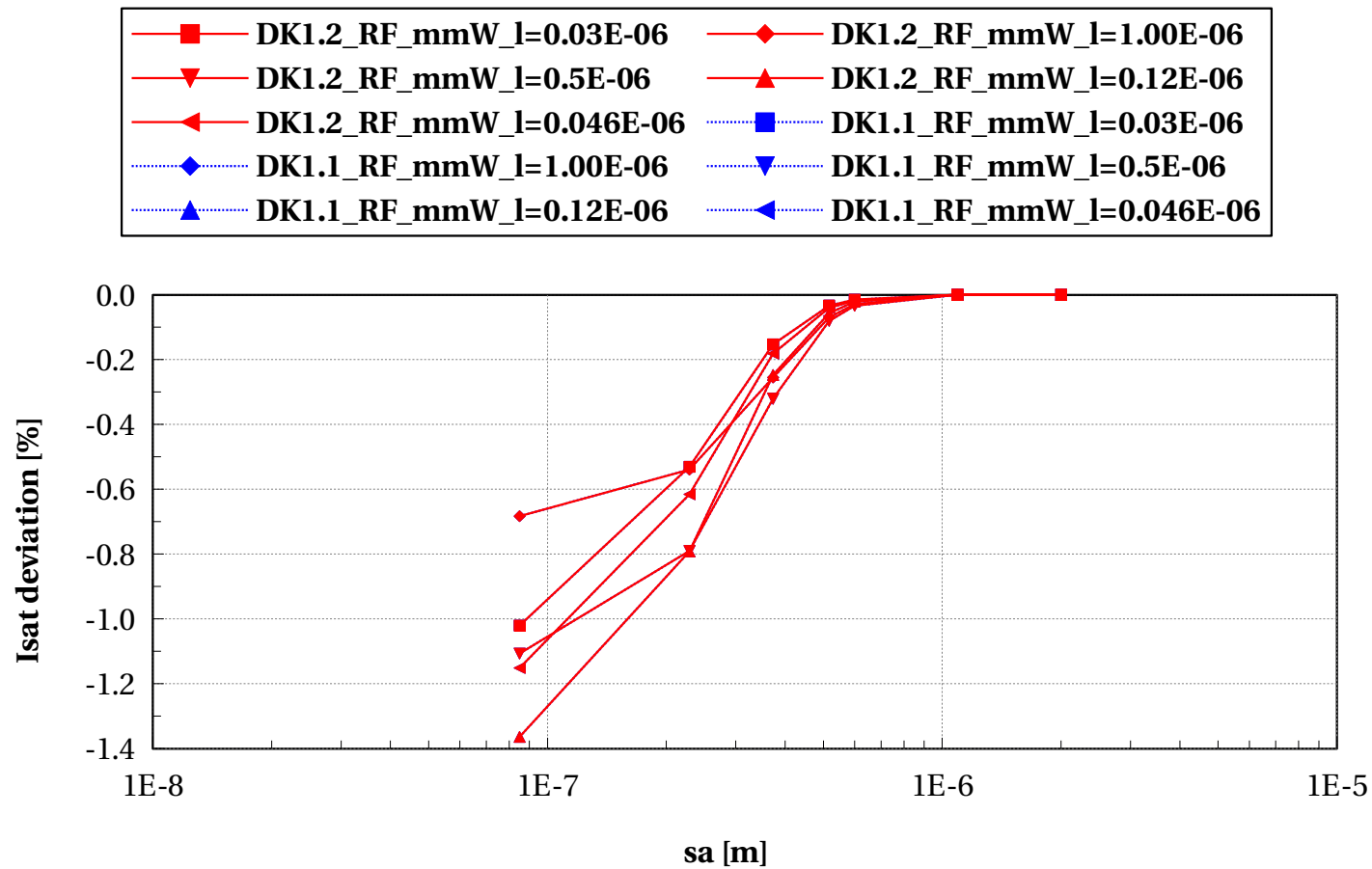
lvtnfet_acc, Vt_sat shift [mV] vs sa [m]

$\rho==25$ and $w==0.3e-6$ and $p_la==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



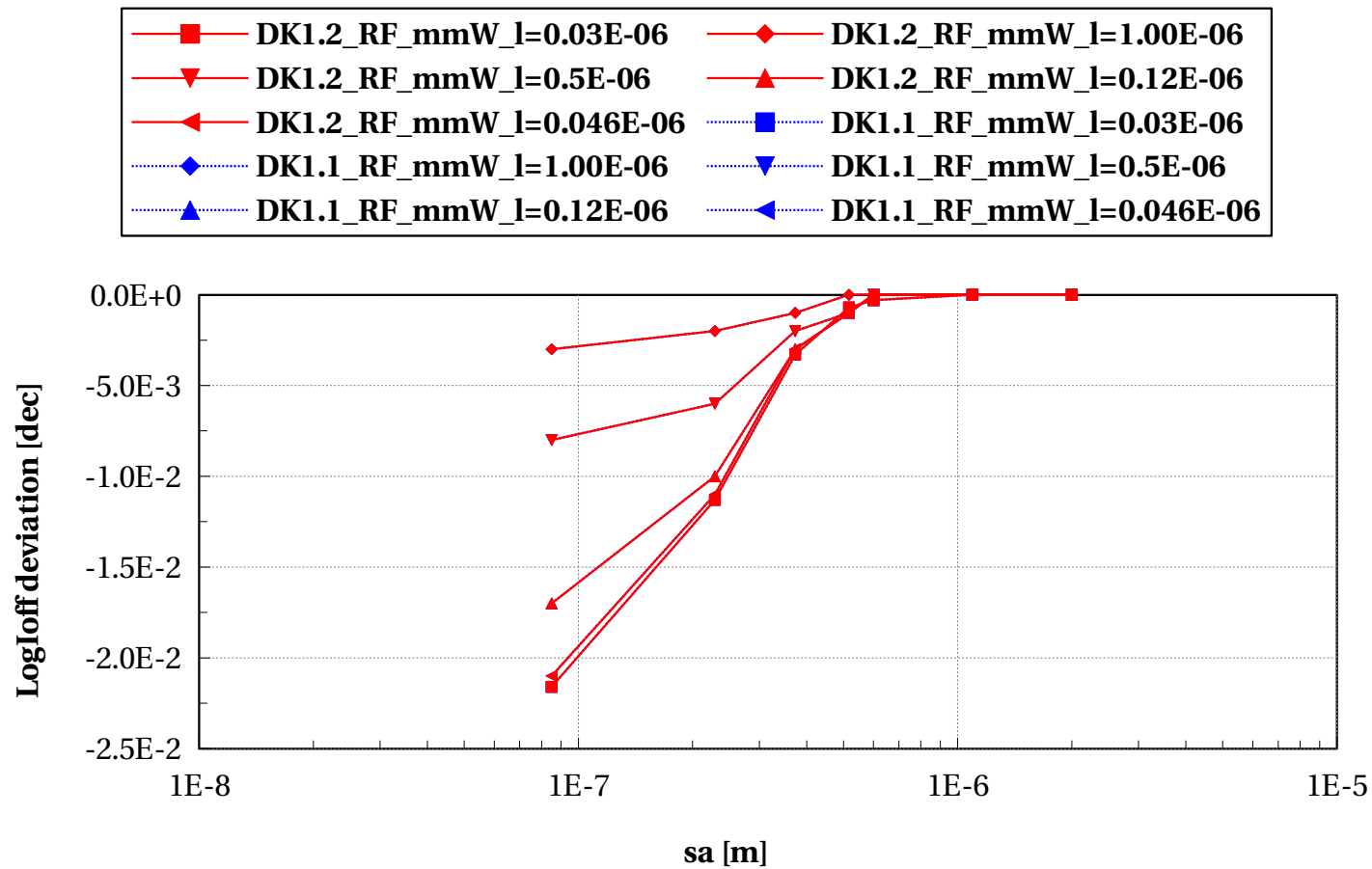
lvtnfet_acc, Isat deviation [%] vs sa [m]

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lvtnfet_acc, LogIoff deviation [dec] vs sa [m]

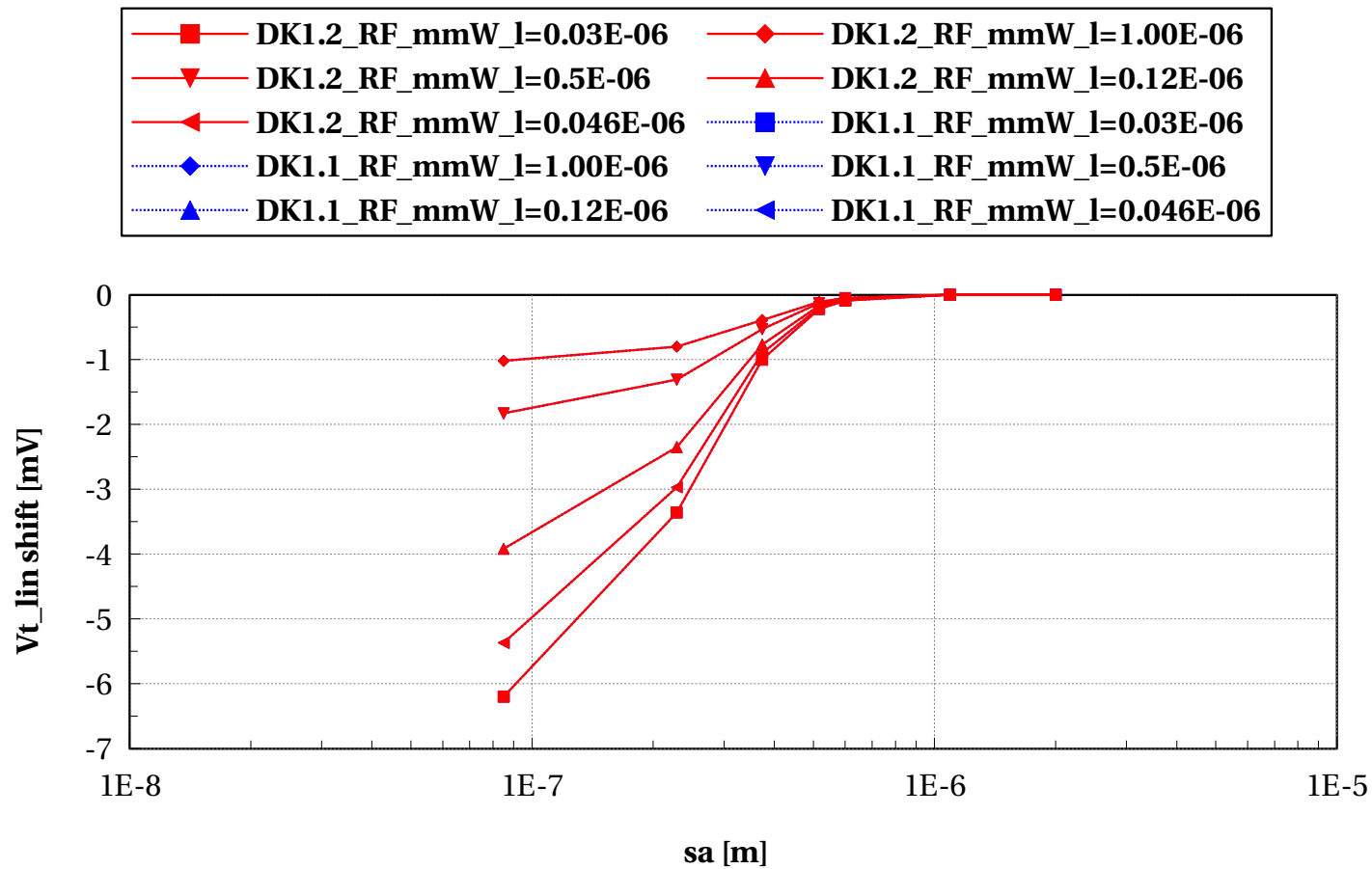
$\rho=25$ and $w=0.3e-6$ and $p_{la}=0$ and ($L=0.030e-6$ or $L=0.046e-6$ or $L=0.12e-6$ or $L=0.5e-6$ or $L=1$)



LOD effect (sa=sb) - Lscaling at $W=0.1\text{e-}6$

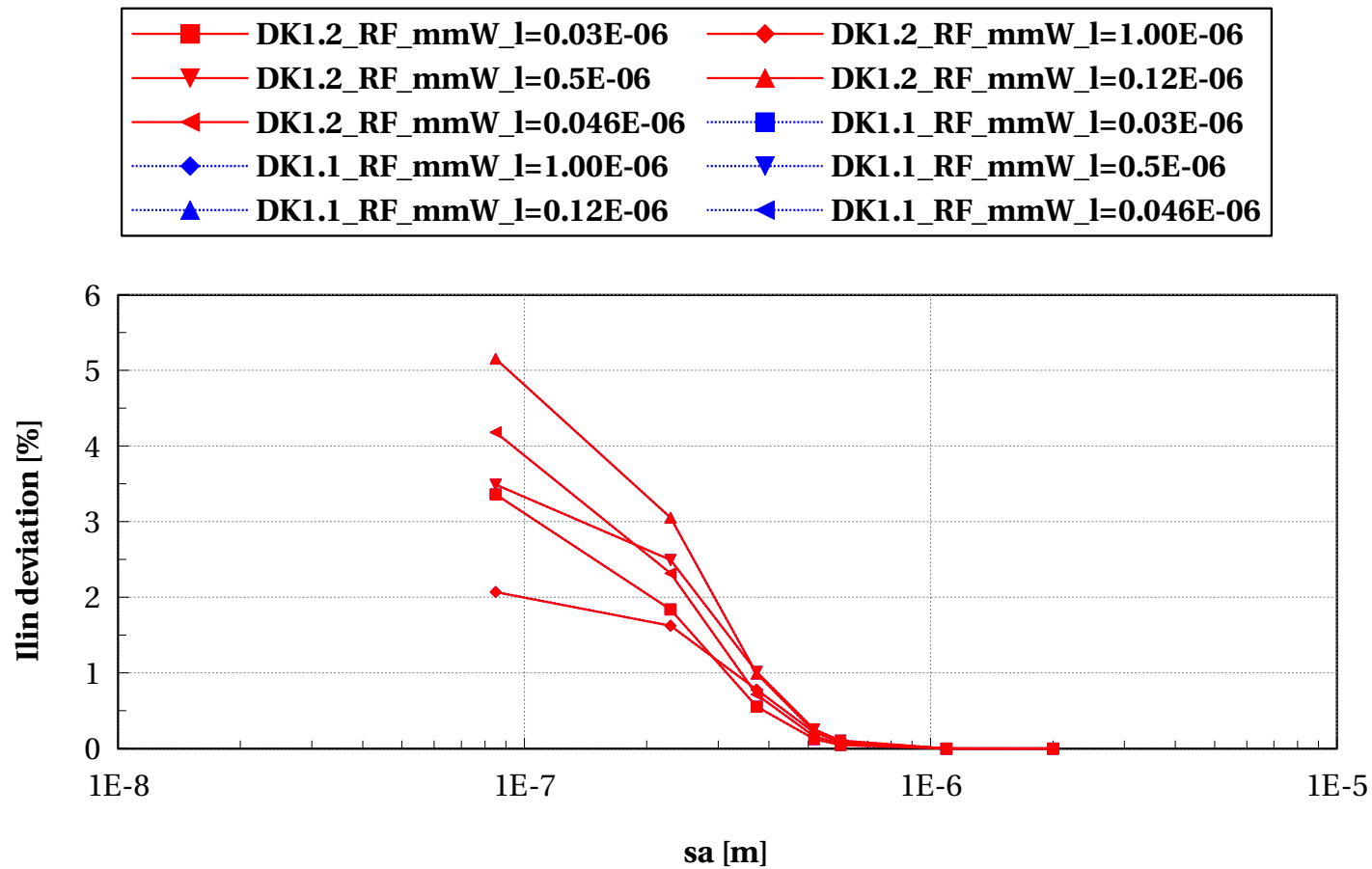
lvtnfet_acc, Vt_lin shift [mV] vs sa [m]

$\rho==25$ and $w==0.1e-6$ and $p_la==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



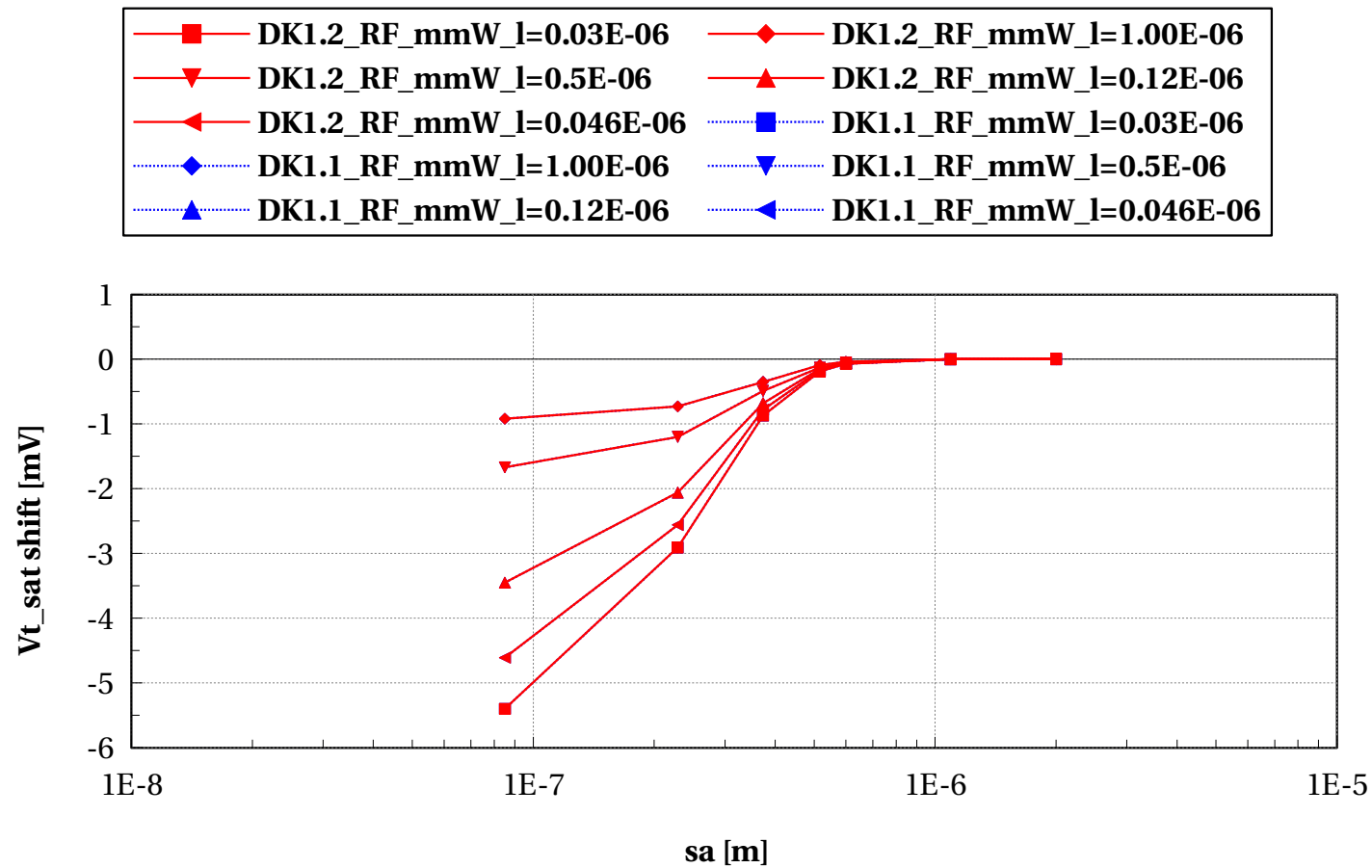
lvtnfet_acc, Ilin deviation [%] vs sa [m]

$\rho==25$ and $w==0.1e-6$ and $p_{la}==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



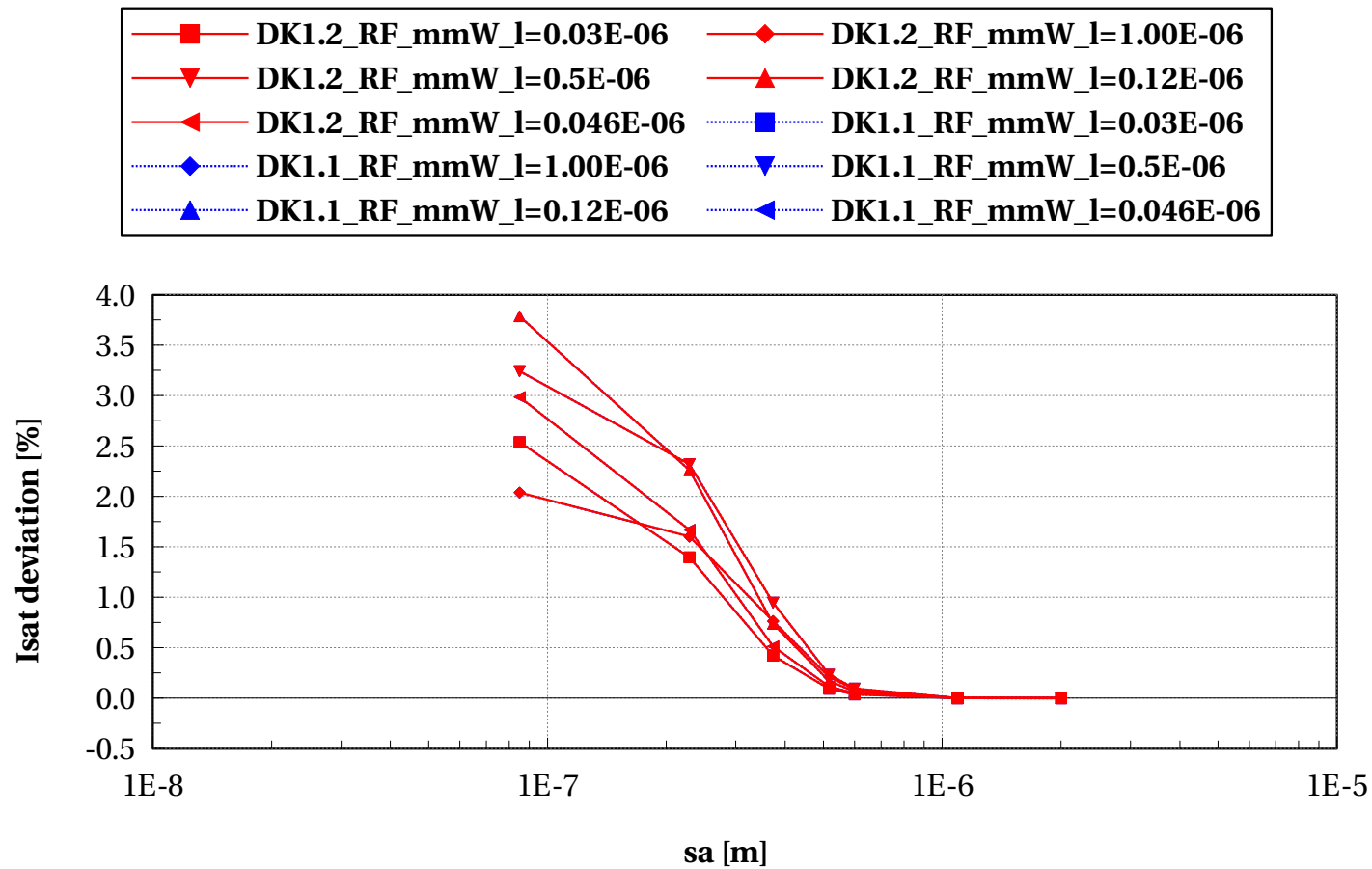
lvtnfet_acc, Vt_sat shift [mV] vs sa [m]

$\rho==25$ and $w==0.1e-6$ and $p_la==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



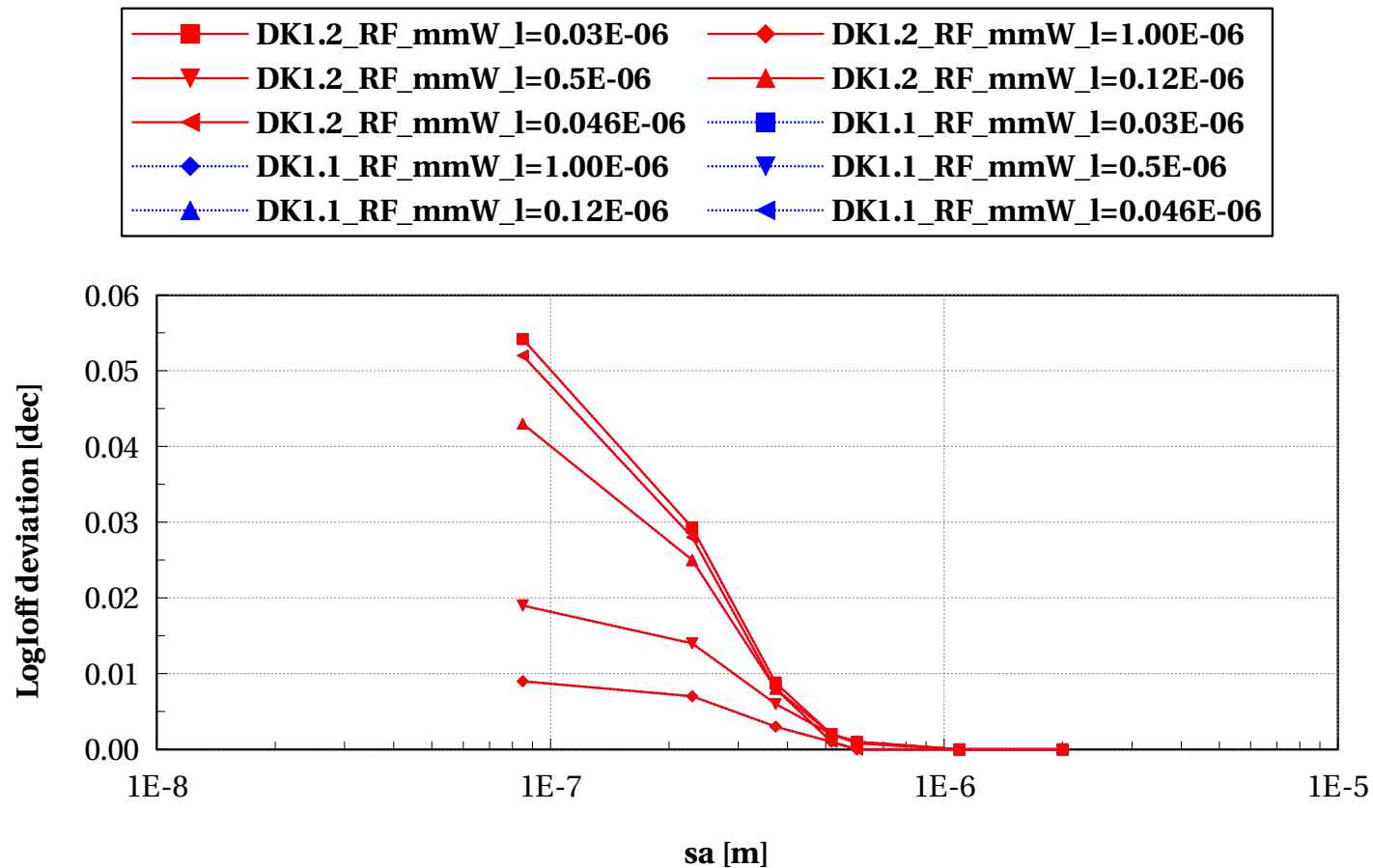
lvtnfet_acc, Isat deviation [%] vs sa [m]

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lvtnfet_acc, LogIoff deviation [dec] vs sa [m]

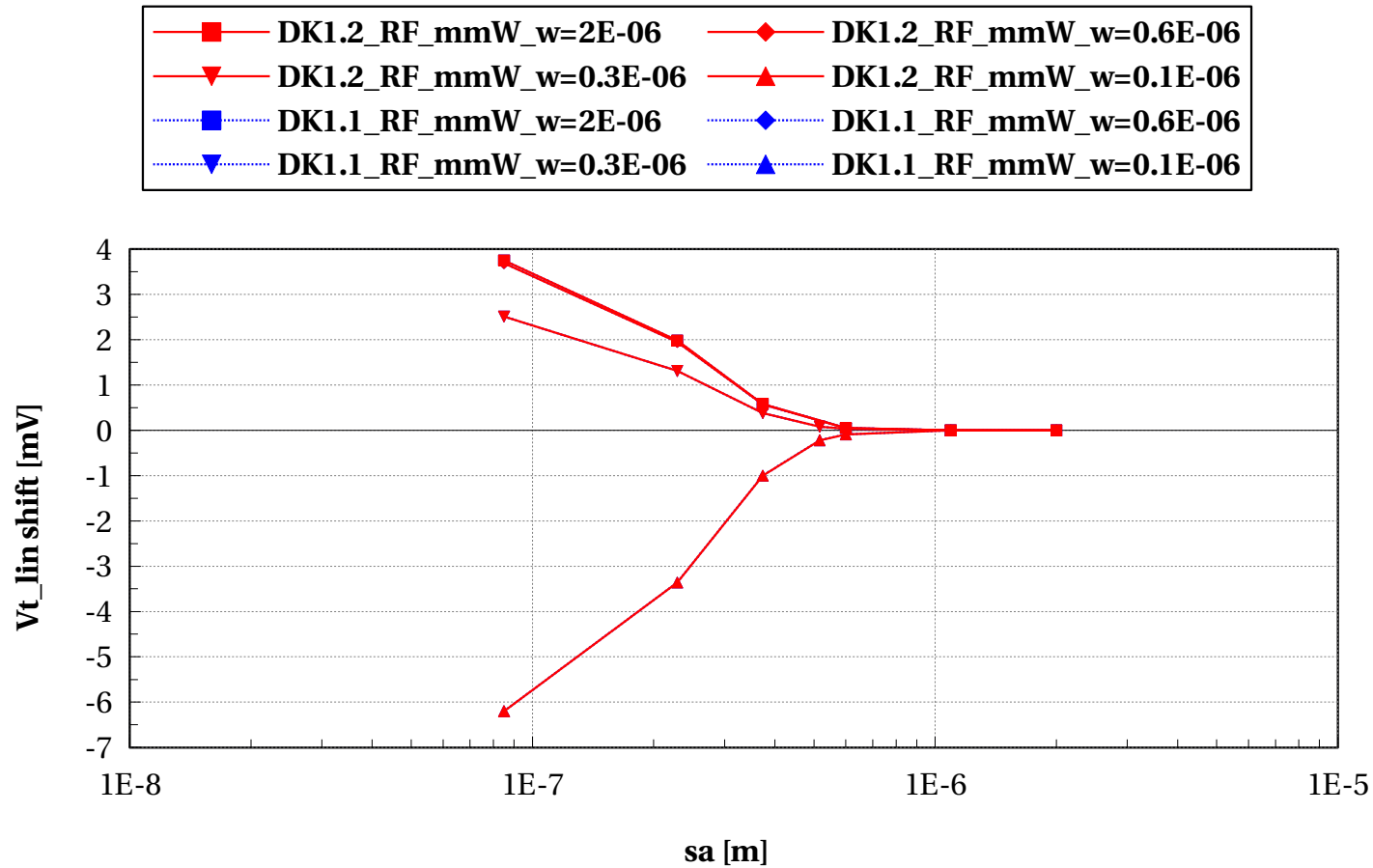
$\rho==25$ and $w==0.1e-6$ and $p_{la}==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



LOD effect (sa=sb) - Wscaling at L=0.03e-6

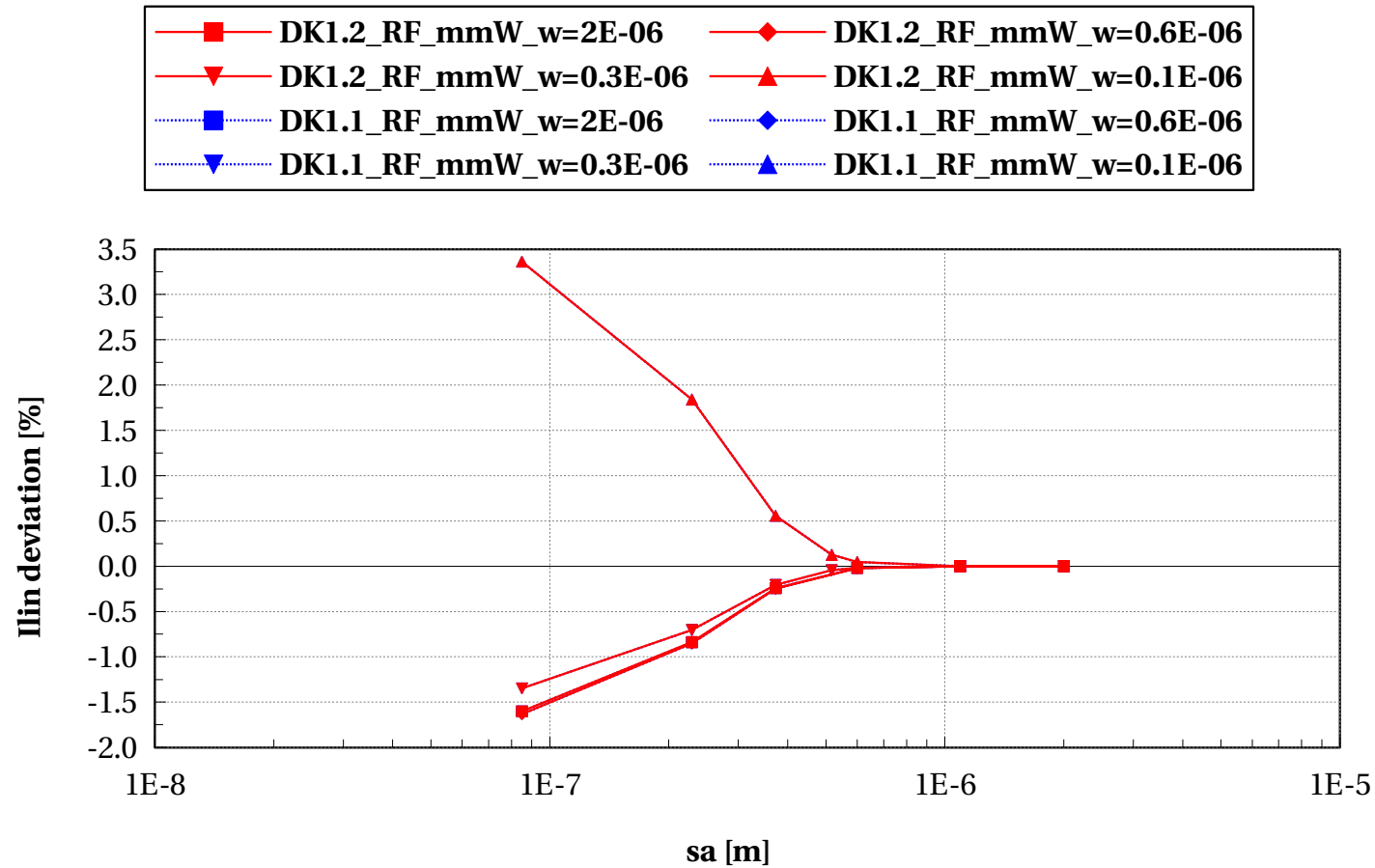
lvtnfet_acc, Vt_lin shift [mV] vs sa [m]

temp==25 and l==0.03e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



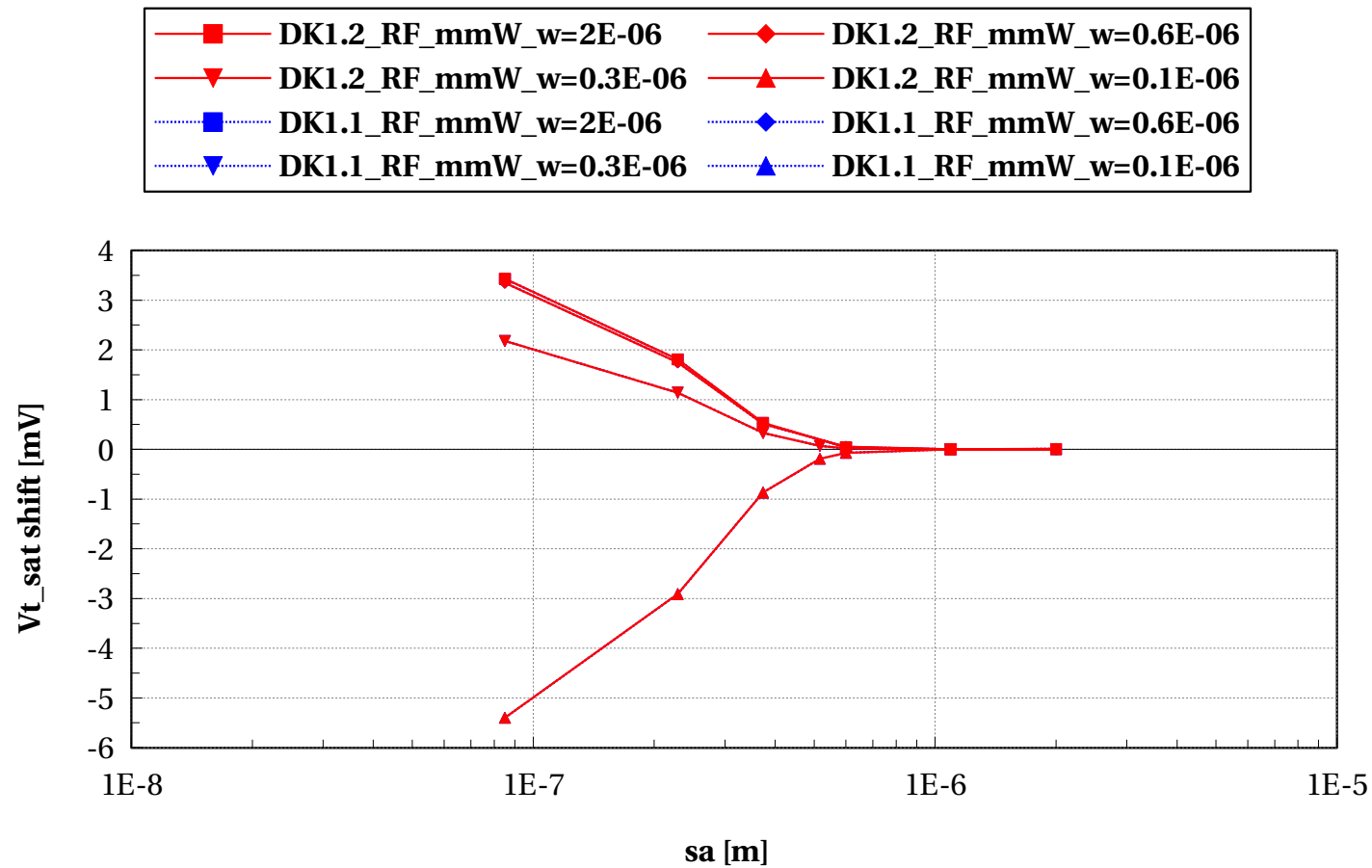
lvtnfet_acc, Ilin deviation [%] vs sa [m]

temp==25 and l==0.03e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



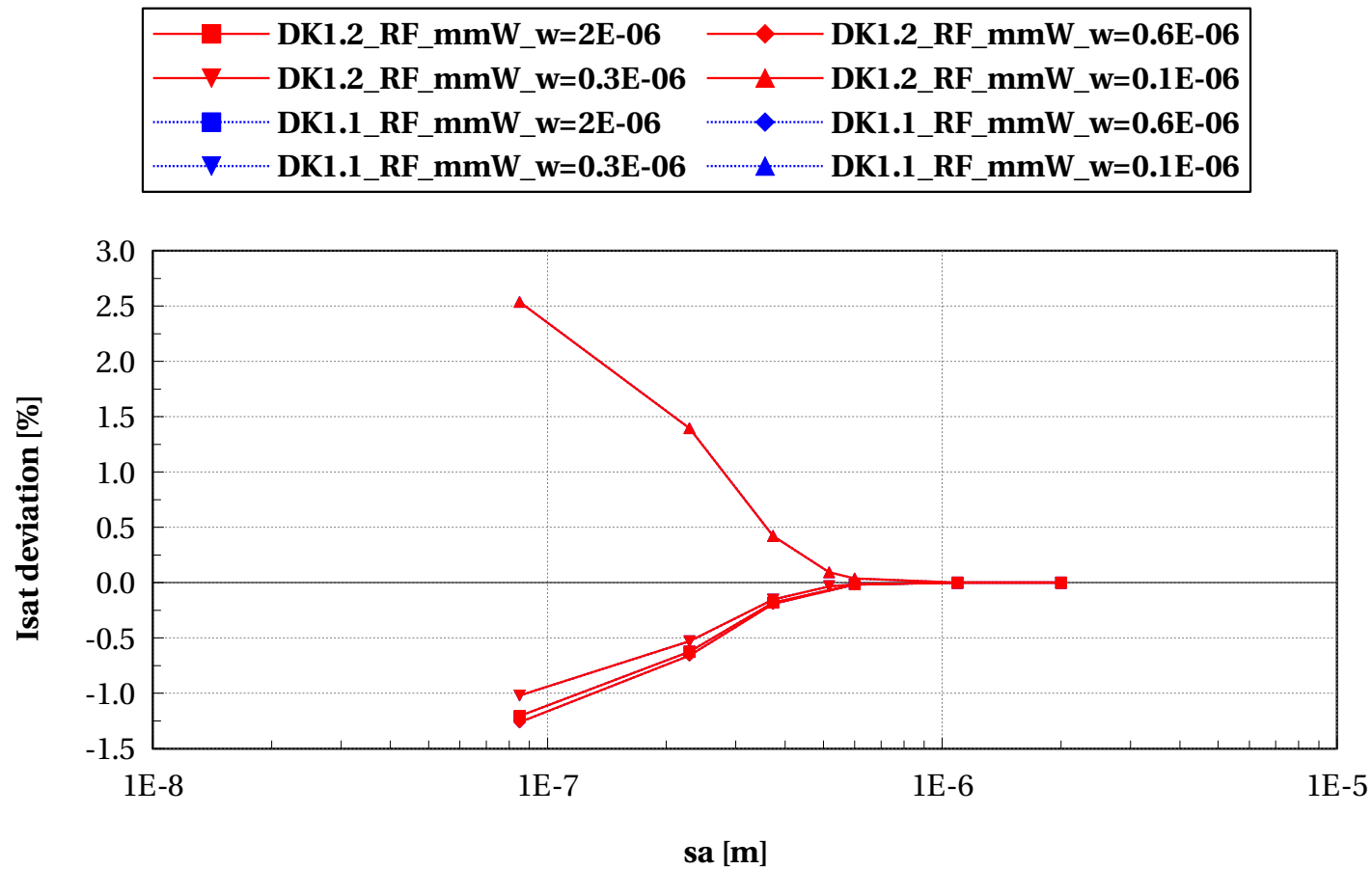
lvtnfet_acc, Vt_sat shift [mV] vs sa [m]

temp==25 and l==0.03e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



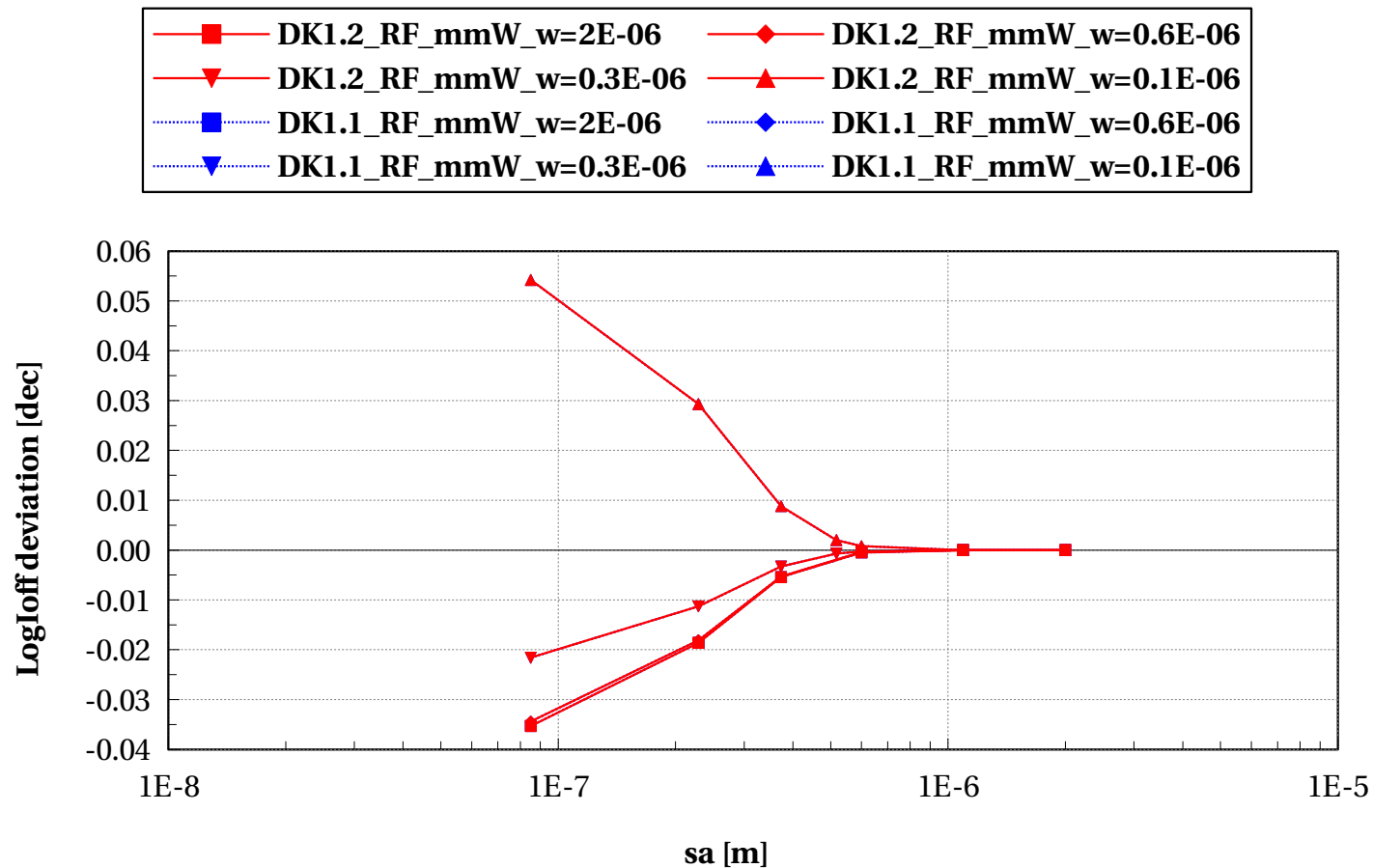
lvtnfet_acc, Isat deviation [%] vs sa [m]

temp==25 and l==0.03e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



lvtnfet_acc, Logloff deviation [dec] vs sa [m]

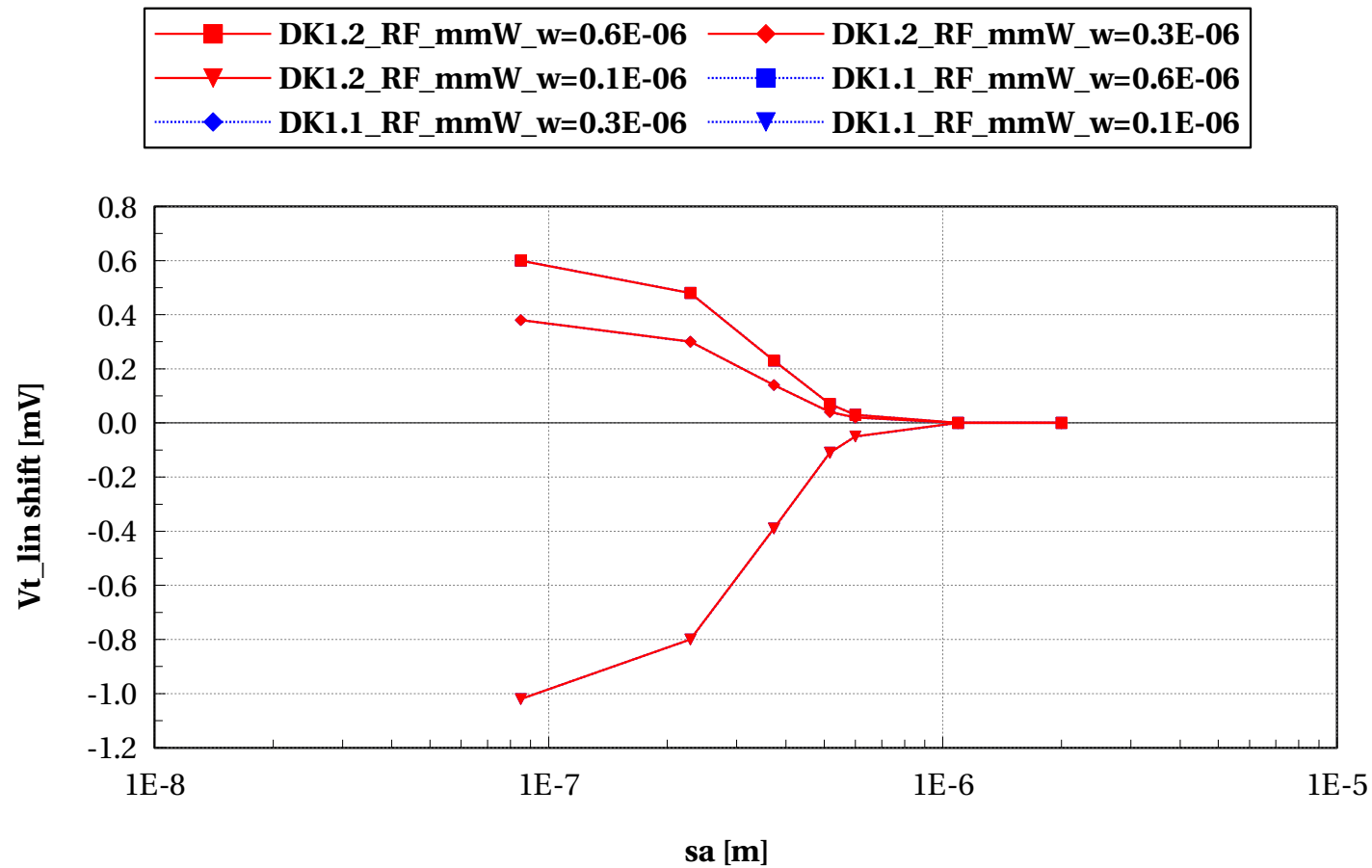
temp==25 and l==0.03e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



LOD effect (sa=sb) - Wscaling at L=1e-6

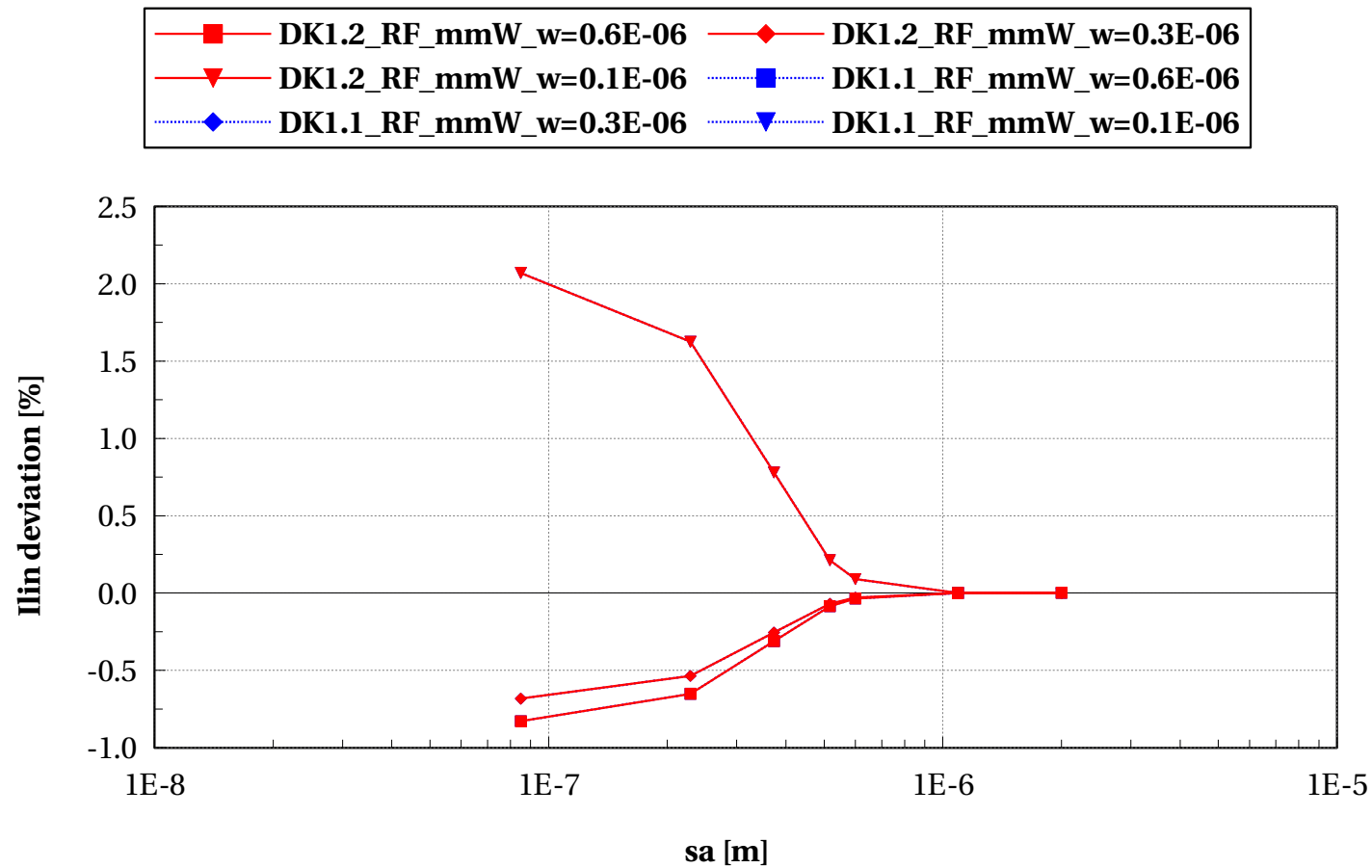
lvtnfet_acc, Vt_lin shift [mV] vs sa [m]

temp==25 and l==1.0e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



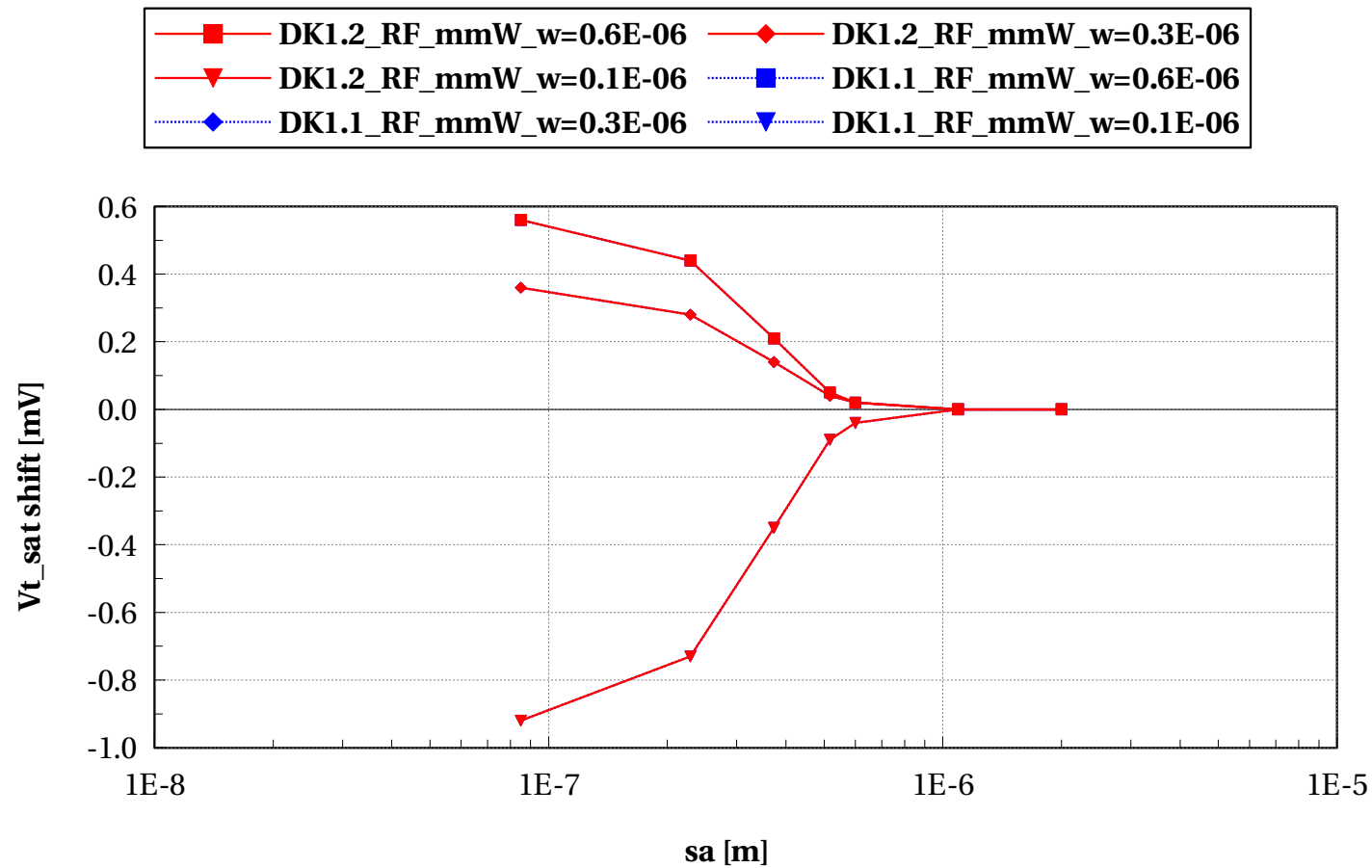
lvtnfet_acc, Ilin deviation [%] vs sa [m]

temp==25 and l==1.0e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



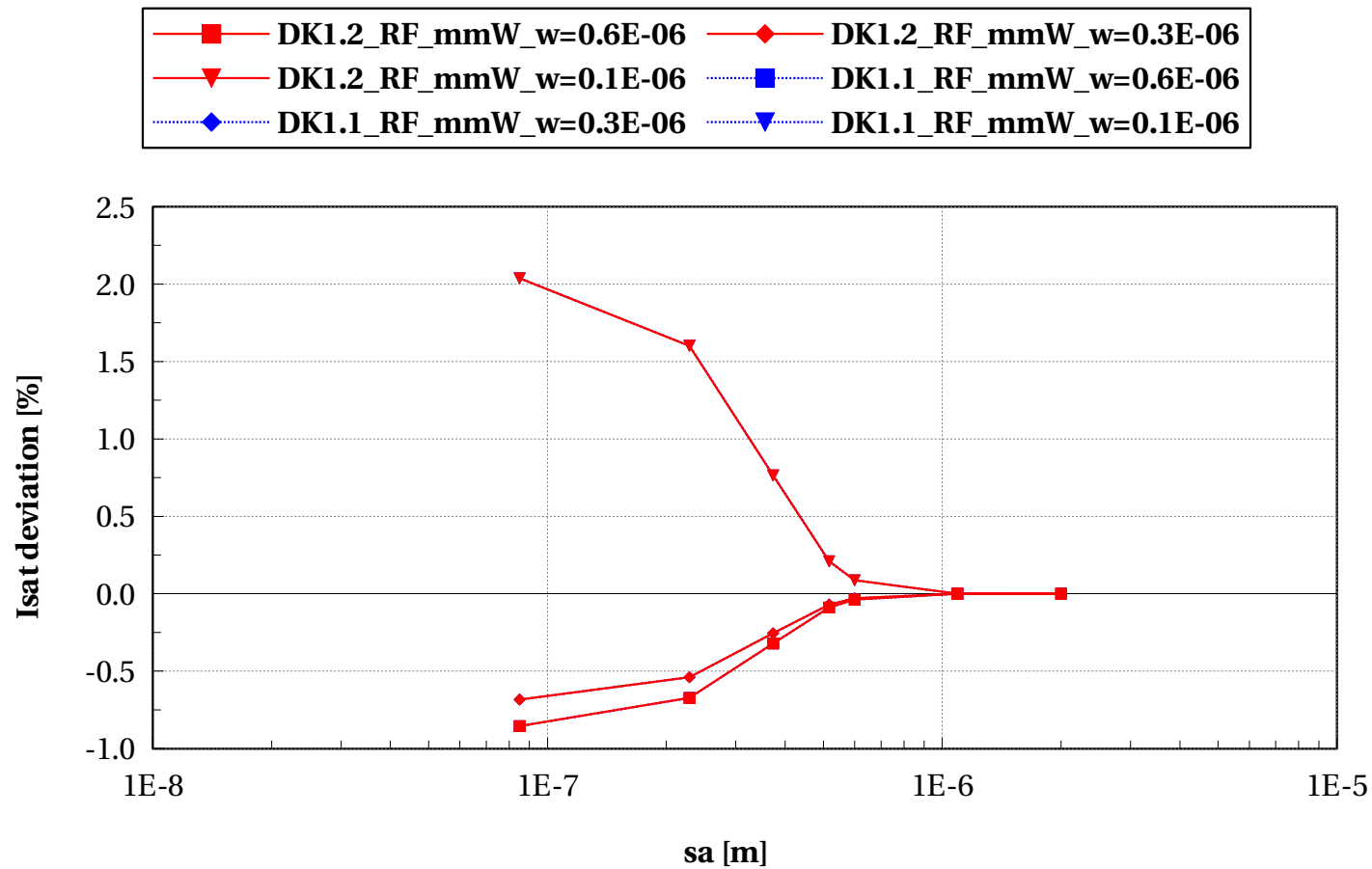
lvtnfet_acc, Vt_sat shift [mV] vs sa [m]

temp==25 and l==1.0e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



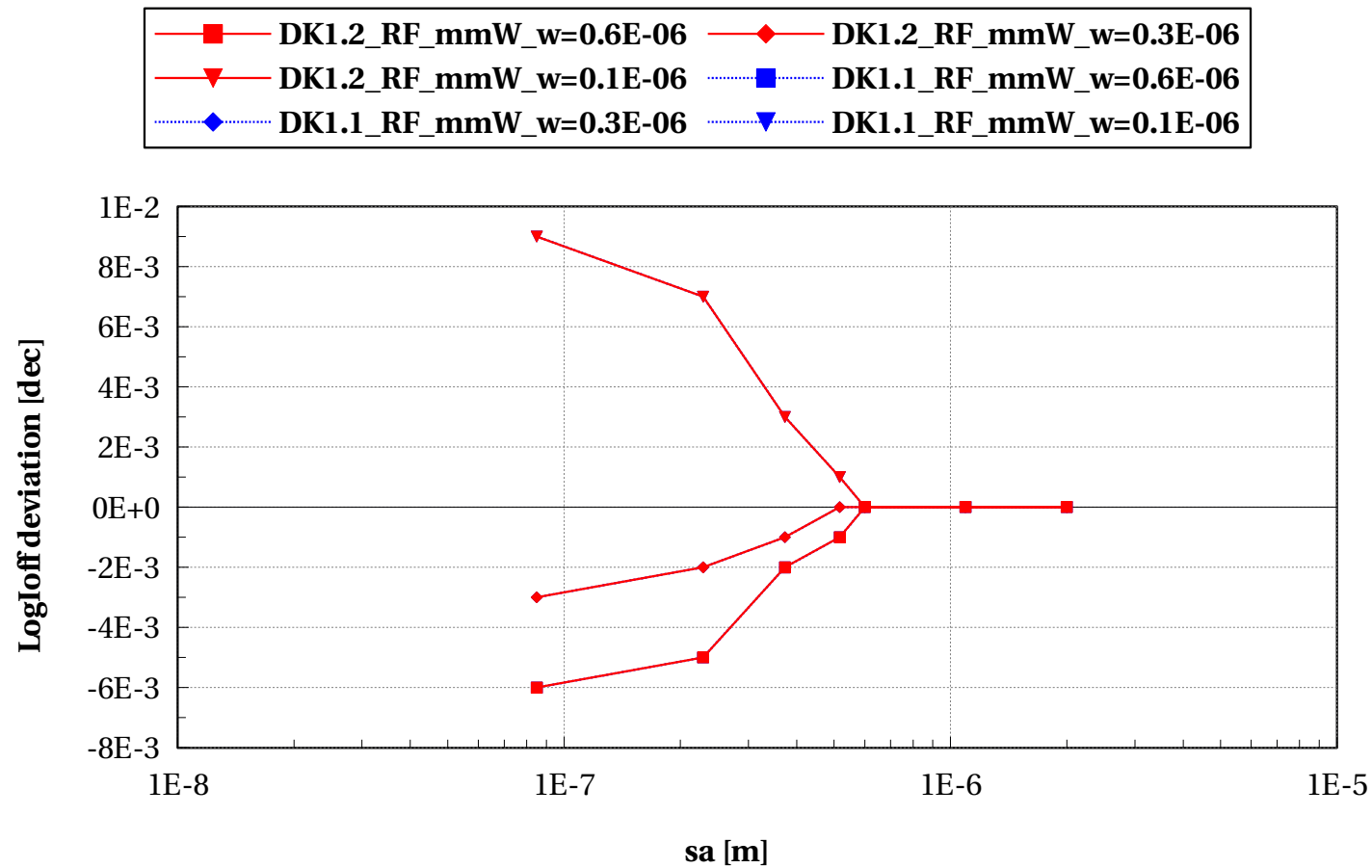
lvtnfet_acc, Isat deviation [%] vs sa [m]

temp==25 and l==1.0e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



lvtnfet_acc, LogIoff deviation [dec] vs sa [m]

temp==25 and l==1.0e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



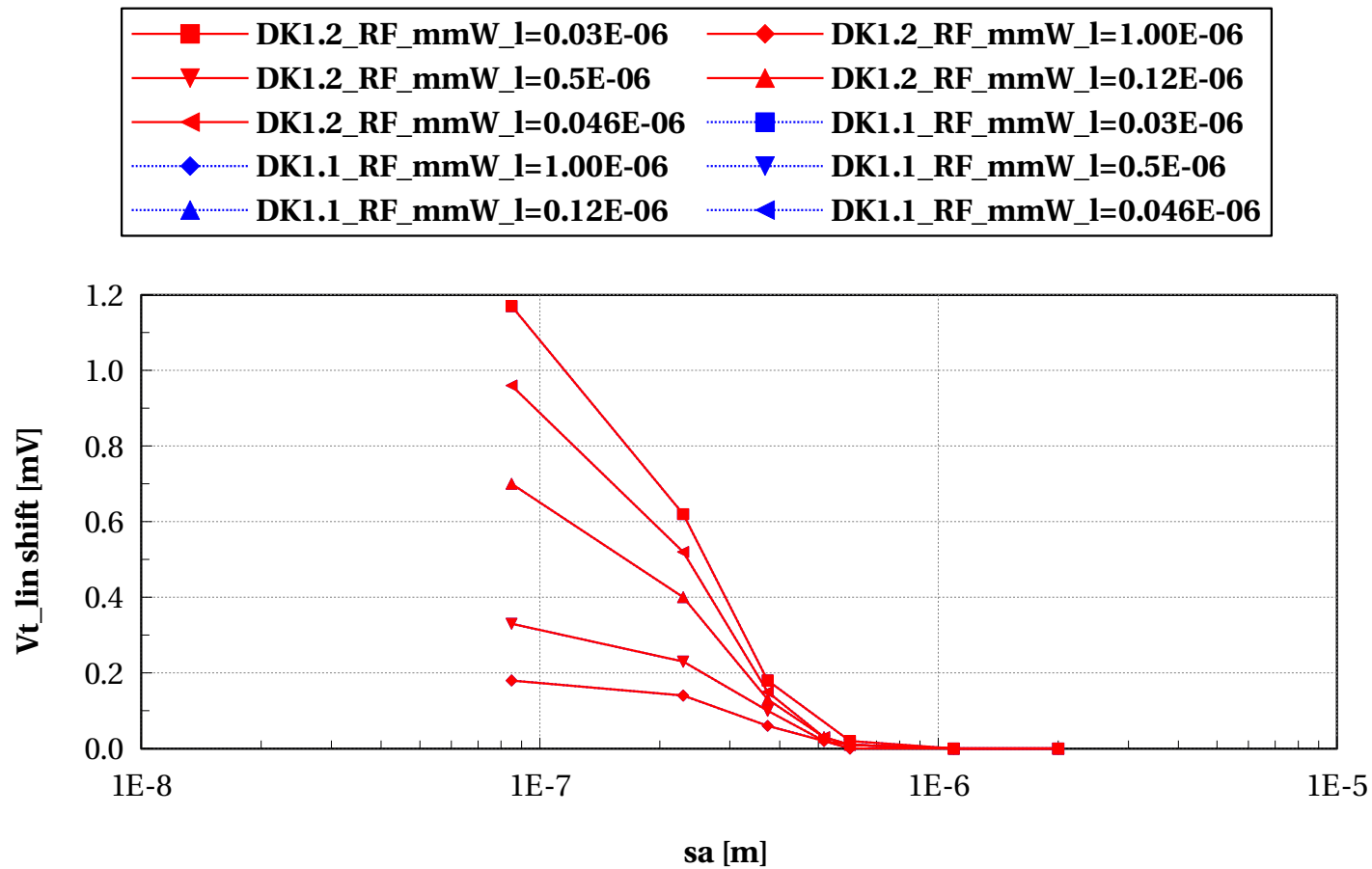
lvtpfet_acc

Electrical characteristics scaling

LOD effect (sa=sb) - Lscaling at $W=1e-6$

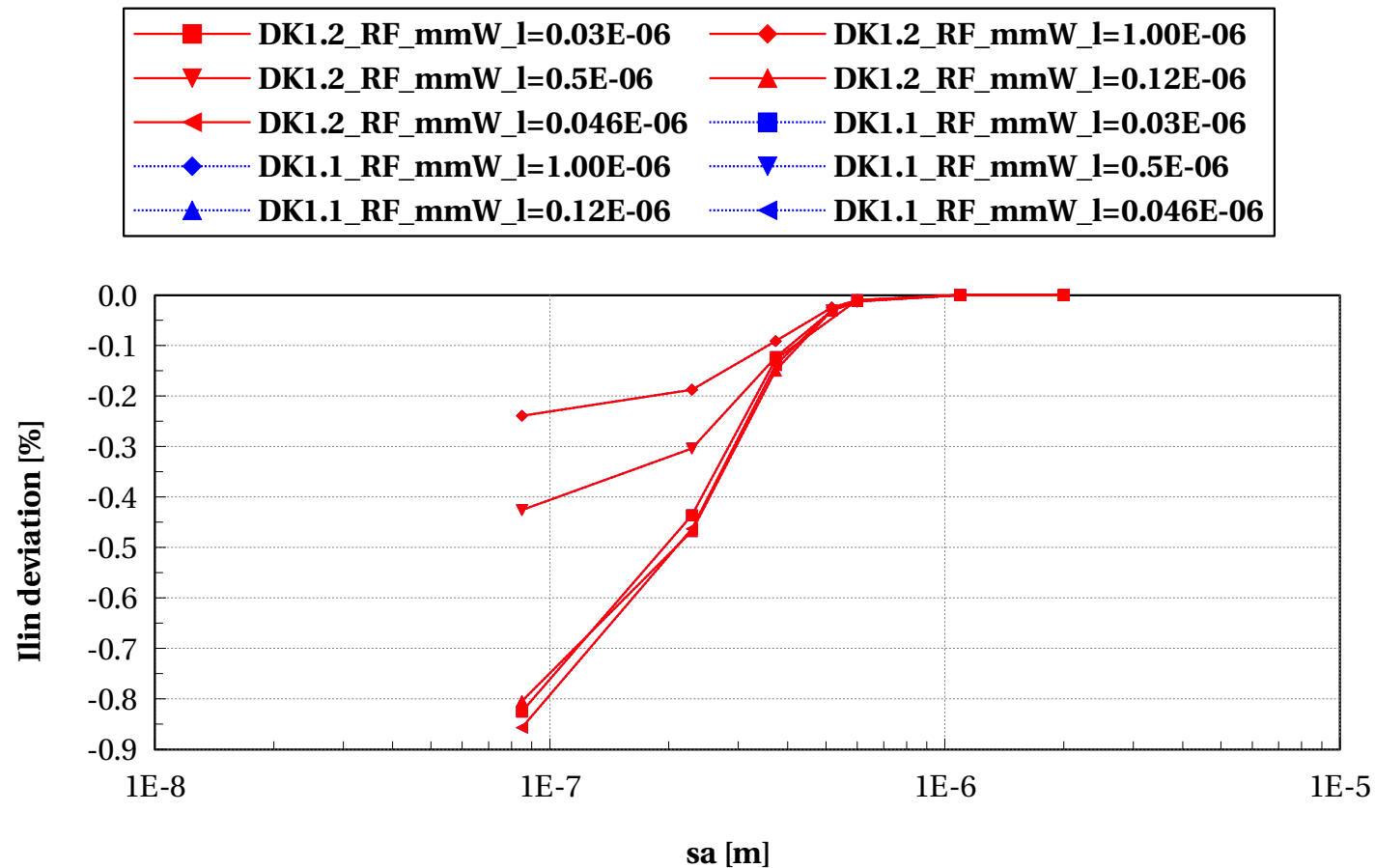
lvtpfet_acc, Vt_lin shift [mV] vs sa [m]

ip==25 and w==1e-6 and p_la==0 and (L==0.030e-6 or L==0.046e-6 or L==0.12e-6 or L==0.5e-6 or L==1e-6)



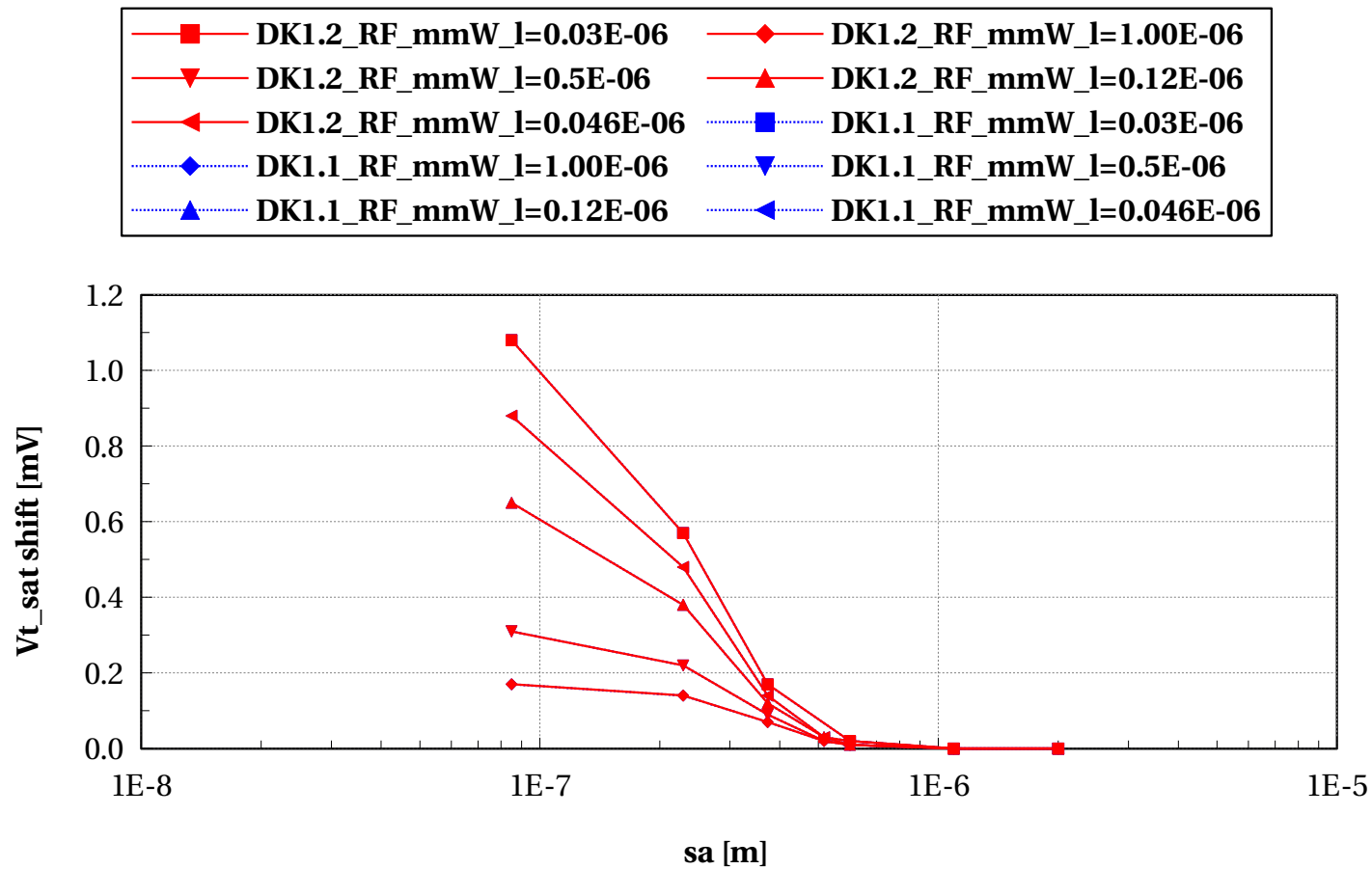
lvtpfet_acc, Ilin deviation [%] vs sa [m]

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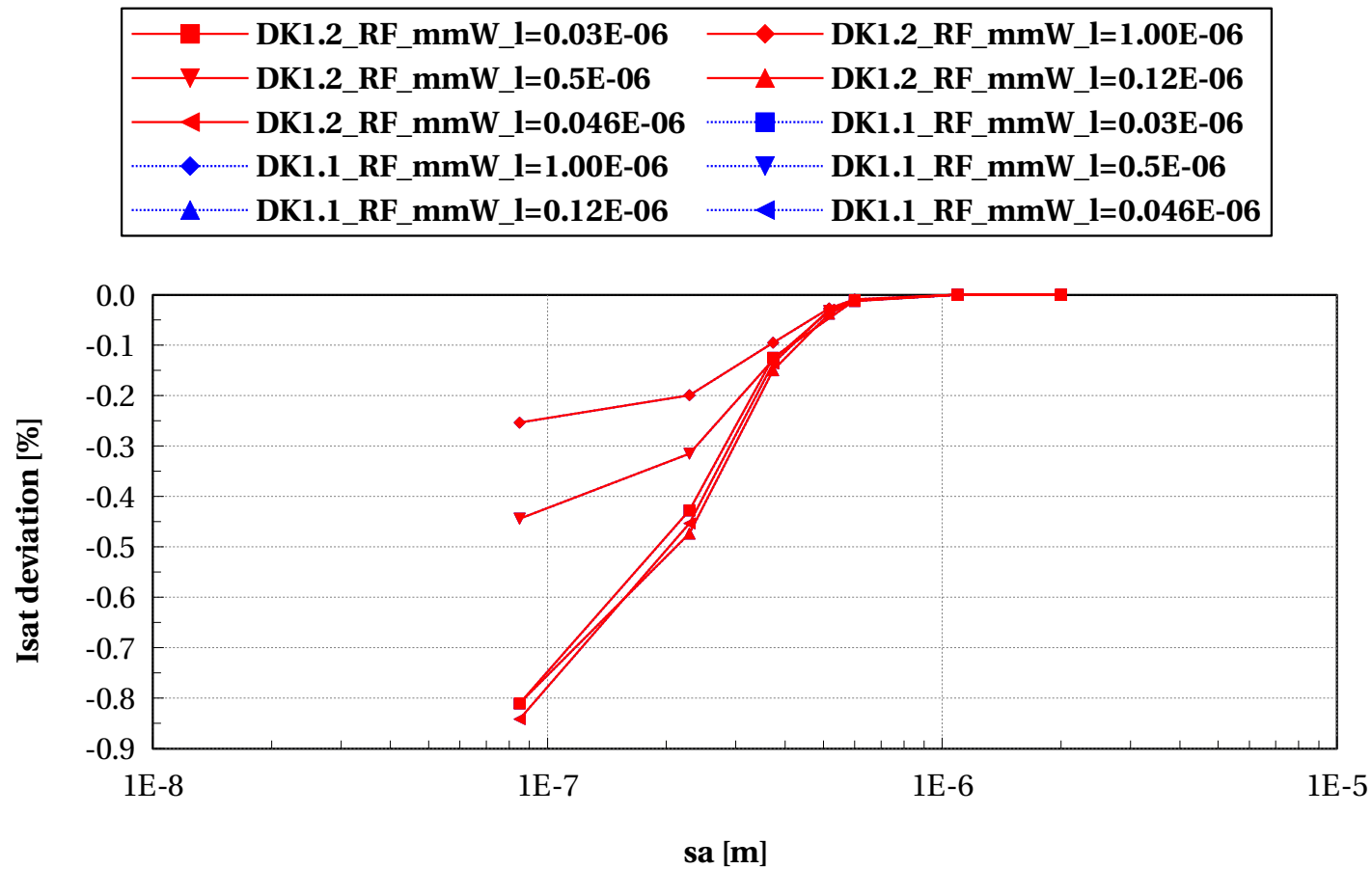
lvtpfet_acc, Vt_sat shift [mV] vs sa [m]

ip==25 and w==1e-6 and p_la==0 and (L==0.030e-6 or L==0.046e-6 or L==0.12e-6 or L==0.5e-6 or L==1e-6)



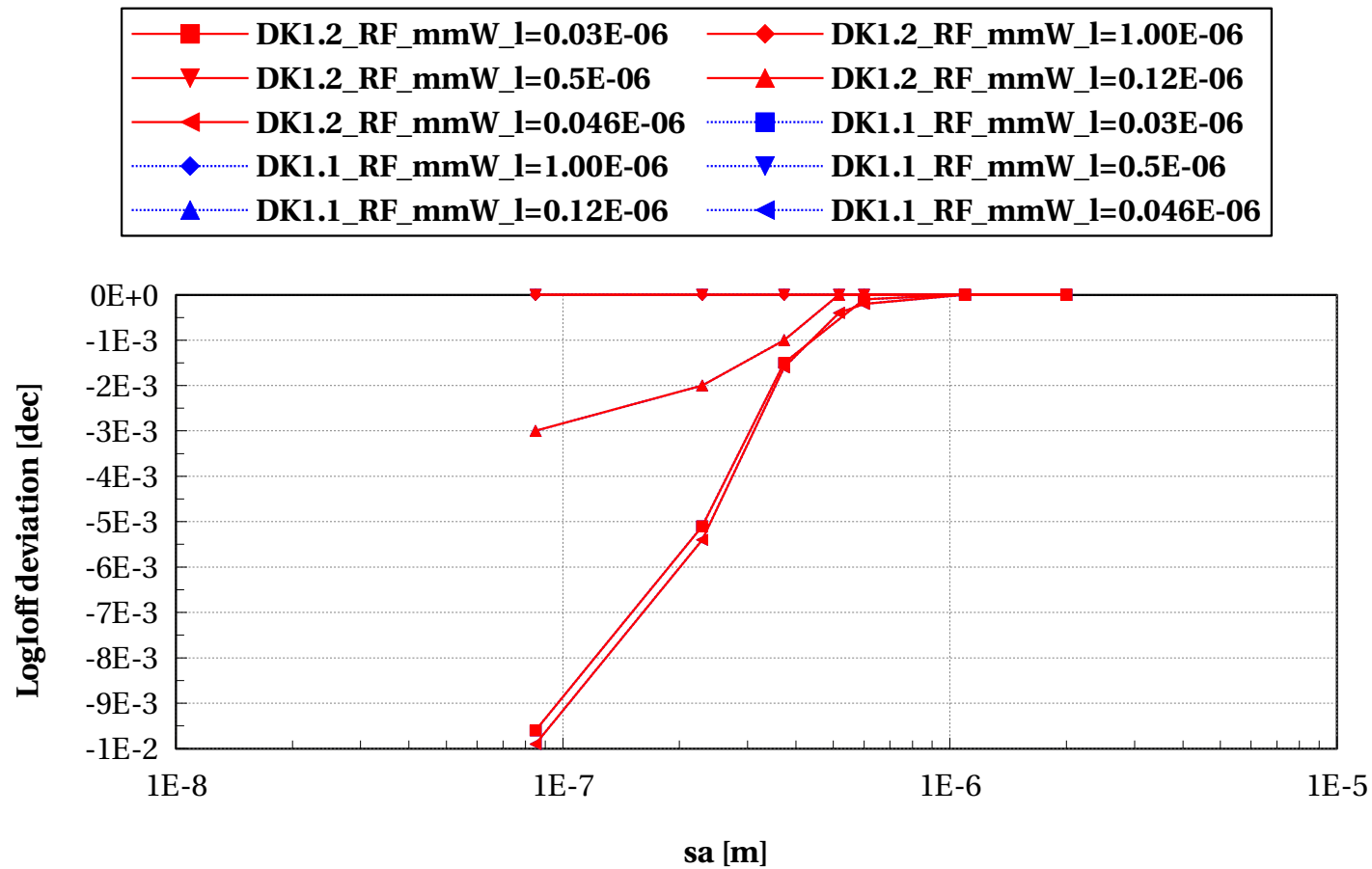
lvtpfet_acc, Isat deviation [%] vs sa [m]

ip==25 and w==1e-6 and p_la==0 and (L==0.030e-6 or L==0.046e-6 or L==0.12e-6 or L==0.5e-6 or L==1e-6)



lvtpfet_acc, LogIoff deviation [dec] vs sa [m]

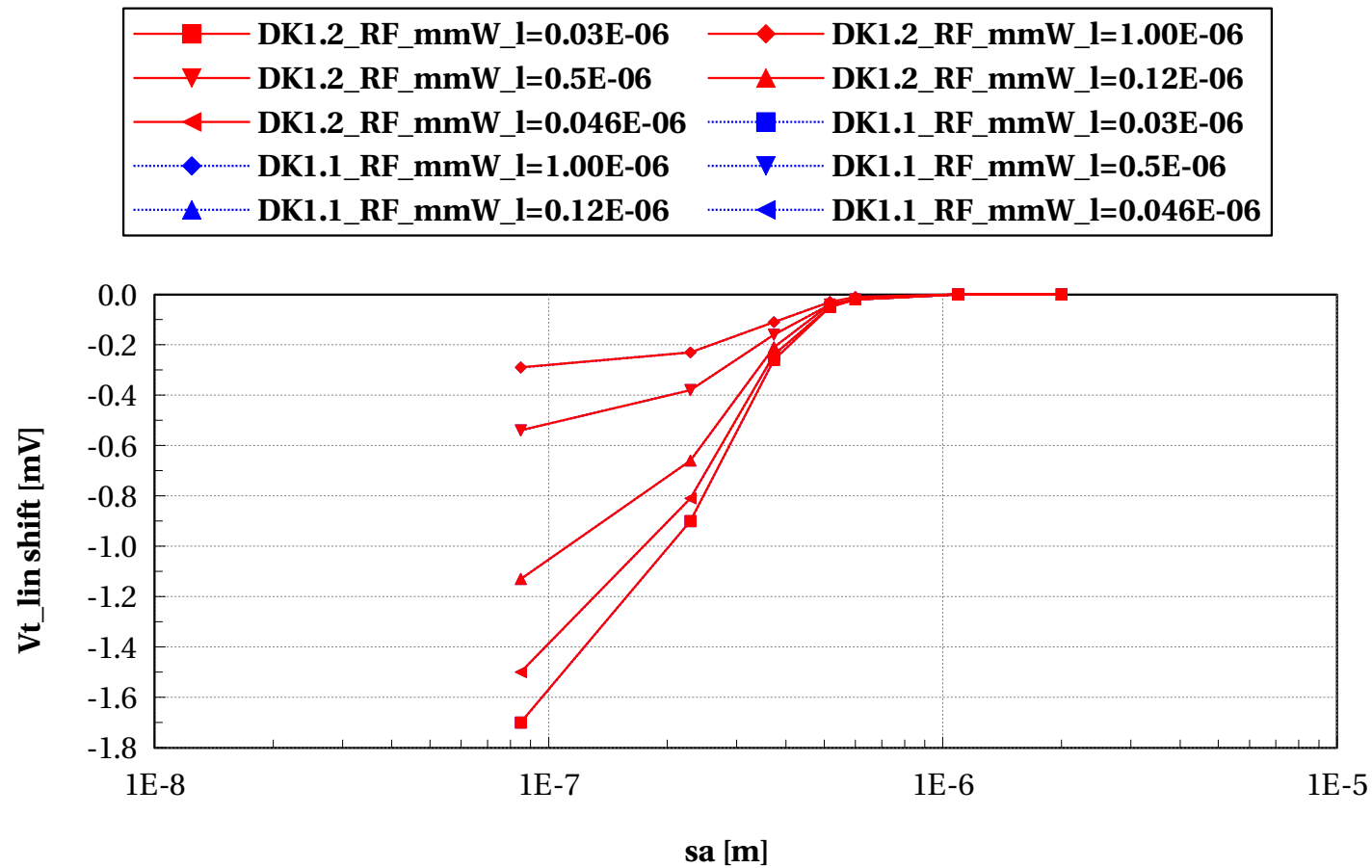
ip==25 and w==1e-6 and p_la==0 and (L==0.030e-6 or L==0.046e-6 or L==0.12e-6 or L==0.5e-6 or L==1e-6)



LOD effect (sa=sb) - Lscaling at $W=0.3e-6$

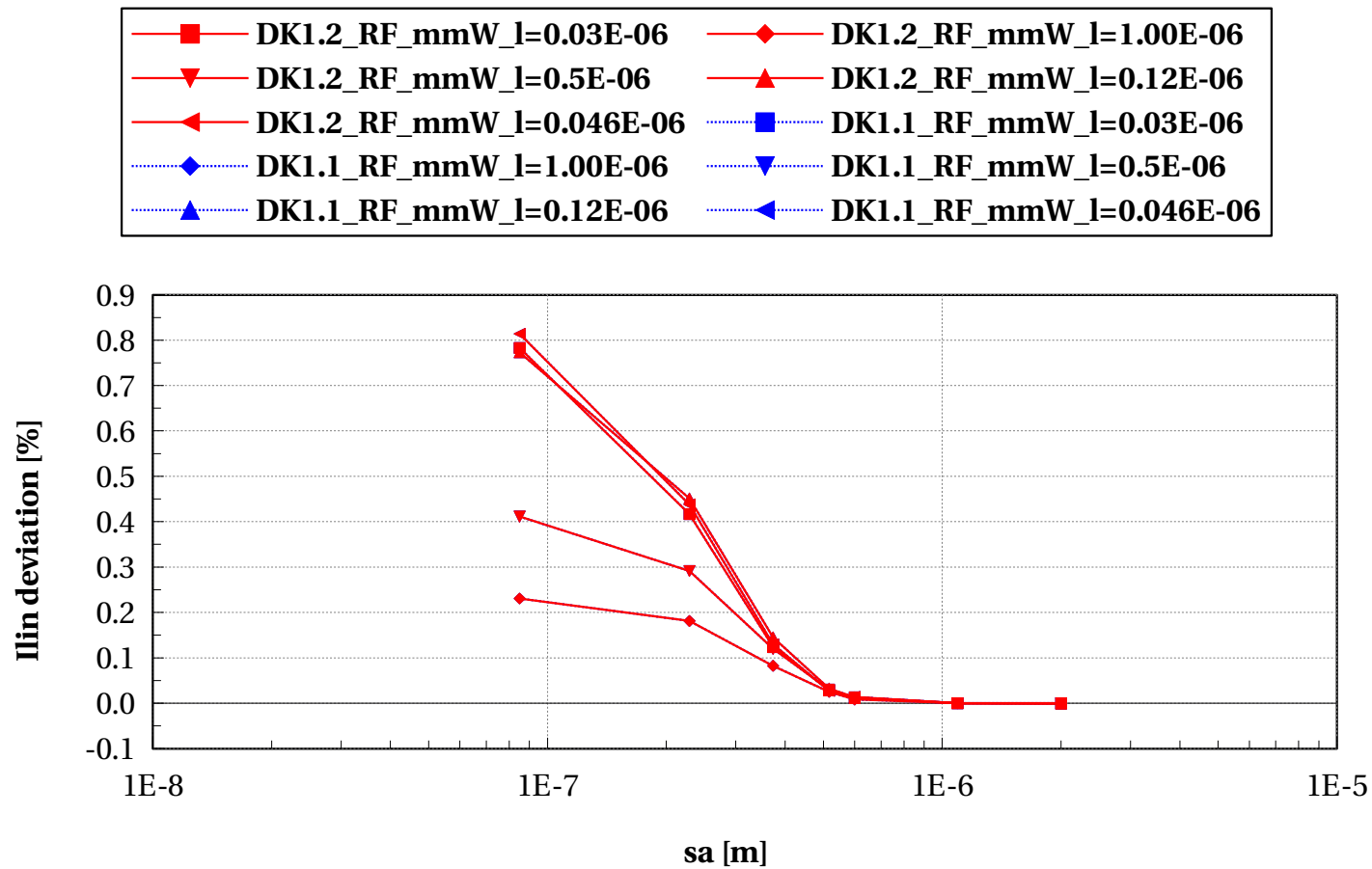
lvtpfet_acc, Vt_lin shift [mV] vs sa [m]

$\rho==25$ and $w==0.3e-6$ and $p_la==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



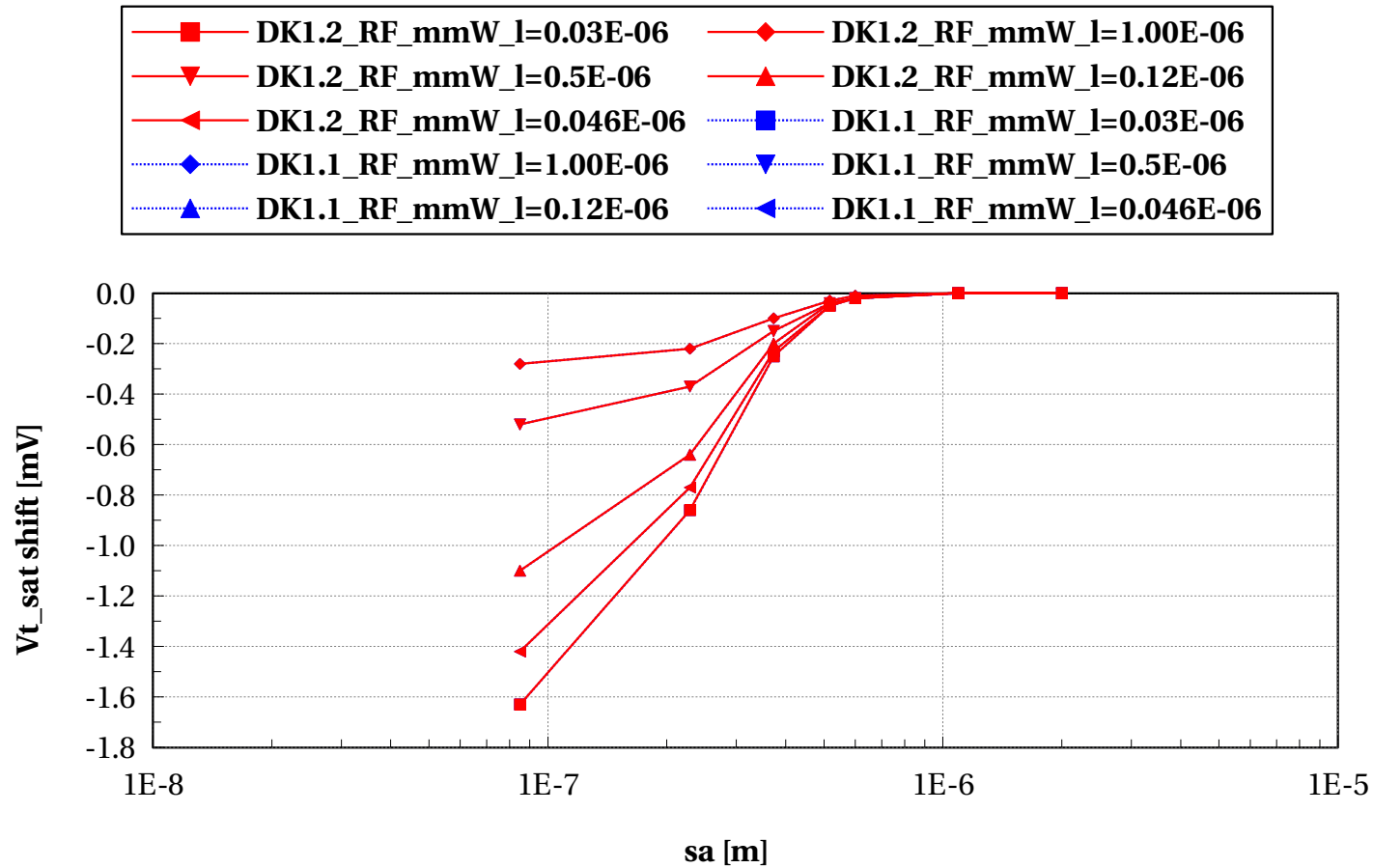
lvtpfet_acc, Ilin deviation [%] vs sa [m]

$\rho==25$ and $w==0.3e-6$ and $p_{la}==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



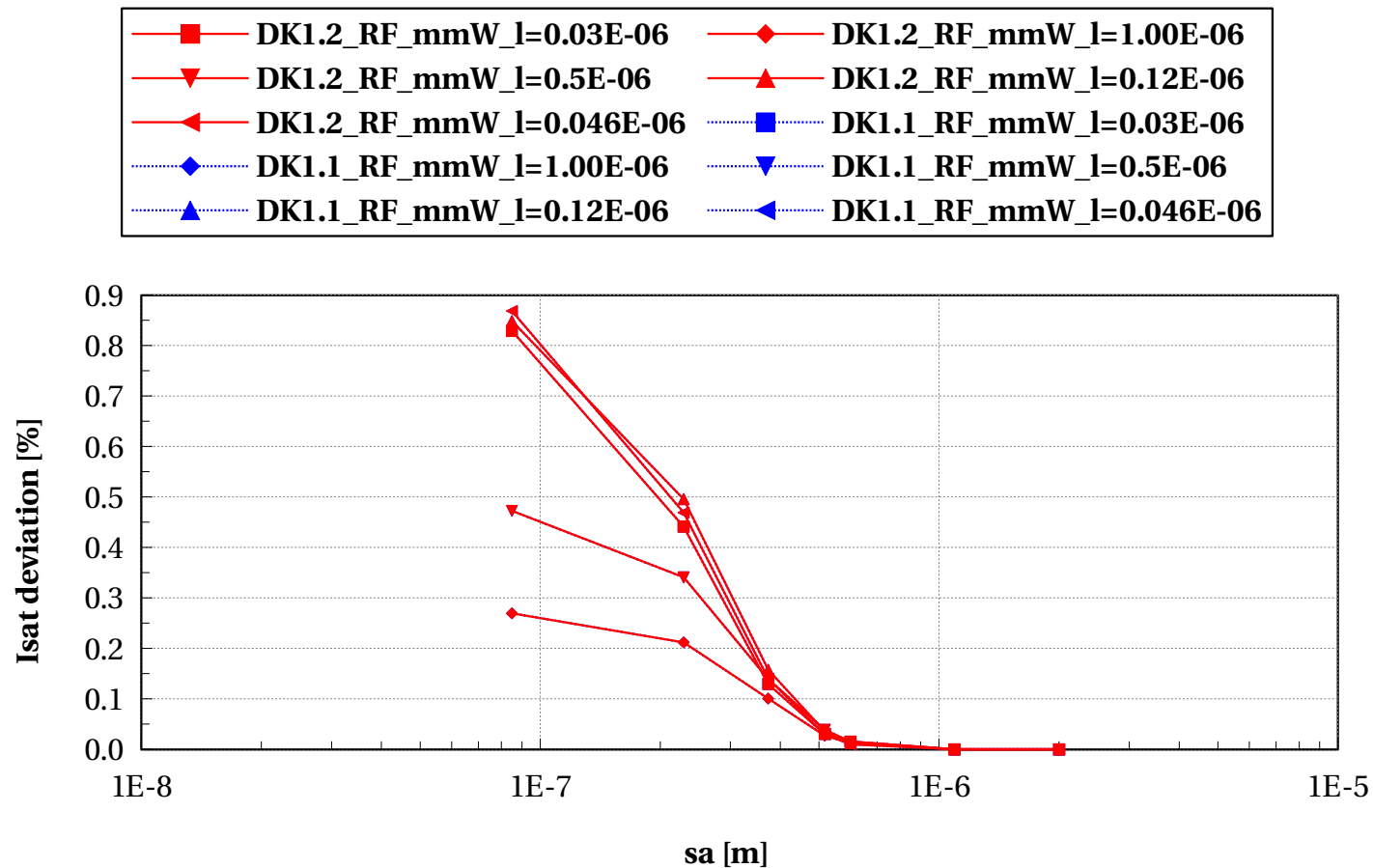
lvtpfet_acc, Vt_sat shift [mV] vs sa [m]

$\rho=25$ and $w=0.3e-6$ and $p_{la}=0$ and ($L=0.030e-6$ or $L=0.046e-6$ or $L=0.12e-6$ or $L=0.5e-6$ or $L=1$)



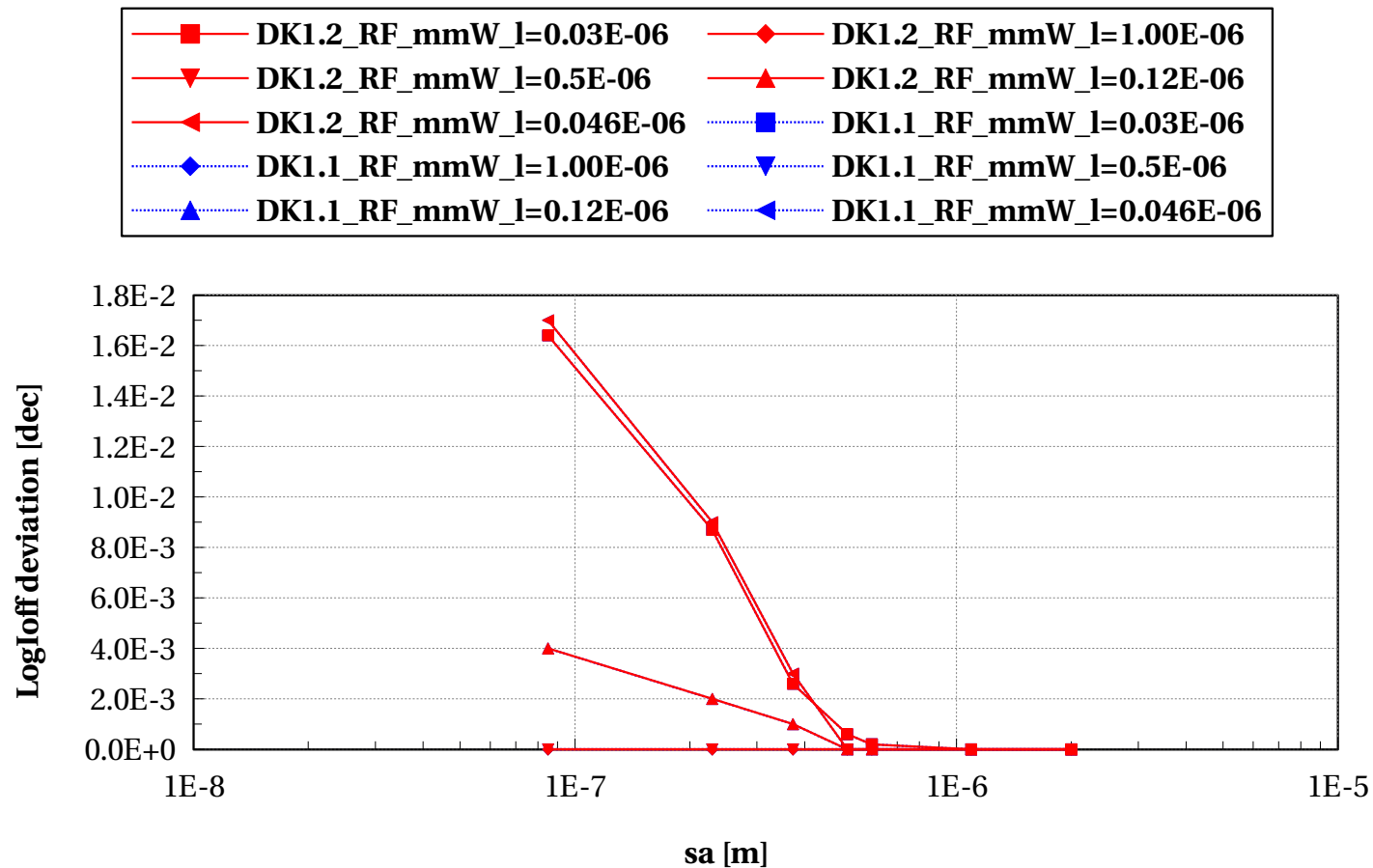
lvtpfet_acc, Isat deviation [%] vs sa [m]

$\rho==25$ and $w==0.3e-6$ and $p_{la}==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



lvtpfet_acc, LogIoff deviation [dec] vs sa [m]

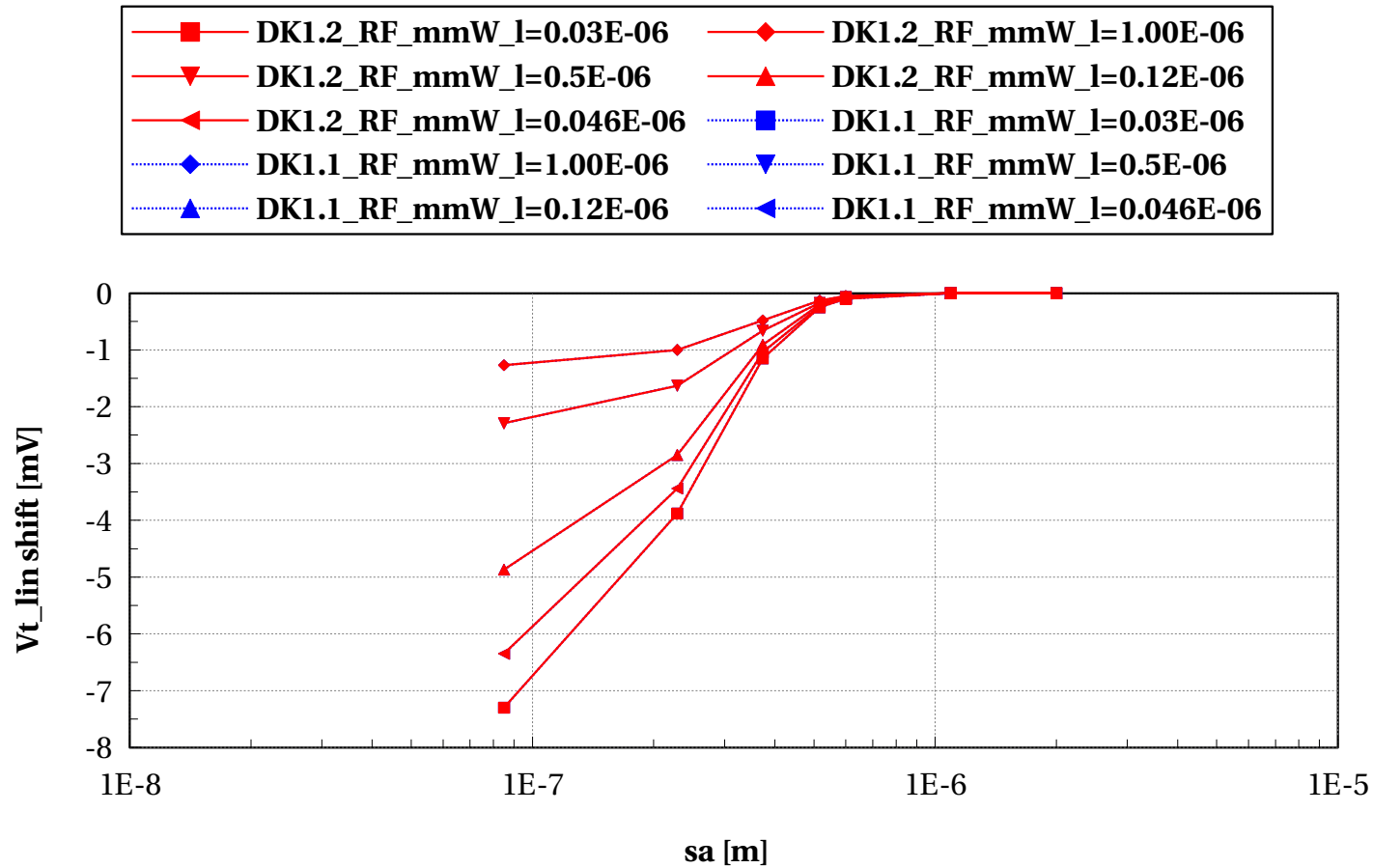
$\rho==25$ and $w==0.3e-6$ and $p_la==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



LOD effect (sa=sb) - Lscaling at $W=0.1\text{e-}6$

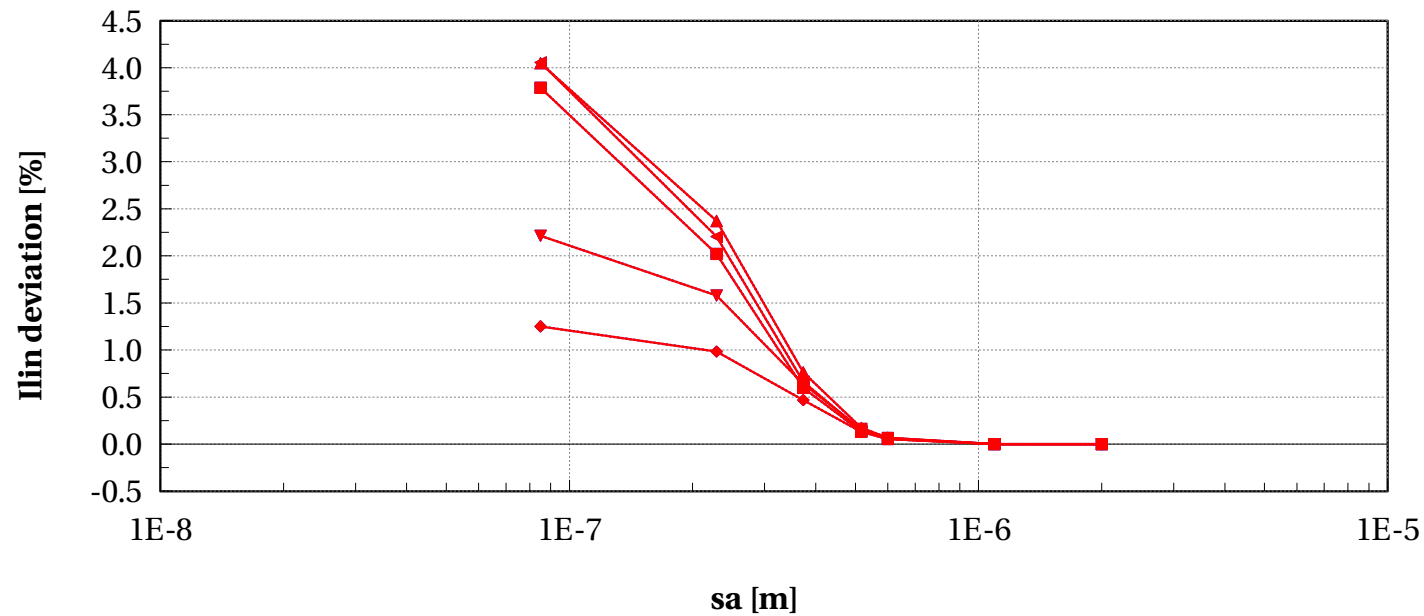
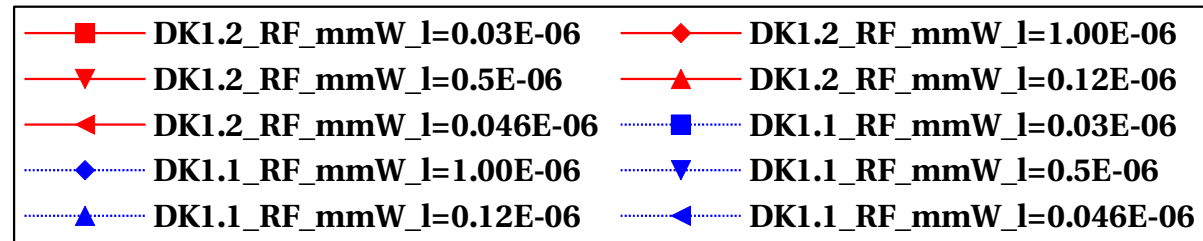
lvtpfet_acc, Vt_lin shift [mV] vs sa [m]

$\rho==25$ and $w==0.1\text{e-}6$ and $p_la==0$ and ($L==0.030\text{e-}6$ or $L==0.046\text{e-}6$ or $L==0.12\text{e-}6$ or $L==0.5\text{e-}6$ or $L==1$)



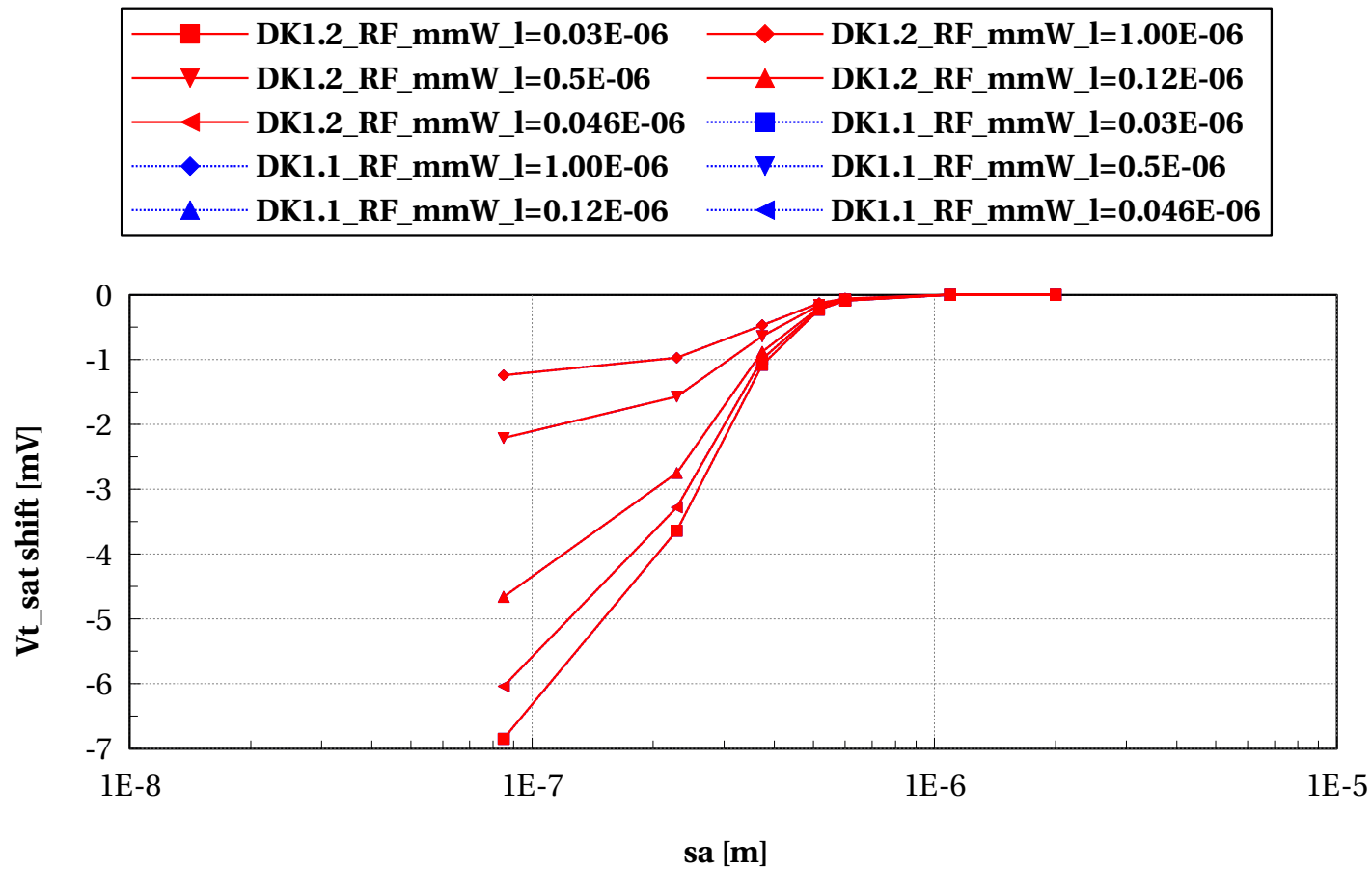
lvtpfet_acc, Ilin deviation [%] vs sa [m]

$\rho==25$ and $w==0.1e-6$ and $p_{la}==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



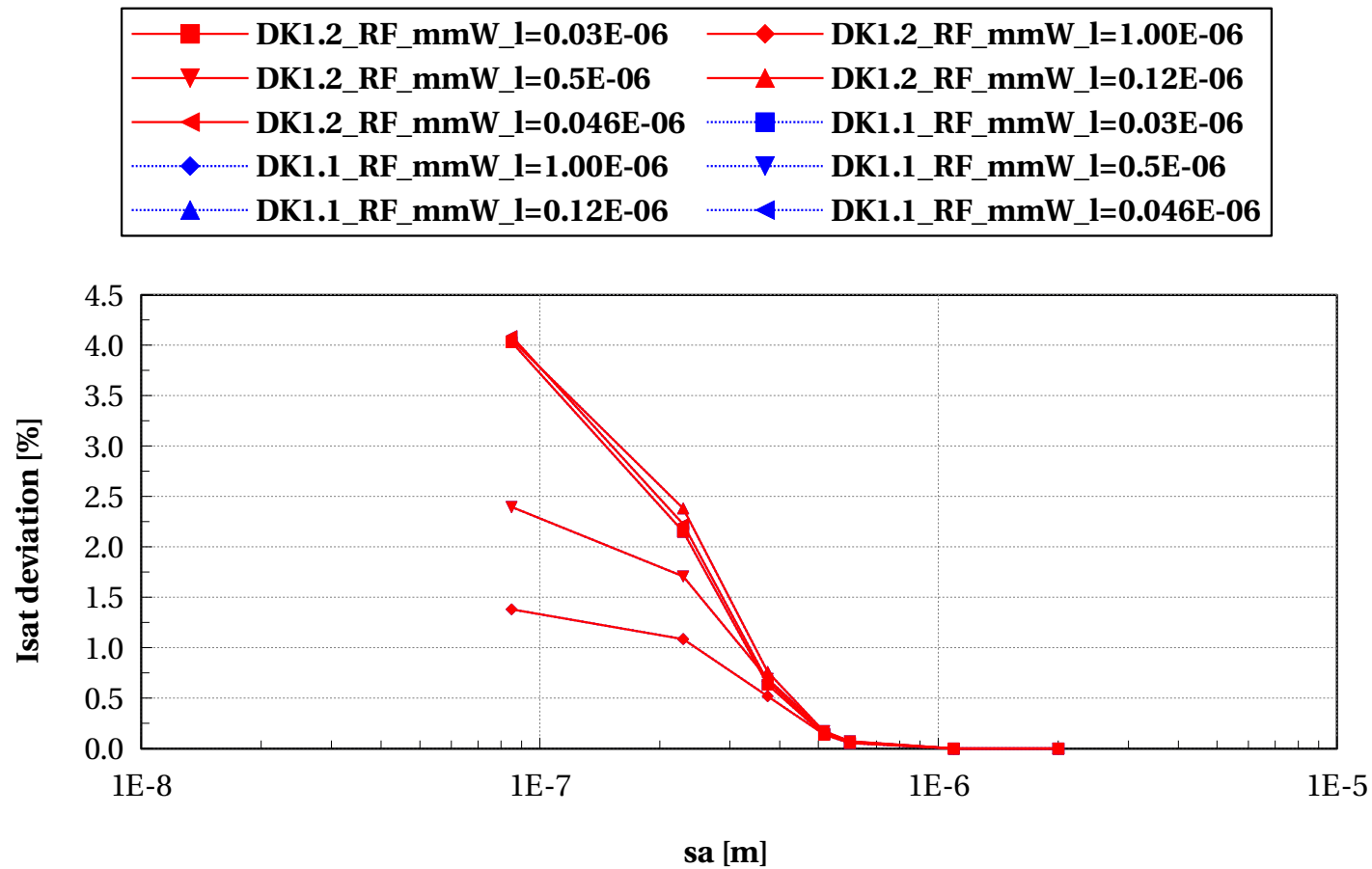
lvtpfet_acc, Vt_sat shift [mV] vs sa [m]

$\rho==25$ and $w==0.1e-6$ and $p_la==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



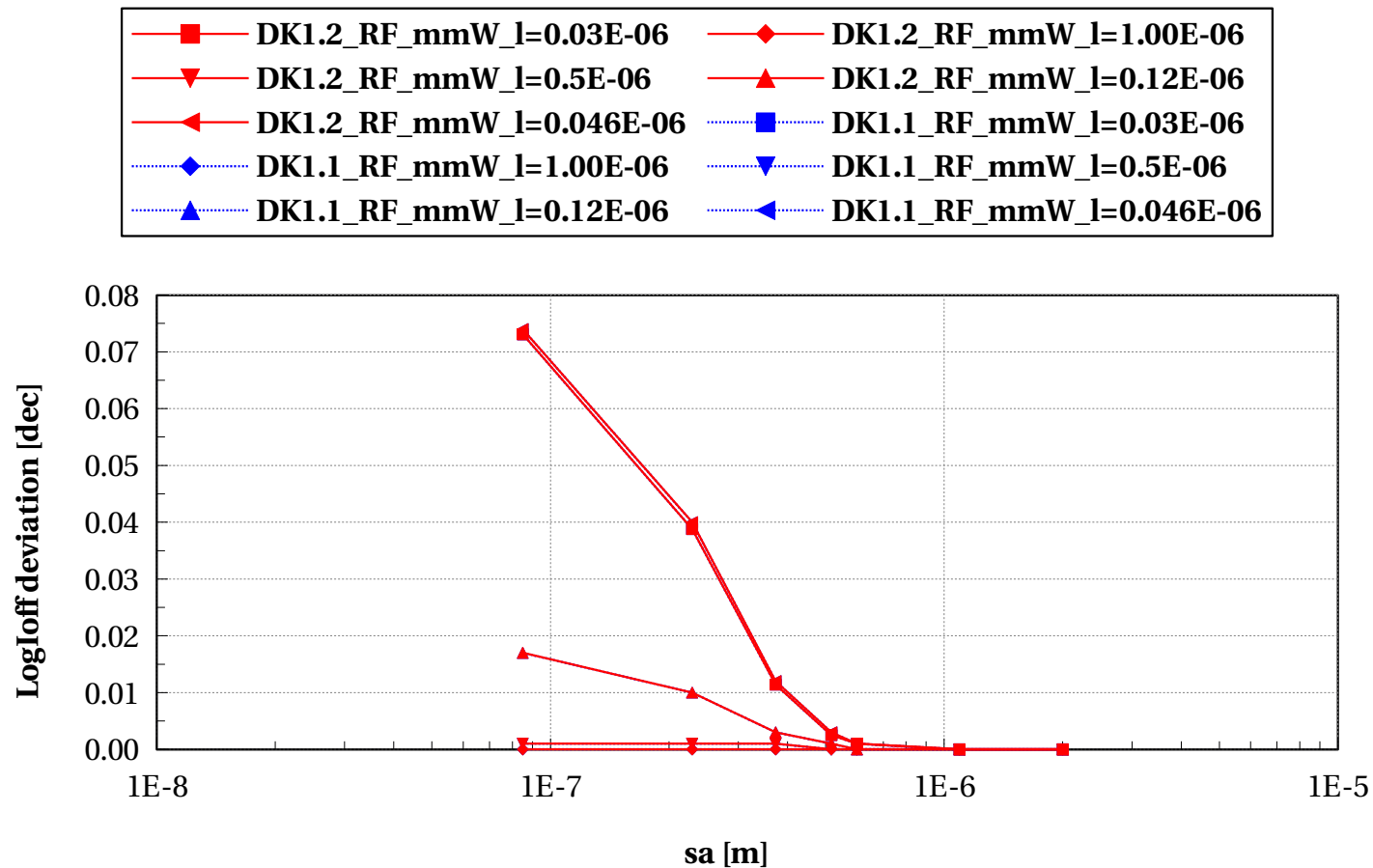
lvtpfet_acc, Isat deviation [%] vs sa [m]

$\rho==25$ and $w==0.1e-6$ and $p_{la}==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



lvtpfet_acc, LogIoff deviation [dec] vs sa [m]

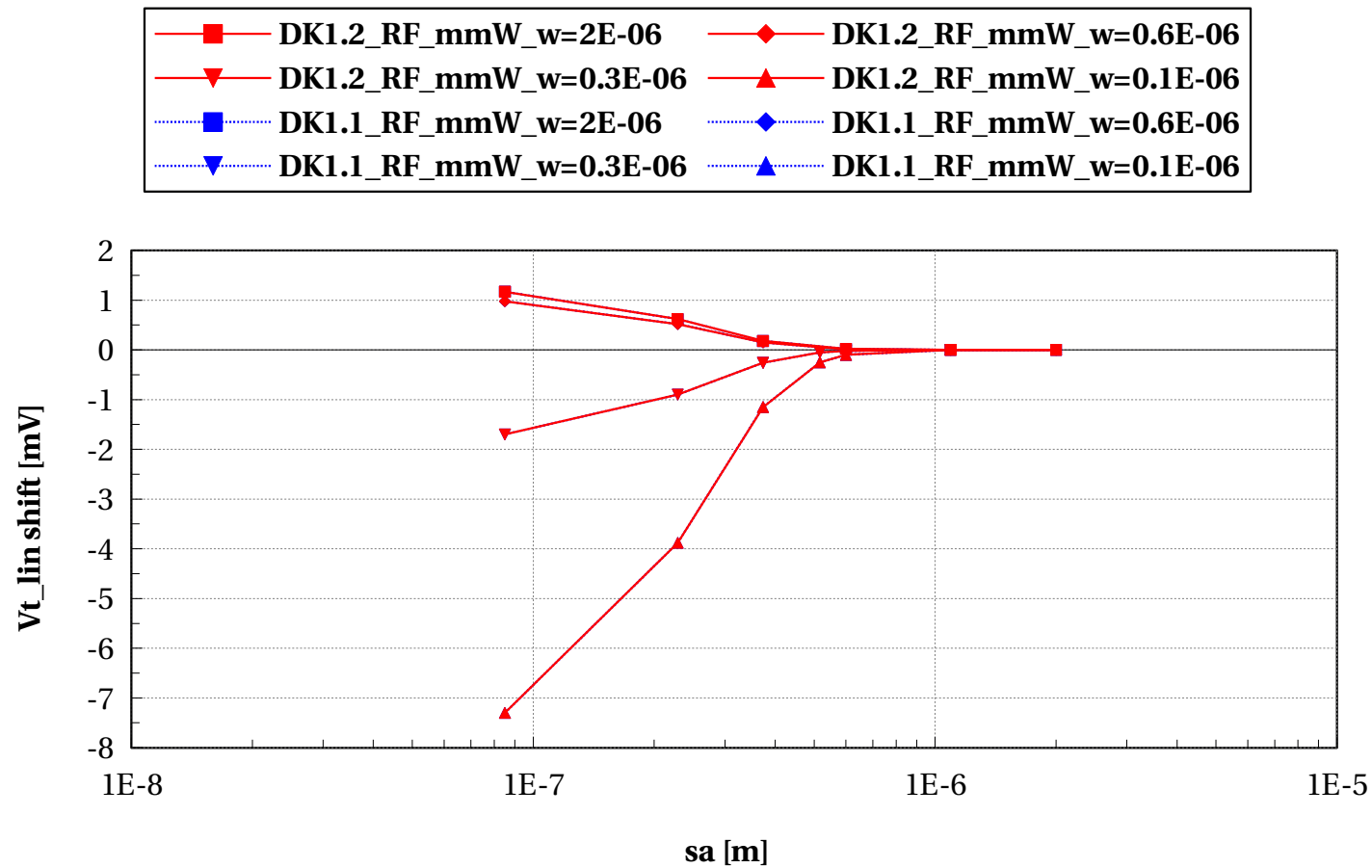
$\rho==25$ and $w==0.1e-6$ and $p_la==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



LOD effect (sa=sb) - Wscaling at L=0.03e-6

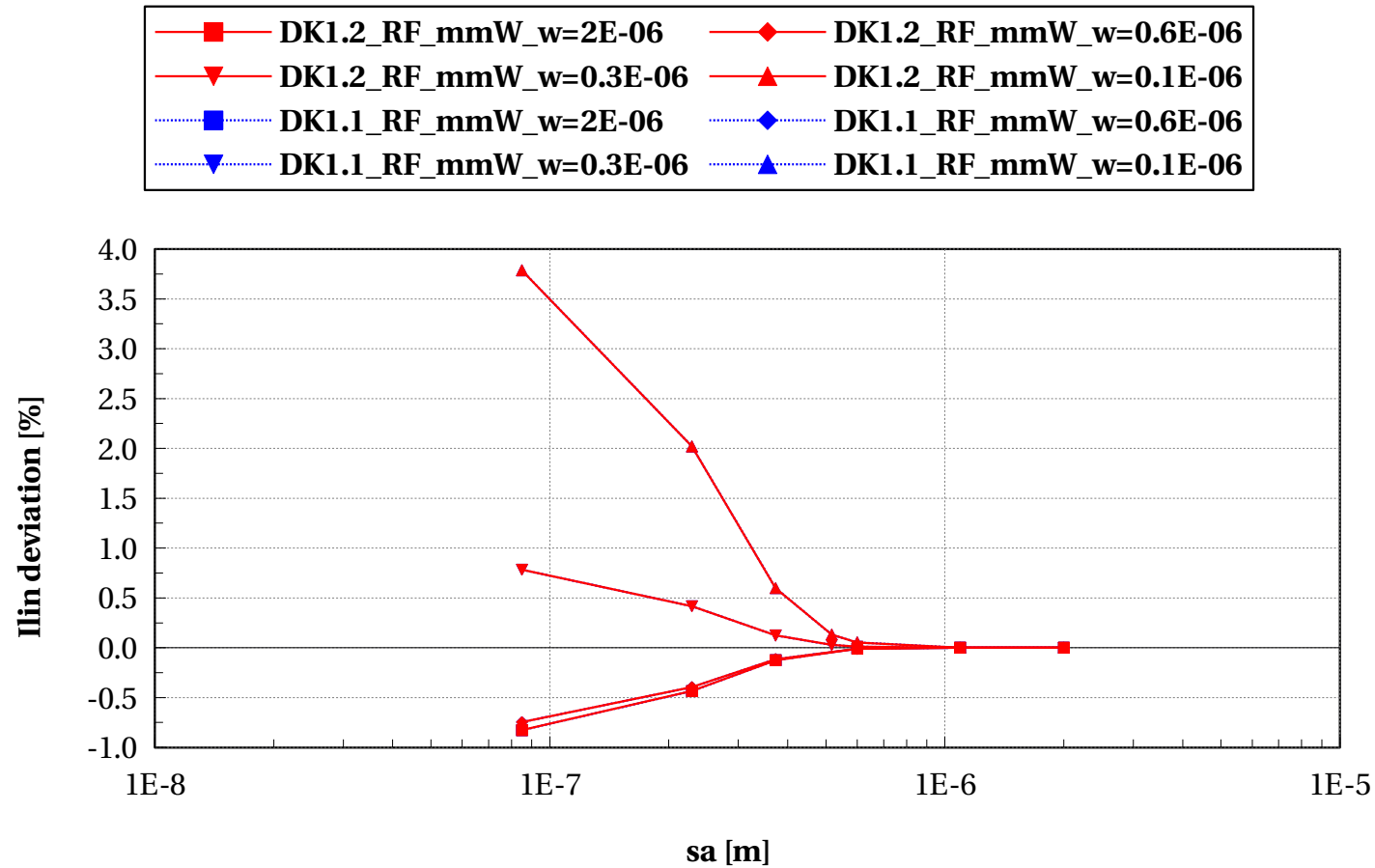
lvtpfet_acc, Vt_lin shift [mV] vs sa [m]

temp==25 and l==0.03e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



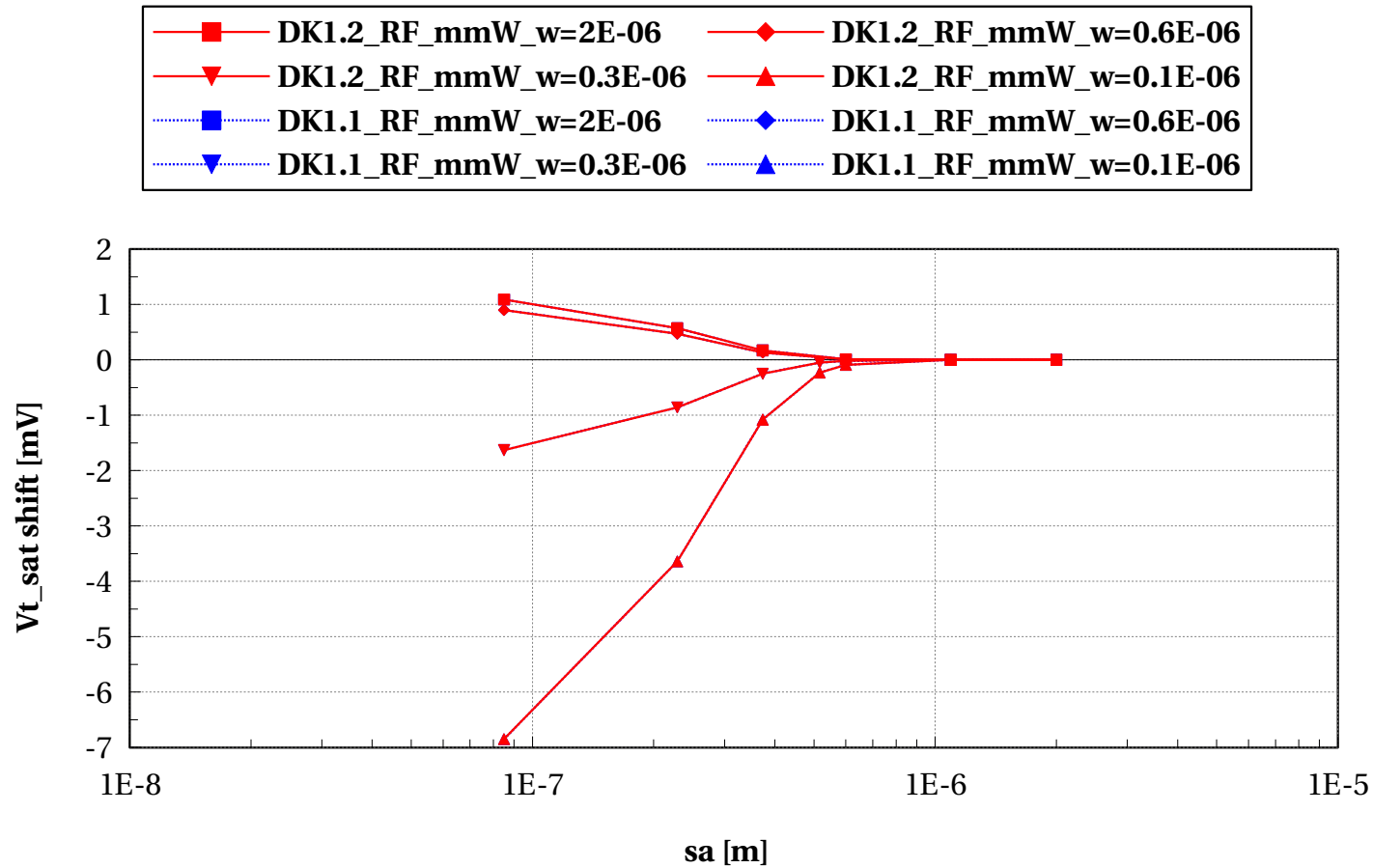
lvtpfet_acc, Ilin deviation [%] vs sa [m]

temp==25 and l==0.03e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



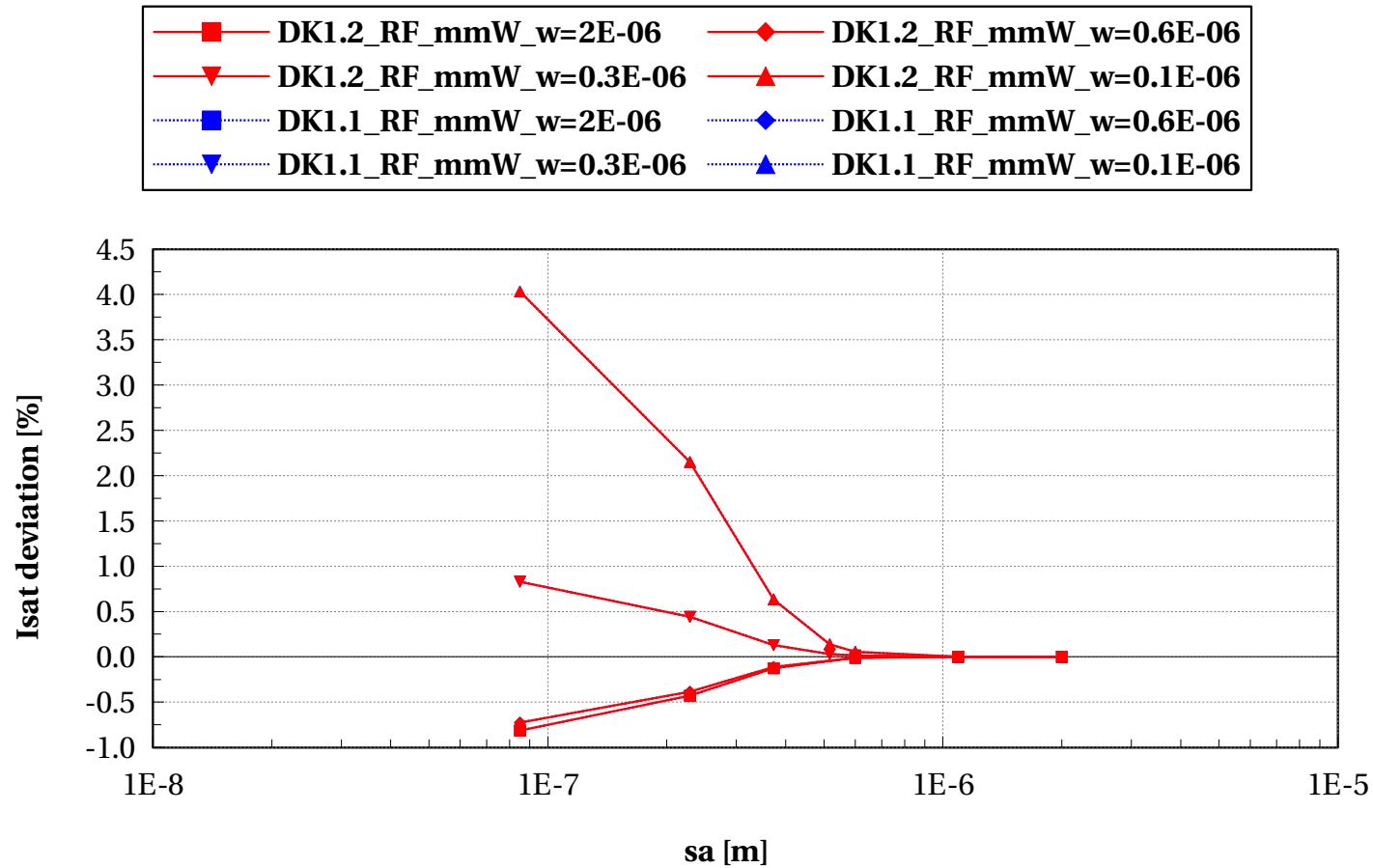
lvtpfet_acc, Vt_sat shift [mV] vs sa [m]

temp==25 and l==0.03e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



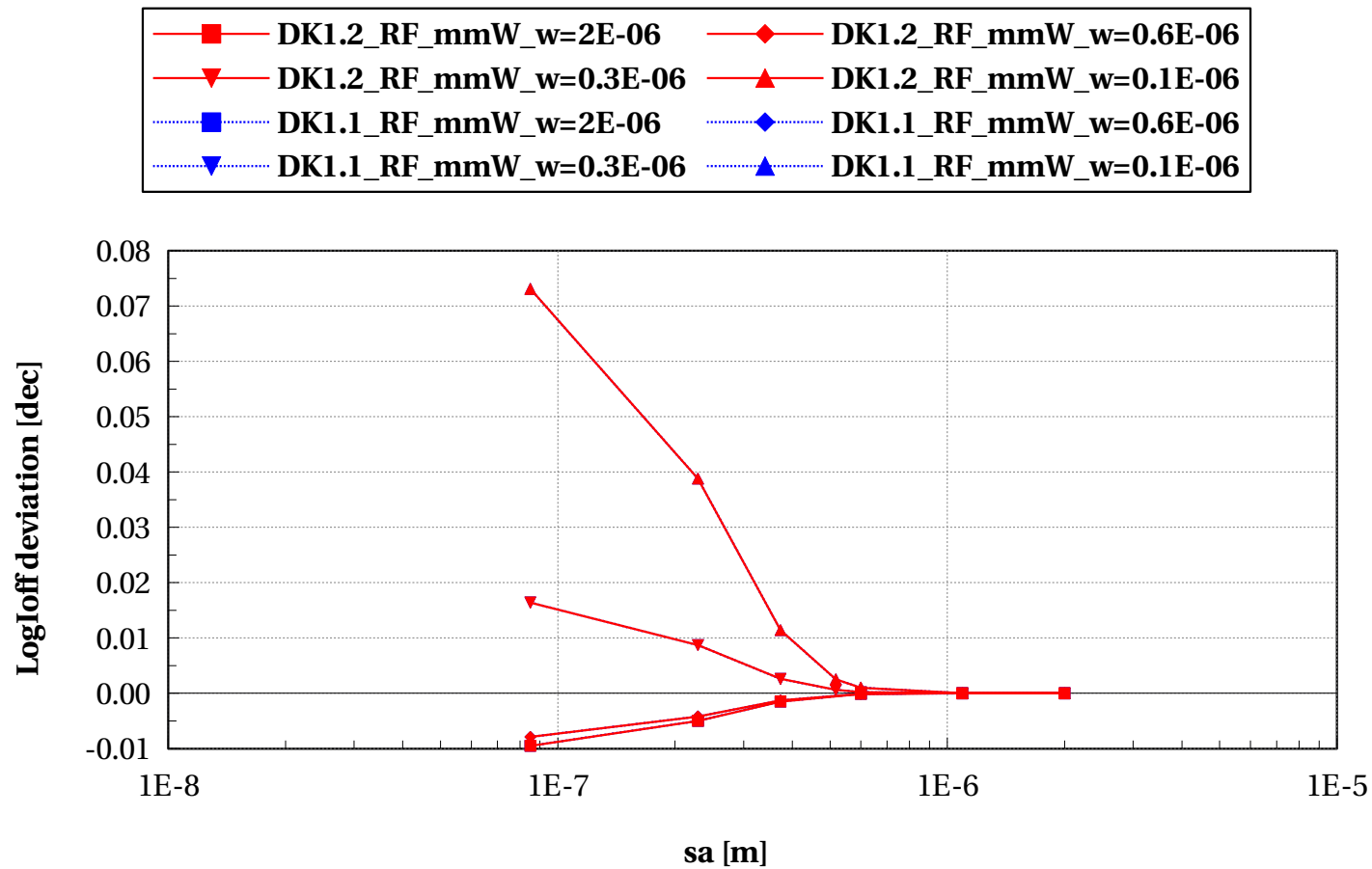
lvtpfet_acc, Isat deviation [%] vs sa [m]

temp==25 and l==0.03e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



lvtpfet_acc, Logloff deviation [dec] vs sa [m]

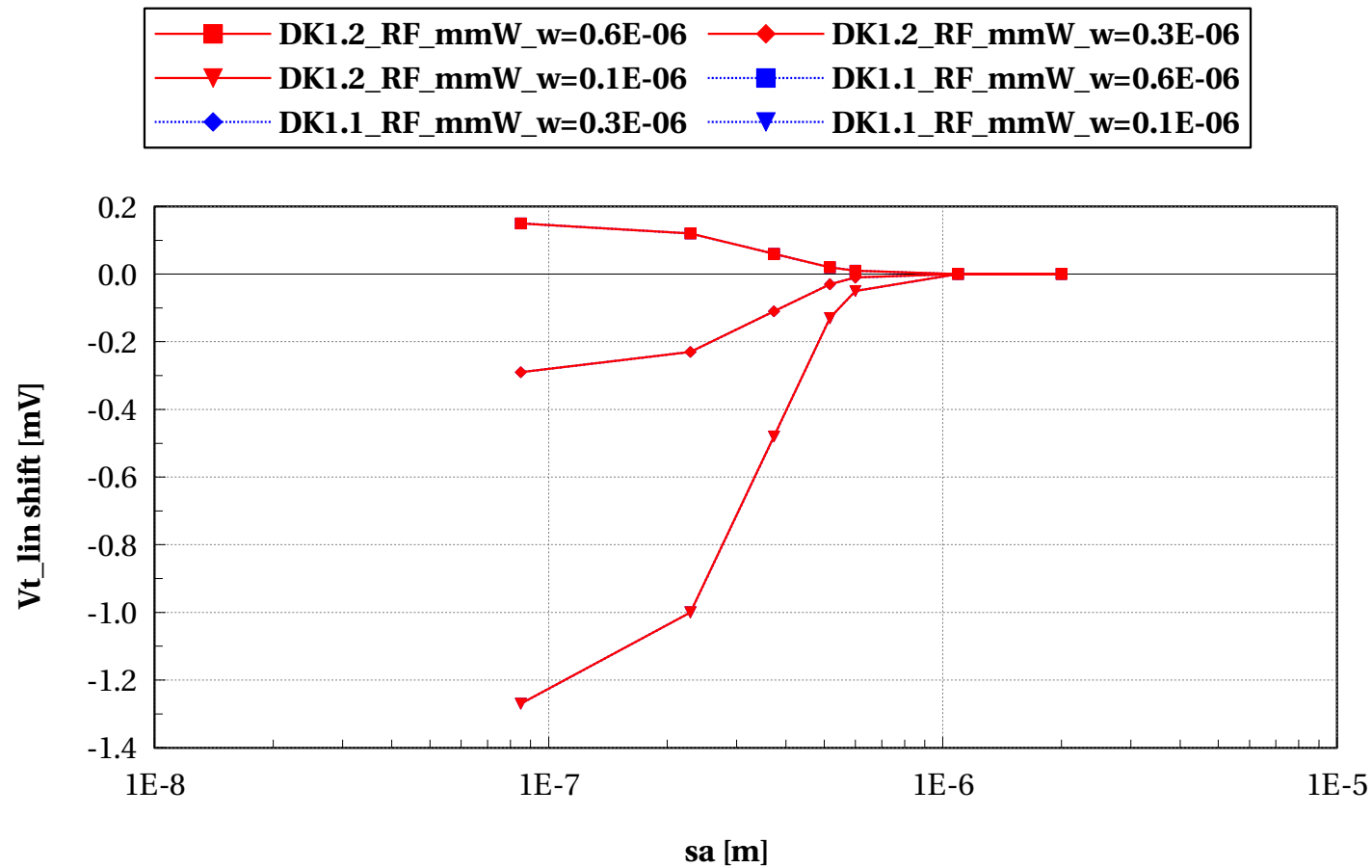
temp==25 and l==0.03e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



LOD effect (sa=sb) - Wscaling at L=1e-6

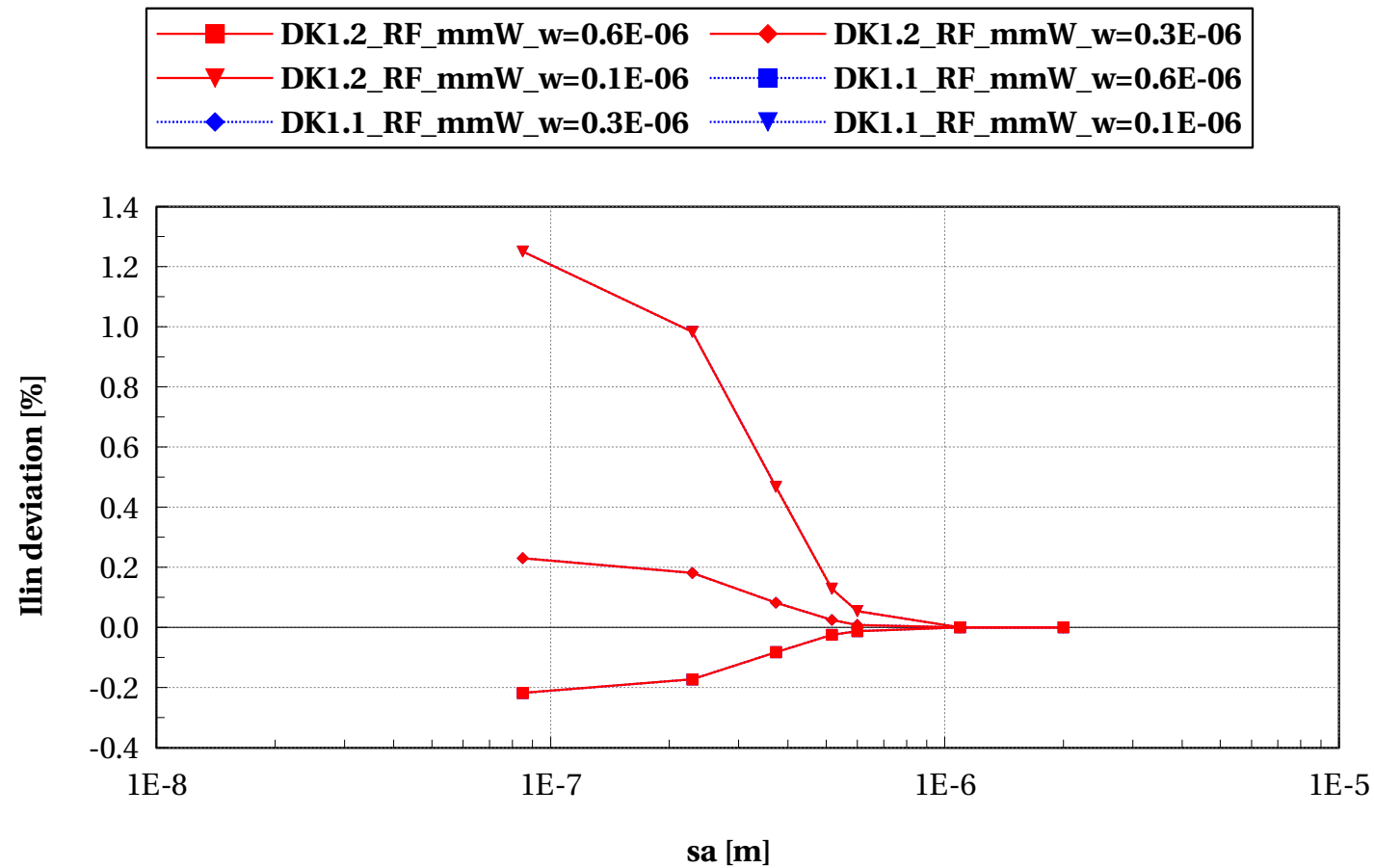
lvtpfet_acc, Vt_lin shift [mV] vs sa [m]

temp==25 and l==1.0e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



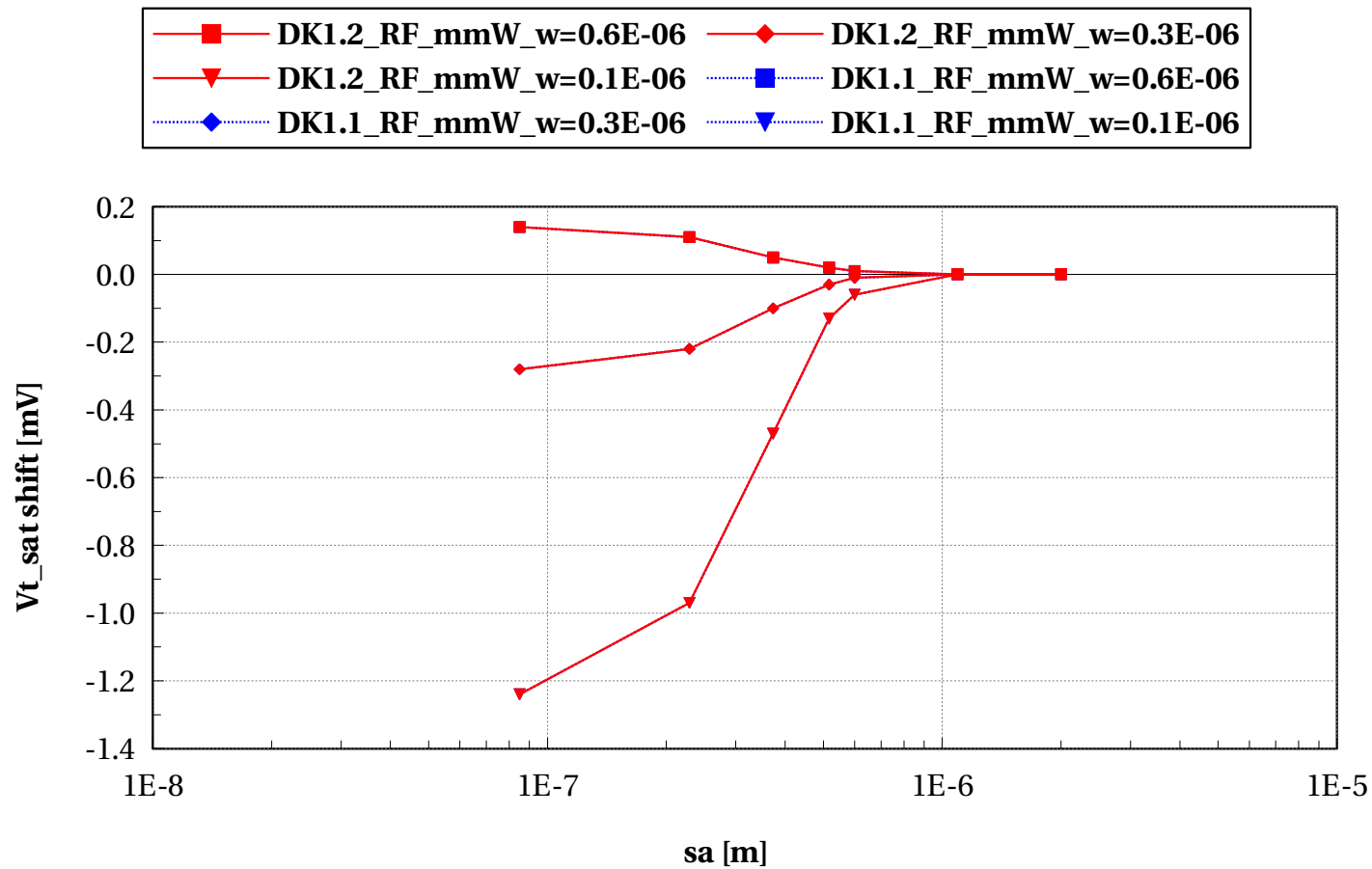
lvtpfet_acc, Ilin deviation [%] vs sa [m]

temp==25 and l==1.0e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



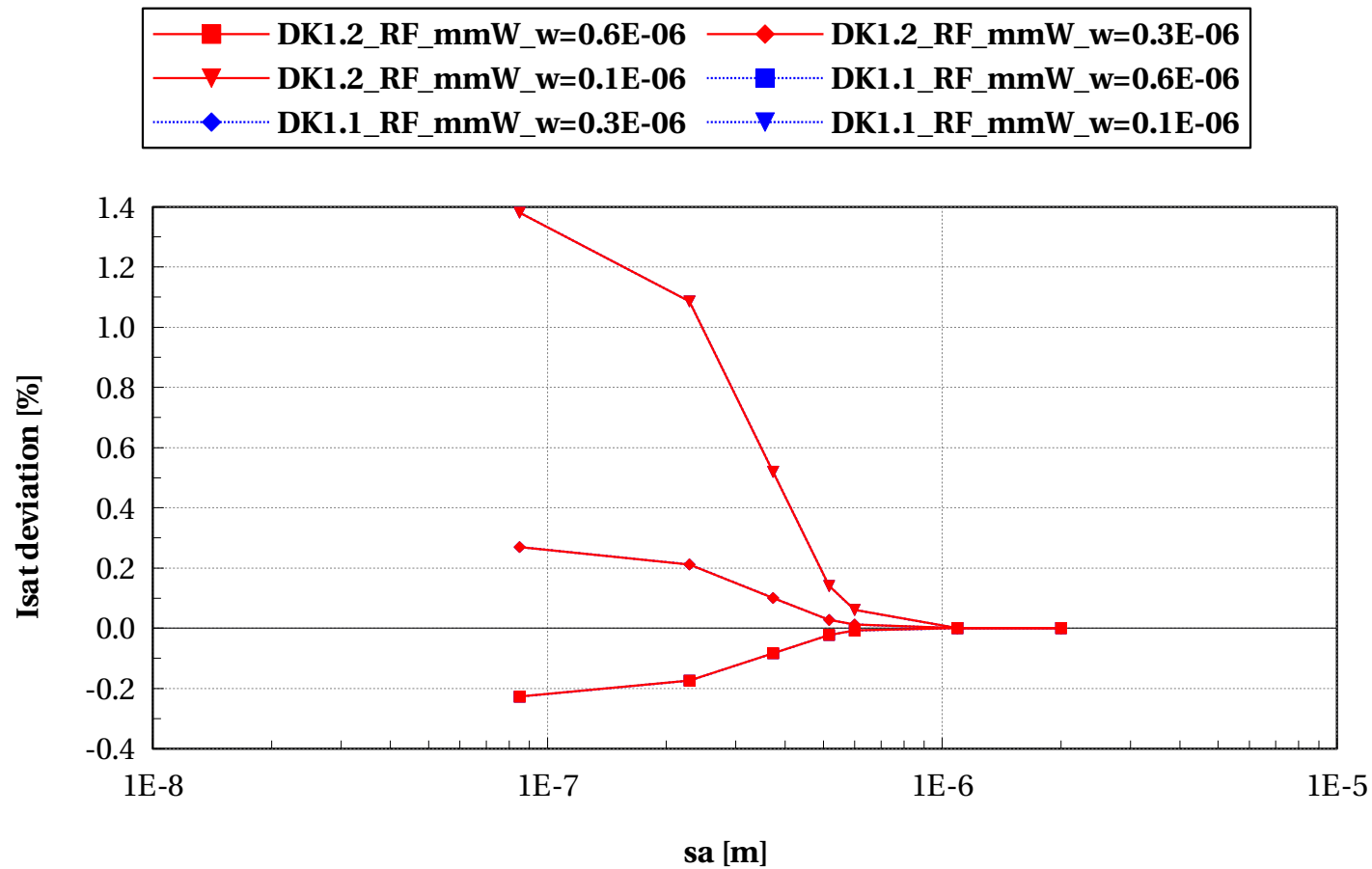
lvtpfet_acc, Vt_sat shift [mV] vs sa [m]

temp==25 and l==1.0e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



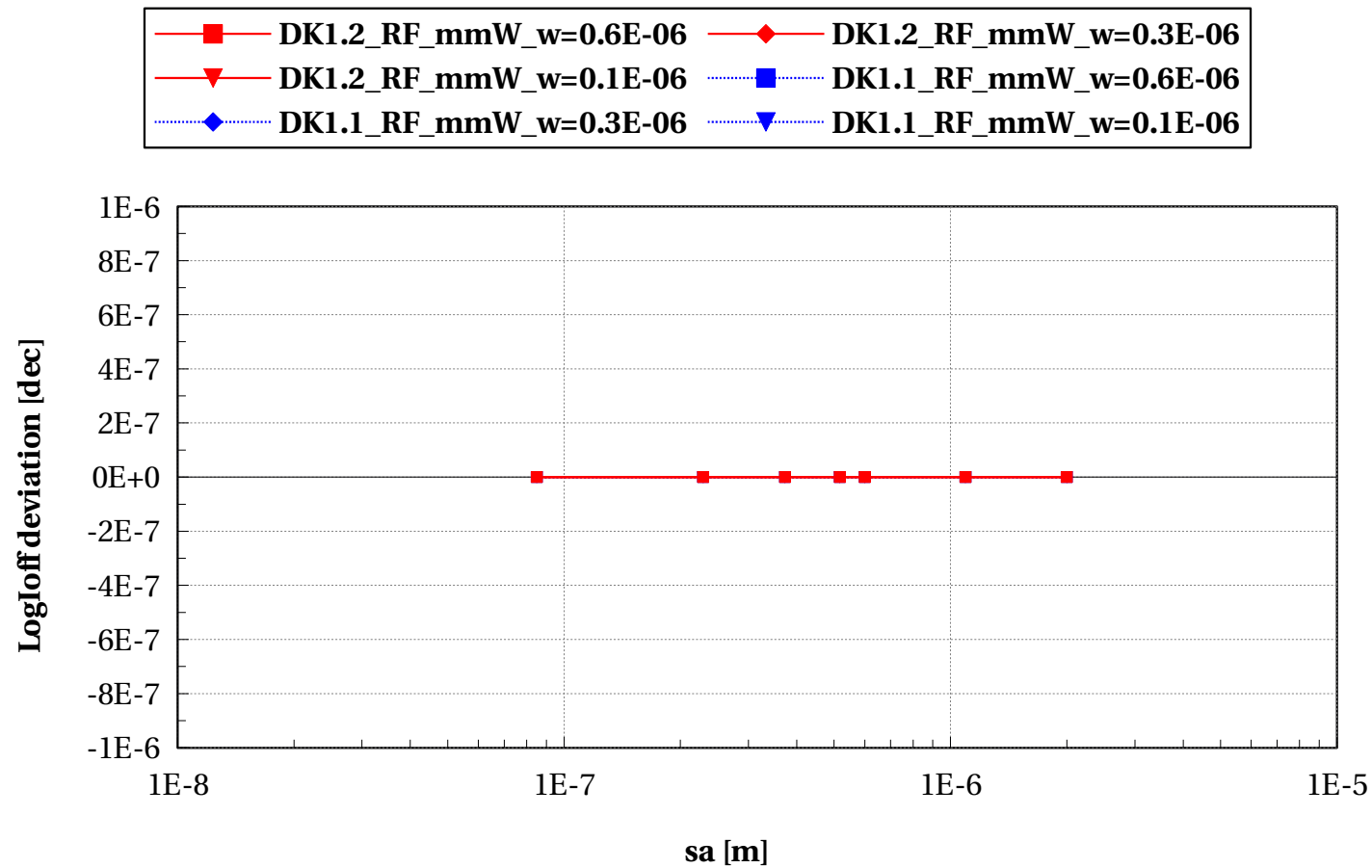
lvtpfet_acc, Isat deviation [%] vs sa [m]

temp==25 and l==1.0e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



lvtpfet_acc, LogIoff deviation [dec] vs sa [m]

temp==25 and l==1.0e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



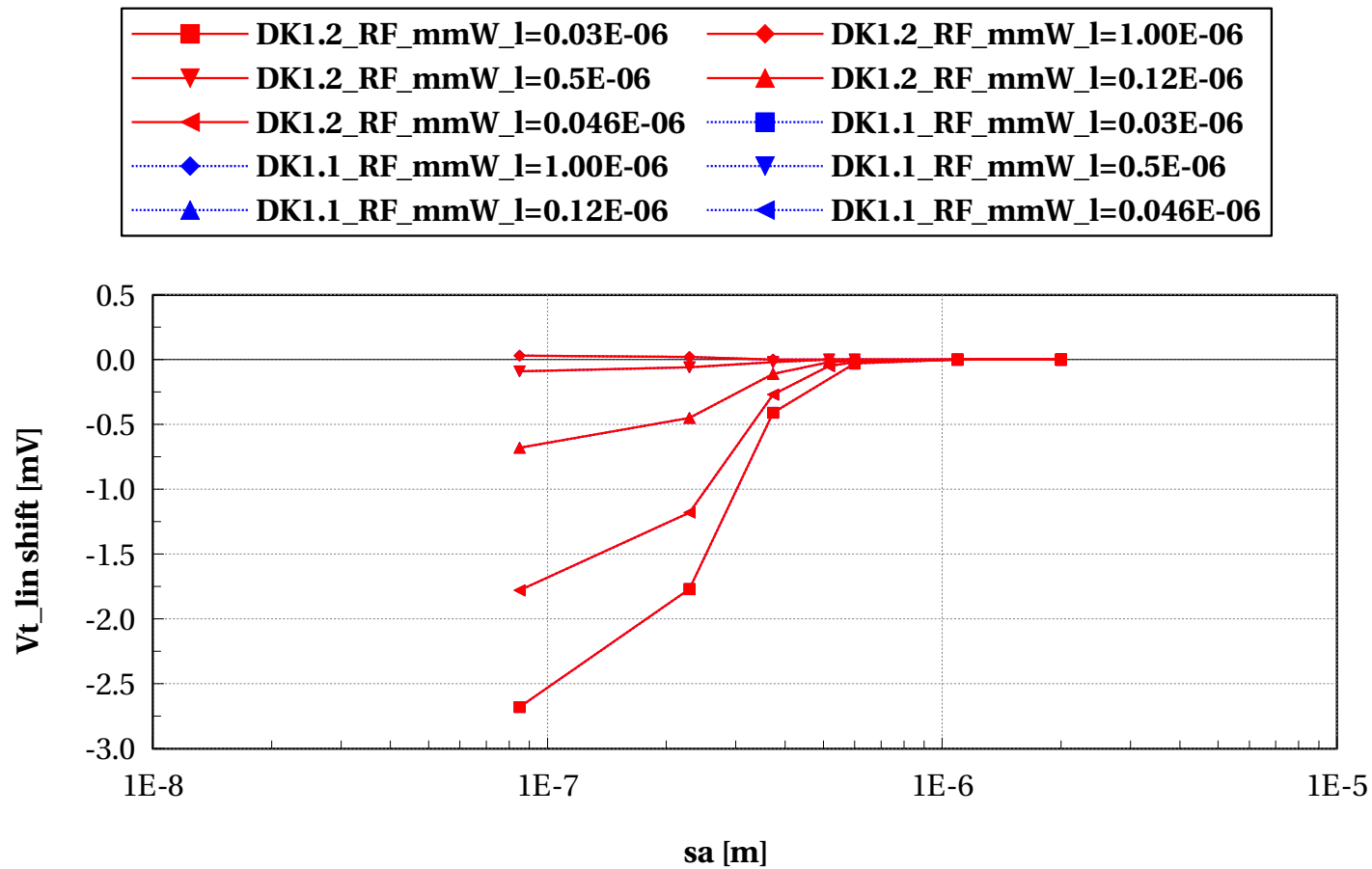
nfet_acc

Electrical characteristics scaling

LOD effect (sa=sb) - Lscaling at $W=1e-6$

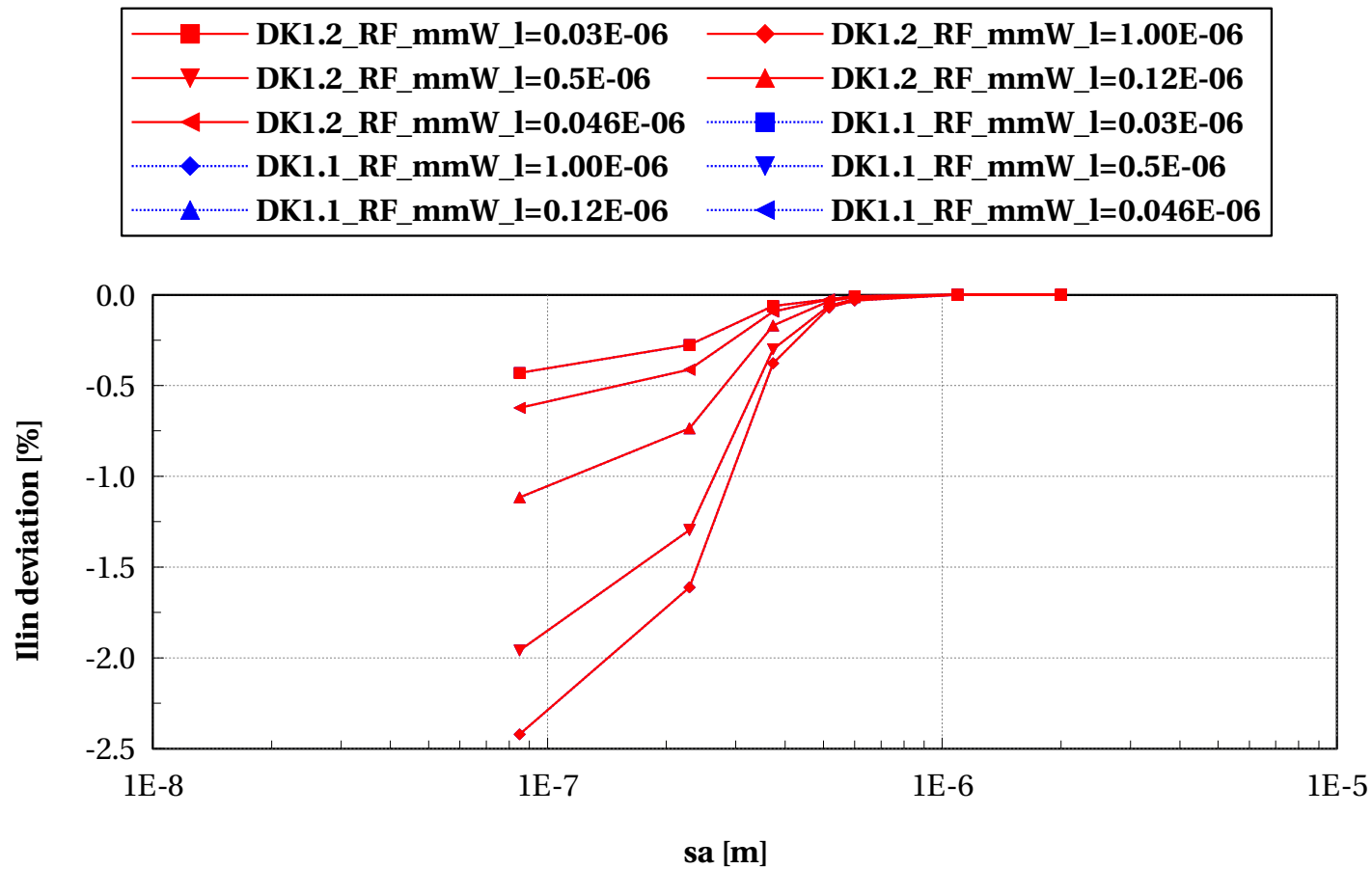
nfet_acc, Vt_lin shift [mV] vs sa [m]

ip==25 and w==1e-6 and p_la==0 and (L==0.030e-6 or L==0.046e-6 or L==0.12e-6 or L==0.5e-6 or L==1e-6)



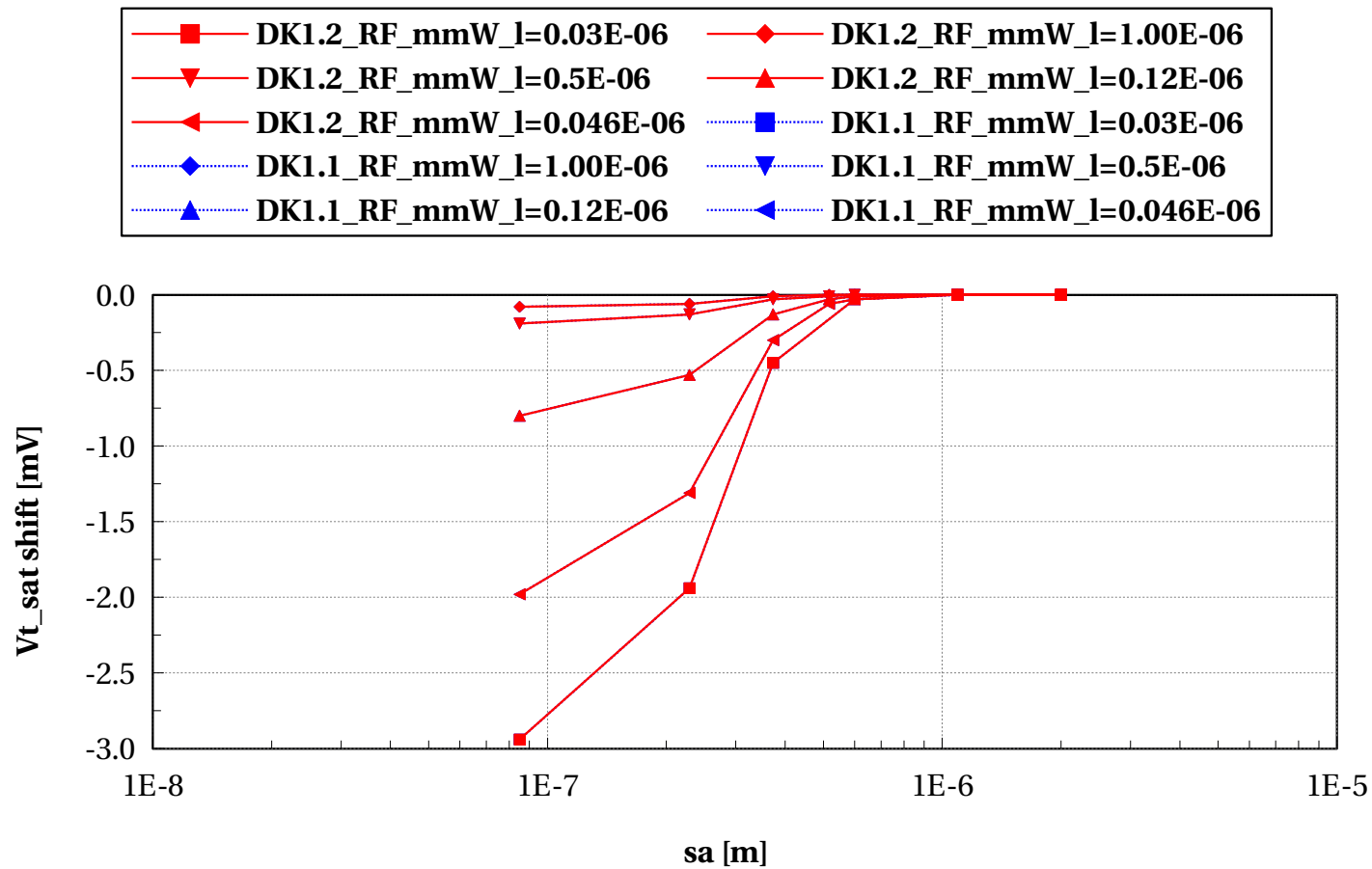
nfet_acc, Ilin deviation [%] vs sa [m]

ip==25 and w==1e-6 and p_la==0 and (L==0.030e-6 or L==0.046e-6 or L==0.12e-6 or L==0.5e-6 or L==1e-6)



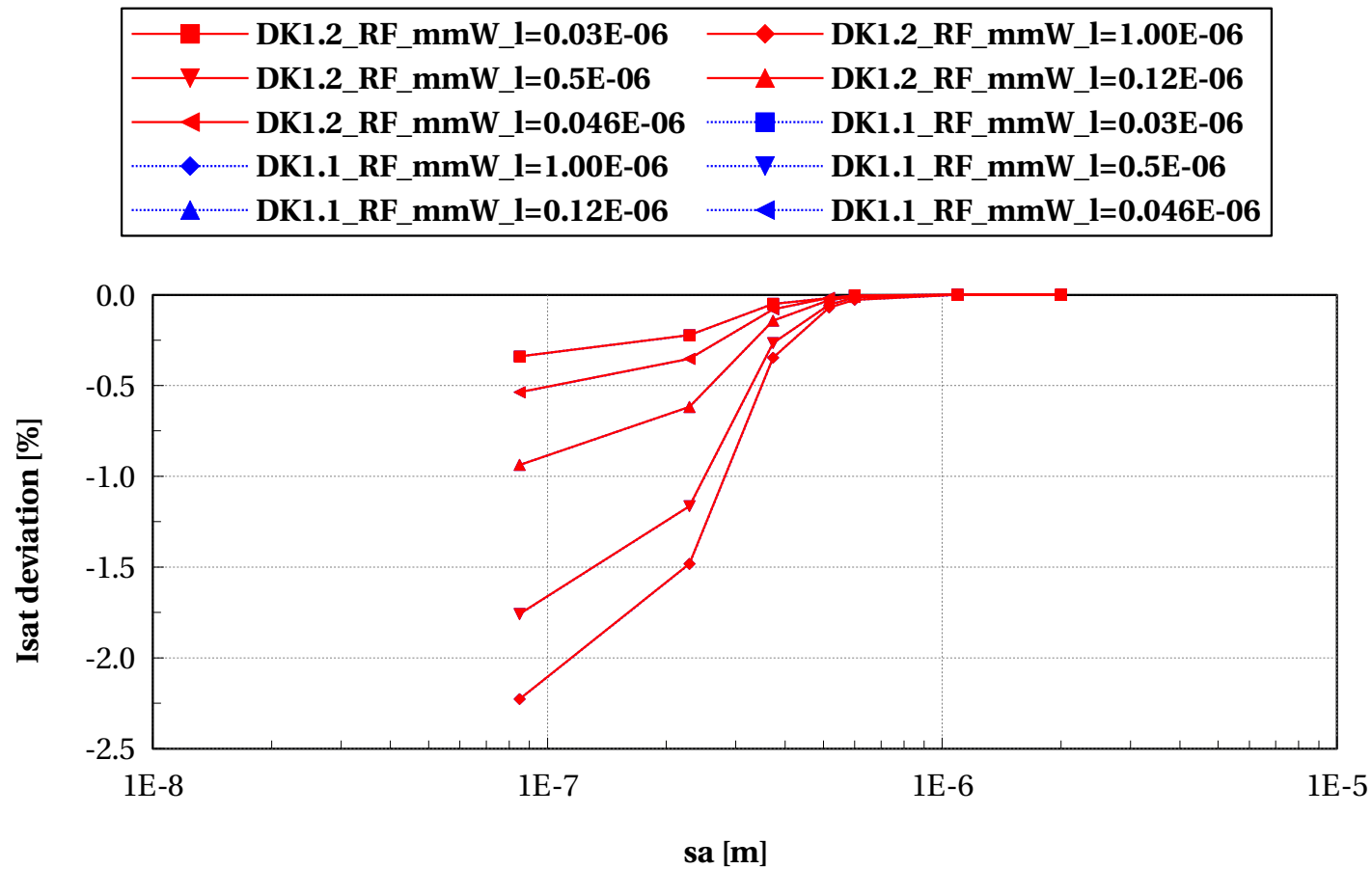
nfet_acc, Vt_sat shift [mV] vs sa [m]

$i_p=25$ and $w=1e-6$ and $p_{la}=0$ and ($L=0.030e-6$ or $L=0.046e-6$ or $L=0.12e-6$ or $L=0.5e-6$ or $L=1e-6$)



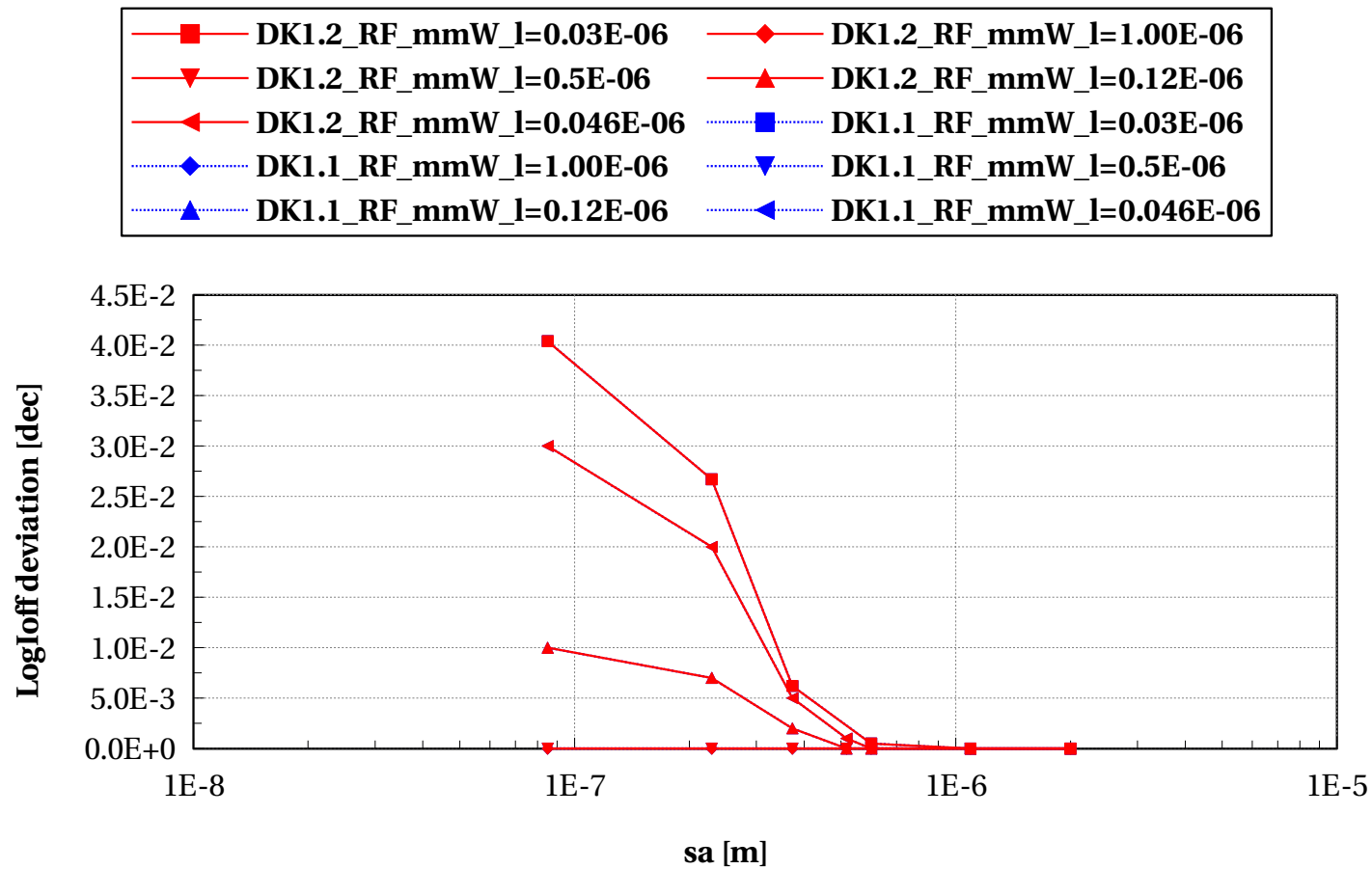
nfet_acc, Isat deviation [%] vs sa [m]

ip==25 and w==1e-6 and p_la==0 and (L==0.030e-6 or L==0.046e-6 or L==0.12e-6 or L==0.5e-6 or L==1e-6)



nfet_acc, LogIoff deviation [dec] vs sa [m]

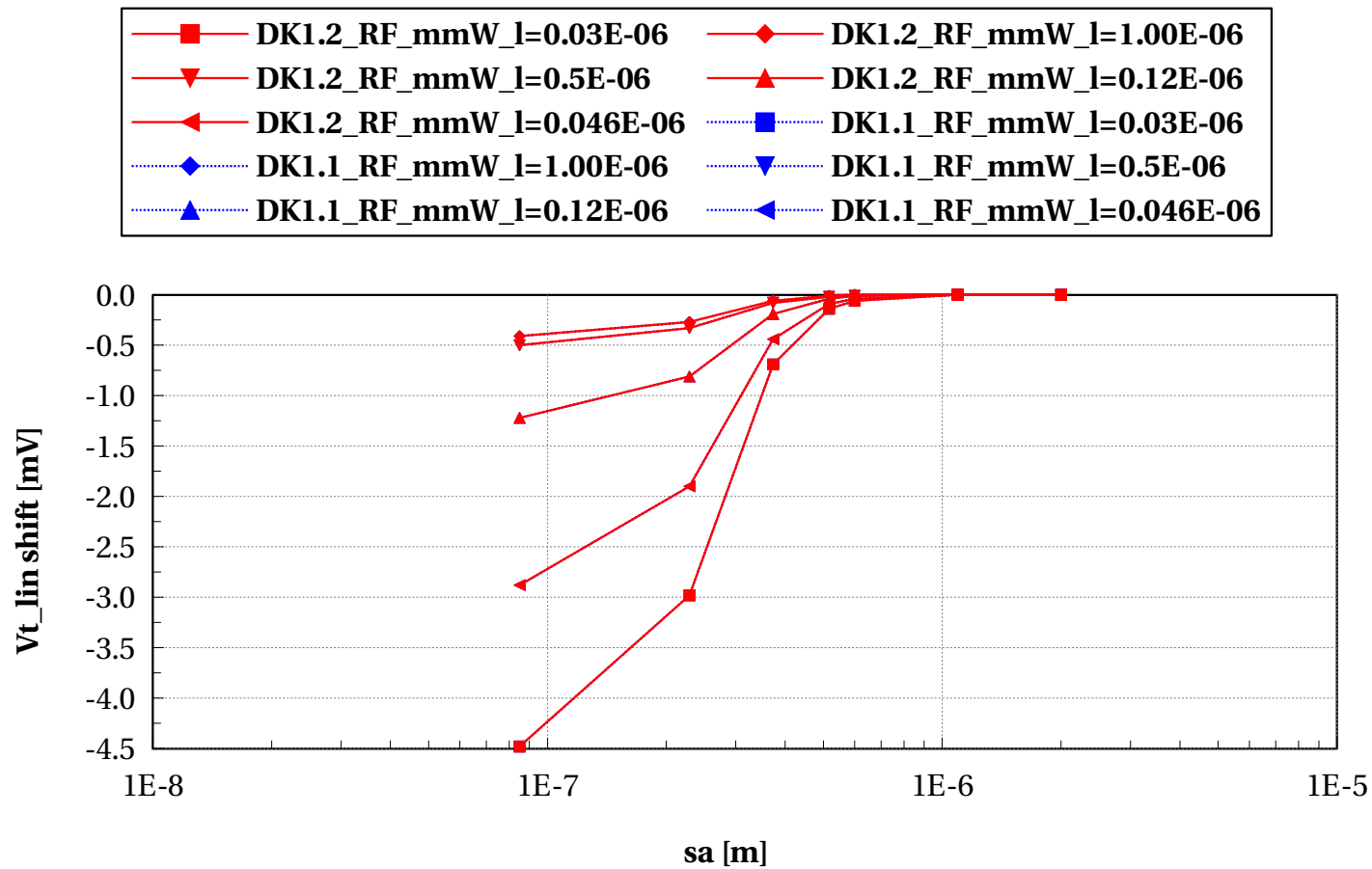
ip==25 and w==1e-6 and p_la==0 and (L==0.030e-6 or L==0.046e-6 or L==0.12e-6 or L==0.5e-6 or L==1e-6)



LOD effect (sa=sb) - Lscaling at $W=0.3e-6$

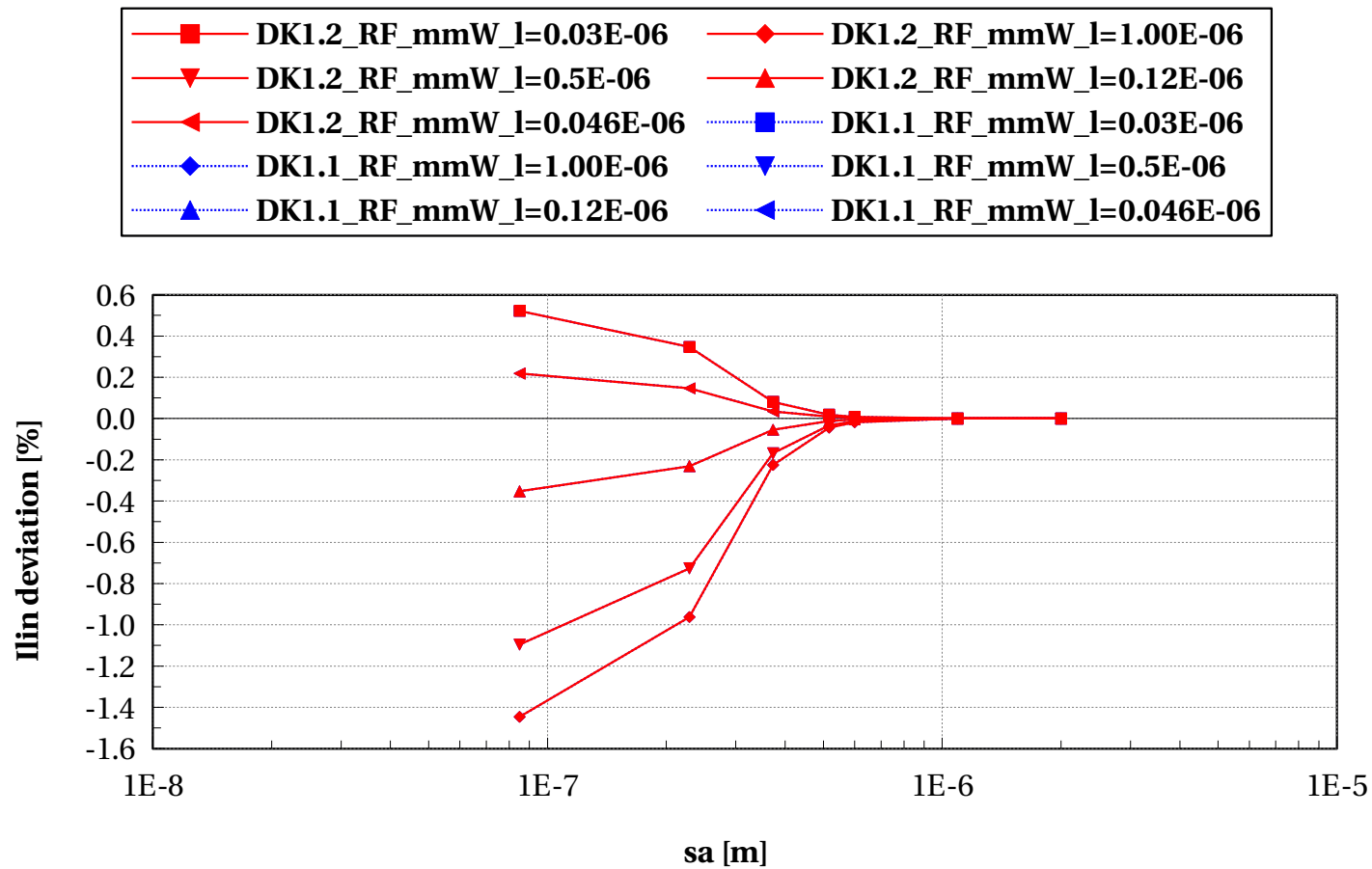
nfet_acc, Vt_lin shift [mV] vs sa [m]

$\rho==25$ and $w==0.3e-6$ and $p_la==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



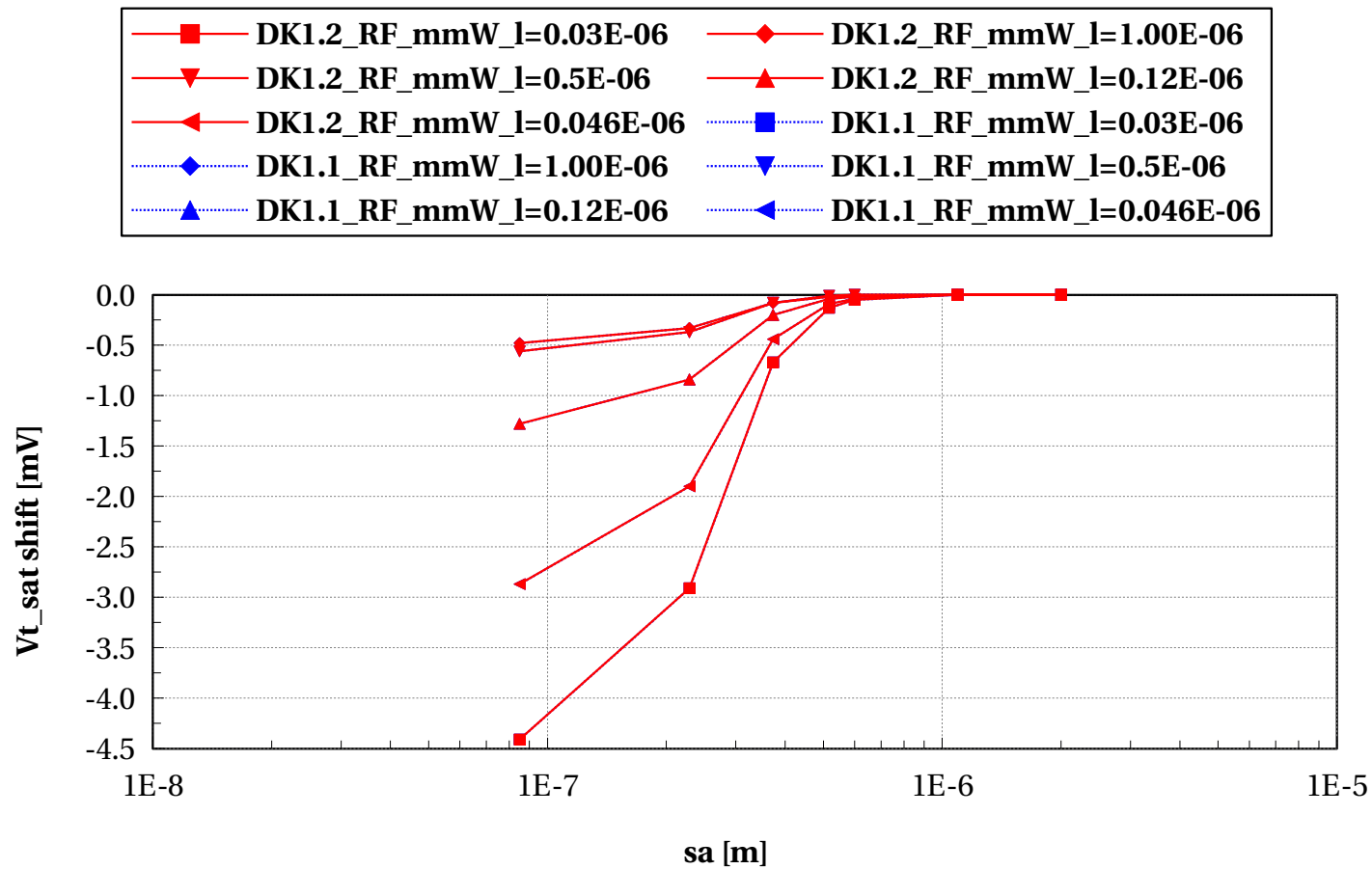
nfet_acc, Ilin deviation [%] vs sa [m]

$p==25$ and $w==0.3e-6$ and $p_{la}==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



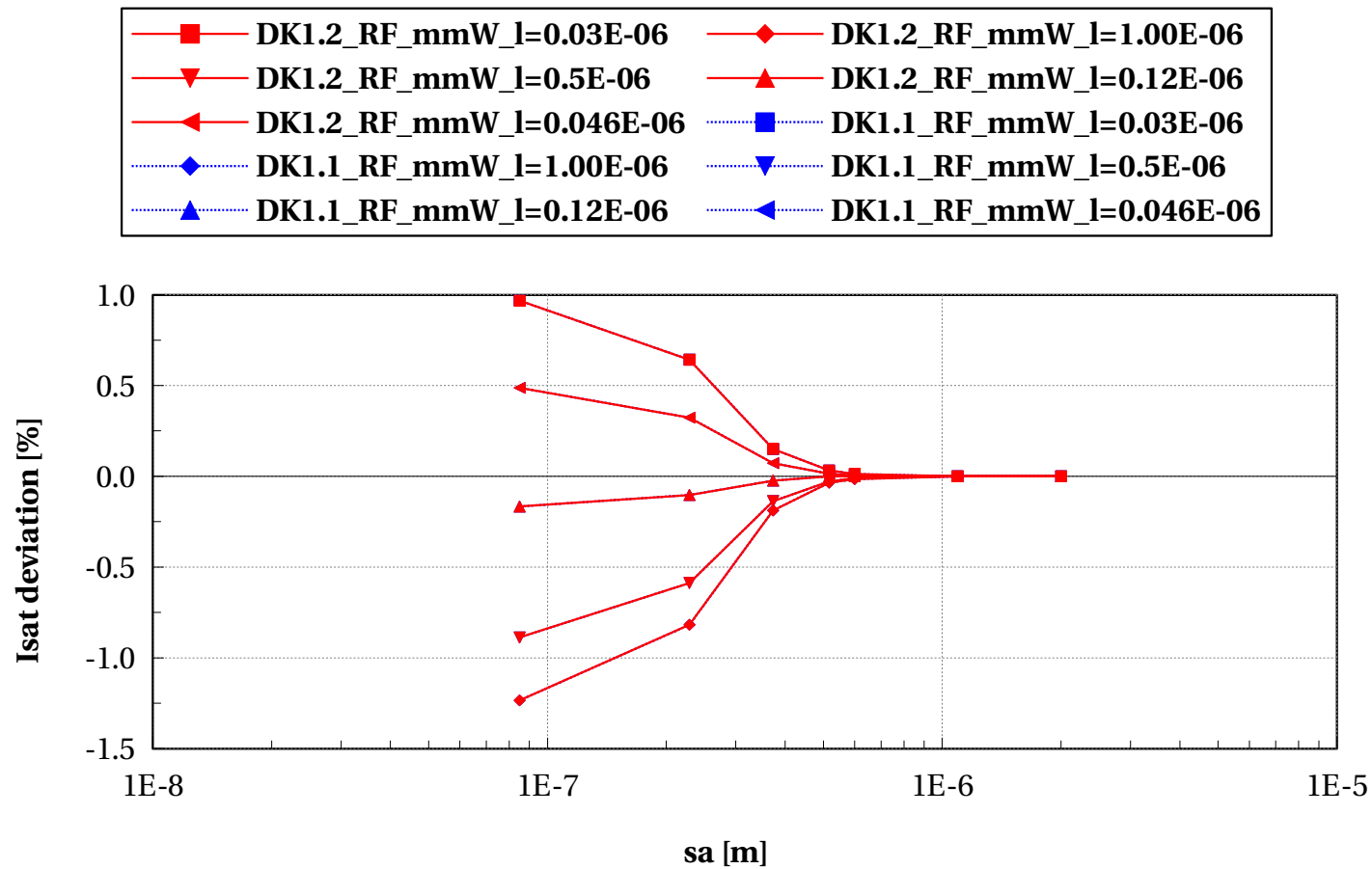
nfet_acc, Vt_sat shift [mV] vs sa [m]

$\rho==25$ and $w==0.3e-6$ and $p_la==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



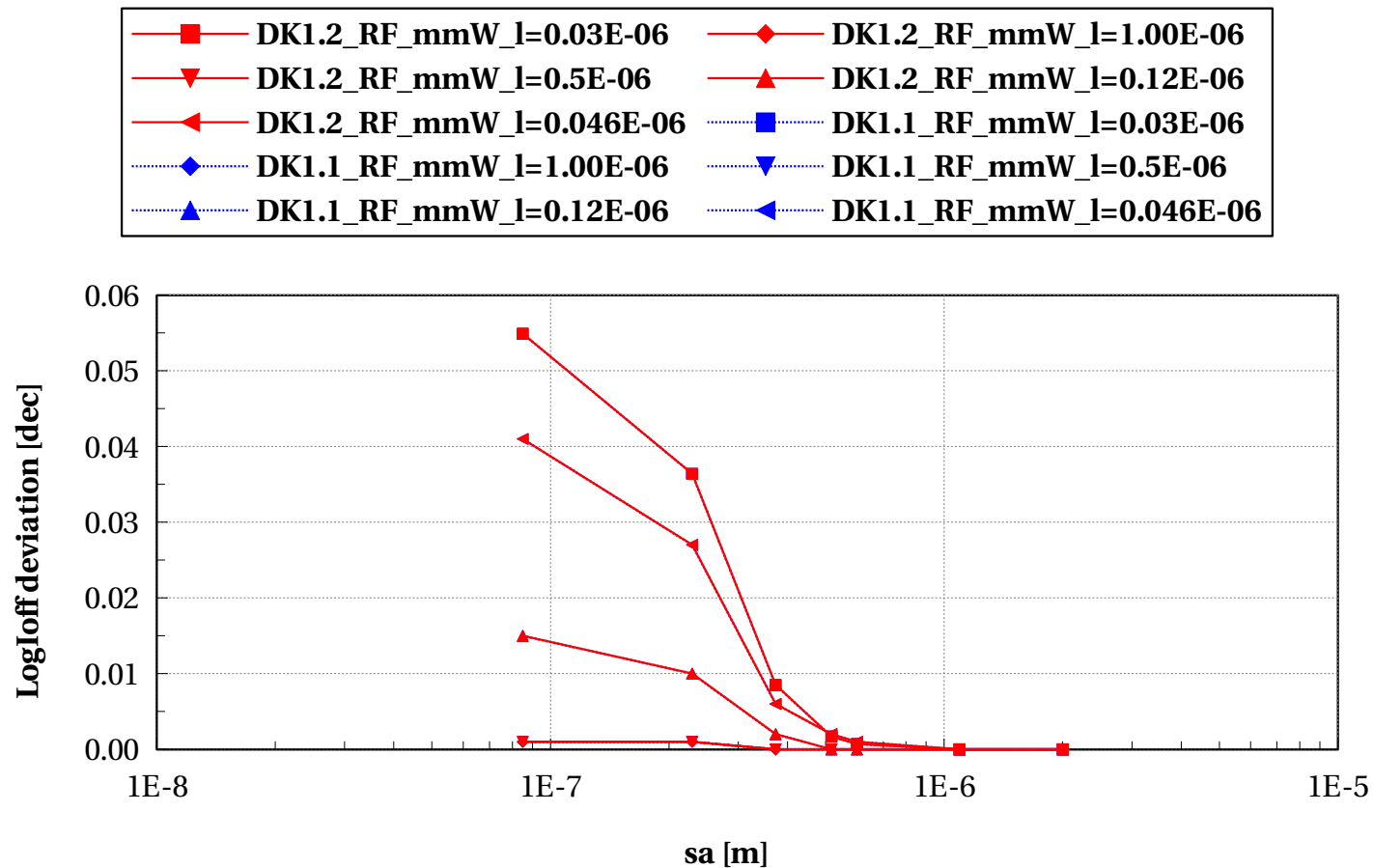
nfet_acc, Isat deviation [%] vs sa [m]

$\rho=25$ and $w=0.3e-6$ and $p_{la}=0$ and ($L=0.030e-6$ or $L=0.046e-6$ or $L=0.12e-6$ or $L=0.5e-6$ or $L=1$)



nfet_acc, LogIoff deviation [dec] vs sa [m]

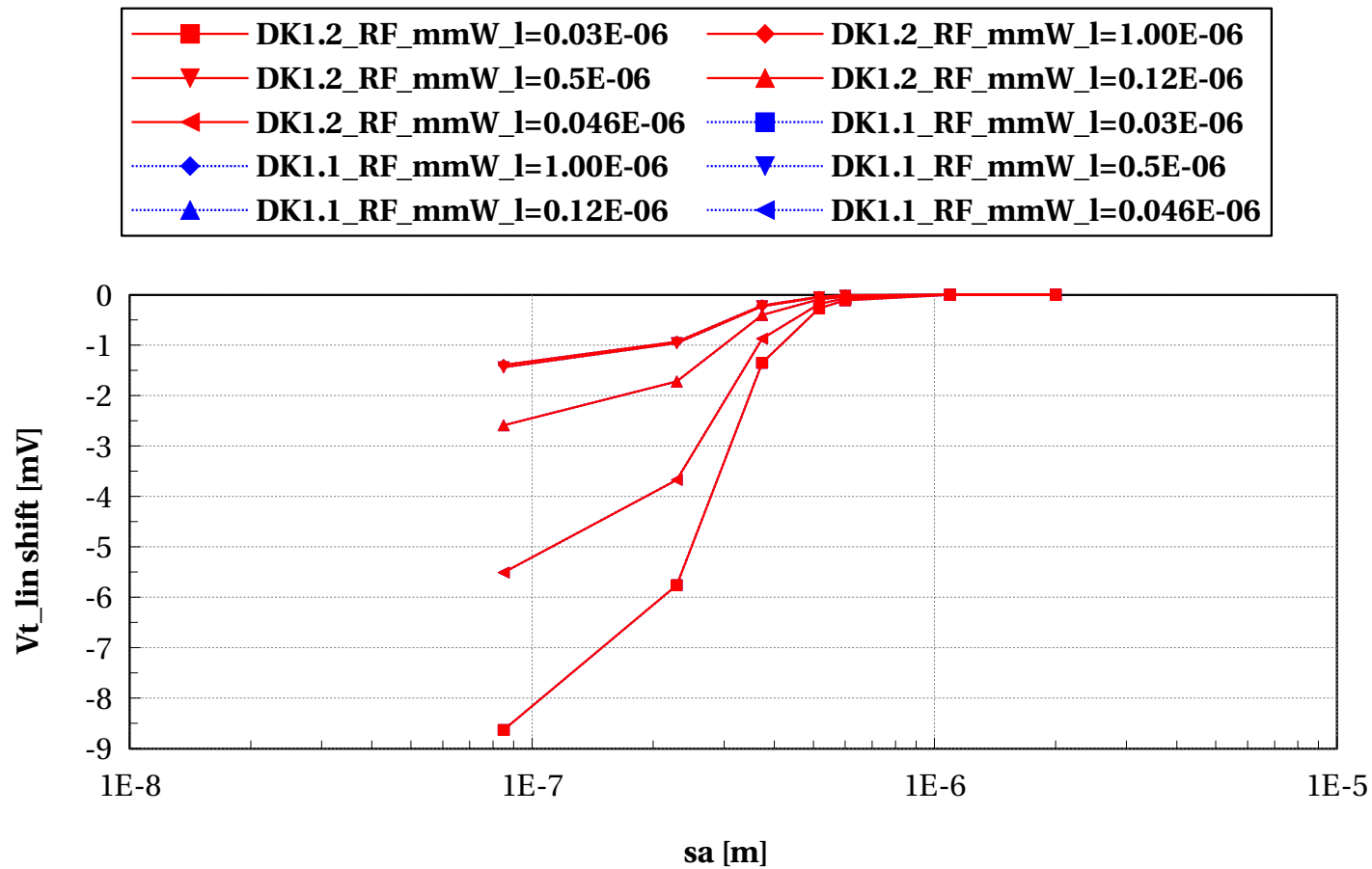
$\rho=25$ and $w=0.3e-6$ and $p_{la}=0$ and ($L=0.030e-6$ or $L=0.046e-6$ or $L=0.12e-6$ or $L=0.5e-6$ or $L=1$)



LOD effect (sa=sb) - Lscaling at $W=0.1\text{e-}6$

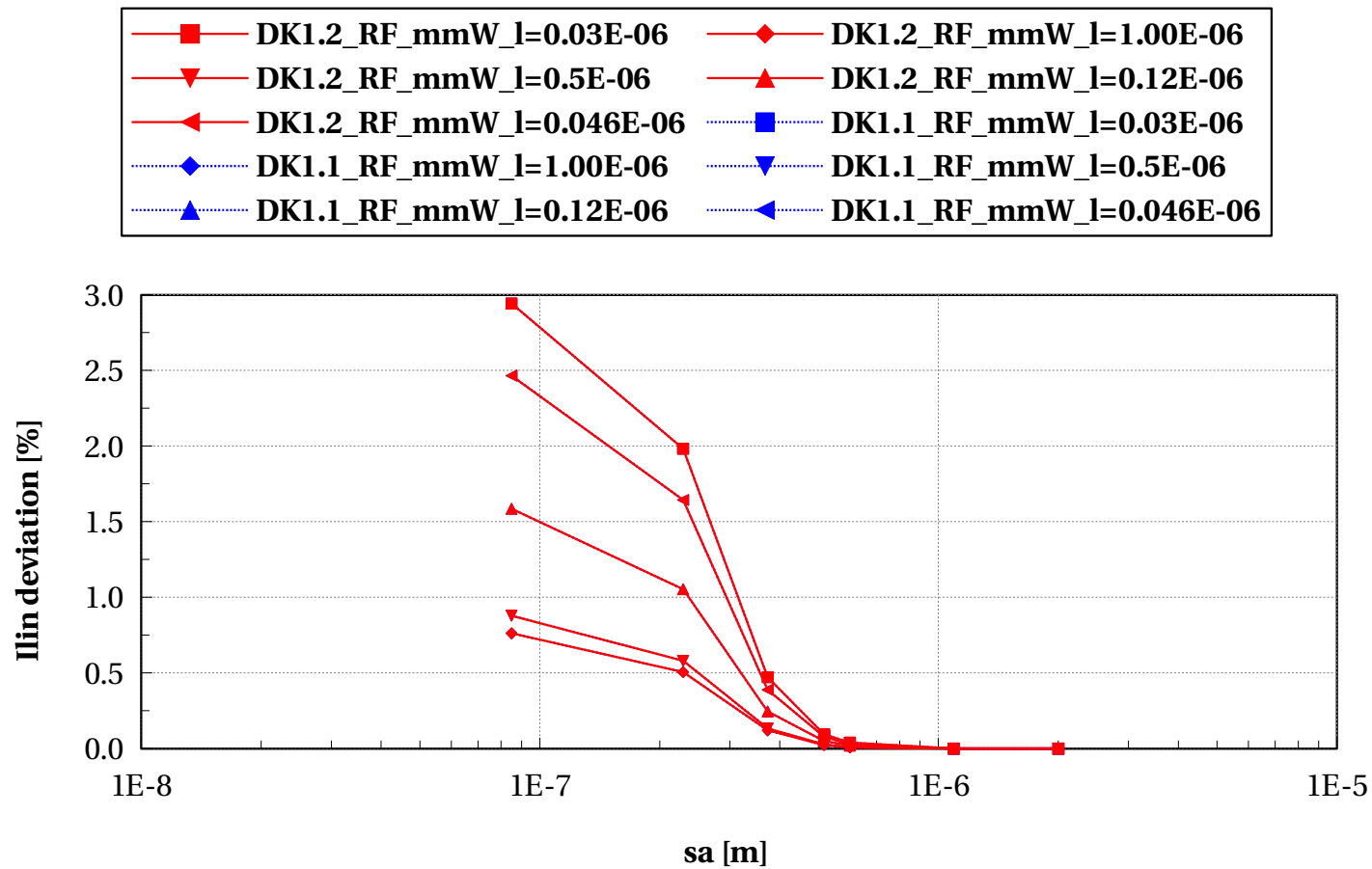
nfet_acc, Vt_lin shift [mV] vs sa [m]

$\rho==25$ and $w==0.1\text{e-}6$ and $p_la==0$ and ($L==0.030\text{e-}6$ or $L==0.046\text{e-}6$ or $L==0.12\text{e-}6$ or $L==0.5\text{e-}6$ or $L==1$)



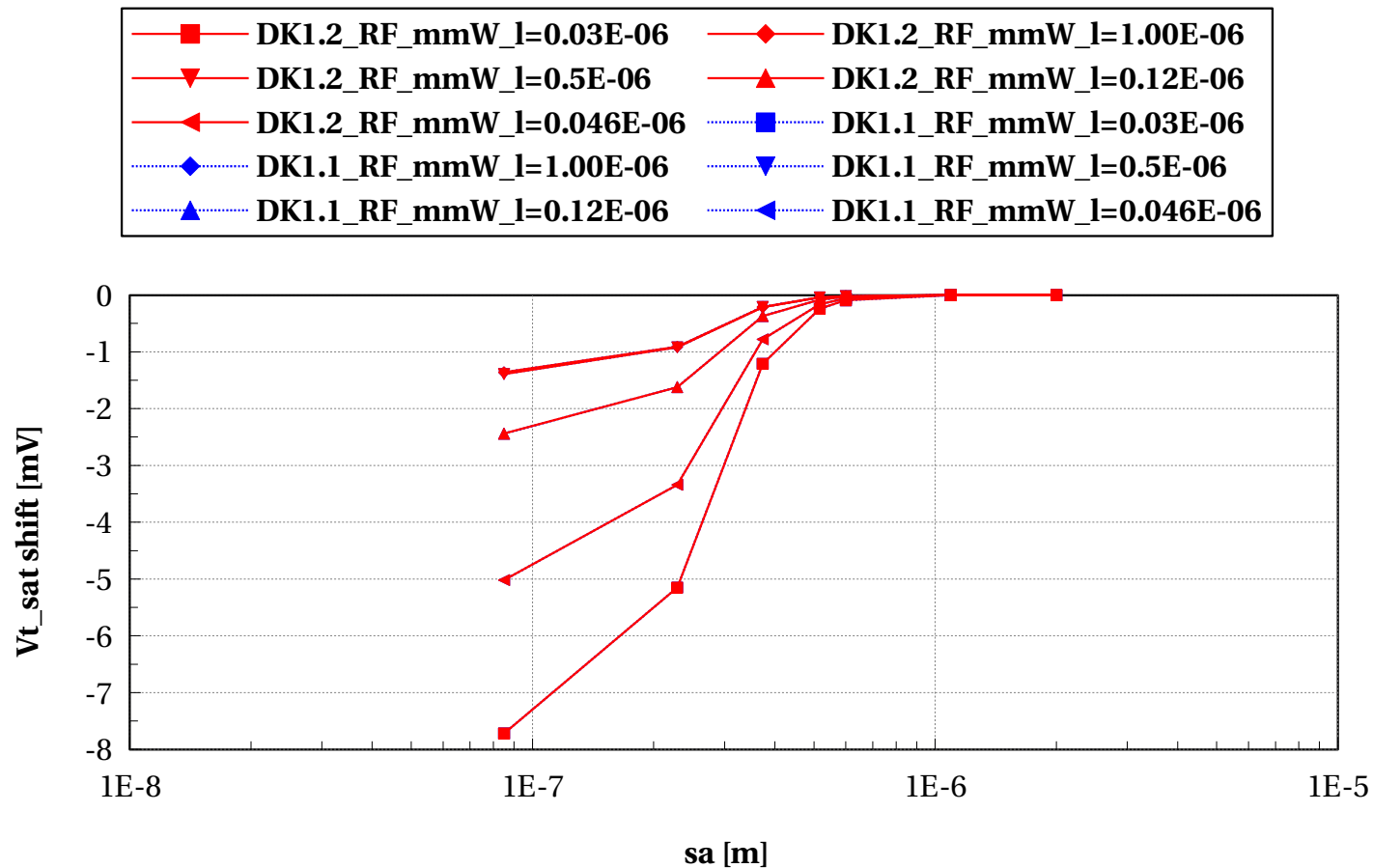
nfet_acc, Ilin deviation [%] vs sa [m]

$\rho=25$ and $w=0.1\text{e-}6$ and $p_{\text{la}}=0$ and ($L=0.030\text{e-}6$ or $L=0.046\text{e-}6$ or $L=0.12\text{e-}6$ or $L=0.5\text{e-}6$ or $L=1$)



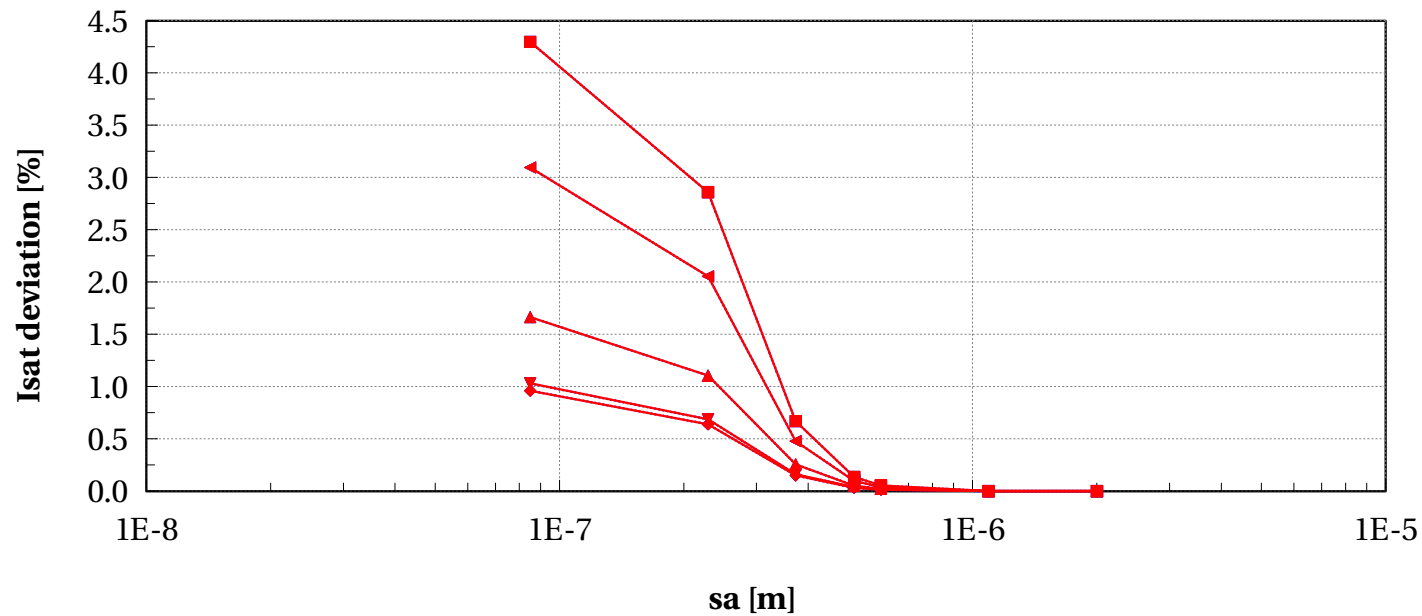
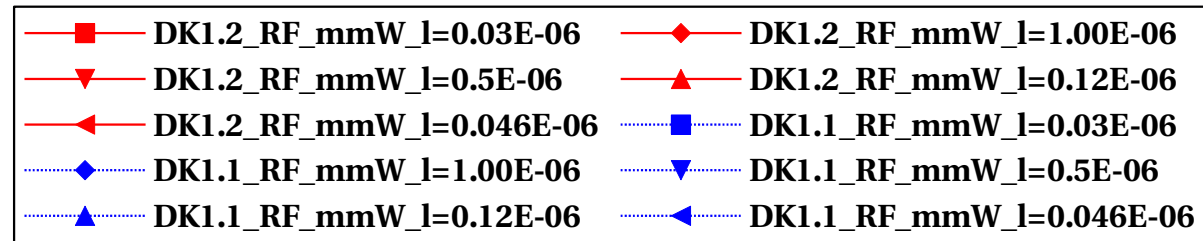
nfet_acc, Vt_sat shift [mV] vs sa [m]

$\rho=25$ and $w=0.1\text{e-}6$ and $p_{\text{la}}=0$ and ($L=0.030\text{e-}6$ or $L=0.046\text{e-}6$ or $L=0.12\text{e-}6$ or $L=0.5\text{e-}6$ or $L=1$)



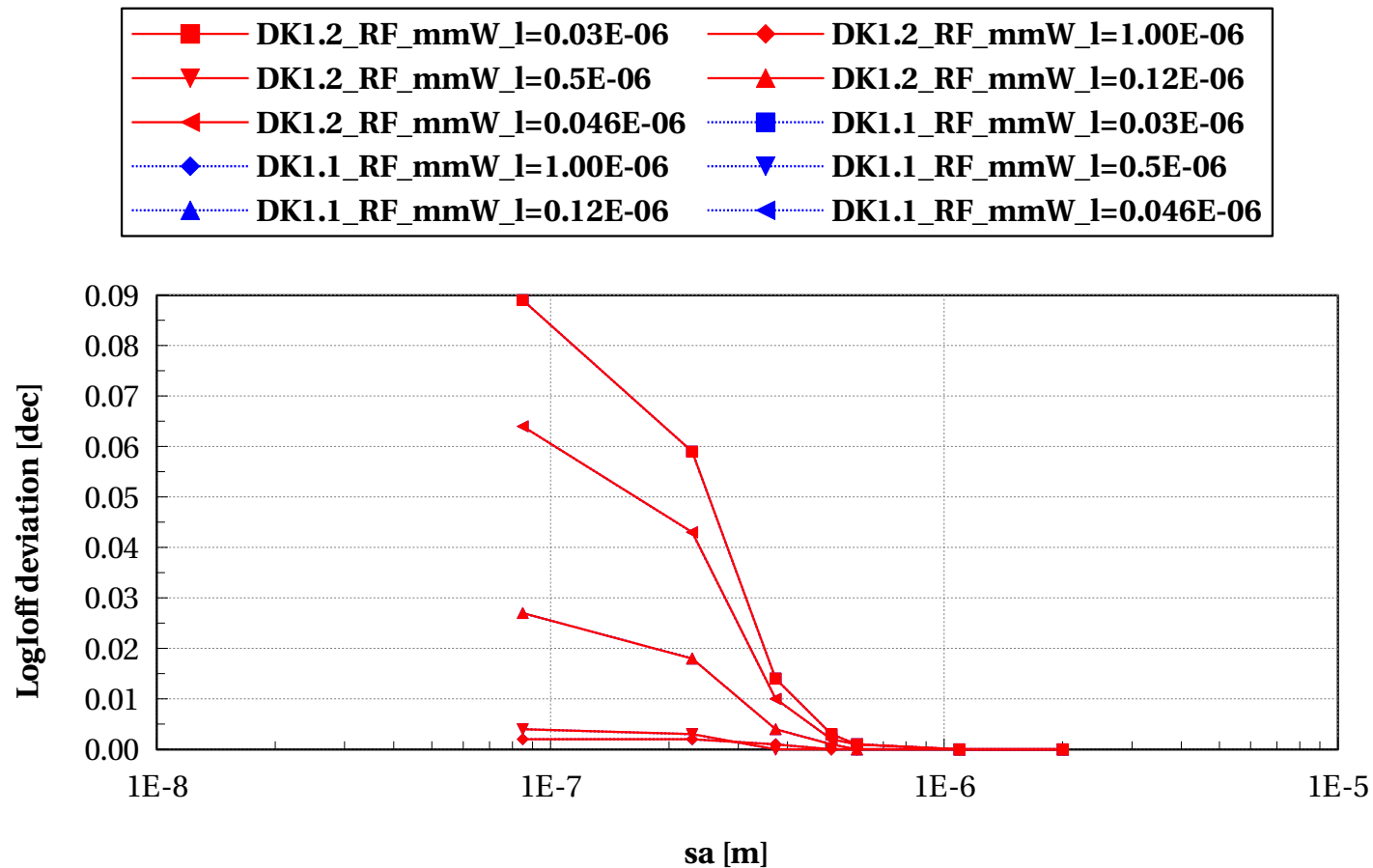
nfet_acc, Isat deviation [%] vs sa [m]

$\rho=25$ and $w=0.1\text{e-}6$ and $p_{\text{la}}=0$ and ($L=0.030\text{e-}6$ or $L=0.046\text{e-}6$ or $L=0.12\text{e-}6$ or $L=0.5\text{e-}6$ or $L=1$)



nfet_acc, LogIoff deviation [dec] vs sa [m]

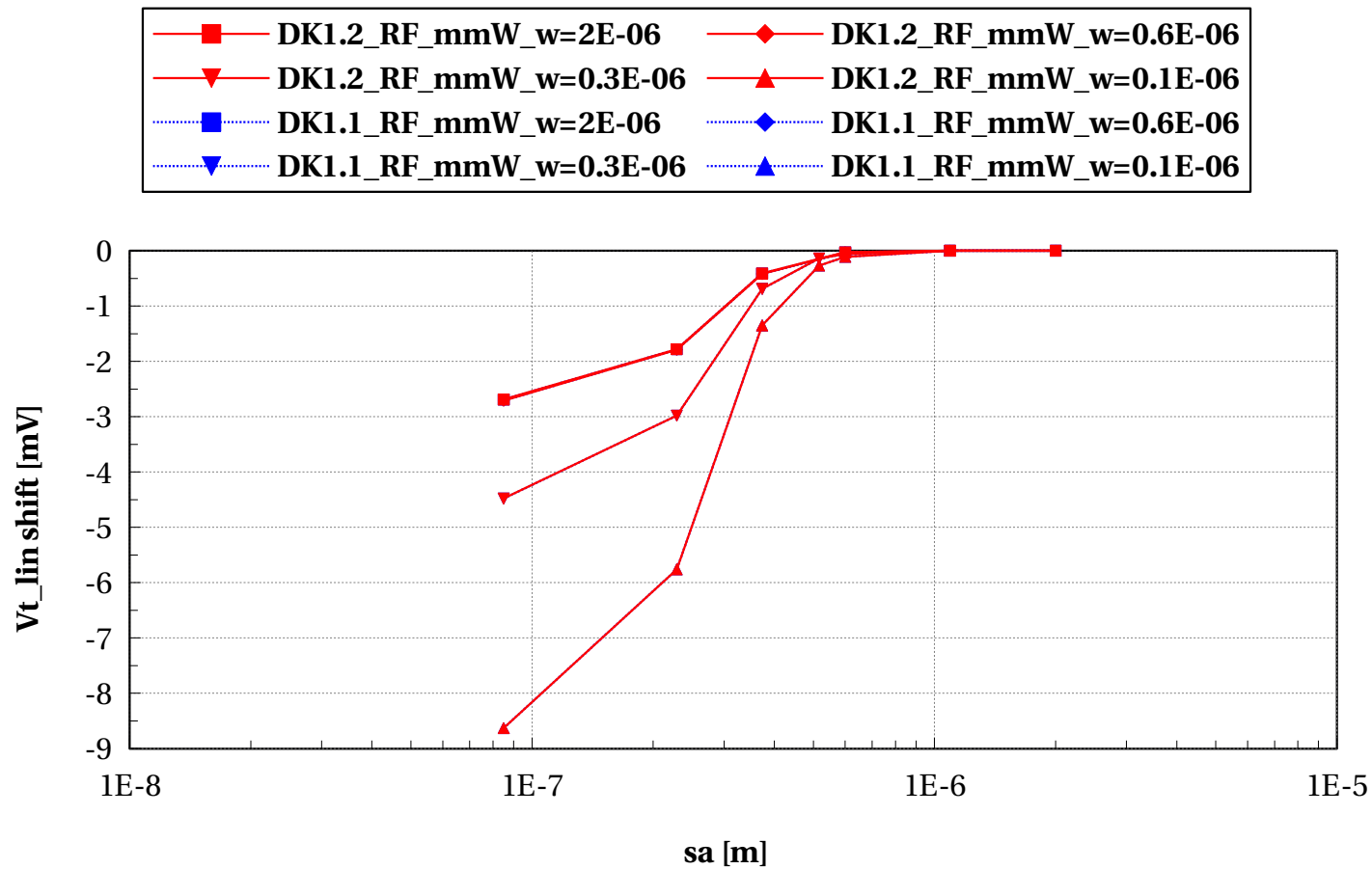
$\rho==25$ and $w==0.1e-6$ and $p_la==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



LOD effect (sa=sb) - Wscaling at L=0.03e-6

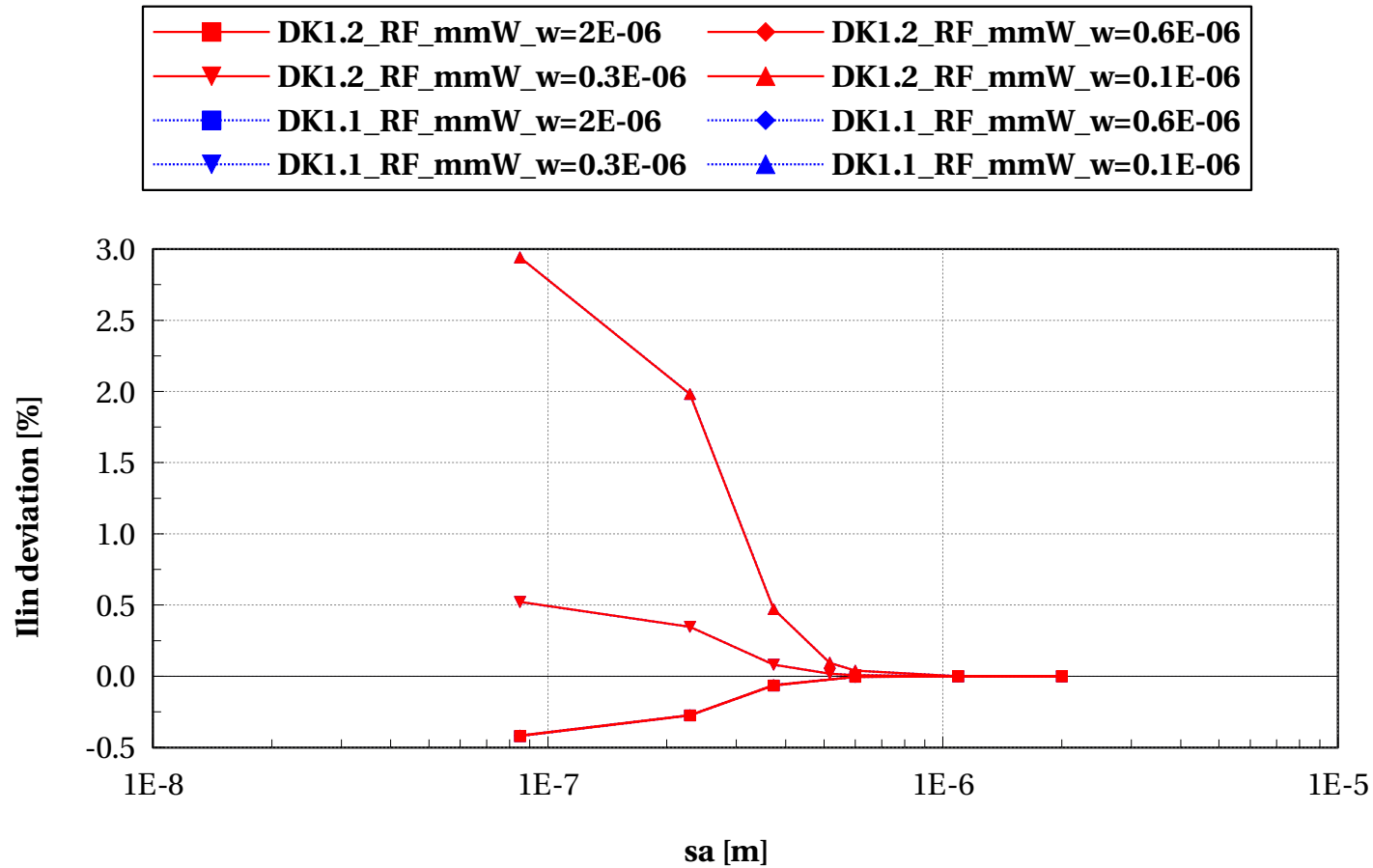
nfet_acc, Vt_lin shift [mV] vs sa [m]

temp==25 and l==0.03e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



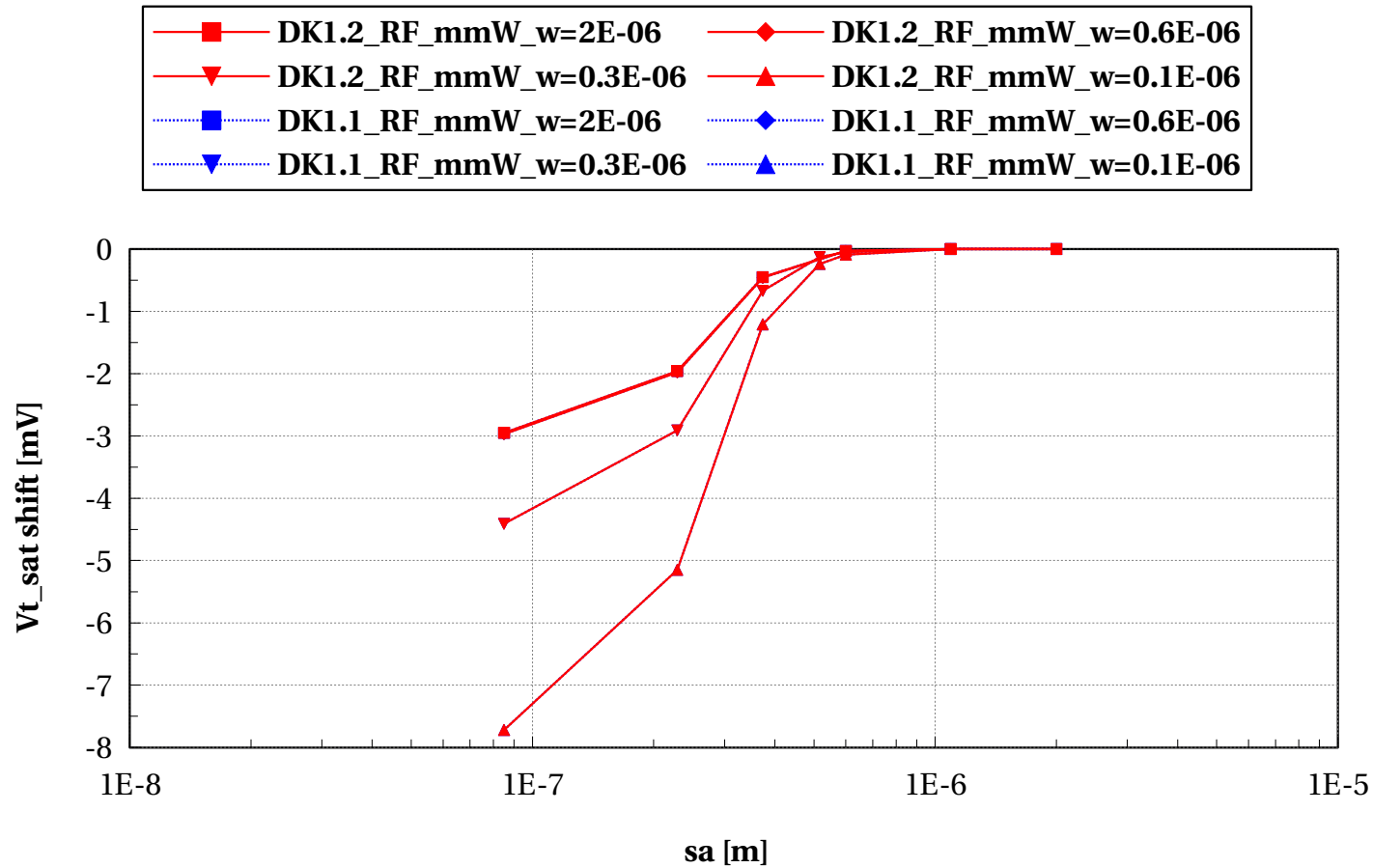
nfet_acc, Ilin deviation [%] vs sa [m]

temp==25 and l==0.03e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



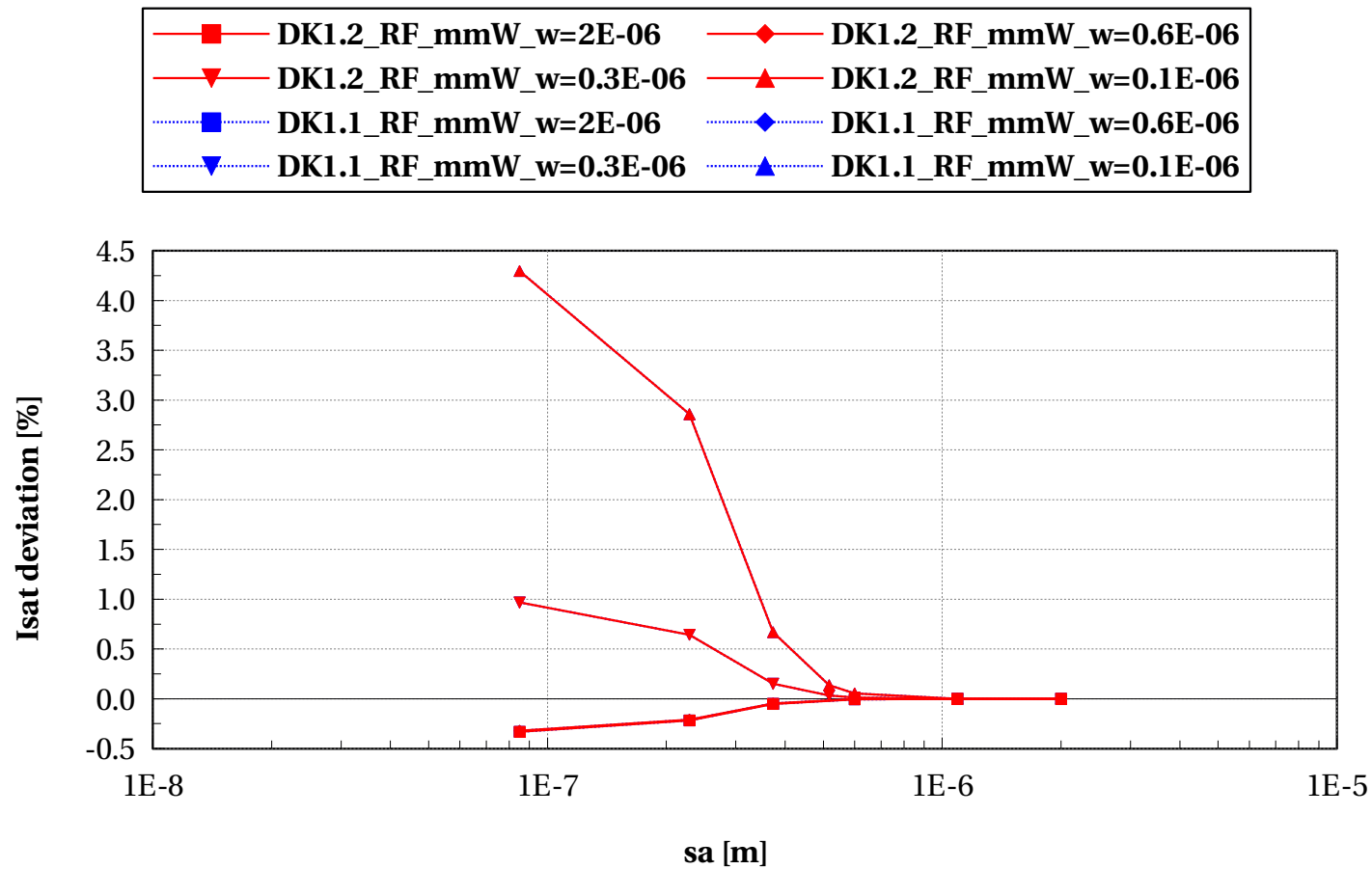
nfet_acc, Vt_sat shift [mV] vs sa [m]

temp==25 and l==0.03e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



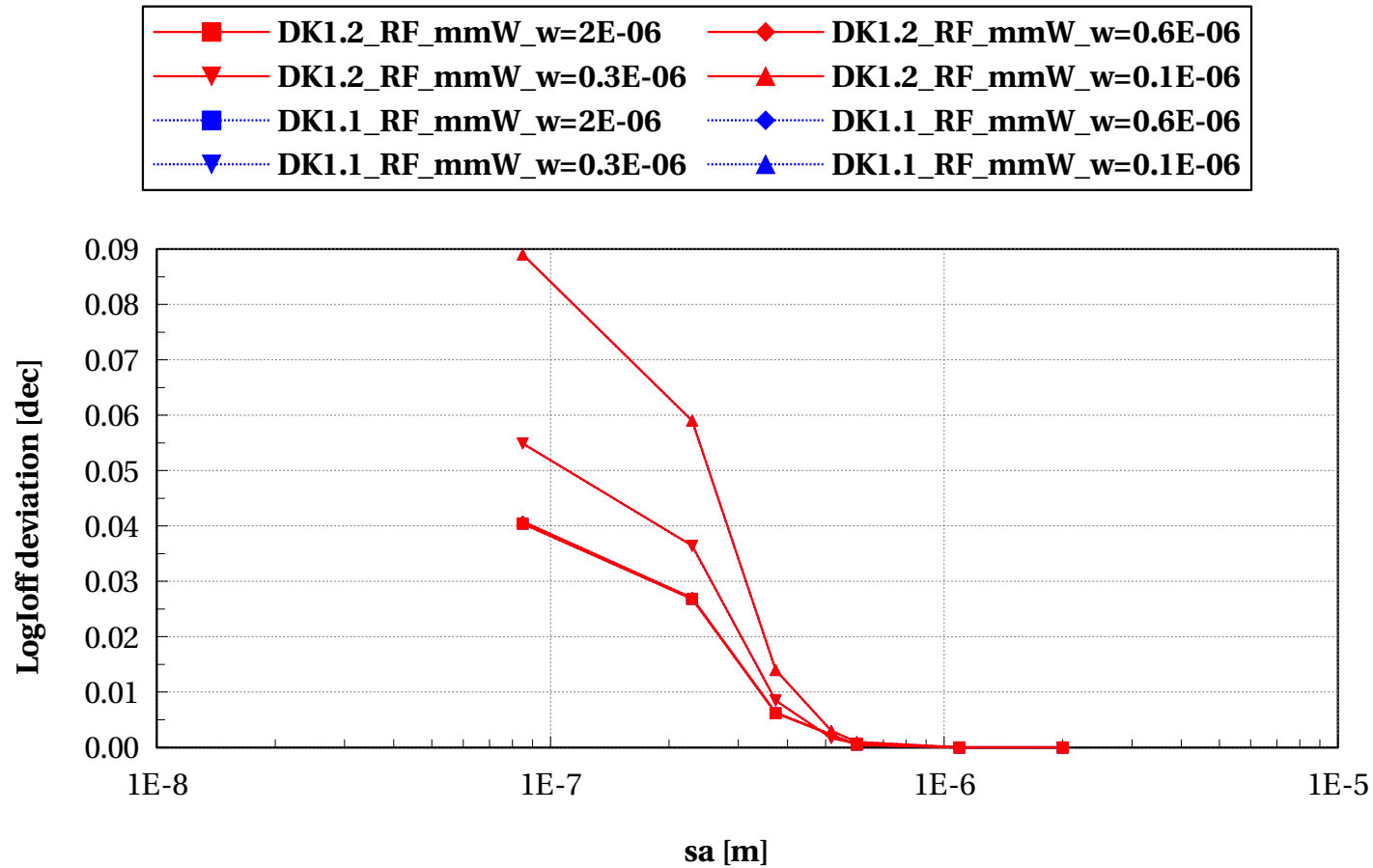
nfet_acc, Isat deviation [%] vs sa [m]

temp==25 and l==0.03e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



nfet_acc, Logloff deviation [dec] vs sa [m]

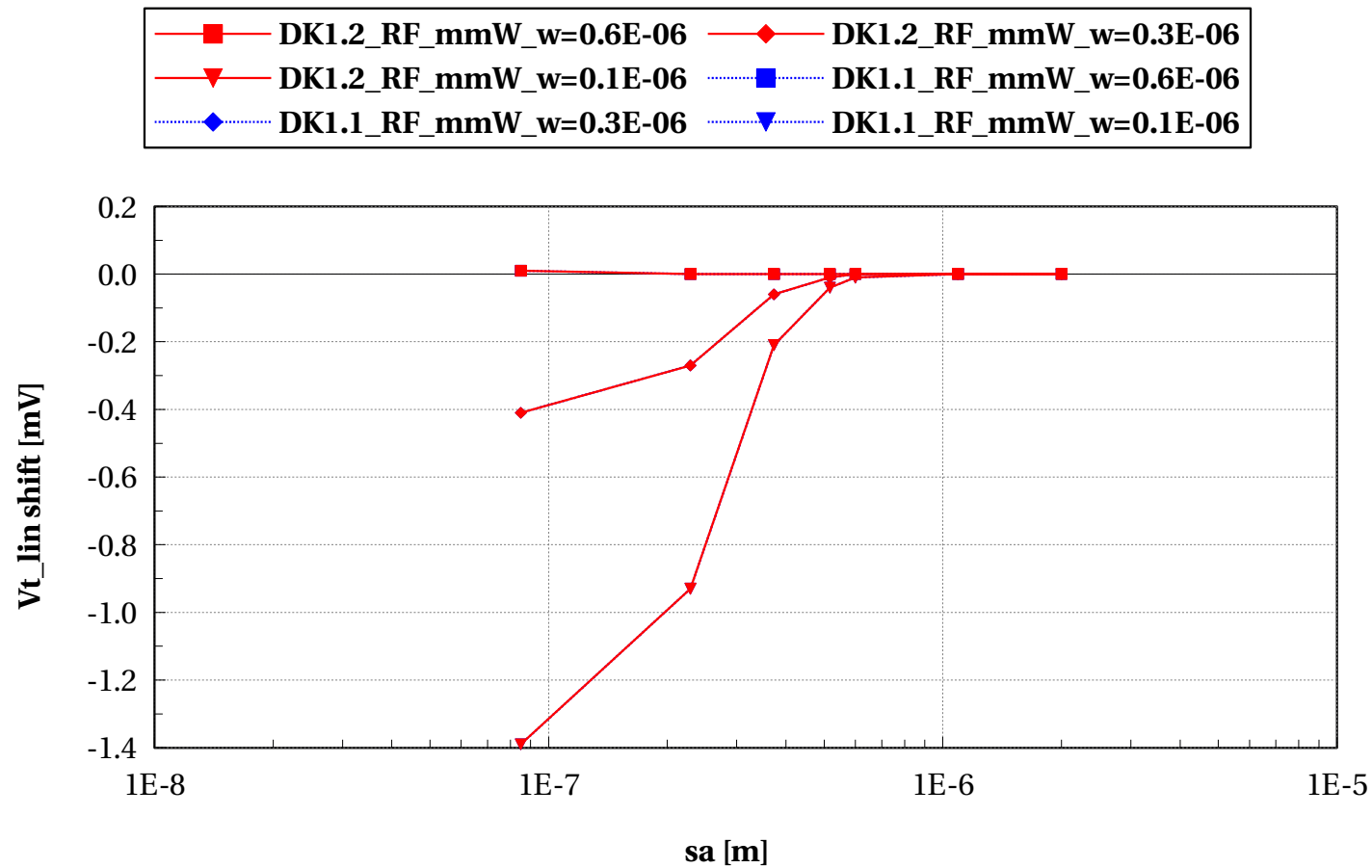
temp==25 and l==0.03e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



LOD effect (sa=sb) - Wscaling at L=1e-6

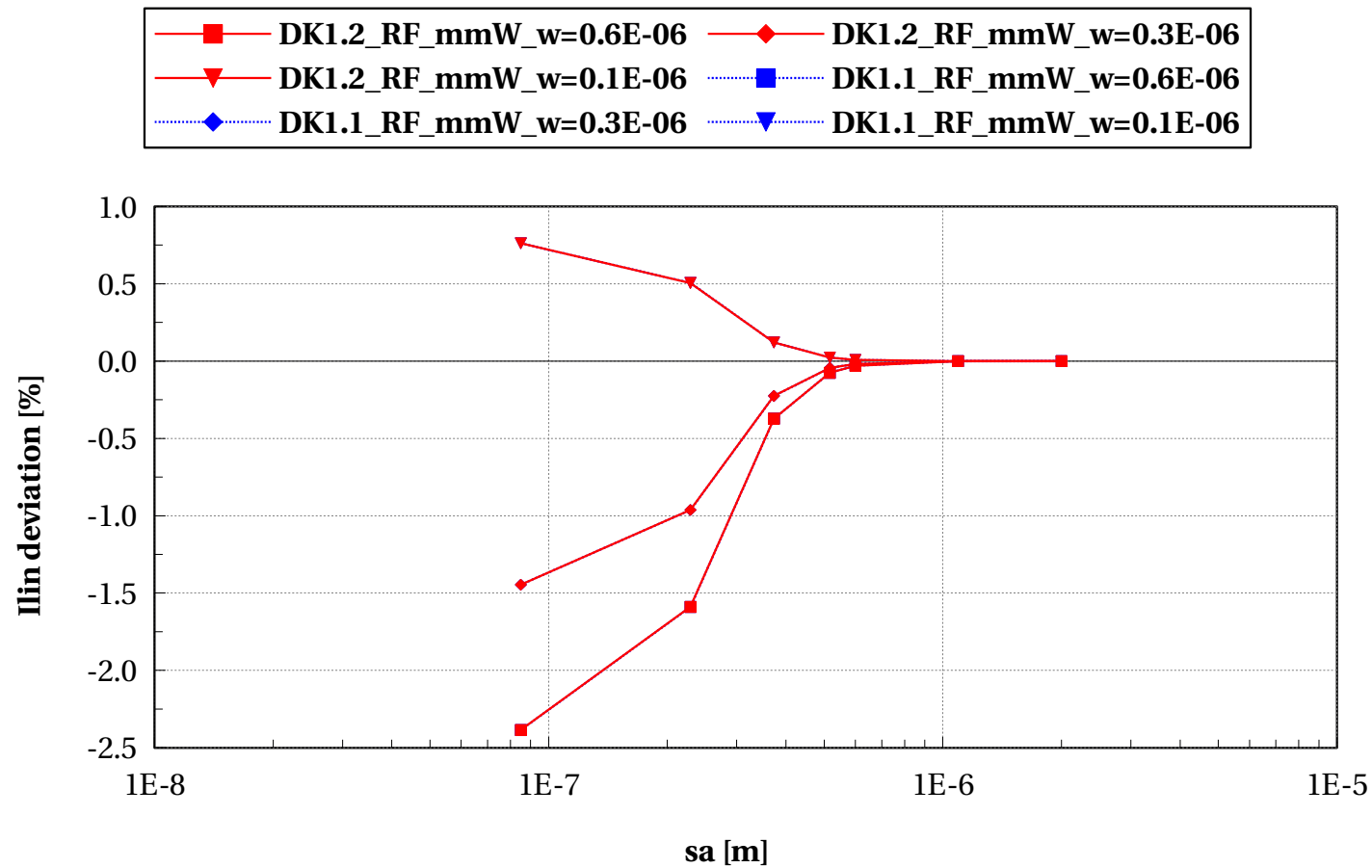
nfet_acc, Vt_lin shift [mV] vs sa [m]

temp==25 and l==1.0e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



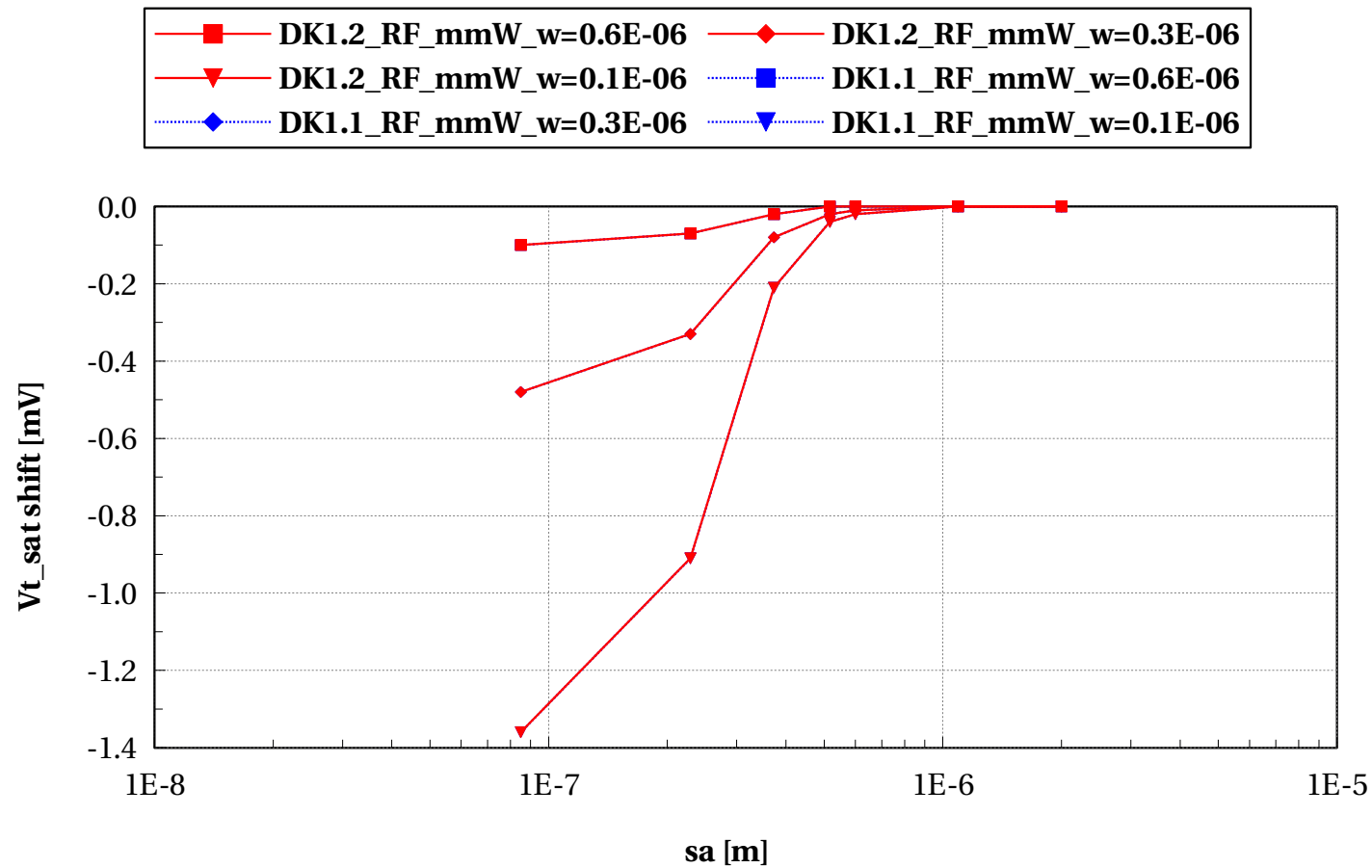
nfet_acc, Ilin deviation [%] vs sa [m]

temp==25 and l==1.0e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



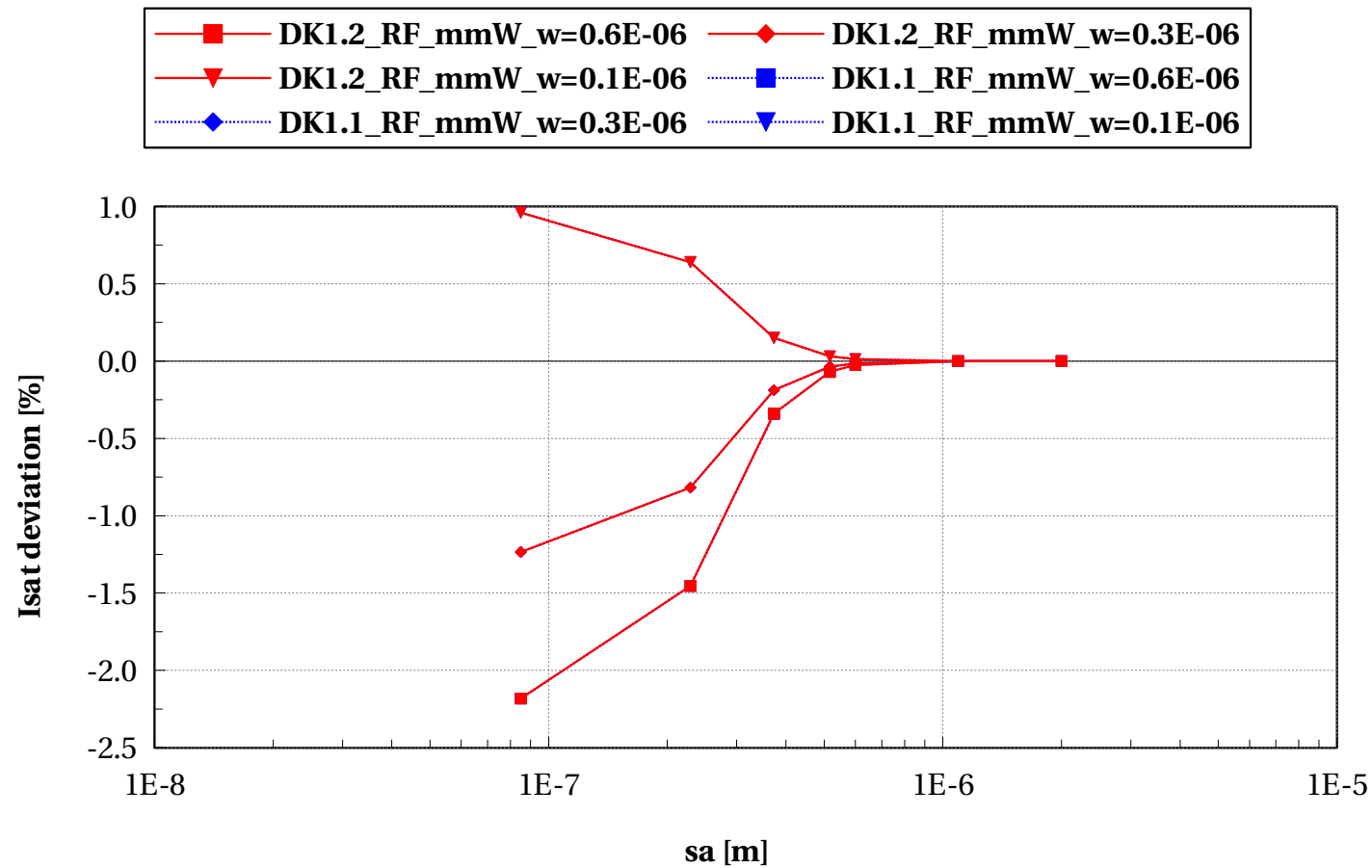
nfet_acc, Vt_sat shift [mV] vs sa [m]

temp==25 and l==1.0e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



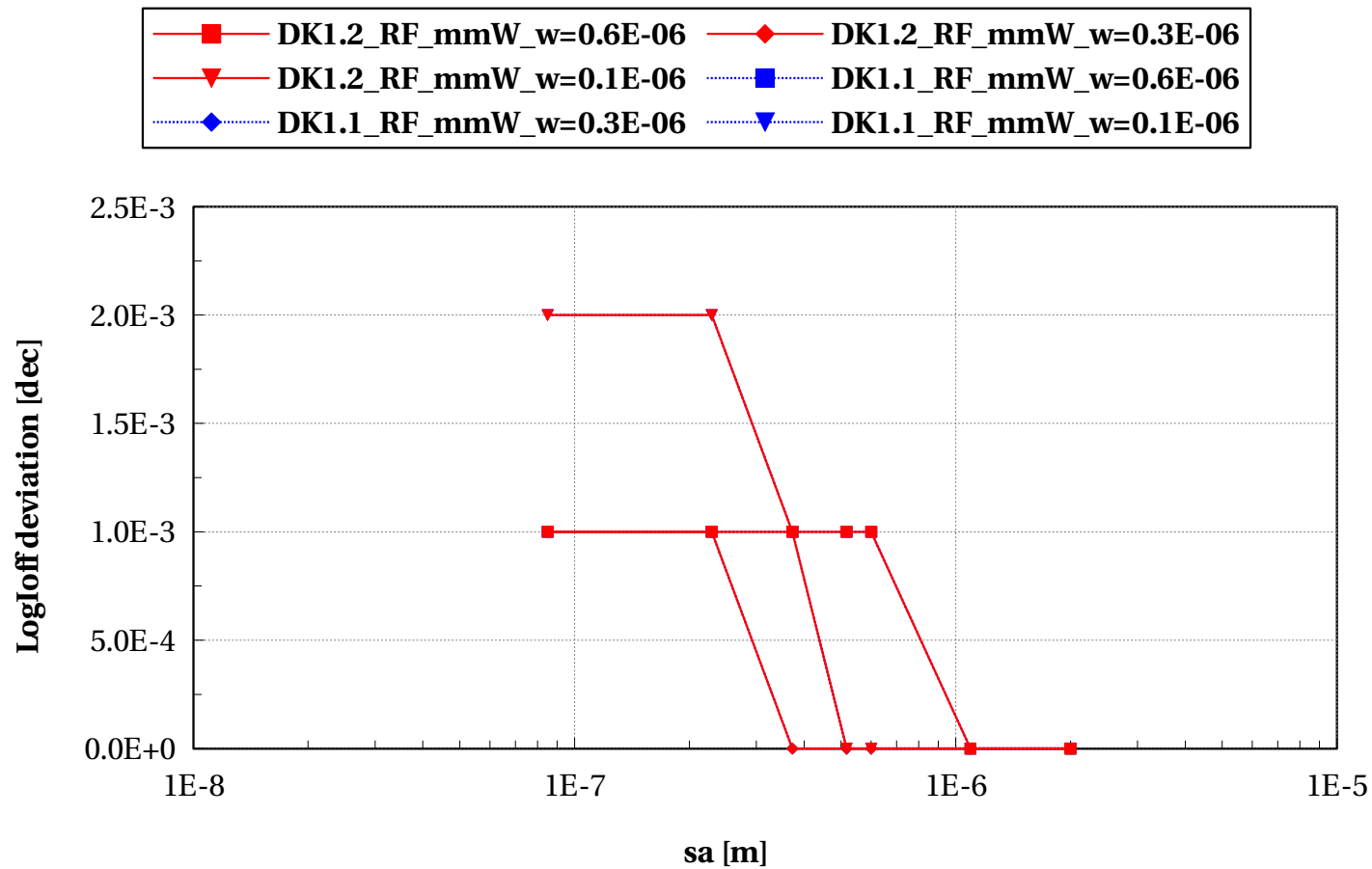
nfet_acc, Isat deviation [%] vs sa [m]

temp==25 and l==1.0e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



nfet_acc, LogIoff deviation [dec] vs sa [m]

temp==25 and l==1.0e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



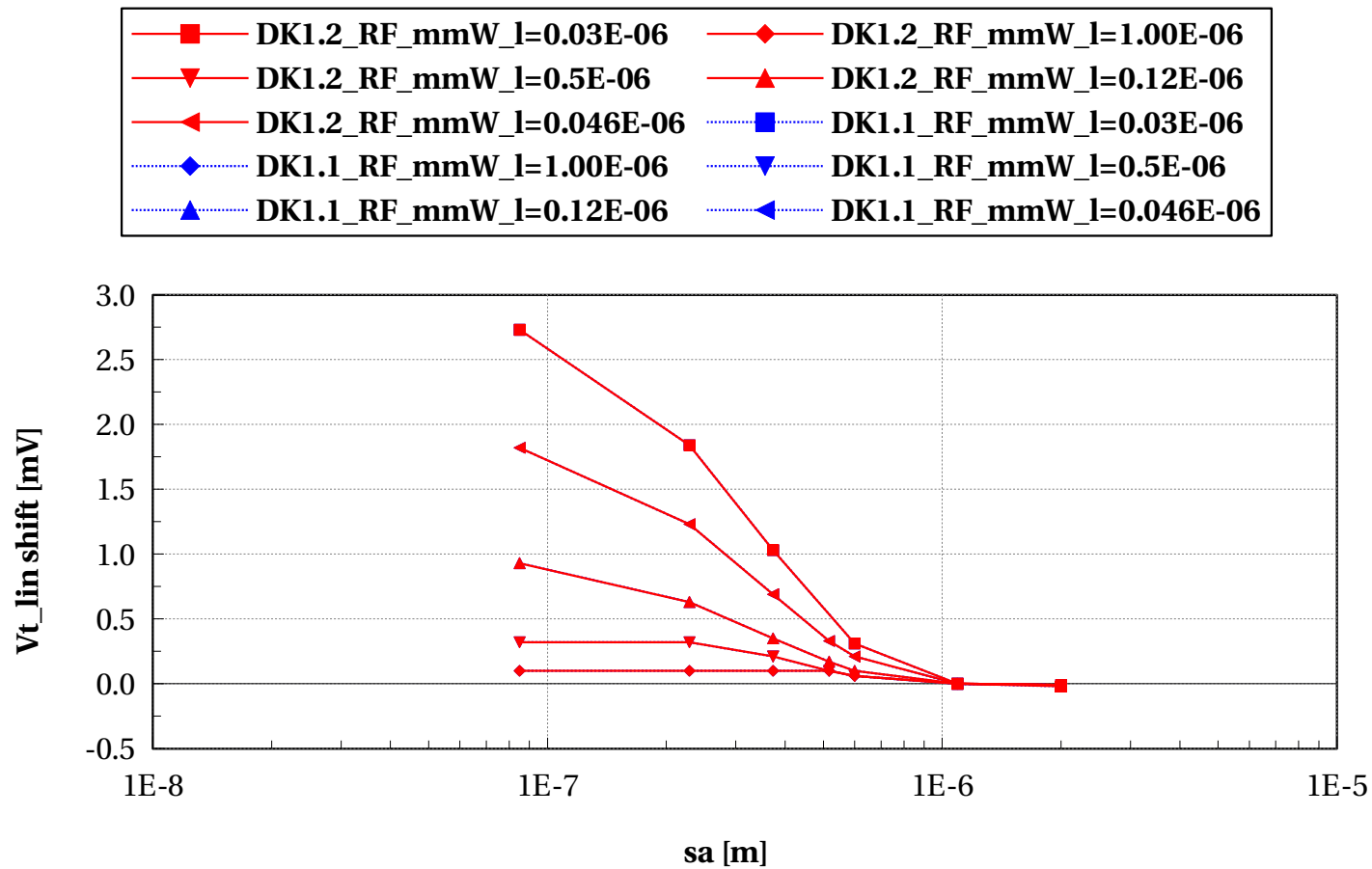
pfet_acc

Electrical characteristics scaling

LOD effect (sa=sb) - Lscaling at $W=1e-6$

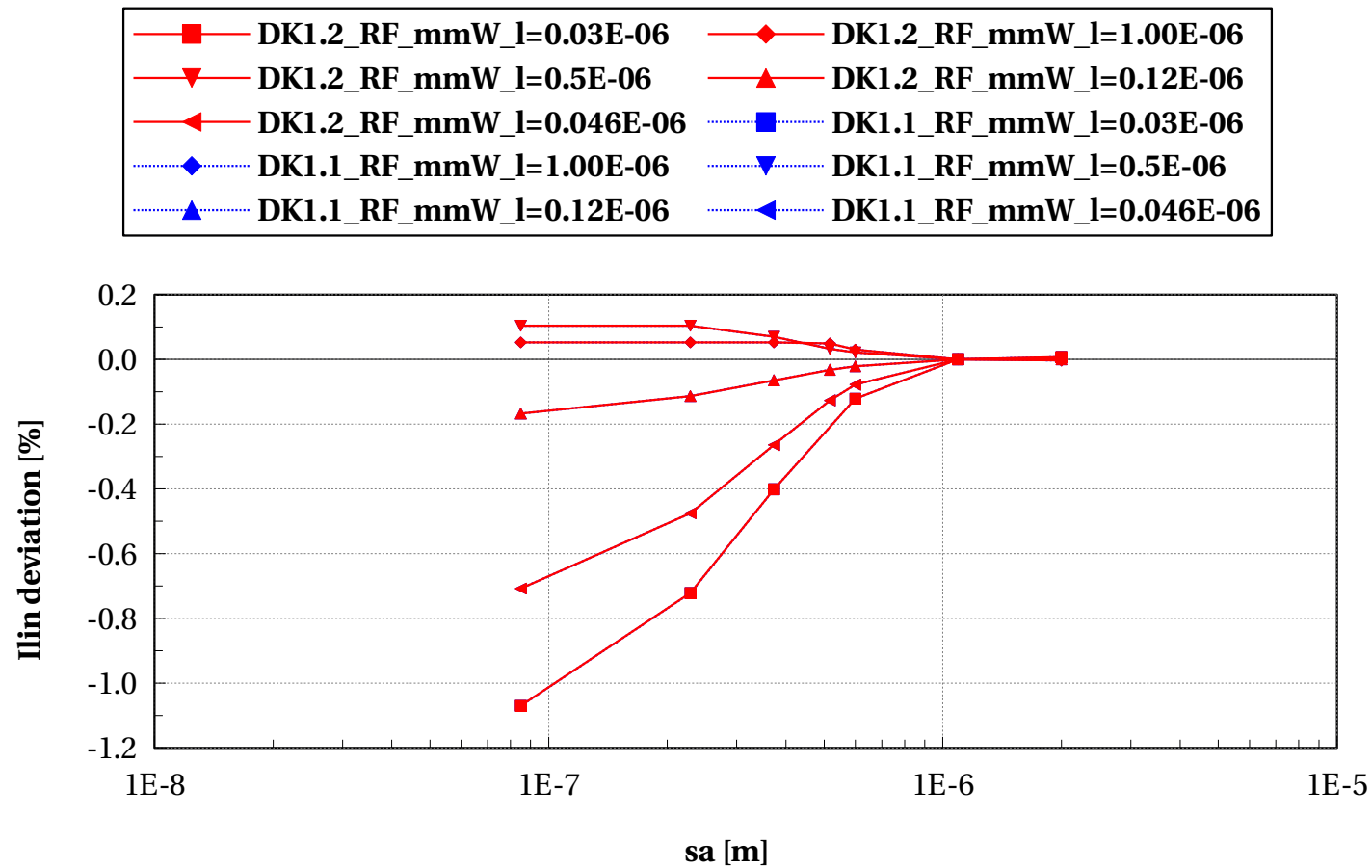
pfet_acc, Vt_lin shift [mV] vs sa [m]

ip==25 and w==1e-6 and p_la==0 and (L==0.030e-6 or L==0.046e-6 or L==0.12e-6 or L==0.5e-6 or L==1e-6)



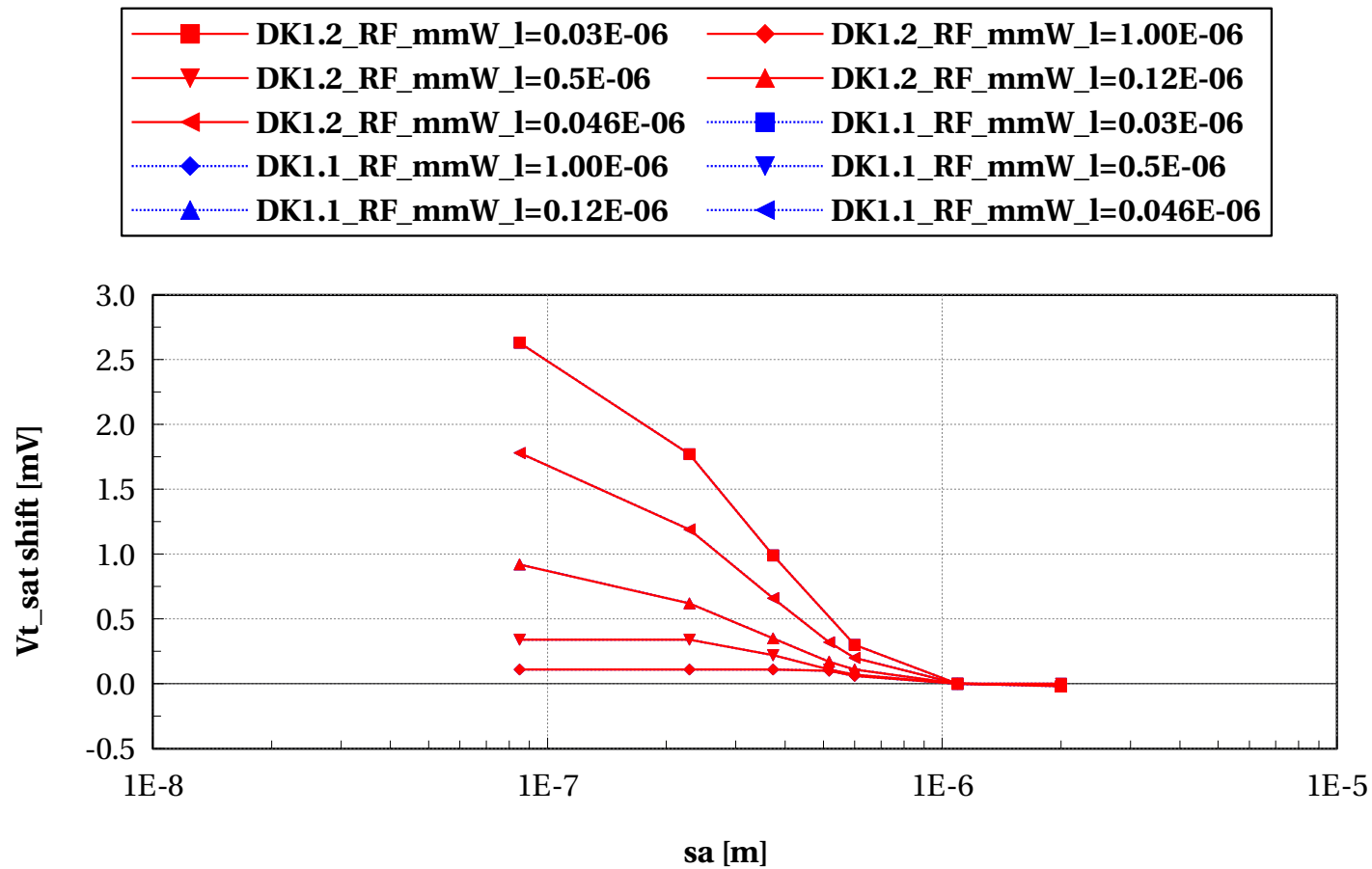
pfet_acc, Ilin deviation [%] vs sa [m]

ip==25 and w==1e-6 and p_la==0 and (L==0.030e-6 or L==0.046e-6 or L==0.12e-6 or L==0.5e-6 or L==1e-6)



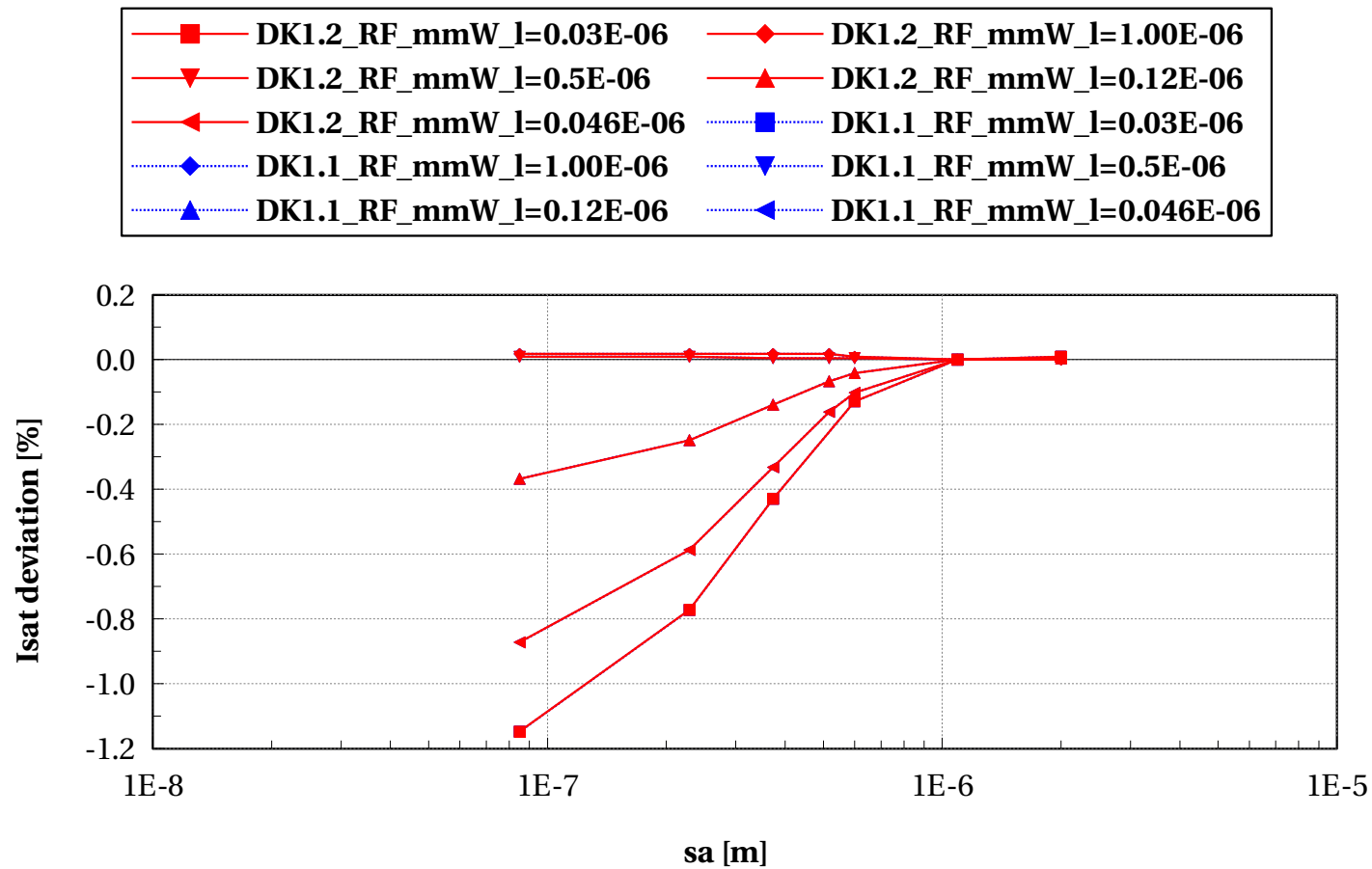
pfet_acc, Vt_sat shift [mV] vs sa [m]

$i_p=25$ and $w=1e-6$ and $p_{la}=0$ and ($L=0.030e-6$ or $L=0.046e-6$ or $L=0.12e-6$ or $L=0.5e-6$ or $L=1e-6$)



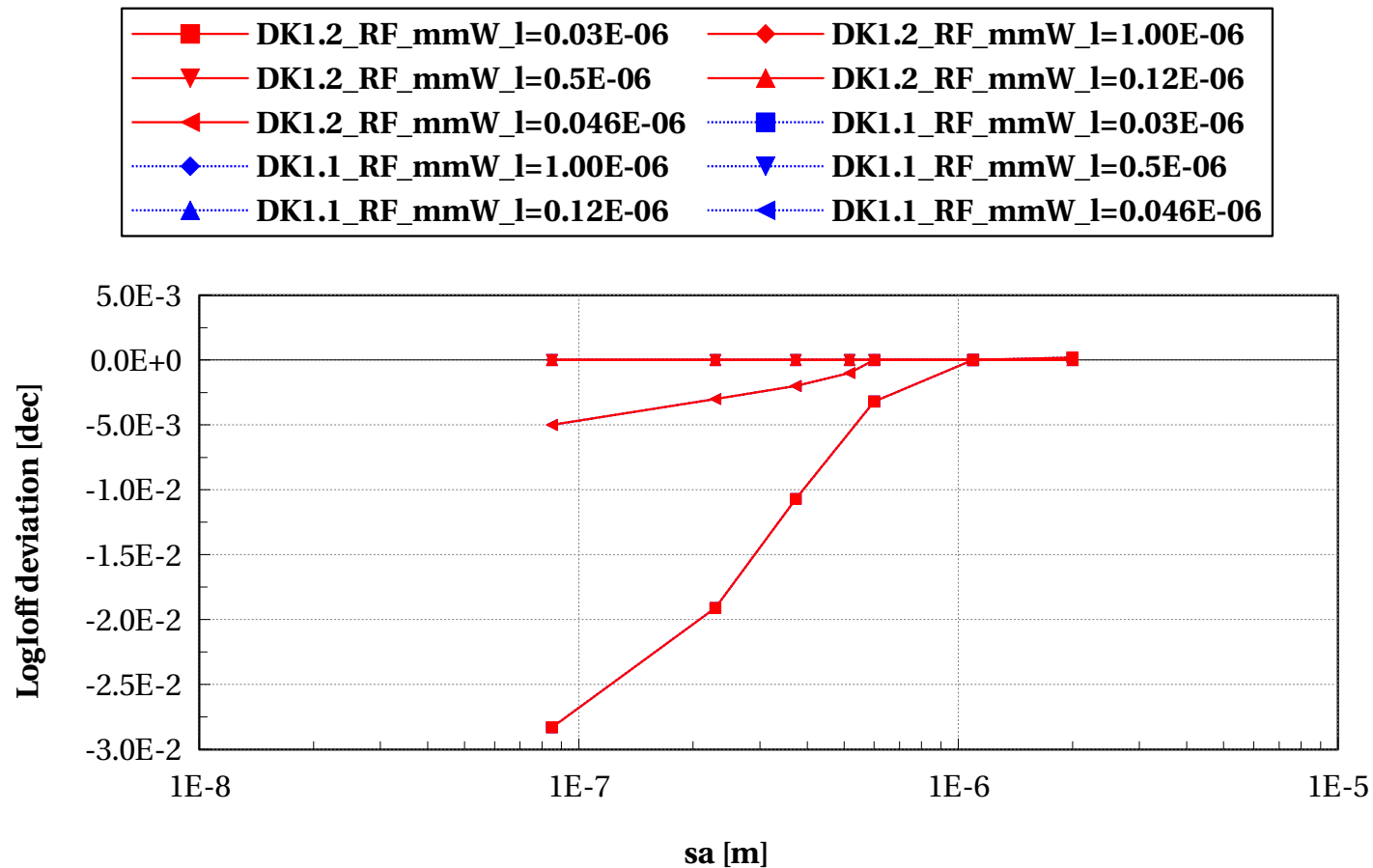
pfet_acc, Isat deviation [%] vs sa [m]

ip==25 and w==1e-6 and p_la==0 and (L==0.030e-6 or L==0.046e-6 or L==0.12e-6 or L==0.5e-6 or L==1e-6)



pfet_acc, LogIoff deviation [dec] vs sa [m]

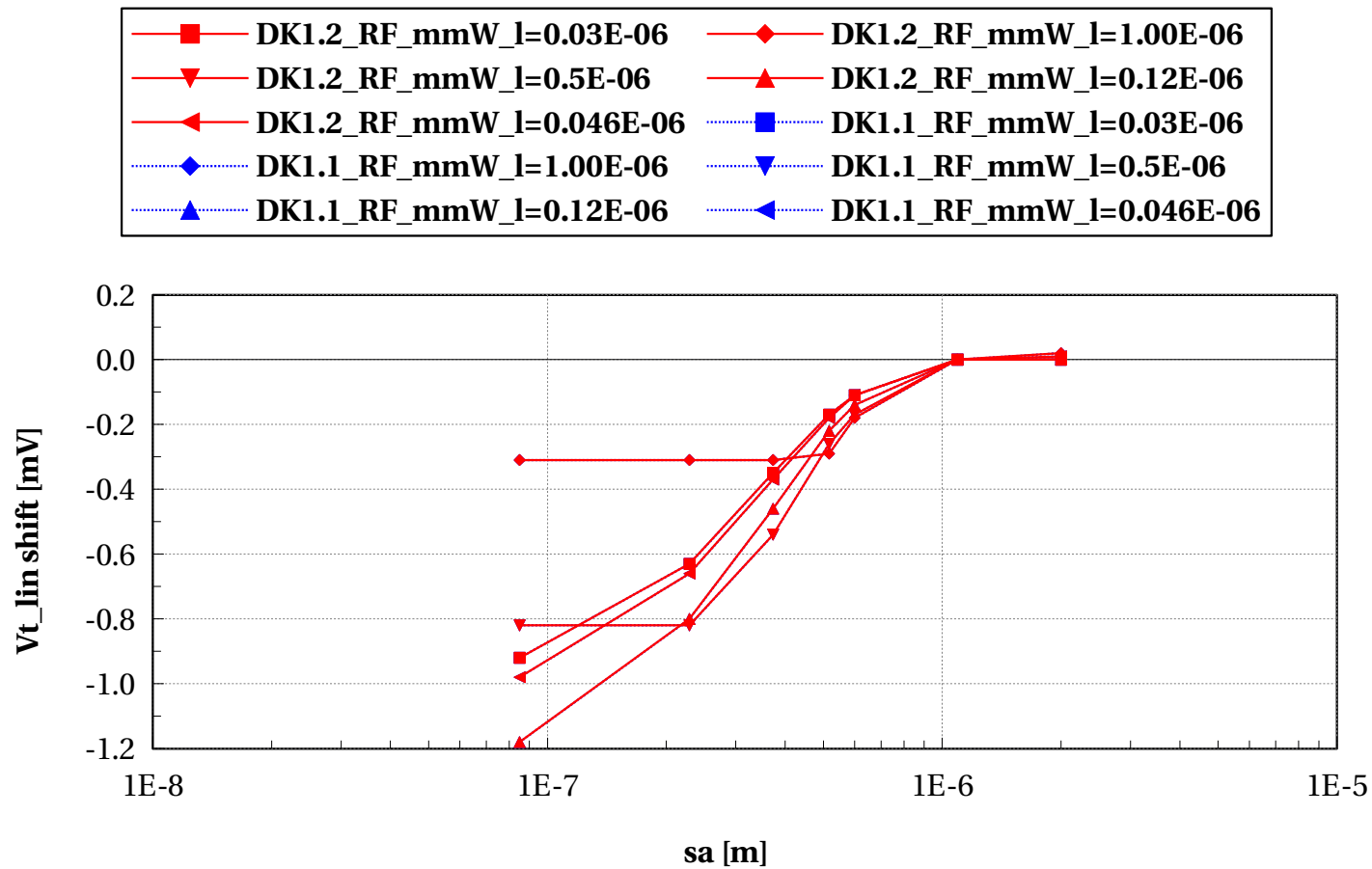
$i_p=25$ and $w=1e-6$ and $p_{la}=0$ and ($L=0.030e-6$ or $L=0.046e-6$ or $L=0.12e-6$ or $L=0.5e-6$ or $L=1e-6$)



LOD effect (sa=sb) - Lscaling at $W=0.3e-6$

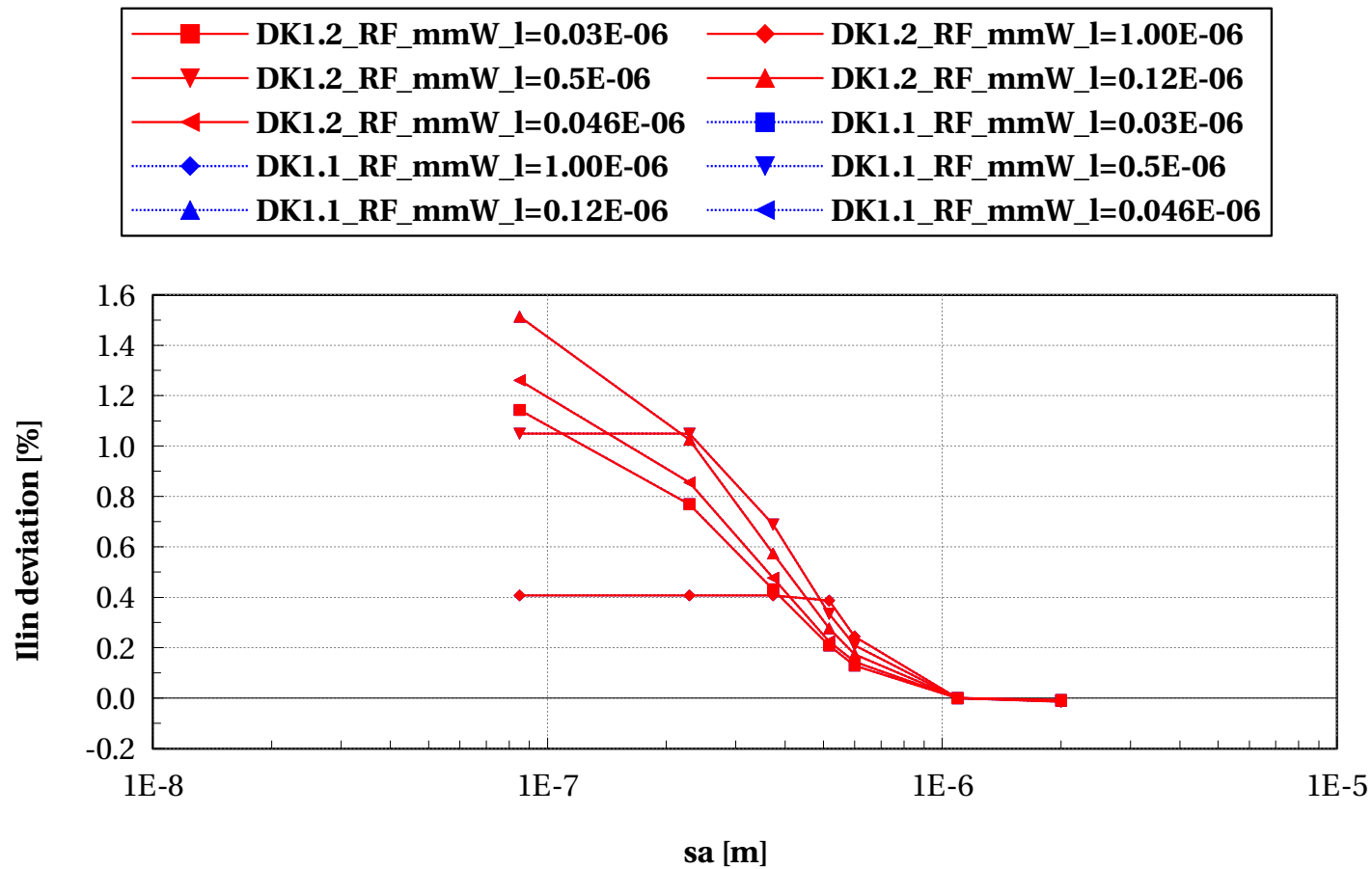
pfet_acc, Vt_lin shift [mV] vs sa [m]

$\rho=25$ and $w=0.3e-6$ and $p_{la}=0$ and ($L=0.030e-6$ or $L=0.046e-6$ or $L=0.12e-6$ or $L=0.5e-6$ or $L=1$)



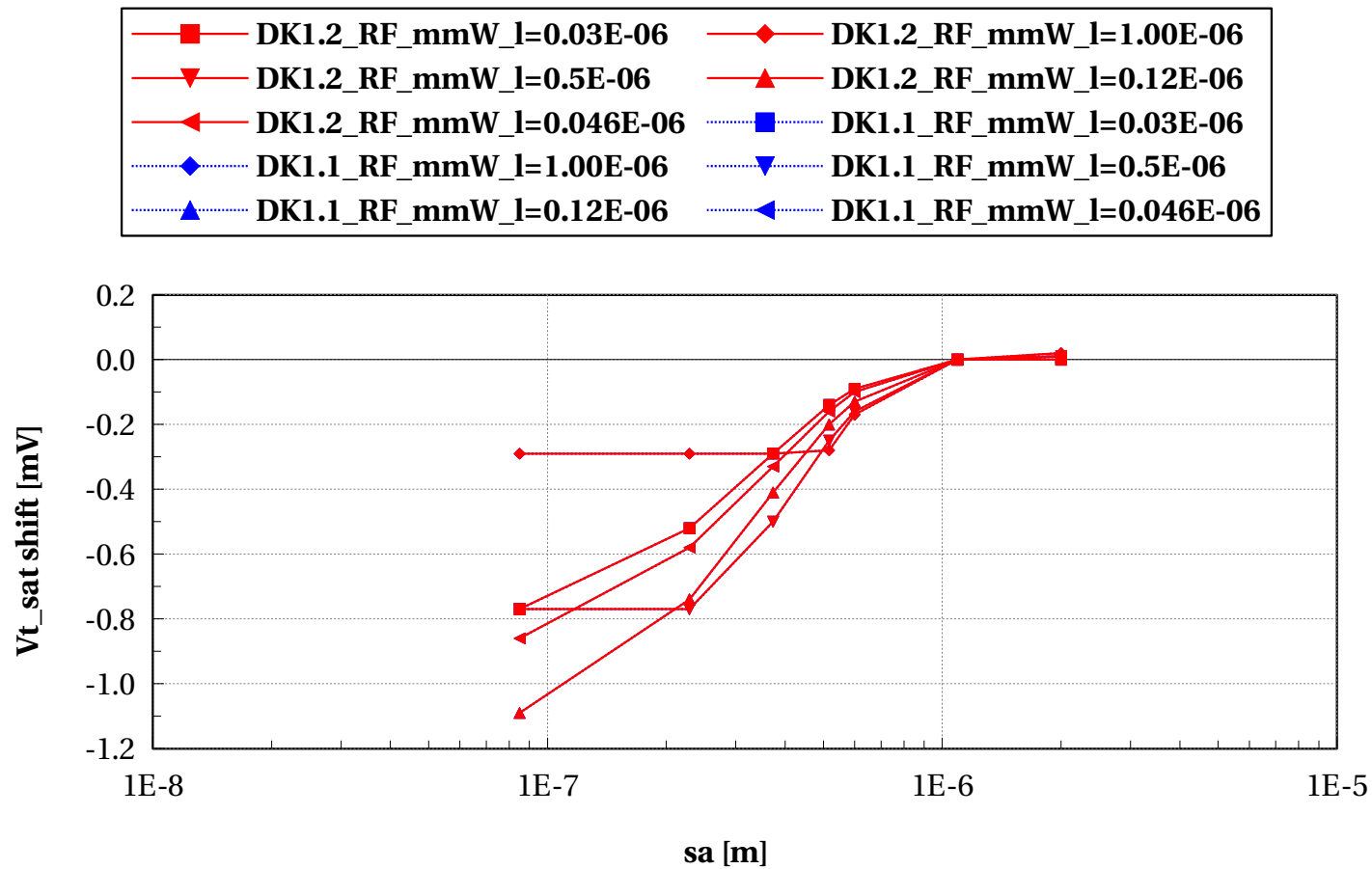
pfet_acc, Ilin deviation [%] vs sa [m]

$\rho=25$ and $w=0.3e-6$ and $p_{la}=0$ and ($L=0.030e-6$ or $L=0.046e-6$ or $L=0.12e-6$ or $L=0.5e-6$ or $L=1$)



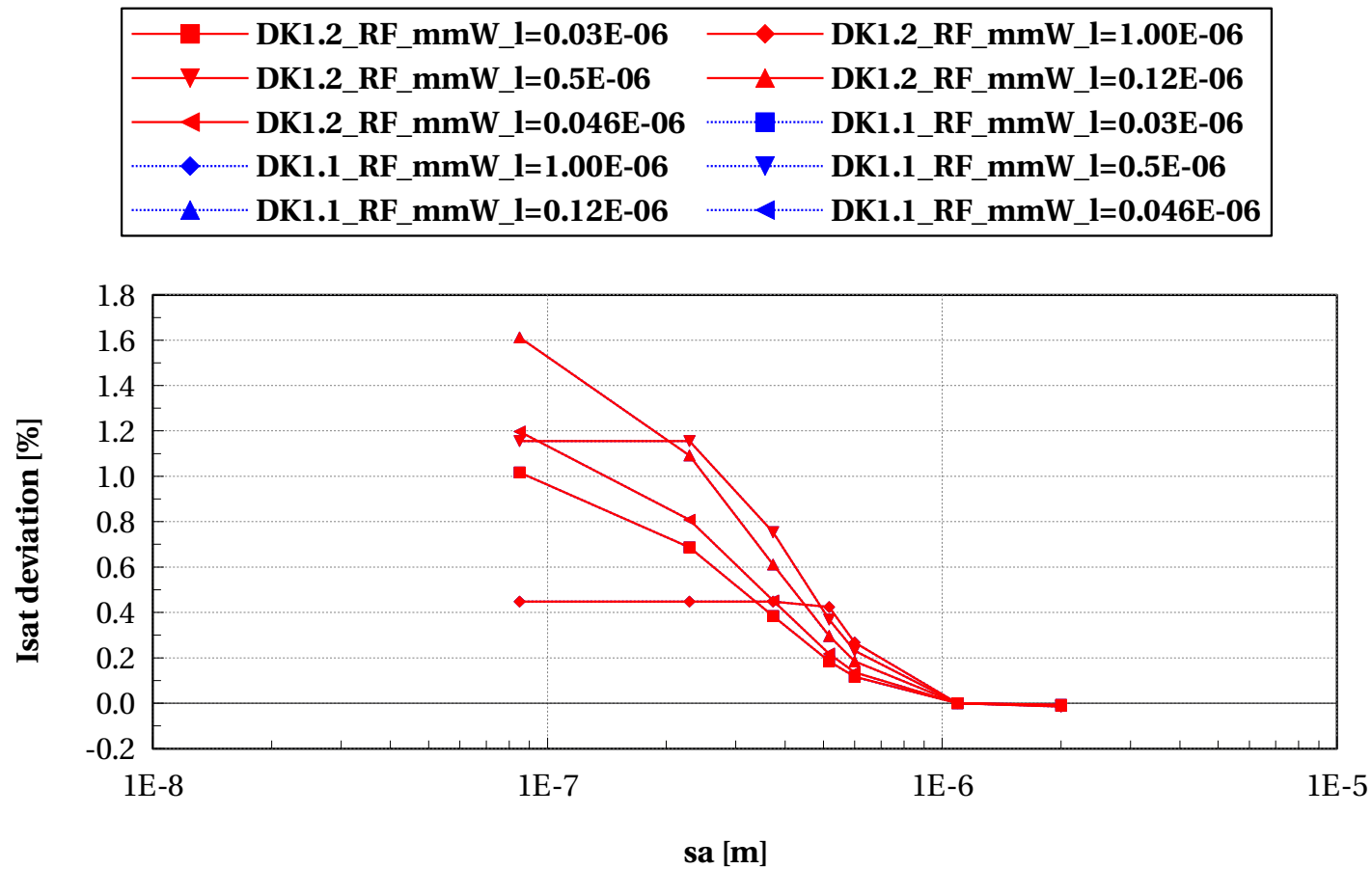
pfet_acc, Vt_sat shift [mV] vs sa [m]

$\rho=25$ and $w=0.3e-6$ and $p_{la}=0$ and ($L=0.030e-6$ or $L=0.046e-6$ or $L=0.12e-6$ or $L=0.5e-6$ or $L=1$)



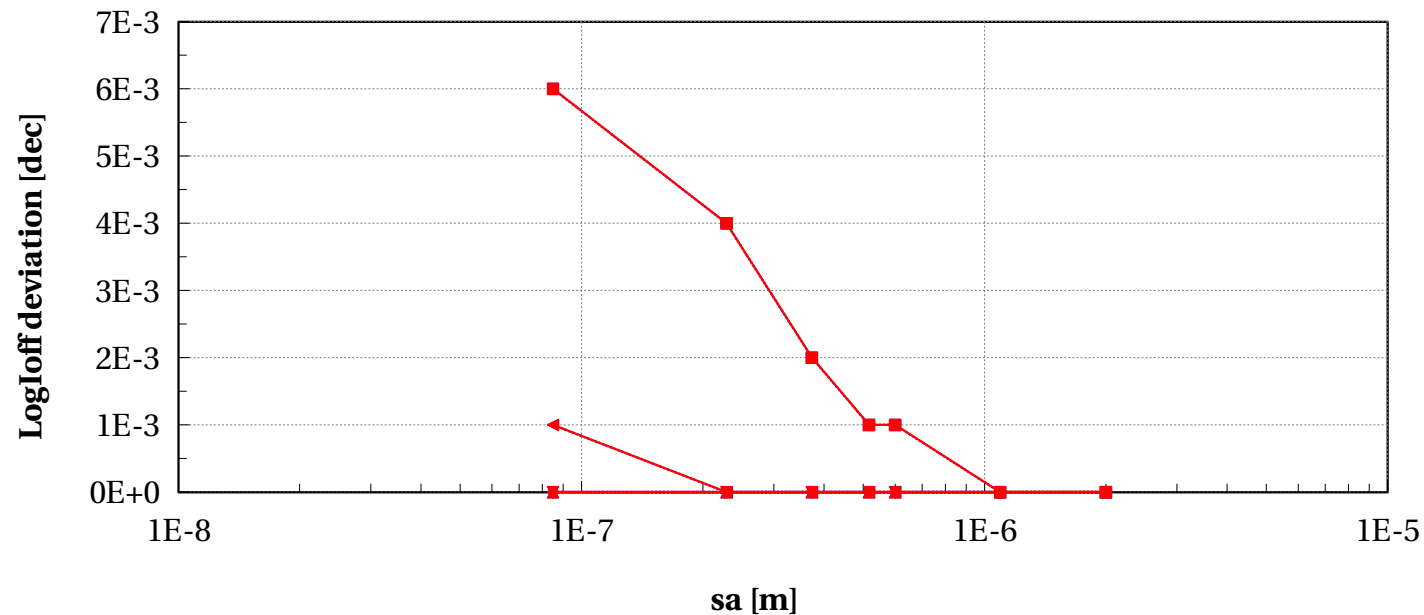
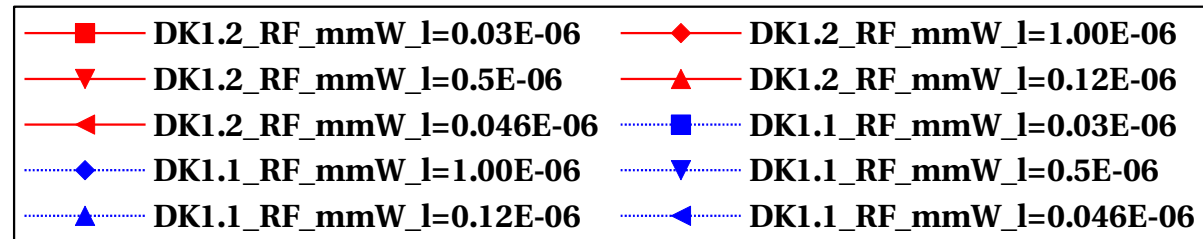
pfet_acc, Isat deviation [%] vs sa [m]

$\rho=25$ and $w=0.3e-6$ and $p_{la}=0$ and ($L=0.030e-6$ or $L=0.046e-6$ or $L=0.12e-6$ or $L=0.5e-6$ or $L=1$)



pfet_acc, LogIoff deviation [dec] vs sa [m]

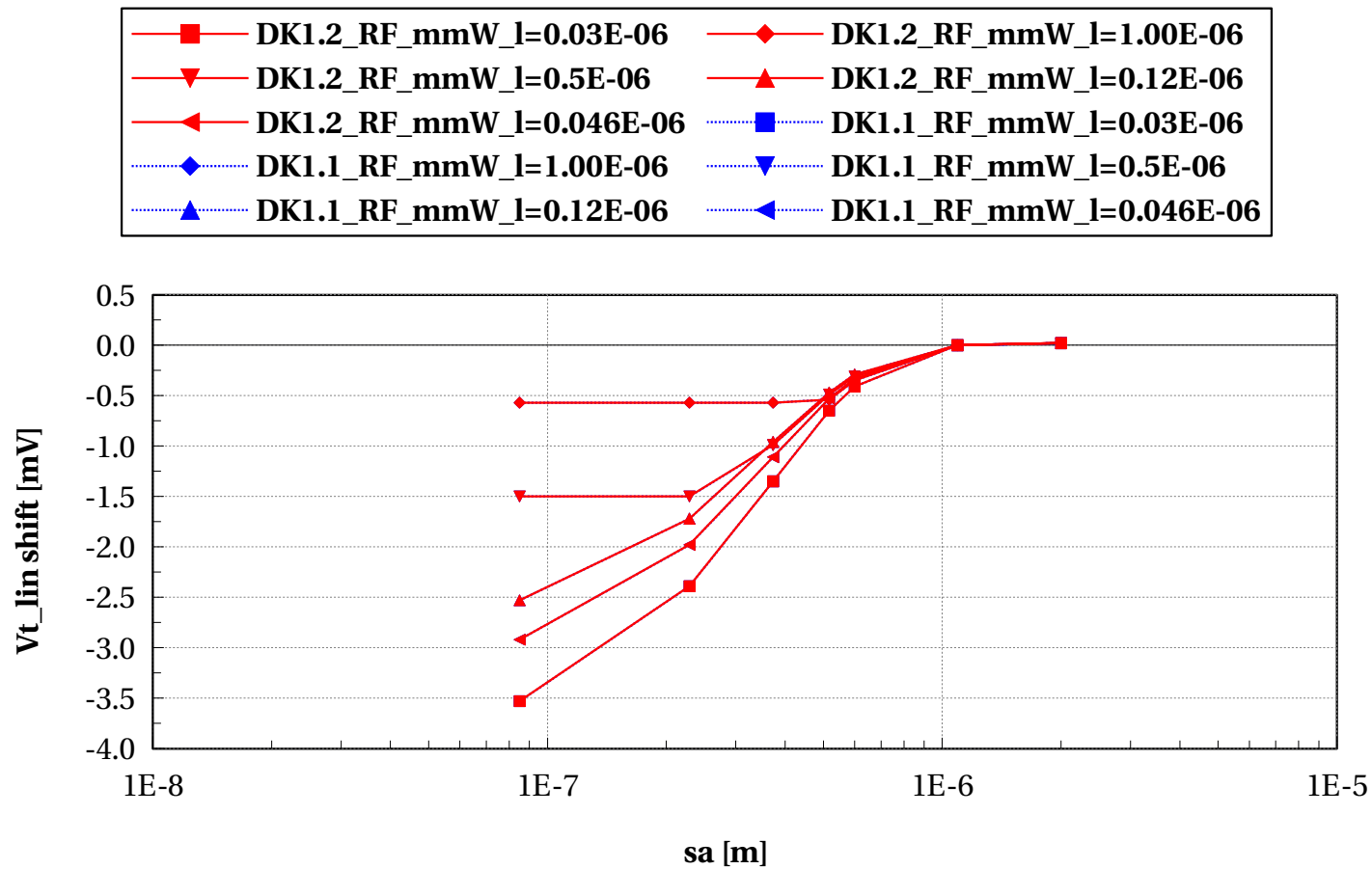
$\rho==25$ and $w==0.3e-6$ and $p_la==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



LOD effect (sa=sb) - Lscaling at $W=0.1\text{e-}6$

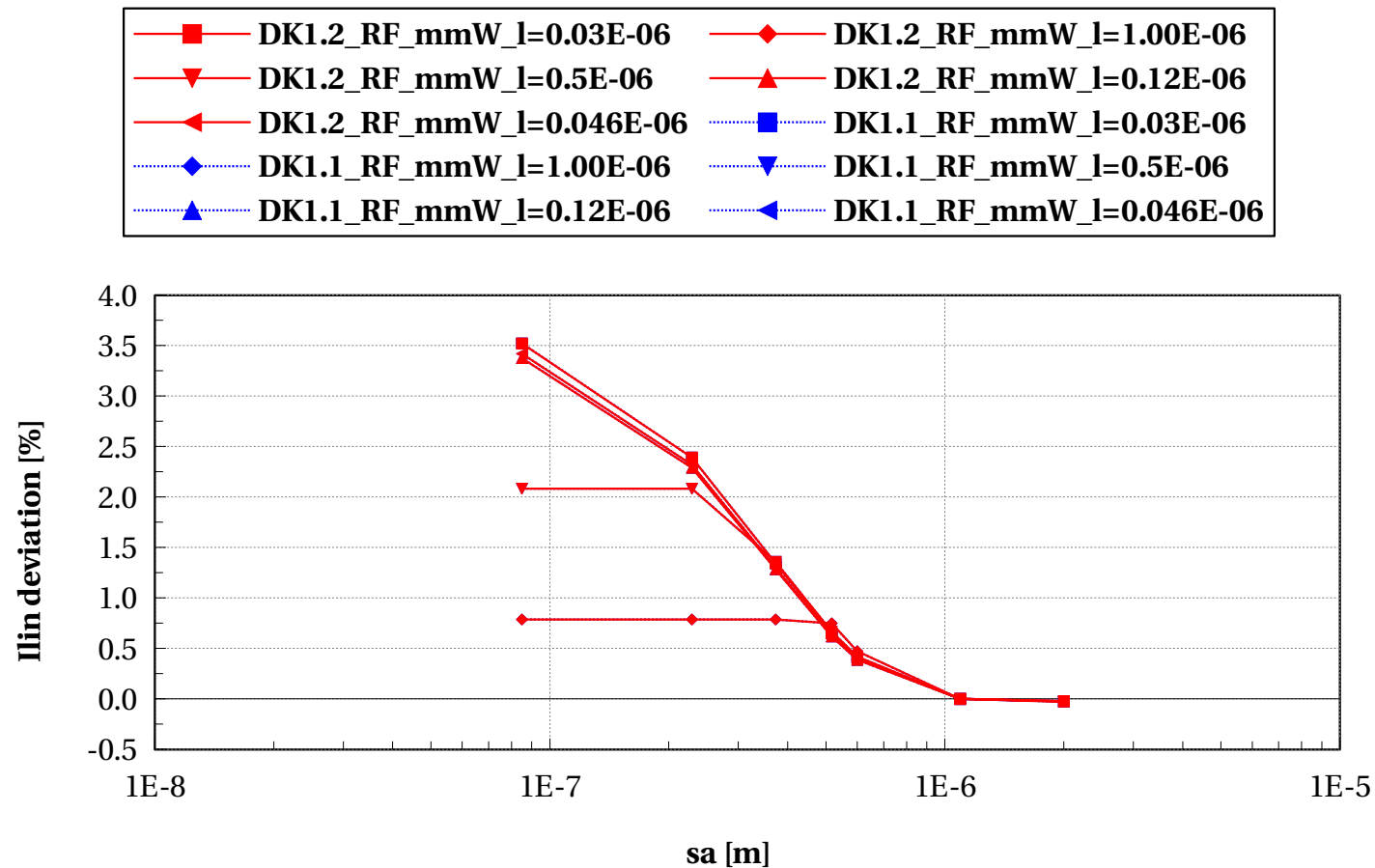
pfet_acc, Vt_lin shift [mV] vs sa [m]

$\rho==25$ and $w==0.1\text{e-}6$ and $p_la==0$ and ($L==0.030\text{e-}6$ or $L==0.046\text{e-}6$ or $L==0.12\text{e-}6$ or $L==0.5\text{e-}6$ or $L==1$)



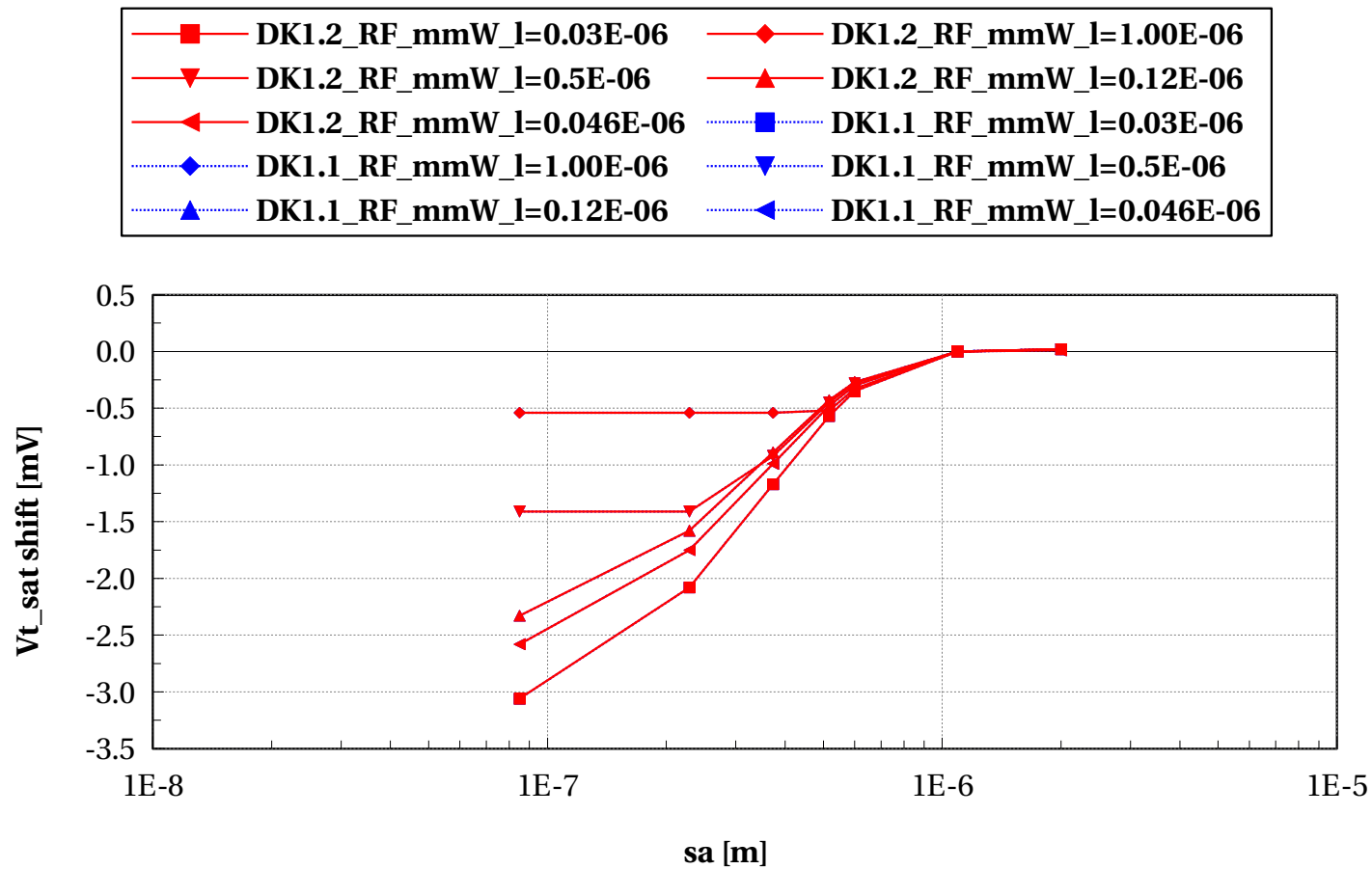
pfet_acc, Ilin deviation [%] vs sa [m]

$\rho=25$ and $w=0.1\text{e-}6$ and $p_{\text{la}}=0$ and ($L=0.030\text{e-}6$ or $L=0.046\text{e-}6$ or $L=0.12\text{e-}6$ or $L=0.5\text{e-}6$ or $L=1$)



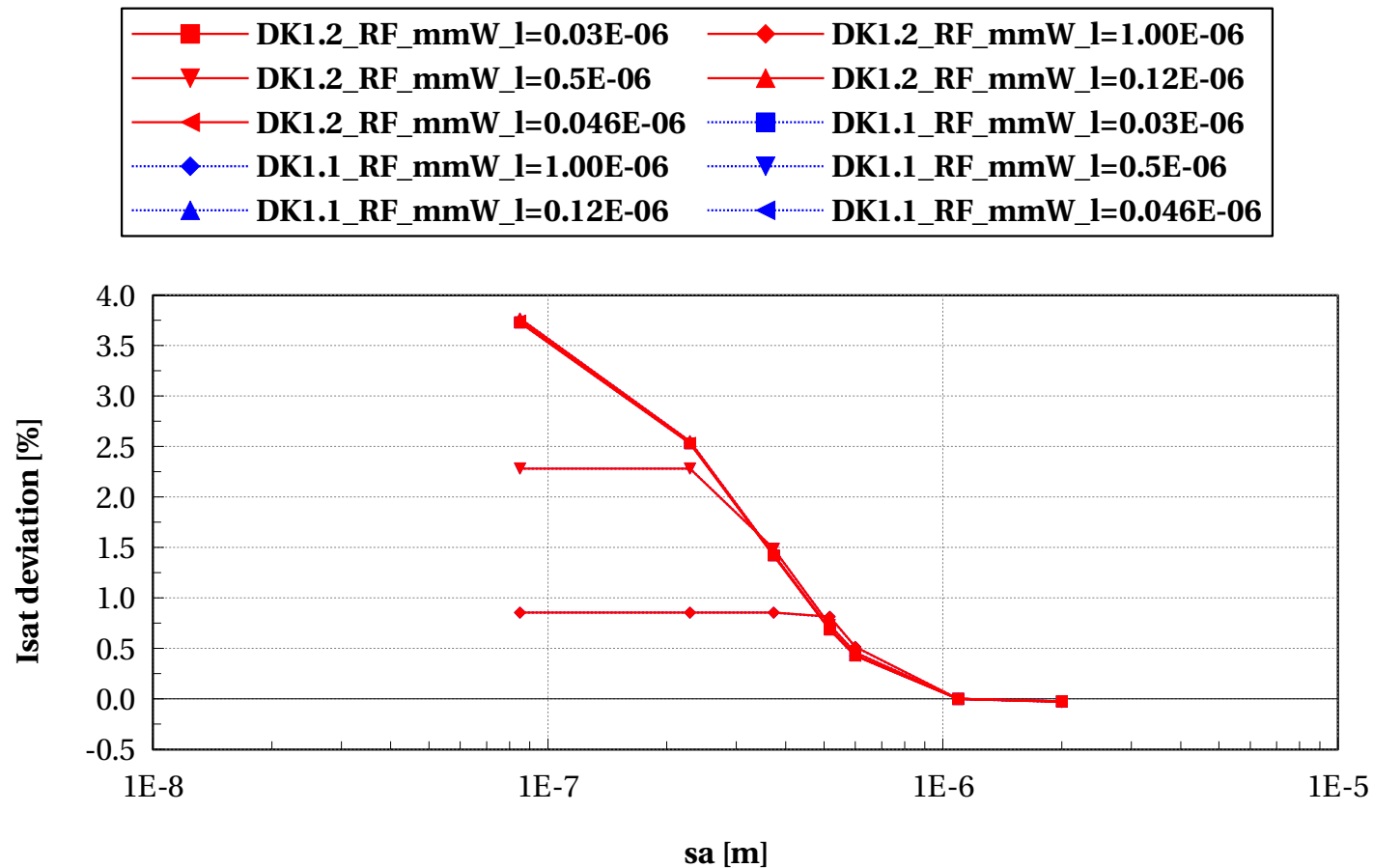
pfet_acc, Vt_sat shift [mV] vs sa [m]

$\rho==25$ and $w==0.1\text{e-}6$ and $p_la==0$ and ($L==0.030\text{e-}6$ or $L==0.046\text{e-}6$ or $L==0.12\text{e-}6$ or $L==0.5\text{e-}6$ or $L==1$)



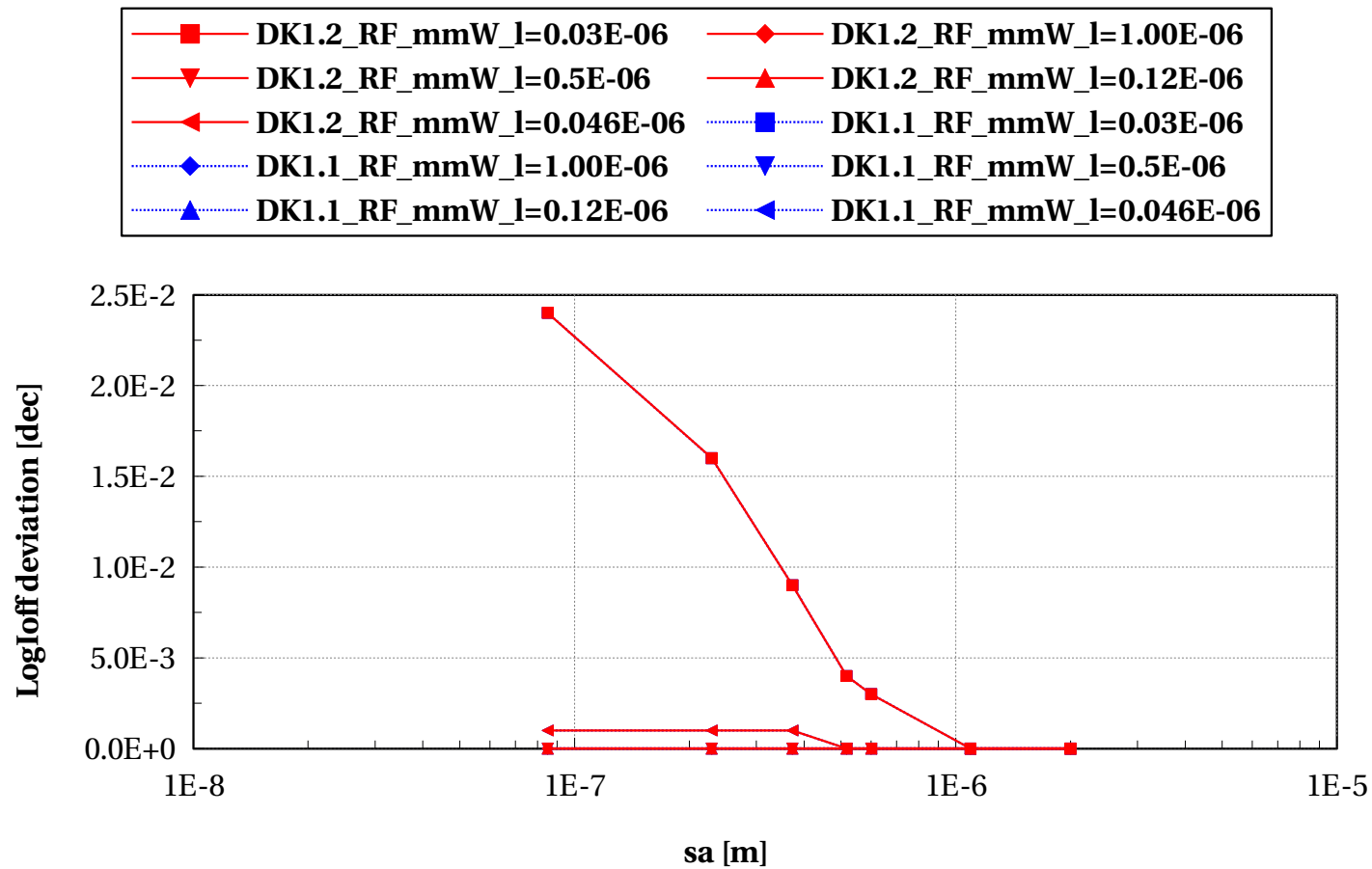
pfet_acc, Isat deviation [%] vs sa [m]

$\rho=25$ and $w=0.1\text{e-}6$ and $p_{\text{la}}=0$ and ($L=0.030\text{e-}6$ or $L=0.046\text{e-}6$ or $L=0.12\text{e-}6$ or $L=0.5\text{e-}6$ or $L=1$)



pfet_acc, LogIoff deviation [dec] vs sa [m]

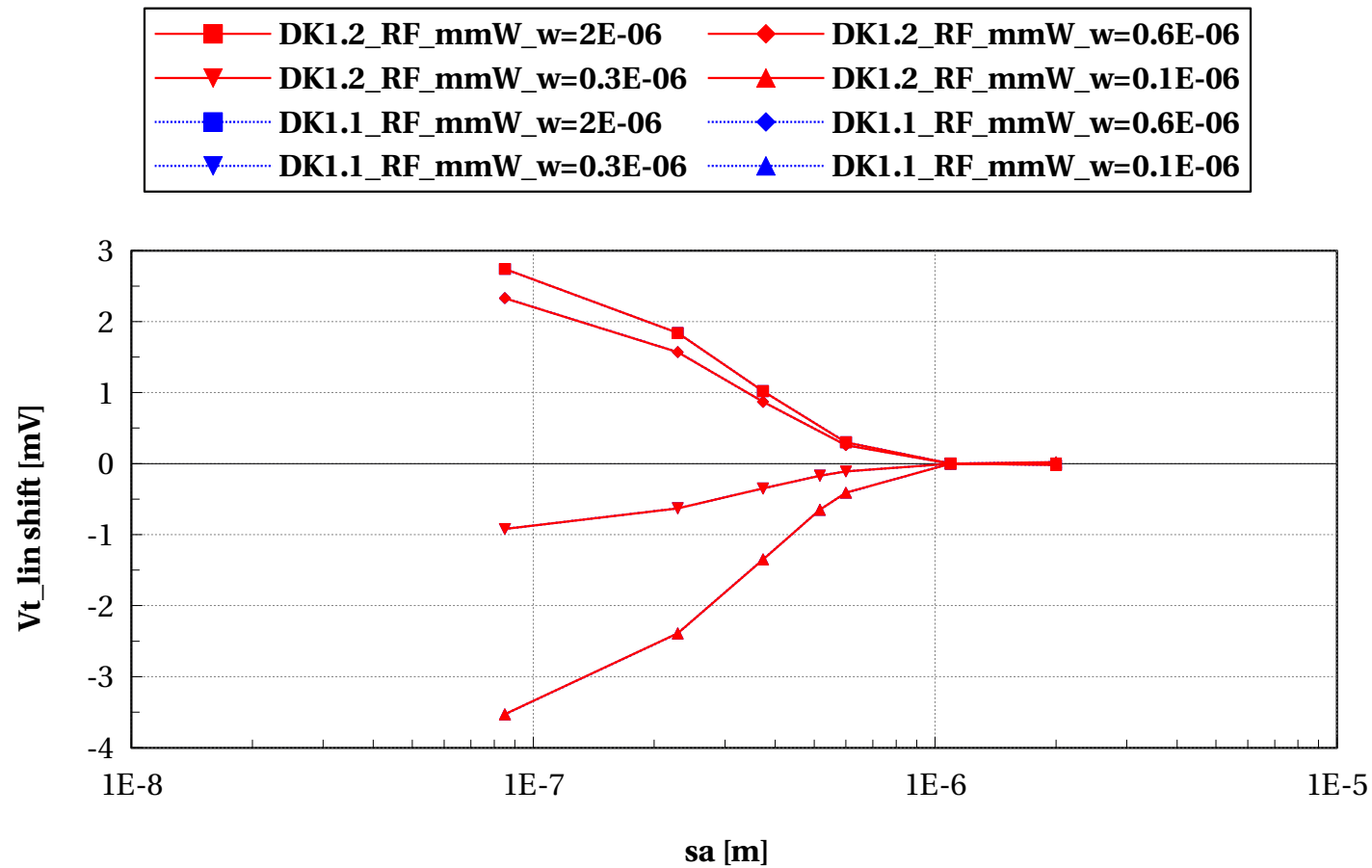
$\rho==25$ and $w==0.1e-6$ and $p_la==0$ and ($L==0.030e-6$ or $L==0.046e-6$ or $L==0.12e-6$ or $L==0.5e-6$ or $L==1$)



LOD effect (sa=sb) - Wscaling at L=0.03e-6

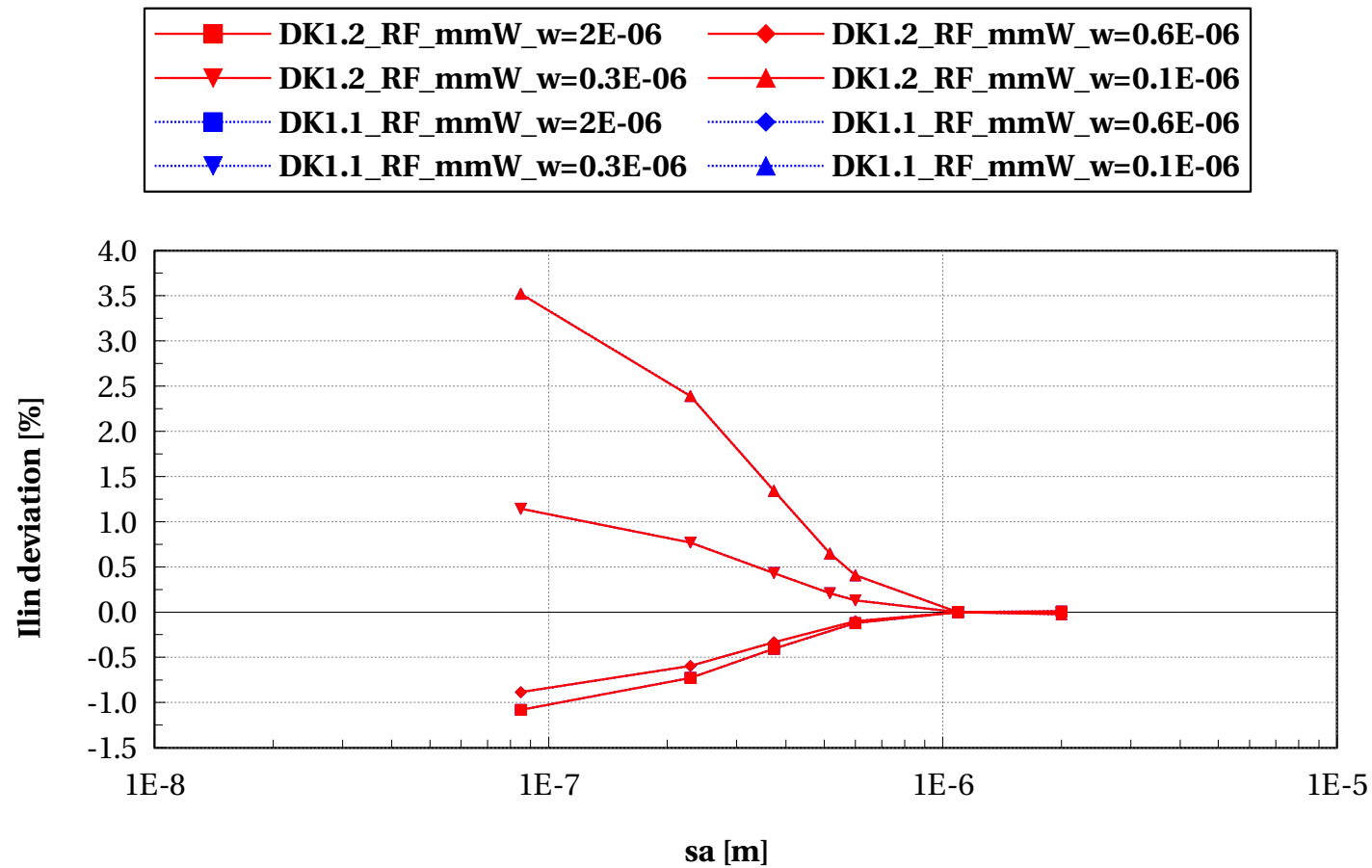
pfet_acc, Vt_lin shift [mV] vs sa [m]

temp==25 and l==0.03e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



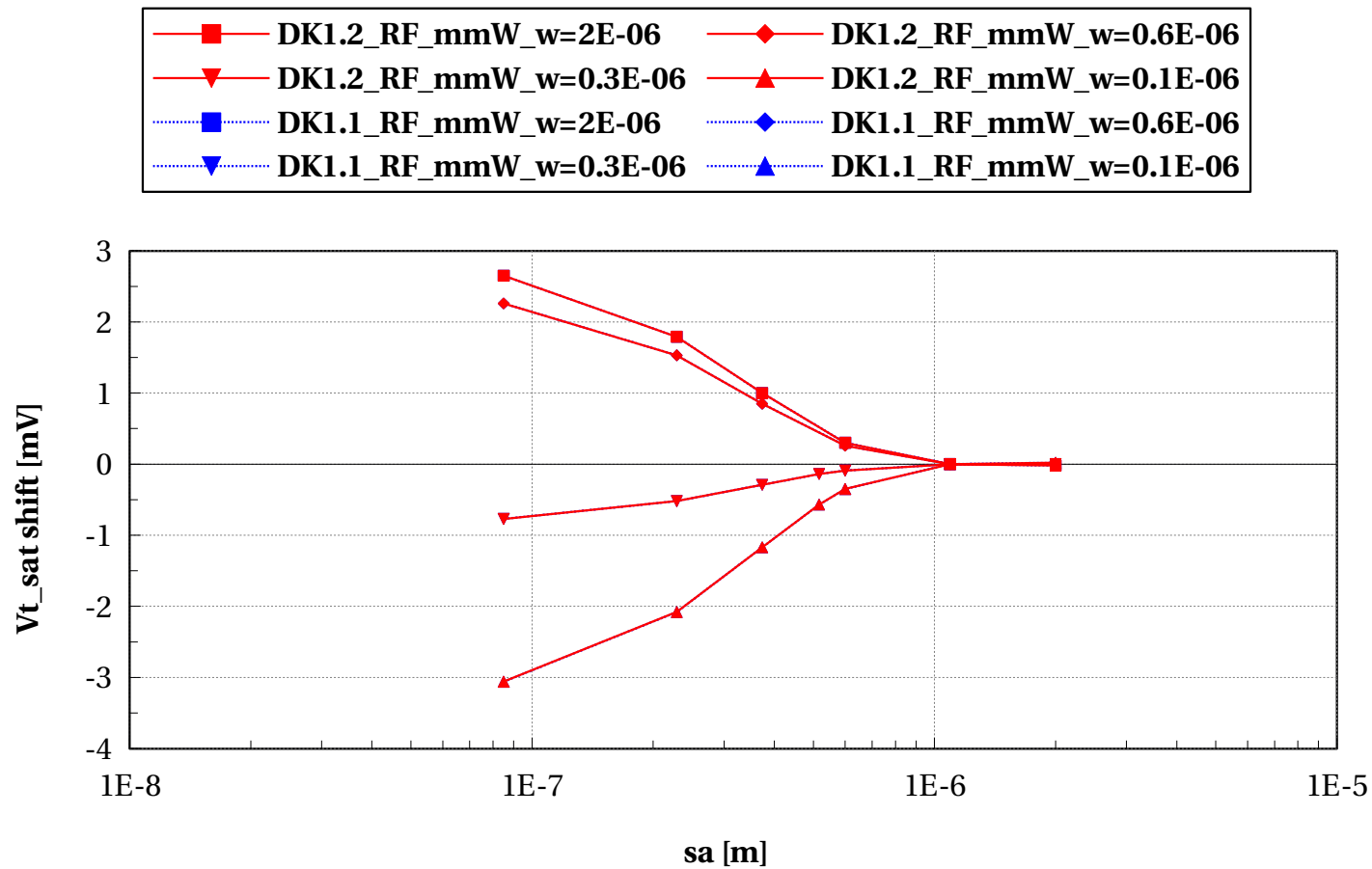
pfet_acc, Ilin deviation [%] vs sa [m]

temp==25 and l==0.03e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



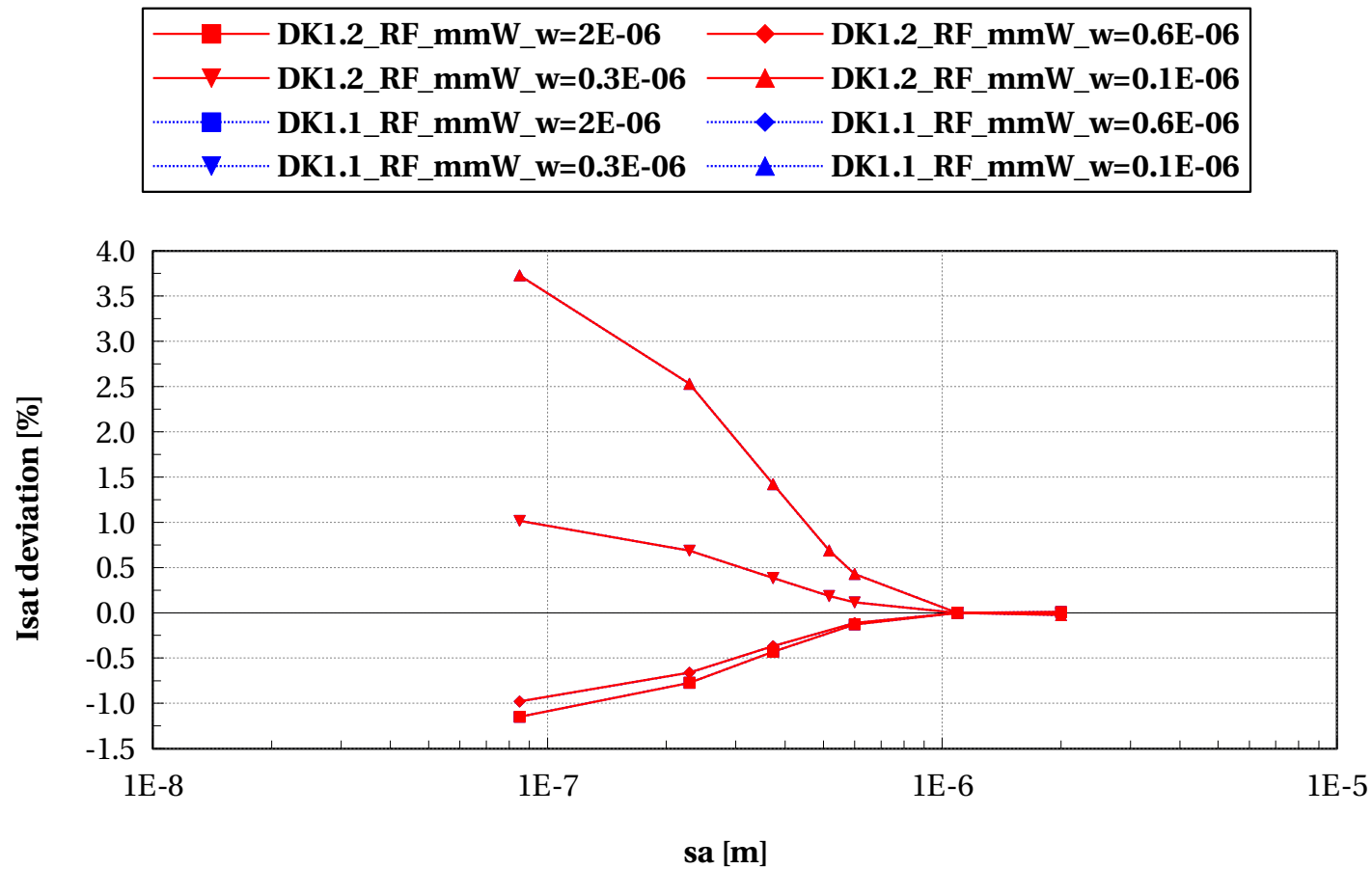
pfet_acc, Vt_sat shift [mV] vs sa [m]

temp==25 and l==0.03e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



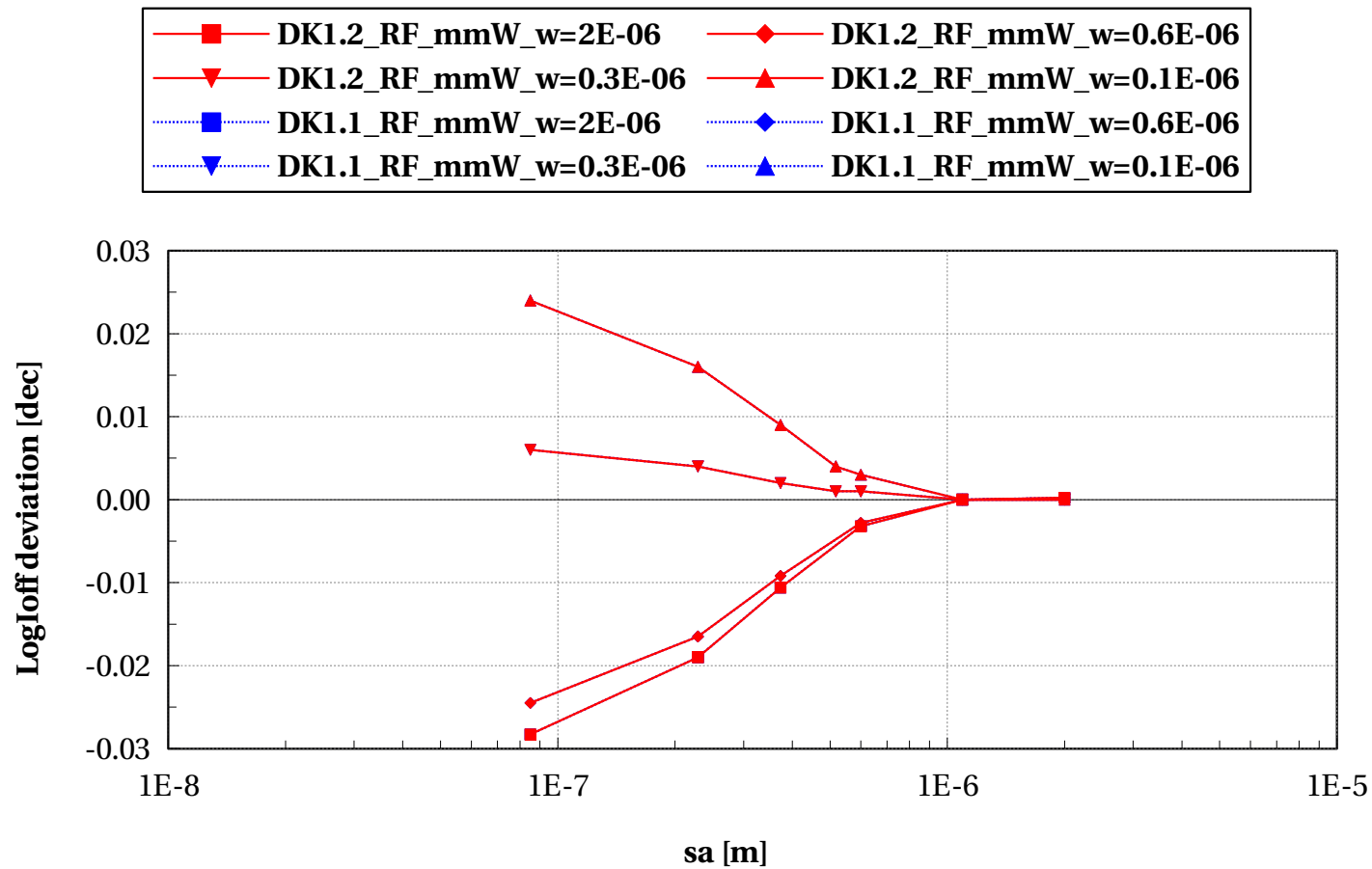
pfet_acc, Isat deviation [%] vs sa [m]

temp==25 and l==0.03e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



pfet_acc, Logloff deviation [dec] vs sa [m]

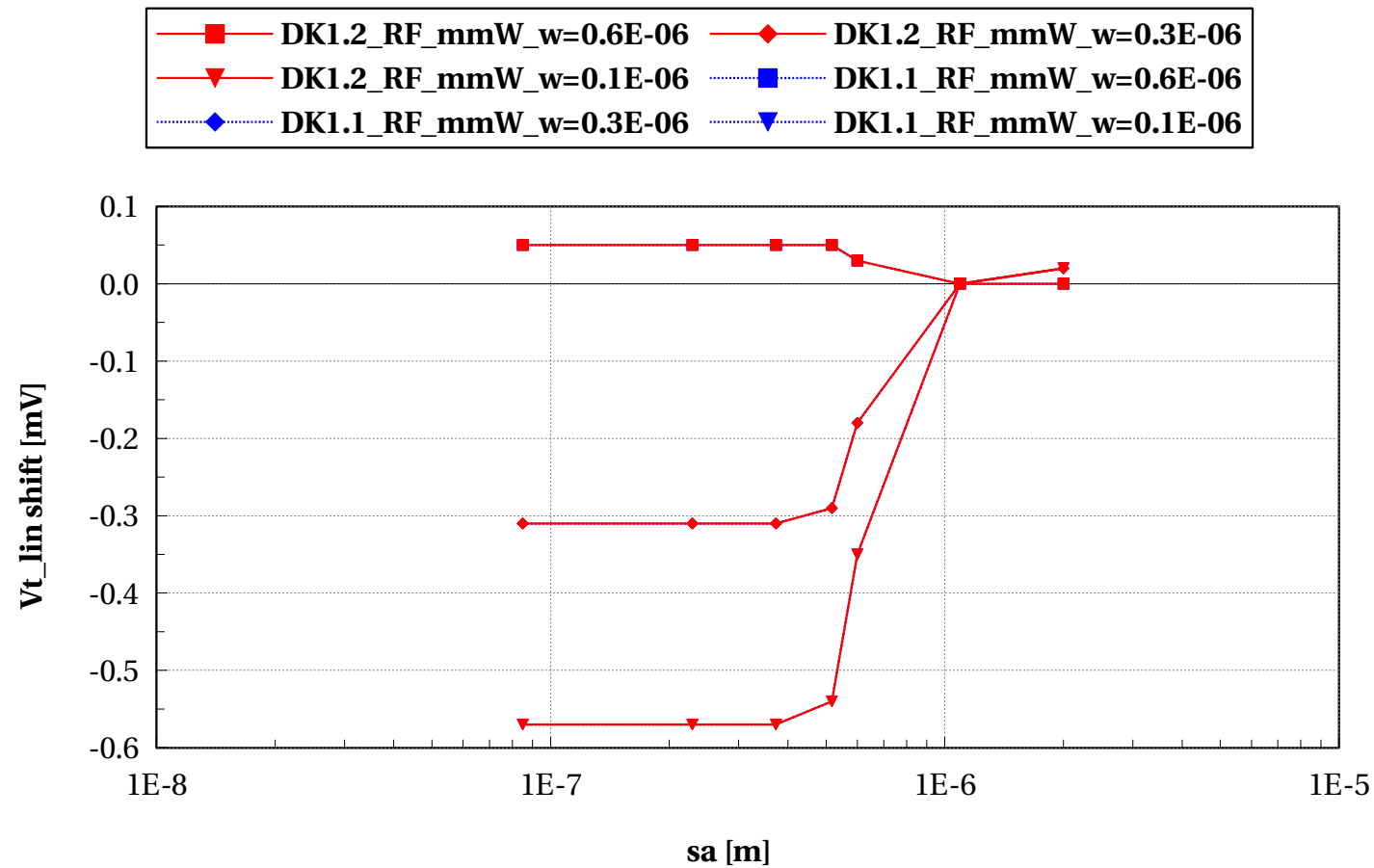
temp==25 and l==0.03e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



LOD effect (sa=sb) - Wscaling at L=1e-6

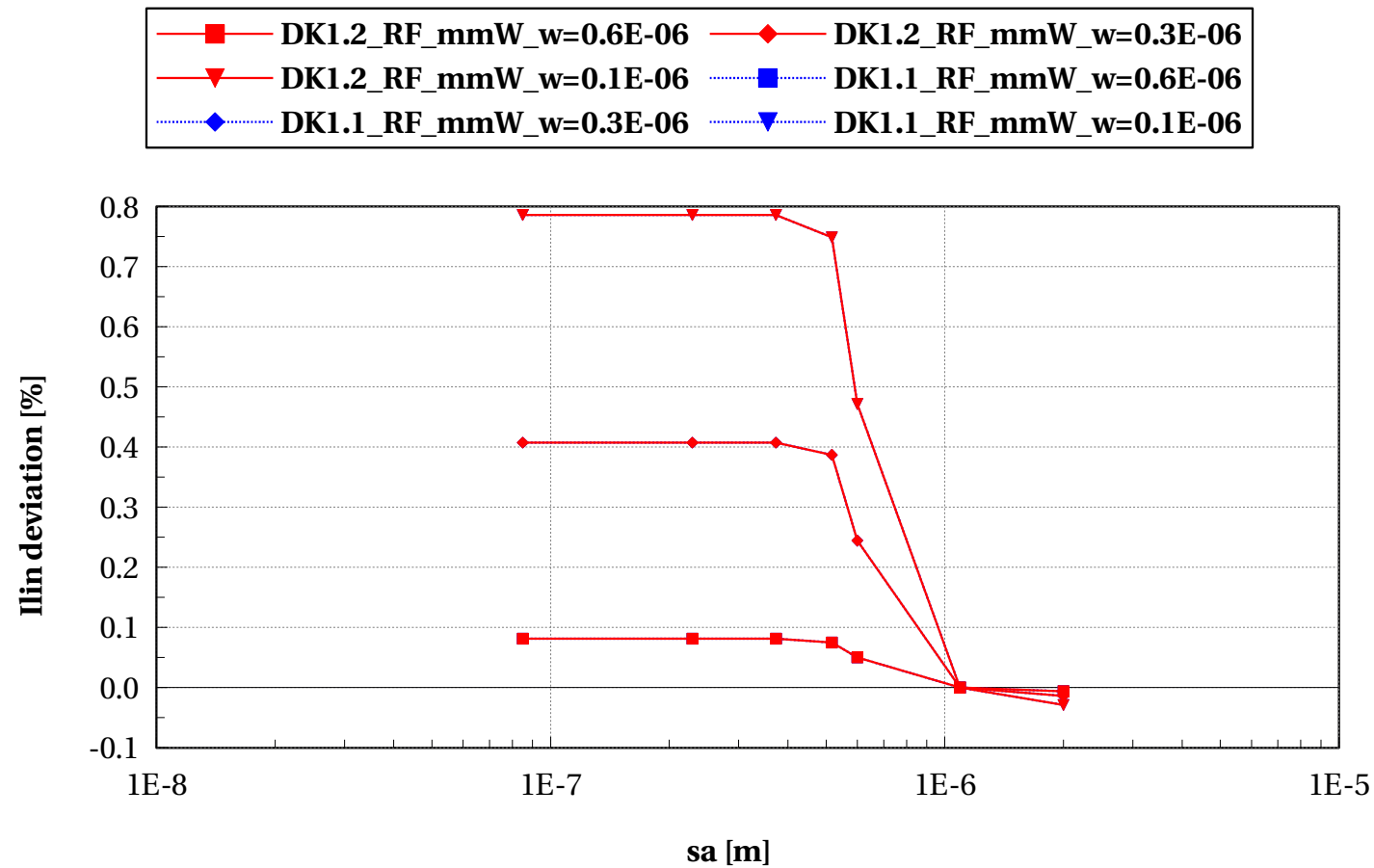
pfet_acc, Vt_lin shift [mV] vs sa [m]

temp==25 and l==1.0e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



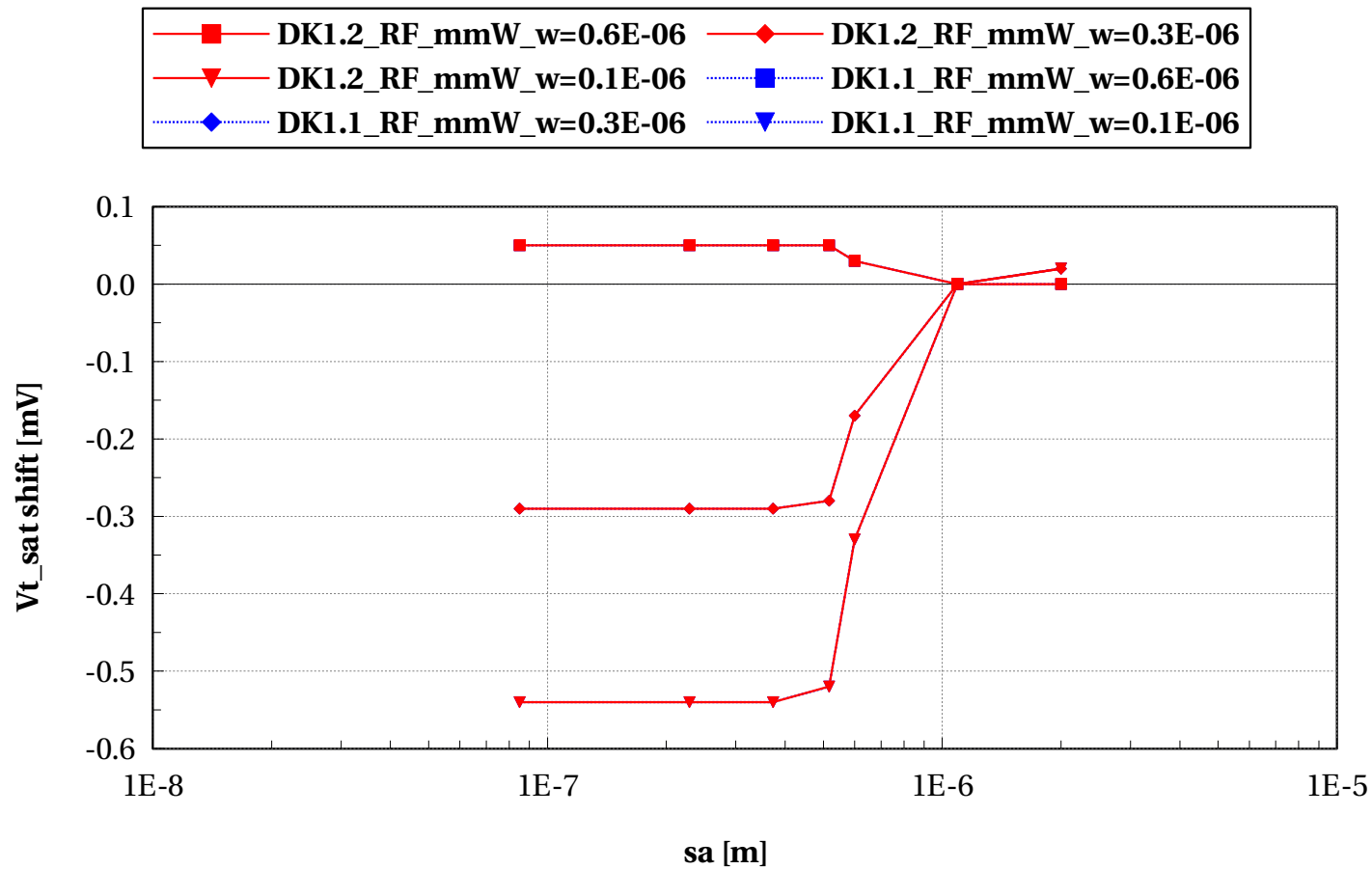
pfet_acc, Ilin deviation [%] vs sa [m]

temp==25 and l==1.0e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



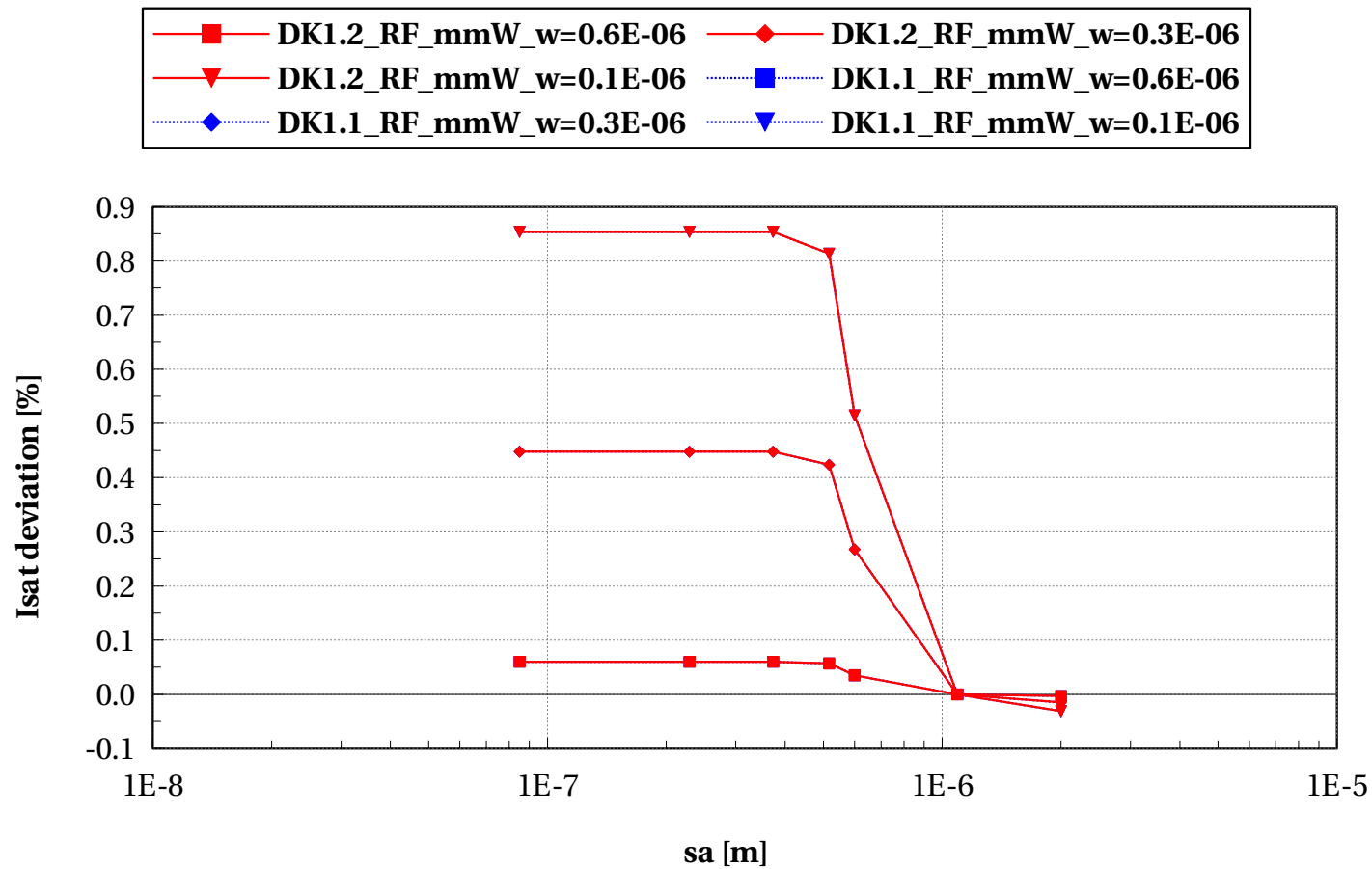
pfet_acc, Vt_sat shift [mV] vs sa [m]

temp==25 and l==1.0e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



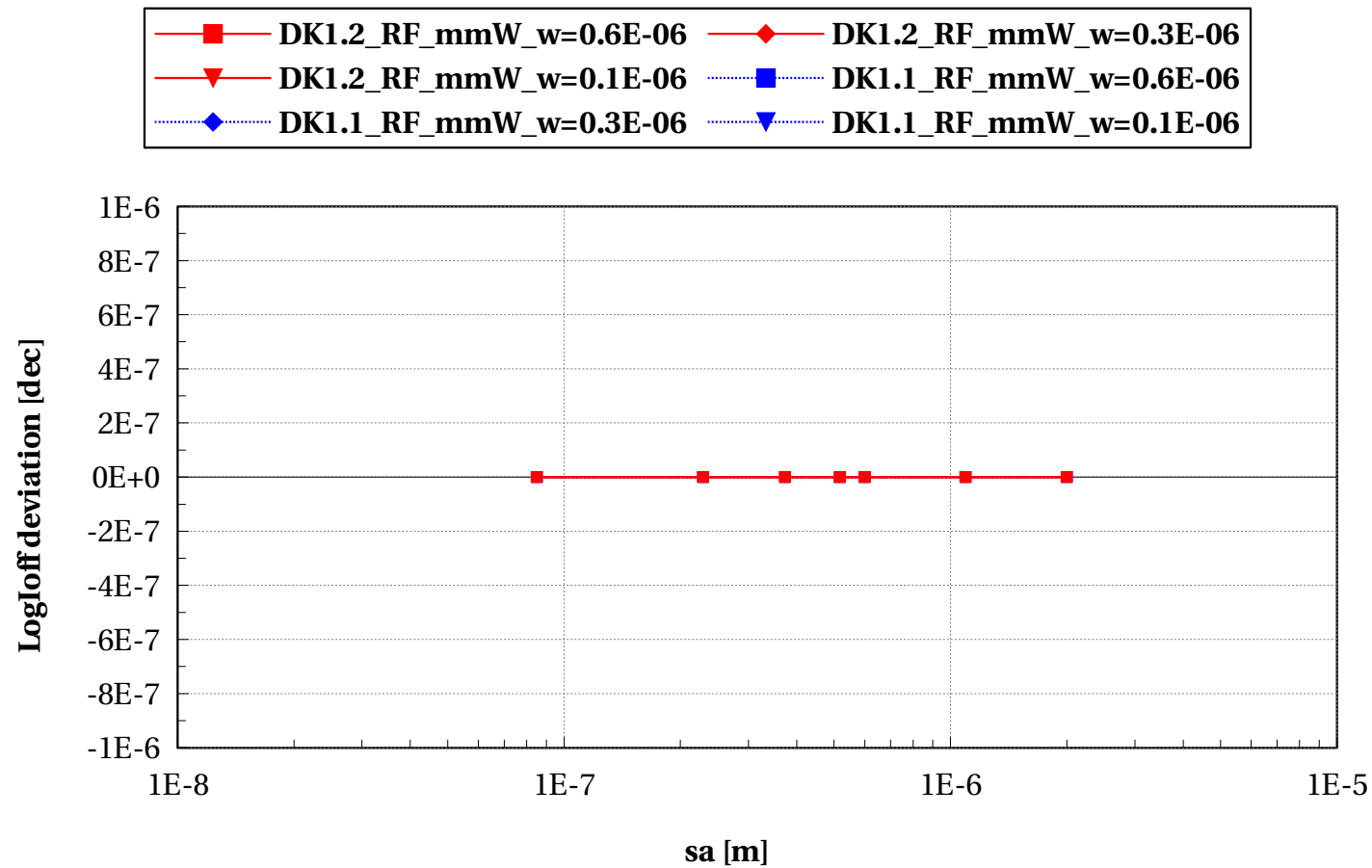
pfet_acc, Isat deviation [%] vs sa [m]

temp==25 and l==1.0e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



pfet_acc, LogIoff deviation [dec] vs sa [m]

temp==25 and l==1.0e-6 and p_la==0 and (W==0.1e-6 or W==0.3e-6 or W==0.6e-6 or W==2.0e-6)



Annex

Conditions of simulations

The simulations were done with SBenchLSF Alpha using Eldo simulator 2018.3.

- Model lvtmfet_acc (DK1.2_RF_mmW)

- ✓ Input Parameters

- ✗ $i_{vt} = 300e-9$ A
- ✗ $mc_runs = 1000$
- ✗ $v_{gs_stop} = v_{dd}$ V
- ✗ $v_{ds_off} = v_{ds_sat}$ V
- ✗ $v_{gs_off} = 0$ V
- ✗ $temp = 25$ °C
- ✗ $v_{gs_start} = -0.5$ V
- ✗ $mc_sens = 0$
- ✗ $v_{ds_lin} = 0.05$ V
- ✗ $sbenchlsf_release = Alpha$
- ✗ $plashrink_i_{vt} = 1$
- ✗ $v_{bs} = 0$ V
- ✗ $ams_release = 2018.3$
- ✗ $model_version = 1.3.e$

- ✗ $mc_nsigma = 3$
- ✗ $ithslwi = 10e-9 \text{ A}$
- ✗ $vstep_ivt = 0.005 \text{ V}$
- ✗ $vds_sat = Vdd \text{ V}$
- ✗ $shrink_ivt = 1$
- ✗ $vdd = 1 \text{ V}$
- ✗ $dlshrink_ivt = 0$
- ✓ Sweep Parameters
- ✓ Extra parameters
 - ✗ $lvt_dev = 0$
 - ✗ $gflag_noisedev_rvt_cmos028fdsoi = 0$
 - ✗ $gflag_noisedev_lvt_cmos028fdsoi = 0$
 - ✗ $rvt_dev = 0$
- Model `lvtpfet_acc` (DK1.2_RF_mmW)
 - ✓ Input Parameters
 - ✗ $ivt = 70e-9 \text{ A}$
 - ✗ $mc_runs = 1000$
 - ✗ $vgs_stop = vdd \text{ V}$
 - ✗ $vds_off = vds_sat \text{ V}$
 - ✗ $vgs_off = 0 \text{ V}$
 - ✗ $temp = 25 \text{ °C}$
 - ✗ $vgs_start = -0.5 \text{ V}$
 - ✗ $mc_sens = 0$
 - ✗ $vds_lin = 0.05 \text{ V}$
 - ✗ $sbenchlsf_release = \text{Alpha}$

- ✗ plashrink_ivt = 1
- ✗ vbs = 1 V
- ✗ ams_release = 2018.3
- ✗ model_version = 1.3.e
- ✗ mc_nsigma = 3
- ✗ ithslwi = 10e-9 A
- ✗ vstep_ivt = 0.005 V
- ✗ vds_sat = Vdd V
- ✗ shrink_ivt = 1
- ✗ vdd = 1 V
- ✗ dlshrink_ivt = 0
- ✓ Sweep Parameters
- ✓ Extra parameters
 - ✗ lvt_dev = 0
 - ✗ gflag__noisedev__rvt__cmos028fdsoi = 0
 - ✗ gflag__noisedev__lvt__cmos028fdsoi = 0
 - ✗ rvt_dev = 0
- Model nfet_acc (DK1.2_RF_mmW)
 - ✓ Input Parameters
 - ✗ ivt = 300e-9 A
 - ✗ mc_runs = 1000
 - ✗ vgs_stop = vdd V
 - ✗ vds_off = vds_sat V
 - ✗ vgs_off = 0 V
 - ✗ temp = 25 °C

- ✗ vgs_start = -0.5 V
- ✗ mc_sens = 0
- ✗ vds_lin = 0.05 V
- ✗ sbenchlsf_release = Alpha
- ✗ plashrink_ivt = 1
- ✗ vbs = 0 V
- ✗ ams_release = 2018.3
- ✗ model_version = 1.2.d
- ✗ mc_nsigma = 3
- ✗ ithslwi = 10e-9 A
- ✗ vstep_ivt = 0.005 V
- ✗ vds_sat = Vdd V
- ✗ shrink_ivt = 1
- ✗ vdd = 1 V
- ✗ dlshrink_ivt = 0
- ✓ Sweep Parameters
- ✓ Extra parameters
 - ✗ lvt_dev = 0
 - ✗ gflag__noisedev__rvt__cmos028fdsoi = 0
 - ✗ gflag__noisedev__lvt__cmos028fdsoi = 0
 - ✗ rvt_dev = 0
- Model pfet_acc (DK1.2_RF_mmW)
 - ✓ Input Parameters
 - ✗ ivt = 70e-9 A
 - ✗ mc_runs = 1000

- ✗ vgs_stop = vdd V
- ✗ vds_off = vds_sat V
- ✗ vgs_off = 0 V
- ✗ temp = 25 °C
- ✗ vgs_start = -0.5 V
- ✗ mc_sens = 0
- ✗ vds_lin = 0.05 V
- ✗ sbenchlsf_release = Alpha
- ✗ plashrink_ivt = 1
- ✗ vbs = 0 V
- ✗ ams_release = 2018.3
- ✗ model_version = 1.2.d
- ✗ mc_nsigma = 3
- ✗ ithslwi = 10e-9 A
- ✗ vstep_ivt = 0.005 V
- ✗ vds_sat = Vdd V
- ✗ shrink_ivt = 1
- ✗ vdd = 1 V
- ✗ dlshrink_ivt = 0
- ✓ Sweep Parameters
- ✓ Extra parameters
 - ✗ lvt_dev = 0
 - ✗ gflag__noisedev__rvt__cmos028fdsoi = 0
 - ✗ gflag__noisedev__lvt__cmos028fdsoi = 0
 - ✗ rvt_dev = 0

● Model lvtinfet_acc (DK1.1_RF_mmW)

✓ Input Parameters

- ✗ $ivt = 300e-9$ A
- ✗ $mc_runs = 1000$
- ✗ $vgs_stop = vdd$ V
- ✗ $vds_off = vds_sat$ V
- ✗ $vgs_off = 0$ V
- ✗ $temp = 25$ °C
- ✗ $vgs_start = -0.5$ V
- ✗ $mc_sens = 0$
- ✗ $vds_lin = 0.05$ V
- ✗ $sbenchlsf_release = \text{Alpha}$
- ✗ $plashrink_ivt = 1$
- ✗ $vbs = 0$ V
- ✗ $ams_release = 2018.3$
- ✗ $model_version = 1.3.d$
- ✗ $mc_nsigma = 3$
- ✗ $ithslwi = 10e-9$ A
- ✗ $vstep_ivt = 0.005$ V
- ✗ $vds_sat = Vdd$ V
- ✗ $shrink_ivt = 1$
- ✗ $vdd = 1$ V
- ✗ $dlshrink_ivt = 0$

✓ Sweep Parameters

✓ Extra parameters

- ✗ lvt_dev = 0
- ✗ gflag__noisedev__rvt__cmos028fdsoi = 0
- ✗ gflag__noisedev__lvt__cmos028fdsoi = 0
- ✗ rvt_dev = 0

● Model lvtpfet_acc (DK1.1_RF_mmW)

✓ Input Parameters

- ✗ ivt = 70e-9 A
- ✗ mc_runs = 1000
- ✗ vgs_stop = vdd V
- ✗ vds_off = vds_sat V
- ✗ vgs_off = 0 V
- ✗ temp = 25 °C
- ✗ vgs_start = -0.5 V
- ✗ mc_sens = 0
- ✗ vds_lin = 0.05 V
- ✗ sbenchlsf_release = Alpha
- ✗ plashrink_ivt = 1
- ✗ vbs = 1 V
- ✗ ams_release = 2018.3
- ✗ model_version = 1.3.d
- ✗ mc_nsigma = 3
- ✗ ithslwi = 10e-9 A
- ✗ vstep_ivt = 0.005 V
- ✗ vds_sat = Vdd V
- ✗ shrink_ivt = 1

- ✗ vdd = 1 V
- ✗ dlshrink_ivt = 0
- ✓ Sweep Parameters
- ✓ Extra parameters
 - ✗ lvt_dev = 0
 - ✗ gflag__noisedev__rvt__cmos028fdsoi = 0
 - ✗ gflag__noisedev__lvt__cmos028fdsoi = 0
 - ✗ rvt_dev = 0
- Model nfet_acc (DK1.1_RF_mmW)
 - ✓ Input Parameters
 - ✗ ivt = 300e-9 A
 - ✗ mc_runs = 1000
 - ✗ vgs_stop = vdd V
 - ✗ vds_off = vds_sat V
 - ✗ vgs_off = 0 V
 - ✗ temp = 25 °C
 - ✗ vgs_start = -0.5 V
 - ✗ mc_sens = 0
 - ✗ vds_lin = 0.05 V
 - ✗ sbenchlsf_release = Alpha
 - ✗ plashrink_ivt = 1
 - ✗ vbs = 0 V
 - ✗ ams_release = 2018.3
 - ✗ model_version = 1.2.c
 - ✗ mc_nsigma = 3

- ✗ $\text{ithslwi} = 10\text{e-}9 \text{ A}$
- ✗ $\text{vstep_ivt} = 0.005 \text{ V}$
- ✗ $\text{vds_sat} = \text{Vdd V}$
- ✗ $\text{shrink_ivt} = 1$
- ✗ $\text{vdd} = 1 \text{ V}$
- ✗ $\text{dlshrink_ivt} = 0$
- ✓ Sweep Parameters
- ✓ Extra parameters
 - ✗ $\text{lv_dev} = 0$
 - ✗ $\text{gflag_noisedev_rvt_cmos028fdsoi} = 0$
 - ✗ $\text{gflag_noisedev_lv_cmos028fdsoi} = 0$
 - ✗ $\text{rvt_dev} = 0$
- Model pfet_acc (DK1.1_RF_mmW)
 - ✓ Input Parameters
 - ✗ $\text{ivt} = 70\text{e-}9 \text{ A}$
 - ✗ $\text{mc_runs} = 1000$
 - ✗ $\text{vgs_stop} = \text{vdd V}$
 - ✗ $\text{vds_off} = \text{vds_sat V}$
 - ✗ $\text{vgs_off} = 0 \text{ V}$
 - ✗ $\text{temp} = 25 \text{ }^\circ\text{C}$
 - ✗ $\text{vgs_start} = -0.5 \text{ V}$
 - ✗ $\text{mc_sens} = 0$
 - ✗ $\text{vds_lin} = 0.05 \text{ V}$
 - ✗ $\text{sbenchlsf_release} = \text{Alpha}$
 - ✗ $\text{plashrink_ivt} = 1$

- ✗ vbs = 0 V
- ✗ ams_release = 2018.3
- ✗ model_version = 1.2.c
- ✗ mc_nsigma = 3
- ✗ ithslwi = 10e-9 A
- ✗ vstep_ivt = 0.005 V
- ✗ vds_sat = Vdd V
- ✗ shrink_ivt = 1
- ✗ vdd = 1 V
- ✗ dlshrink_ivt = 0
- ✓ Sweep Parameters
- ✓ Extra parameters
 - ✗ lvt_dev = 0
 - ✗ gflag__noisedev__rvt__cmos028fdsoi = 0
 - ✗ gflag__noisedev__lvt__cmos028fdsoi = 0
 - ✗ rvt_dev = 0