

July 2017

## IO\_NMOSBIAS\_EXT\_1V8\_NEG\_CSF\_CL\_LIN

## Cell Description

IO\_NMOSBIAS\_EXT\_1V8\_NEG\_CSF\_CL\_LIN

- The cell has "dont\_use" attribute set in the Synopsys STF.
- The cell has "dont\_touch" attribute set in the Synopsys STF.

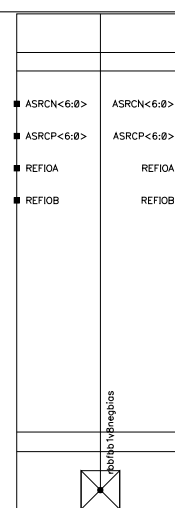
## Physical Dimensions

Area(um2) : 4252.000

## Glossary

Tr : Input Transition time  
 C : Output (capacitive) load  
 R : Rising edge  
 F : Falling edge

## Logical Symbol



## Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000

ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000
ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

**Default Leakage Power**

Default Leakage Power (mW)	vdd	vdde	rbbfbb1v8negbias
best 1.10 125	0.000e+00	5.766e-05	4.080e-03
worst 0.90 -40	9.427e-24	4.708e-08	3.144e-06

**IO\_NMOSBIAS\_EXT\_1V8\_NEG\_CSF\_FC\_LIN****Cell Description**

IO\_NMOSBIAS\_EXT\_1V8\_NEG\_CSF\_FC\_LIN

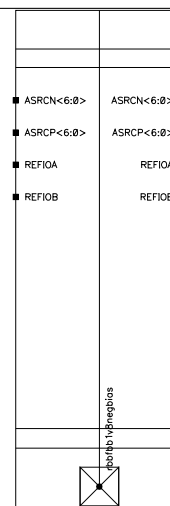
- The cell has "dont\_use" attribute set in the Synopsys STF.
- The cell has "dont\_touch" attribute set in the Synopsys STF.

**Physical Dimensions**

Area(um2) : 3632.000

**Glossary**

Tr : Input Transition time  
 C : Output (capacitive) load  
 R : Rising edge  
 F : Falling edge

**Logical Symbol****Cell Capacitance**

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

**Default Leakage Power**

Default Leakage Power (mW)	vdd	vdde	rbbfbb1v8negbias
best 1.10 125	0.000e+00	5.766e-05	4.080e-03
worst 0.90 -40	9.427e-24	4.708e-08	3.144e-06

**IO\_NMOSBIAS\_EXT\_1V8\_POS\_CSF\_CL\_LIN****Cell Description**

IO\_NMOSBIAS\_EXT\_1V8\_POS\_CSF\_CL\_LIN

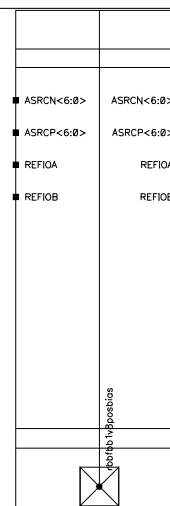
- The cell has "dont\_use" attribute set in the Synopsys STF.
- The cell has "dont\_touch" attribute set in the Synopsys STF.

**Physical Dimensions**

Area(um2) : 4252.000

**Glossary**

Tr : Input Transition time  
 C : Output (capacitive) load  
 R : Rising edge  
 F : Falling edge

**Logical Symbol****Cell Capacitance**

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

**Default Leakage Power**

Default Leakage Power (mW)	vdd	vdde	rbbfbb1v8posbias
best 1.10 125	0.000e+00	2.518e-06	4.086e-03
worst 0.90 -40	9.454e-24	5.445e-09	3.161e-06

**IO\_NMOSBIAS\_EXT\_1V8\_POS\_CSF\_FC\_LIN****Cell Description**

IO\_NMOSBIAS\_EXT\_1V8\_POS\_CSF\_FC\_LIN

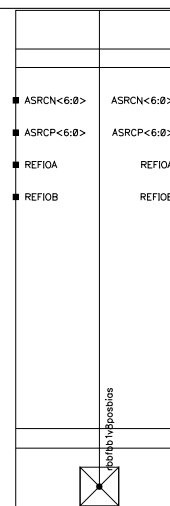
- The cell has "dont\_use" attribute set in the Synopsys STF.
- The cell has "dont\_touch" attribute set in the Synopsys STF.

**Physical Dimensions**

Area(um2) : 3632.000

**Glossary**

Tr : Input Transition time  
 C : Output (capacitive) load  
 R : Rising edge  
 F : Falling edge

**Logical Symbol****Cell Capacitance**

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

**Default Leakage Power**

Default Leakage Power (mW)	vdd	vdde	rbbfbb1v8posbias
best 1.10 125	0.000e+00	2.518e-06	4.086e-03
worst 0.90 -40	9.454e-24	5.445e-09	3.161e-06



**IO\_PMOSBIAS\_EXT\_1V8\_NEG\_CSF\_CL\_LIN****Cell Description**

IO\_PMOSBIAS\_EXT\_1V8\_NEG\_CSF\_CL\_LIN

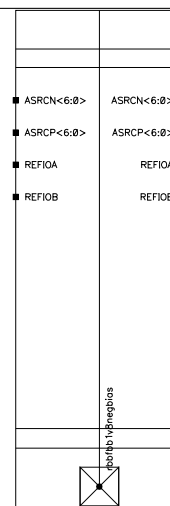
- The cell has "dont\_use" attribute set in the Synopsys STF.
- The cell has "dont\_touch" attribute set in the Synopsys STF.

**Physical Dimensions**

Area(um2) : 4252.000

**Glossary**

Tr : Input Transition time  
 C : Output (capacitive) load  
 R : Rising edge  
 F : Falling edge

**Logical Symbol****Cell Capacitance**

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

**Default Leakage Power**

Default Leakage Power (mW)	vdd	vdde	rbbfbb1v8negbias
best 1.10 125	0.000e+00	5.766e-05	4.080e-03
worst 0.90 -40	9.427e-24	4.708e-08	3.144e-06

**IO\_PMOSBIAS\_EXT\_1V8\_NEG\_CSF\_FC\_LIN****Cell Description**

IO\_PMOSBIAS\_EXT\_1V8\_NEG\_CSF\_FC\_LIN

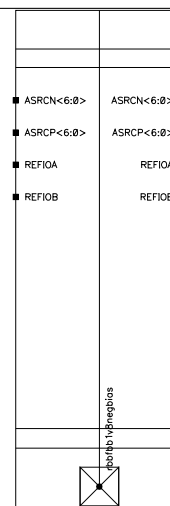
- The cell has "dont\_use" attribute set in the Synopsys STF.
- The cell has "dont\_touch" attribute set in the Synopsys STF.

**Physical Dimensions**

Area(um2) : 3632.000

**Glossary**

Tr : Input Transition time  
 C : Output (capacitive) load  
 R : Rising edge  
 F : Falling edge

**Logical Symbol****Cell Capacitance**

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

**Default Leakage Power**

Default Leakage Power (mW)	vdd	vdde	rbbfbb1v8negbias
best 1.10 125	0.000e+00	5.766e-05	4.080e-03
worst 0.90 -40	9.427e-24	4.708e-08	3.144e-06

## IO\_PMOSBIAS\_EXT\_1V8\_POS\_CSF\_CL\_LIN

### Cell Description

IO\_PMOSBIAS\_EXT\_1V8\_POS\_CSF\_CL\_LIN

- The cell has "dont\_use" attribute set in the Synopsys STF.
- The cell has "dont\_touch" attribute set in the Synopsys STF.

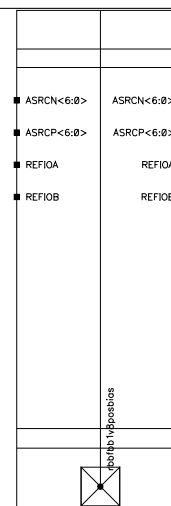
### Physical Dimensions

Area(um2) : 4252.000

### Glossary

Tr : Input Transition time  
C : Output (capacitive) load  
R : Rising edge  
F : Falling edge

### Logical Symbol



### Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

**Default Leakage Power**

Default Leakage Power (mW)	vdd	vdde	rbbfbb1v8posbias
best 1.10 125	0.000e+00	2.518e-06	4.086e-03
worst 0.90 -40	9.454e-24	5.445e-09	3.161e-06

## IO\_PMOSBIAS\_EXT\_1V8\_POS\_CSF\_FC\_LIN

### Cell Description

IO\_PMOSBIAS\_EXT\_1V8\_POS\_CSF\_FC\_LIN

- The cell has "dont\_use" attribute set in the Synopsys STF.
- The cell has "dont\_touch" attribute set in the Synopsys STF.

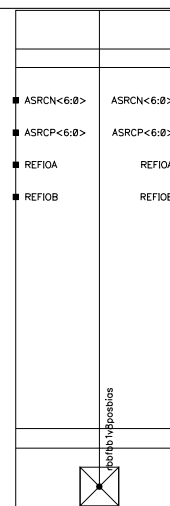
### Physical Dimensions

Area(um2) : 3632.000

### Glossary

Tr : Input Transition time  
C : Output (capacitive) load  
R : Rising edge  
F : Falling edge

### Logical Symbol



### Cell Capacitance

Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000

ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

**Default Leakage Power**

Default Leakage Power (mW)	vdd	vdde	rbbfbb1v8posbias
best 1.10 125	0.000e+00	2.518e-06	4.086e-03
worst 0.90 -40	9.454e-24	5.445e-09	3.161e-06



## NMOSBIAS\_EXT\_NEGESDCLAMP\_NOB2B\_1V8

### Cell Description

NMOSBIAS\_EXT\_NEGESDCLAMP\_NOB2B\_1V8

- The cell has "dont\_use" attribute set in the Synopsys STF.
- The cell has "dont\_touch" attribute set in the Synopsys STF.

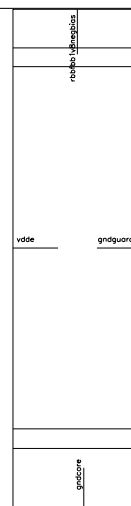
### Physical Dimensions

Area(um2) : 4080.260

### Glossary

Tr : Input Transition time  
C : Output (capacitive) load  
R : Rising edge  
F : Falling edge

### Logical Symbol



### Default Leakage Power

Default Leakage Power (mW)	vdde	rbbfb1v8negbias
best 1.10 125	6.854e-05	6.854e-05
worst 0.90 -40	6.559e-08	6.568e-08

NMOSBIAS\_EXT\_NEGESDCLAMP\_WITH\_B2B\_1V8

Cell Description

NMOSBIAS\_EXT\_NEGESDCLAMP\_WITH\_B2B\_1V8

- The cell has "dont\_use" attribute set in the Synopsys STF.
- The cell has "dont\_touch" attribute set in the Synopsys STF.

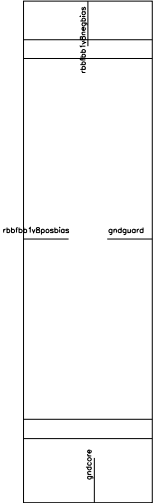
Physical Dimensions

Area(um2) : 4566.140

Glossary

Tr : Input Transition time  
C : Output (capacitive) load  
R : Rising edge  
F : Falling edge

Logical Symbol



Default Leakage Power

Default Leakage Power (mW)	vdde	rbbfb1v8negbias
best 1.10 125	7.716e-05	7.716e-05
worst 0.90 -40	1.118e-07	1.119e-07

## NMOSBIAS\_EXT\_POSEDCLAMP\_NOB2B\_1V8

### Cell Description

NMOSBIAS\_EXT\_POSEDCLAMP\_NOB2B\_1V8

- The cell has "dont\_use" attribute set in the Synopsys STF.
- The cell has "dont\_touch" attribute set in the Synopsys STF.

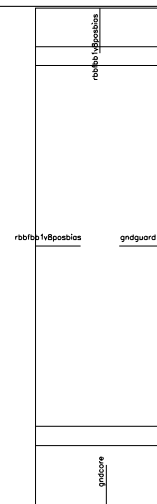
### Physical Dimensions

Area(um2) : 4080.260

### Glossary

Tr : Input Transition time  
C : Output (capacitive) load  
R : Rising edge  
F : Falling edge

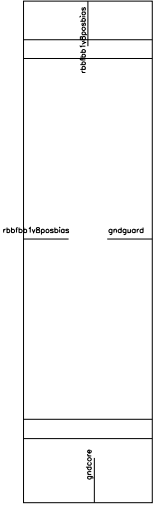
### Logical Symbol



### Default Leakage Power

Default Leakage Power (mW)	rbbfb1v8posbias
best 1.10 125	0.000e+00
worst 0.90 -40	0.000e+00

# NMOSBIAS\_EXT\_POSEDCLAMP\_WITH\_B2B\_1V8

<b>Cell Description</b>	<b>Logical Symbol</b>
<p>NMOSBIAS_EXT_POSEDCLAMP_WITH_B2B_1V8</p> <ul style="list-style-type: none"><li>• The cell has "dont_use" attribute set in the Synopsys STF.</li><li>• The cell has "dont_touch" attribute set in the Synopsys STF.</li></ul>	

**Physical Dimensions**

Area(um2) : 4566.140

**Glossary**

Tr : Input Transition time  
C : Output (capacitive) load  
R : Rising edge  
F : Falling edge

**Default Leakage Power**

Default Leakage Power (mW)	rbbfb1v8posbias
best 1.10 125	0.000e+00
worst 0.90 -40	0.000e+00

## PMOSBIAS\_EXT\_NEGESDCLAMP\_NOB2B\_1V8

### Cell Description

PMOSBIAS\_EXT\_NEGESDCLAMP\_NOB2B\_1V8

- The cell has "dont\_use" attribute set in the Synopsys STF.
- The cell has "dont\_touch" attribute set in the Synopsys STF.

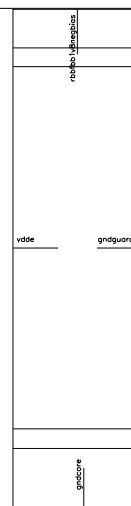
### Physical Dimensions

Area(um2) : 4080.260

### Glossary

Tr : Input Transition time  
C : Output (capacitive) load  
R : Rising edge  
F : Falling edge

### Logical Symbol



### Default Leakage Power

Default Leakage Power (mW)	vdde	rbbfb1v8negbias
best 1.10 125	6.854e-05	6.854e-05
worst 0.90 -40	6.559e-08	6.568e-08

# PMOSBIAS\_EXT\_NEGESDCLAMP\_WITH\_B2B\_1V8

<b>Cell Description</b>	<b>Logical Symbol</b>
<p>PMOSBIAS_EXT_NEGESDCLAMP_WITH_B2B_1V8</p> <ul style="list-style-type: none"><li>• The cell has "dont_use" attribute set in the Synopsys STF.</li><li>• The cell has "dont_touch" attribute set in the Synopsys STF.</li></ul>	

## Physical Dimensions

Area(um2) : 4566.140

## Glossary

Tr : Input Transition time  
C : Output (capacitive) load  
R : Rising edge  
F : Falling edge

## Default Leakage Power

Default Leakage Power (mW)	vdde	rbbfb1v8negbias
best 1.10 125	7.716e-05	7.716e-05
worst 0.90 -40	1.118e-07	1.119e-07

## PMOSBIAS\_EXT\_POSEDCLAMP\_NOB2B\_1V8

### Cell Description

PMOSBIAS\_EXT\_POSEDCLAMP\_NOB2B\_1V8

- The cell has "dont\_use" attribute set in the Synopsys STF.
- The cell has "dont\_touch" attribute set in the Synopsys STF.

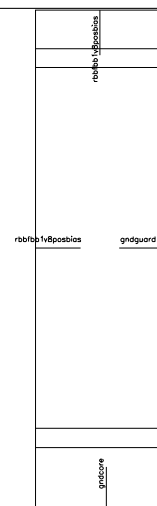
### Physical Dimensions

Area(um2) : 4080.260

### Glossary

Tr : Input Transition time  
C : Output (capacitive) load  
R : Rising edge  
F : Falling edge

### Logical Symbol



### Default Leakage Power

Default Leakage Power (mW)	rbbfb1v8posbias
best 1.10 125	0.000e+00
worst 0.90 -40	0.000e+00

## PMOSBIAS\_EXT\_POSEDCLAMP\_WITH\_B2B\_1V8

### Cell Description

PMOSBIAS\_EXT\_POSEDCLAMP\_WITH\_B2B\_1V8

- The cell has "dont\_use" attribute set in the Synopsys STF.
- The cell has "dont\_touch" attribute set in the Synopsys STF.

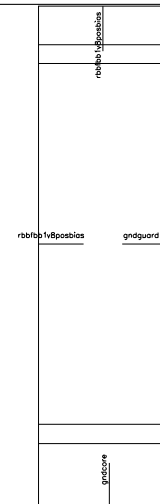
### Physical Dimensions

Area(um2) : 4566.140

### Glossary

Tr : Input Transition time  
C : Output (capacitive) load  
R : Rising edge  
F : Falling edge

### Logical Symbol



### Default Leakage Power

Default Leakage Power (mW)	rbbfb1v8posbias
best 1.10 125	0.000e+00
worst 0.90 -40	0.000e+00





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