

C28SOI_IO_EXT_CSF_BASIC_EG Databook

August 2016

EMPTYCELL_EXT_CSF_FC_INNER

Cell Description

EMPTYCELL_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

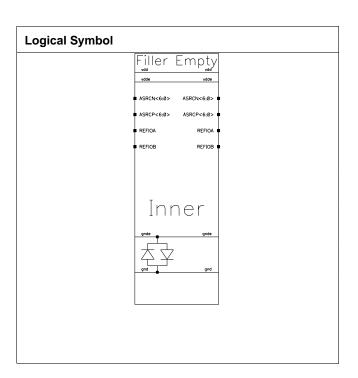
Physical Dimensions

Area(um2): 3632.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Faiailletei	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000

ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000
ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	6.200e-06
worst 0.90 -40	0.000e+00	2.230e-08



EMPTYCELL_EXT_CSF_FC_OUTER

Cell Description

EMPTYCELL_EXT_CSF_FC_OUTER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

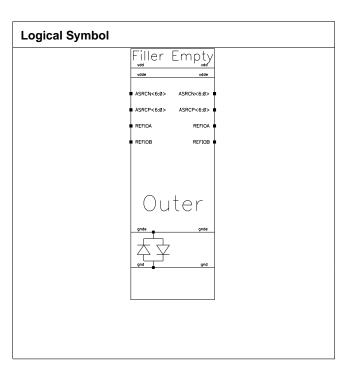
Physical Dimensions

Area(um2): 8307.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Valu	e(pF)
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	6.200e-06
worst 0.90 -40	0.000e+00	2.230e-08



FILLCELL_1GRID_EXT_CSF_CL_LIN

Cell Description

FILLCELL_1GRID_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

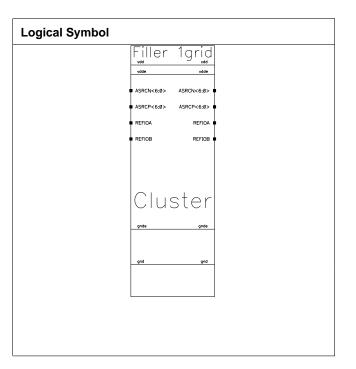
Physical Dimensions

Area(um2): 10.630

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Valu	e(pF)
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_1GRID_EXT_CSF_FC_2ROWS

Cell Description

FILLCELL_1GRID_EXT_CSF_FC_2ROWS

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

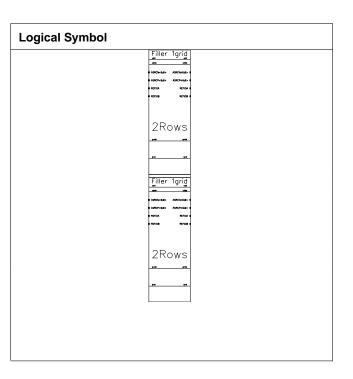
Physical Dimensions

Area(um2): 18.460

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Valu	e(pF)
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_1GRID_EXT_CSF_FC_INNER

Cell Description

FILLCELL_1GRID_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

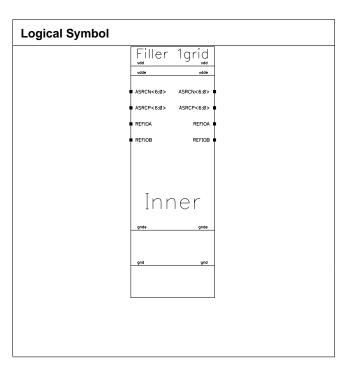
Physical Dimensions

Area(um2): 9.080

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_1GRID_EXT_CSF_FC_LIN

Cell Description

FILLCELL_1GRID_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

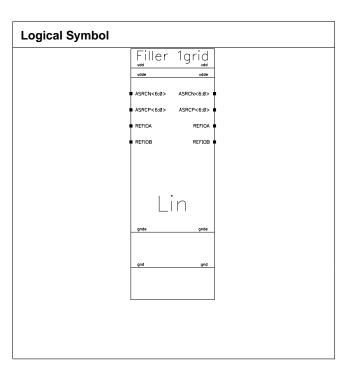
Physical Dimensions

Area(um2): 9.080

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_1GRID_EXT_CSF_FC_OUTER

Cell Description

FILLCELL_1GRID_EXT_CSF_FC_OUTER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

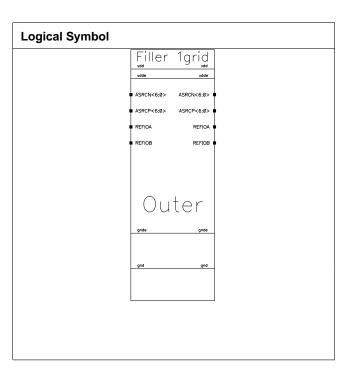
Physical Dimensions

Area(um2): 9.380

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_1UM_EXT_CSF_CL_LIN

Cell Description

FILLCELL_1UM_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

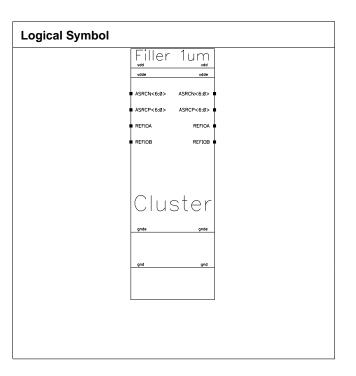
Physical Dimensions

Area(um2): 106.300

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value	e(pF)
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_1UM_EXT_CSF_FC_2ROWS

Cell Description

FILLCELL_1UM_EXT_CSF_FC_2ROWS

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

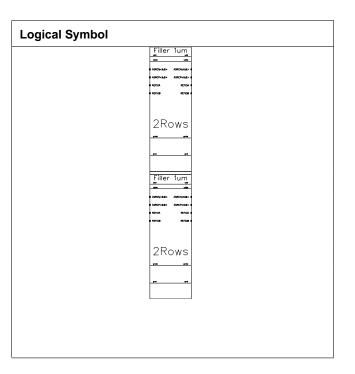
Physical Dimensions

Area(um2): 184.600

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Valu	e(pF)
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_1UM_EXT_CSF_FC_INNER

Cell Description

FILLCELL_1UM_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

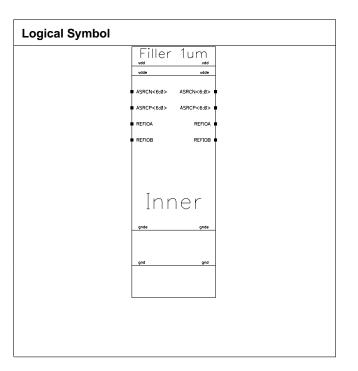
Physical Dimensions

Area(um2): 90.800

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Valu	e(pF)
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_1UM_EXT_CSF_FC_LIN

Cell Description

FILLCELL_1UM_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

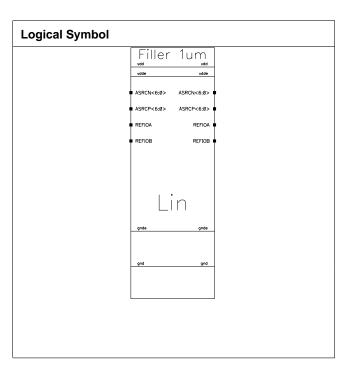
Physical Dimensions

Area(um2): 90.800

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_1UM_EXT_CSF_FC_OUTER

Cell Description

FILLCELL_1UM_EXT_CSF_FC_OUTER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

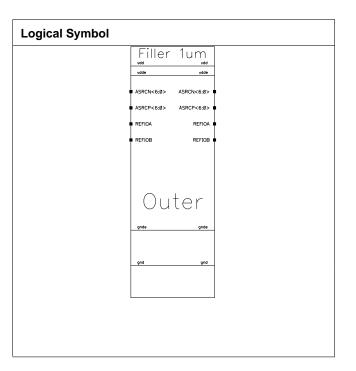
Physical Dimensions

Area(um2): 93.800

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_5UM_EXT_CSF_CL_LIN

Cell Description

FILLCELL_5UM_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

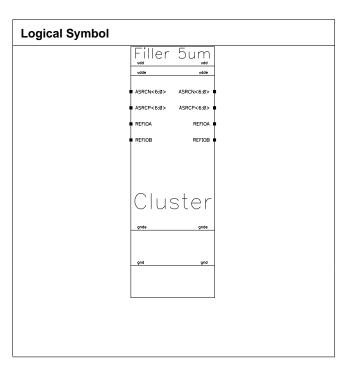
Physical Dimensions

Area(um2): 531.500

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_5UM_EXT_CSF_FC_2ROWS

Cell Description

FILLCELL_5UM_EXT_CSF_FC_2ROWS

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

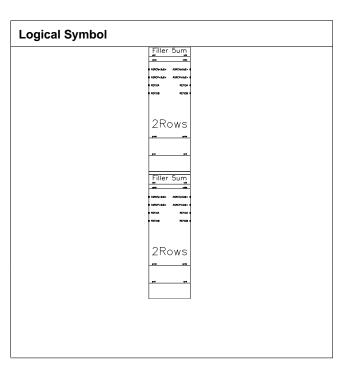
Physical Dimensions

Area(um2): 923.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_5UM_EXT_CSF_FC_INNER

Cell Description

FILLCELL_5UM_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

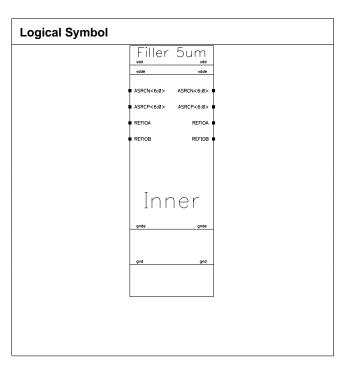
Physical Dimensions

Area(um2): 454.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_5UM_EXT_CSF_FC_LIN

Cell Description

FILLCELL_5UM_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

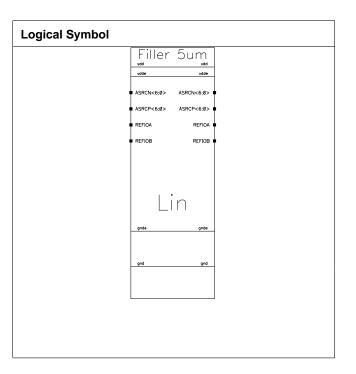
Physical Dimensions

Area(um2): 454.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_5UM_EXT_CSF_FC_OUTER

Cell Description

FILLCELL_5UM_EXT_CSF_FC_OUTER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

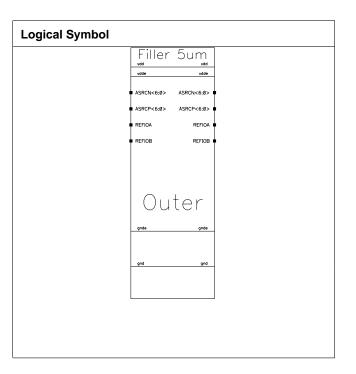
Physical Dimensions

Area(um2): 469.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_10UM_EXT_CSF_CL_LIN

Cell Description

FILLCELL_10UM_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

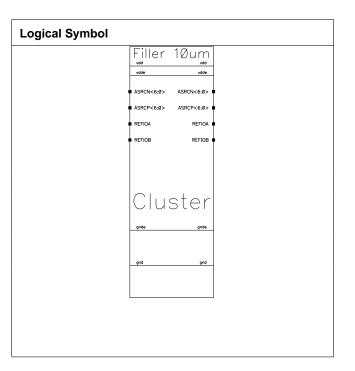
Physical Dimensions

Area(um2): 1063.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_10UM_EXT_CSF_FC_2ROWS

Cell Description

FILLCELL_10UM_EXT_CSF_FC_2ROWS

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

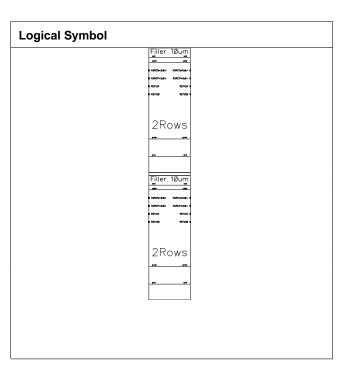
Physical Dimensions

Area(um2): 1846.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



0.0100	0.0100
10000.000	10000.000
0.0100	0.0100
10000.000	10000.000
0.0100	0.0100
10000.000	10000.000
0.0100	0.0100
10000.000	10000.000
0.0100	0.0100
10000.000	10000.000
	10000.000 0.0100 10000.000 0.0100 10000.000 0.0100 10000.000 0.0100

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_10UM_EXT_CSF_FC_INNER

Cell Description

FILLCELL_10UM_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

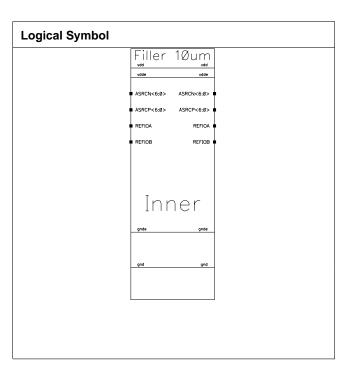
Physical Dimensions

Area(um2): 908.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Valu	e(pF)
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_10UM_EXT_CSF_FC_LIN

Cell Description

FILLCELL_10UM_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

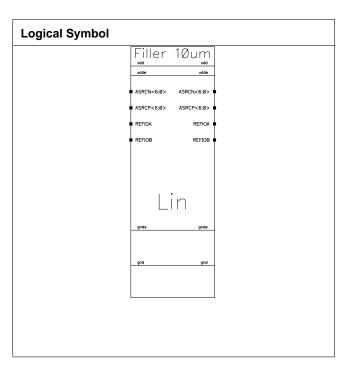
Physical Dimensions

Area(um2): 908.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_10UM_EXT_CSF_FC_OUTER

Cell Description

FILLCELL_10UM_EXT_CSF_FC_OUTER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

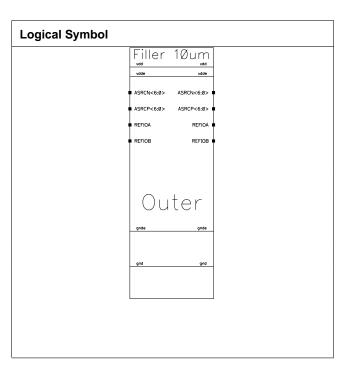
Physical Dimensions

Area(um2): 938.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_END_LEFT_EXT_CSF_CL_LIN

Cell Description

FILLCELL_END_LEFT_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

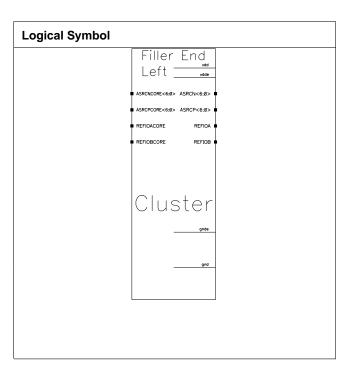
Physical Dimensions

Area(um2): 1807.100

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)		Valu	e(pF)
Parameter	best 1.10 125	worst 0.90 -40		
ASRCN[0] Input Cap.	0.0100	0.0100		
ASRCN[0] Max Load	10000.000	10000.000		
ASRCN[1] Input Cap.	0.0100	0.0100		
ASRCN[1] Max Load	10000.000	10000.000		
ASRCN[2] Input Cap.	0.0100	0.0100		
ASRCN[2] Max Load	10000.000	10000.000		
ASRCN[3] Input Cap.	0.0100	0.0100		
ASRCN[3] Max Load	10000.000	10000.000		
ASRCN[4] Input Cap.	0.0100	0.0100		
ASRCN[4] Max Load	10000.000	10000.000		
ASRCN[5] Input Cap.	0.0100	0.0100		
ASRCN[5] Max Load	10000.000	10000.000		
ASRCN[6] Input Cap.	0.0100	0.0100		
ASRCN[6] Max Load	10000.000	10000.000		
ASRCNCORE[0] Input Cap.	0.0100	0.0100		
ASRCNCORE[0] Max Load	10000.000	10000.000		
ASRCNCORE[1] Input Cap.	0.0100	0.0100		
ASRCNCORE[1] Max Load	10000.000	10000.000		
ASRCNCORE[2] Input Cap.	0.0100	0.0100		
ASRCNCORE[2] Max Load	10000.000	10000.000		
ASRCNCORE[3] Input Cap.	0.0100	0.0100		
ASRCNCORE[3] Max Load	10000.000	10000.000		



ASRCNCORE[4] Input Cap.	0.0100	0.0100
ASRCNCORE[4] Max Load	10000.000	10000.000
ASRCNCORE[5] Input Cap.	0.0100	0.0100
ASRCNCORE[5] Max Load	10000.000	10000.000
ASRCNCORE[6] Input Cap.	0.0100	0.0100
ASRCNCORE[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000
ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
ASRCPCORE[0] Input Cap.	0.0100	0.0100
ASRCPCORE[0] Max Load	10000.000	10000.000
ASRCPCORE[1] Input Cap.	0.0100	0.0100
ASRCPCORE[1] Max Load	10000.000	10000.000
ASRCPCORE[2] Input Cap.	0.0100	0.0100
ASRCPCORE[2] Max Load	10000.000	10000.000
ASRCPCORE[3] Input Cap.	0.0100	0.0100
ASRCPCORE[3] Max Load	10000.000	10000.000
ASRCPCORE[4] Input Cap.	0.0100	0.0100
ASRCPCORE[4] Max Load	10000.000	10000.000
ASRCPCORE[5] Input Cap.	0.0100	0.0100
ASRCPCORE[5] Max Load	10000.000	10000.000
ASRCPCORE[6] Input Cap.	0.0100	0.0100
ASRCPCORE[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOACORE Input Cap.	0.0100	0.0100
REFIOACORE Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000
REFIOBCORE Input Cap.	0.0100	0.0100
REFIOBCORE Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	1.012e-05
worst 0.90 -40	0.000e+00	5.445e-09



FILLCELL_END_LEFT_EXT_CSF_FC_2ROWS

Cell Description

FILLCELL_END_LEFT_EXT_CSF_FC_2ROWS

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

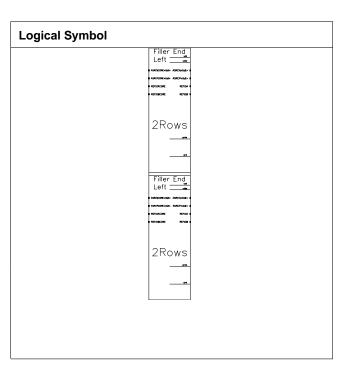
Physical Dimensions

Area(um2): 3876.600

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCNCORE[0] Input Cap.	0.0100	0.0100
ASRCNCORE[0] Max Load	10000.000	10000.000
ASRCNCORE[1] Input Cap.	0.0100	0.0100
ASRCNCORE[1] Max Load	10000.000	10000.000
ASRCNCORE[2] Input Cap.	0.0100	0.0100
ASRCNCORE[2] Max Load	10000.000	10000.000
ASRCNCORE[3] Input Cap.	0.0100	0.0100
ASRCNCORE[3] Max Load	10000.000	10000.000



ASRCNCORE[5] Input Cap. 0.0100 0.0000 ASRCNCORE[6] Input Cap. 0.0100 0.0100 ASRCNCORE[6] Input Cap. 0.0100 0.0100 ASRCNCORE[6] Imax Load 10000.000 10000.000 ASRCNCORE[6] Max Load 10000.000 10000.000 ASRCNCORE[6] Max Load 10000.000 10000.000 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0	ASRCNCORE[4] Input Cap.	0.0100	0.0100
ASRCNCORE[5] Max Load 10000.000 10000.000	ASRCNCORE[4] Max Load	10000.000	10000.000
ASRCNCORE[5] Max Load 10000.000 10000.000	ASRCNCORE[5] Input Cap.	0.0100	0.0100
ASRCNCORE[6] Max Load ASRCP[0] Input Cap. ASRCP[0] Max Load ASRCP[1] Input Cap. ASRCP[1] Imput Cap. ASRCP[1] Max Load ASRCP[1] Imput Cap. ASRCP[2] Imput Cap. ASRCP[2] Imput Cap. ASRCP[2] Imput Cap. ASRCP[2] Imput Cap. ASRCP[3] Imput Cap. ASRCP[4] Max Load ASRCP[4] Imput Cap. ASRCP[4] Max Load ASRCP[4] Imput Cap. ASRCP[4] Imput Cap. ASRCP[4] Imput Cap. ASRCP[6] Imput Cap. ASRCPCORE[0] Imput Cap. ASRCPCORE[1] Imput Cap. ASRCPCORE[2] Imput Cap. ASRCPCORE[3] Imput Cap. ASRCPCORE[4] Imput Cap. ASRCPCORE[3] Imput Cap. ASRCPCORE[4] Imput Cap. ASRCPCORE[6] Imput Cap. ASRCPCO			10000.000
ASRCP(0) Input Cap. 0.0100 0.0100 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[3] Max Load 10000.000 10000.000 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0		0.0100	0.0100
ASRCP[0] Max Load 10000.000 10000.000	ASRCNCORE[6] Max Load		10000.000
ASRCP[1] Input Cap. 0.0100 0.0100 10000.000 10000.000 ASRCP[2] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 0.0100 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 0.0100 0.0100 0.050	ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[2] Input Cap. 0.0100 0.0000 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.01000 ASRCPCORE[6] Input Cap. 0.0100 0.0100	ASRCP[0] Max Load	10000.000	10000.000
ASRCP[2] Input Cap. 0.0100 0.0100 10000.000 10000.000 ASRCP[3] Input Cap. 0.0100 0.010	ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load 10000.000 10000.000	ASRCP[1] Max Load	10000.000	10000.000
ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[4] Max Load 10000.000 10000.000 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100	ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load 10000.000 10000.000 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100	ASRCP[2] Max Load	10000.000	10000.000
ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[4] Max Load 10000.000 10000.000 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100	ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load 10000.000 10000.000 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100	ASRCP[3] Max Load	10000.000	10000.000
ASRCP[4] Max Load 10000.000 10000.000 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100	ASRCP[4] Input Cap.	0.0100	0.0100
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ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Max Load 10000.000 10000.000 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000		0.0100	0.0100
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ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000		0.0100	0.0100
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ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Max Load 10000.000 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100	ASRCPCORE[2] Input Cap.	0.0100	0.0100
ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Max Load 10000.000 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100 REFIOB Max Load 10000.000 0.0100 REFIOB Max Load 10000.000 0.0100	ASRCPCORE[2] Max Load	10000.000	10000.000
ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Max Load 10000.000 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 0.0100 REFIOB Max Load 10000.000 0.0100	ASRCPCORE[3] Input Cap.	0.0100	0.0100
ASRCPCORE[4] Max Load 10000.000 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100 REFIOB Max Load 10000.000 0.0100	ASRCPCORE[3] Max Load	10000.000	10000.000
ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100		0.0100	0.0100
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ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100			0.0100
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REFIOB Max Load 10000.000 10000.000 REFIOBCORE Input Cap. 0.0100 0.0100			
REFIOBCORE Input Cap. 0.0100 0.0100	• • • • • • • • • • • • • • • • • • • •		
REFIOBCORE Max Load 10000.000 10000.000			
	REFIOBCORE Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	2.683e-05
worst 0.90 -40	0.000e+00	5.445e-09



FILLCELL_END_LEFT_EXT_CSF_FC_INNER

Cell Description

FILLCELL_END_LEFT_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

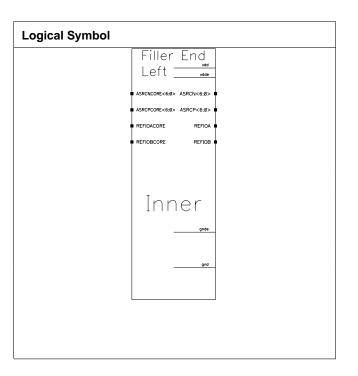
Physical Dimensions

Area(um2): 1543.600

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)		Valu	e(pF)
Parameter	best 1.10 125	worst 0.90 -40		
ASRCN[0] Input Cap.	0.0100	0.0100		
ASRCN[0] Max Load	10000.000	10000.000		
ASRCN[1] Input Cap.	0.0100	0.0100		
ASRCN[1] Max Load	10000.000	10000.000		
ASRCN[2] Input Cap.	0.0100	0.0100		
ASRCN[2] Max Load	10000.000	10000.000		
ASRCN[3] Input Cap.	0.0100	0.0100		
ASRCN[3] Max Load	10000.000	10000.000		
ASRCN[4] Input Cap.	0.0100	0.0100		
ASRCN[4] Max Load	10000.000	10000.000		
ASRCN[5] Input Cap.	0.0100	0.0100		
ASRCN[5] Max Load	10000.000	10000.000		
ASRCN[6] Input Cap.	0.0100	0.0100		
ASRCN[6] Max Load	10000.000	10000.000		
ASRCNCORE[0] Input Cap.	0.0100	0.0100		
ASRCNCORE[0] Max Load	10000.000	10000.000		
ASRCNCORE[1] Input Cap.	0.0100	0.0100		
ASRCNCORE[1] Max Load	10000.000	10000.000		
ASRCNCORE[2] Input Cap.	0.0100	0.0100		
ASRCNCORE[2] Max Load	10000.000	10000.000		
ASRCNCORE[3] Input Cap.	0.0100	0.0100		
ASRCNCORE[3] Max Load	10000.000	10000.000		



ASRCNCORE[5] Input Cap. 0.0100 0.0000 ASRCNCORE[6] Input Cap. 0.0100 0.0100 ASRCNCORE[6] Input Cap. 0.0100 0.0100 ASRCNCORE[6] Imax Load 10000.000 10000.000 ASRCNCORE[6] Max Load 10000.000 10000.000 ASRCNCORE[6] Max Load 10000.000 10000.000 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0	ASRCNCORE[4] Input Cap.	0.0100	0.0100
ASRCNCORE[5] Max Load 10000.000 10000.000	ASRCNCORE[4] Max Load	10000.000	10000.000
ASRCNCORE[5] Max Load 10000.000 10000.000	ASRCNCORE[5] Input Cap.	0.0100	0.0100
ASRCNCORE[6] Max Load ASRCP[0] Input Cap. ASRCP[0] Max Load ASRCP[1] Input Cap. ASRCP[1] Imput Cap. ASRCP[1] Max Load ASRCP[1] Imput Cap. ASRCP[2] Imput Cap. ASRCP[2] Imput Cap. ASRCP[2] Imput Cap. ASRCP[2] Imput Cap. ASRCP[3] Imput Cap. ASRCP[4] Max Load ASRCP[4] Imput Cap. ASRCP[4] Max Load ASRCP[4] Imput Cap. ASRCP[4] Imput Cap. ASRCP[4] Imput Cap. ASRCP[6] Imput Cap. ASRCPCORE[0] Imput Cap. ASRCPCORE[1] Imput Cap. ASRCPCORE[2] Imput Cap. ASRCPCORE[3] Imput Cap. ASRCPCORE[4] Imput Cap. ASRCPCORE[3] Imput Cap. ASRCPCORE[4] Imput Cap. ASRCPCORE[6] Imput Cap. ASRCPCO			10000.000
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ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB CORE Input Cap. 0.0100 0.0100		10000.000	10000.000
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REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOBCORE Input Cap. 0.0100 0.0100			
REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOBCORE Input Cap. 0.0100 0.0100			
REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOBCORE Input Cap. 0.0100 0.0100			
REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOBCORE Input Cap. 0.0100 0.0100	• • • • • • • • • • • • • • • • • • • •		
REFIOB Max Load 10000.000 10000.000 REFIOBCORE Input Cap. 0.0100 0.0100			
REFIOBCORE Input Cap. 0.0100 0.0100	• • • • • • • • • • • • • • • • • • • •		
REFIOBCORE Max Load 10000.000 10000.000			
	REFIOBCORE Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	9.261e-06
worst 0.90 -40	0.000e+00	5.445e-09



FILLCELL_END_LEFT_EXT_CSF_FC_LIN

Cell Description

FILLCELL_END_LEFT_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

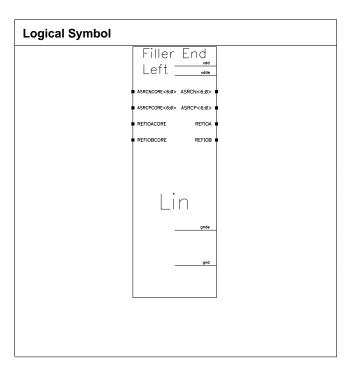
Physical Dimensions

Area(um2): 1543.600

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)		Valu	e(pF)
Parameter	best 1.10 125	worst 0.90 -40		
ASRCN[0] Input Cap.	0.0100	0.0100		
ASRCN[0] Max Load	10000.000	10000.000		
ASRCN[1] Input Cap.	0.0100	0.0100		
ASRCN[1] Max Load	10000.000	10000.000		
ASRCN[2] Input Cap.	0.0100	0.0100		
ASRCN[2] Max Load	10000.000	10000.000		
ASRCN[3] Input Cap.	0.0100	0.0100		
ASRCN[3] Max Load	10000.000	10000.000		
ASRCN[4] Input Cap.	0.0100	0.0100		
ASRCN[4] Max Load	10000.000	10000.000		
ASRCN[5] Input Cap.	0.0100	0.0100		
ASRCN[5] Max Load	10000.000	10000.000		
ASRCN[6] Input Cap.	0.0100	0.0100		
ASRCN[6] Max Load	10000.000	10000.000		
ASRCNCORE[0] Input Cap.	0.0100	0.0100		
ASRCNCORE[0] Max Load	10000.000	10000.000		
ASRCNCORE[1] Input Cap.	0.0100	0.0100		
ASRCNCORE[1] Max Load	10000.000	10000.000		
ASRCNCORE[2] Input Cap.	0.0100	0.0100		
ASRCNCORE[2] Max Load	10000.000	10000.000		
ASRCNCORE[3] Input Cap.	0.0100	0.0100		
ASRCNCORE[3] Max Load	10000.000	10000.000		



ASRCNCORE[4] Input Cap.	0.0100	0.0100
ASRCNCORE[4] Max Load	10000.000	10000.000
ASRCNCORE[5] Input Cap.	0.0100	0.0100
ASRCNCORE[5] Max Load	10000.000	10000.000
ASRCNCORE[6] Input Cap.	0.0100	0.0100
ASRCNCORE[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000
ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
ASRCPCORE[0] Input Cap.	0.0100	0.0100
ASRCPCORE[0] Max Load	10000.000	10000.000
ASRCPCORE[1] Input Cap.	0.0100	0.0100
ASRCPCORE[1] Max Load	10000.000	10000.000
ASRCPCORE[2] Input Cap.	0.0100	0.0100
ASRCPCORE[2] Max Load	10000.000	10000.000
ASRCPCORE[3] Input Cap.	0.0100	0.0100
ASRCPCORE[3] Max Load	10000.000	10000.000
ASRCPCORE[4] Input Cap.	0.0100	0.0100
ASRCPCORE[4] Max Load	10000.000	10000.000
ASRCPCORE[5] Input Cap.	0.0100	0.0100
ASRCPCORE[5] Max Load	10000.000	10000.000
ASRCPCORE[6] Input Cap.	0.0100	0.0100
ASRCPCORE[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOACORE Input Cap.	0.0100	0.0100
REFIOACORE Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000
REFIOBCORE Input Cap.	0.0100	0.0100
REFIOBCORE Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	9.261e-06
worst 0.90 -40	0.000e+00	5.445e-09



FILLCELL_END_RIGHT_EXT_CSF_CL_LIN

Cell Description

FILLCELL_END_RIGHT_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

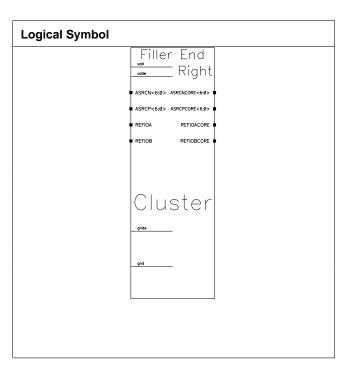
Physical Dimensions

Area(um2): 1807.100

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)		Va
Parameter	best 1.10 125	worst 0.90 -40	
ASRCN[0] Input Cap.	0.0100	0.0100	
ASRCN[0] Max Load	10000.000	10000.000	
ASRCN[1] Input Cap.	0.0100	0.0100	
ASRCN[1] Max Load	10000.000	10000.000	
ASRCN[2] Input Cap.	0.0100	0.0100	
ASRCN[2] Max Load	10000.000	10000.000	
ASRCN[3] Input Cap.	0.0100	0.0100	
ASRCN[3] Max Load	10000.000	10000.000	
ASRCN[4] Input Cap.	0.0100	0.0100	
ASRCN[4] Max Load	10000.000	10000.000	
ASRCN[5] Input Cap.	0.0100	0.0100	
ASRCN[5] Max Load	10000.000	10000.000	
ASRCN[6] Input Cap.	0.0100	0.0100	
ASRCN[6] Max Load	10000.000	10000.000	
ASRCNCORE[0] Input Cap.	0.0100	0.0100	
ASRCNCORE[0] Max Load	10000.000	10000.000	
ASRCNCORE[1] Input Cap.	0.0100	0.0100	
ASRCNCORE[1] Max Load	10000.000	10000.000	
ASRCNCORE[2] Input Cap.	0.0100	0.0100	
ASRCNCORE[2] Max Load	10000.000	10000.000	
ASRCNCORE[3] Input Cap.	0.0100	0.0100	
ASRCNCORE[3] Max Load	10000.000	10000.000	



ASRCNCORE[5] Input Cap. 0.0100 0.0000 ASRCNCORE[6] Input Cap. 0.0100 0.0100 ASRCNCORE[6] Input Cap. 0.0100 0.0100 ASRCNCORE[6] Imax Load 10000.000 10000.000 ASRCNCORE[6] Max Load 10000.000 10000.000 ASRCNCORE[6] Max Load 10000.000 10000.000 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Ca	ASRCNCORE[4] Input Cap.	0.0100	0.0100
ASRCNCORE[5] Max Load 10000.000 10000.000 ASRCNCORE[6] Input Cap. 0.0100 0.0100 0.0100 ASRCNCORE[6] Max Load 10000.000 10000.000 10000.000 ASRCP[0] Input Cap. 0.0100 0.0100 0.0100 ASRCP[0] Input Cap. 0.0100 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[2] Max Load 10000.000 10000.000 10000.000 ASRCP[2] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 0.0100 0.0100 0.0000 ASRCP[4] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.05RCP[6] Input Cap. 0.0100 0.0100 0.0100 0.05RCP[6] Input Cap. 0.0100 0.0100 0.0100 0.05RCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0000 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0000 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0000 ASRCPCORE[0] Input Cap. 0.0100 0.0100 0.0100 0.0000 ASRCPCORE[0] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.010	ASRCNCORE[4] Max Load	10000.000	10000.000
ASRCNCORE[5] Max Load 10000.000 10000.000 ASRCNCORE[6] Input Cap. 0.0100 0.0100 0.0100 ASRCNCORE[6] Max Load 10000.000 10000.000 10000.000 ASRCP[0] Input Cap. 0.0100 0.0100 0.0100 ASRCP[0] Input Cap. 0.0100 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[2] Max Load 10000.000 10000.000 10000.000 ASRCP[2] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 0.0100 0.0100 0.0000 ASRCP[4] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.05RCP[6] Input Cap. 0.0100 0.0100 0.0100 0.05RCP[6] Input Cap. 0.0100 0.0100 0.0100 0.05RCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0000 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0000 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0000 ASRCPCORE[0] Input Cap. 0.0100 0.0100 0.0100 0.0000 ASRCPCORE[0] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.010	ASRCNCORE[5] Input Cap.	0.0100	0.0100
ASRCNCORE[6] Input Cap. 0.0100 0.0100 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[4] IMax Load 10000.000 10000.000 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100		10000.000	10000.000
ASRCP(0) Input Cap. 0.0100 0.0100 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[3] Max Load 10000.000 10000.000 ASRCP[3] Max Load 10000.000 10000.000 ASRCP[3] Max Load 10000.000 10000.000 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap.		0.0100	0.0100
ASRCP[0] Max Load 10000.000 10000.000	ASRCNCORE[6] Max Load		10000.000
ASRCP[1] Input Cap. 0.0100 0.0100 10000.000 10000.000 ASRCP[2] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 0.0100 0.0100 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 0.0100 0.0100 0.0100 ASRCP[4] Input Cap. 0.01000 0.01000 0.01000 0.01000 0.01000 0.0100 0.0100 0.01000 0.01000 0.01000 0.	ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load 10000.000 10000.000	ASRCP[0] Max Load	10000.000	10000.000
ASRCP[2] Input Cap. 0.0100 0.0100 10000.000 10000.000 ASRCP[3] Input Cap. 0.0100 0.010	ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load 10000.000 10000.000 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[3] Max Load 10000.000 10000.000 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[4] Max Load 10000.000 10000.000 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100	ASRCP[1] Max Load	10000.000	10000.000
ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[4] Max Load 10000.000 10000.000 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100	ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load 10000.000 10000.000 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 10000.000 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 10000.000 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Cap. 0.0100 0.0100 REFIOA CORE Input Cap. 0.0100 0.0100 REFIOA CORE Input Cap. 0.0100 0.0100 REFIOA Dax Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOA Dax Load 10000.000 10000.000 REFIOA Core Input Cap. 0.0100 0.0100 REFIOA Dax Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100	ASRCP[2] Max Load	10000.000	10000.000
ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[4] Max Load 10000.000 10000.000 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100	ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load 10000.000 10000.000 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100	ASRCP[3] Max Load	10000.000	10000.000
ASRCP[5] Input Cap. 0.0100 0.0100 10000.000 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCPCORE[0] Imput Cap. 0.01000 0.01000 0.01000 0.01000 0.01000 0.0100 0.0100 0.0100 0.0100 0.0100 0.0100 0.0100 0.0100 0.0100 0.0100 0.0100 0.01000 0.0100 0.0100 0.0100 0.0100 0.0100 0.010	ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load 10000.000 10000.000 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100	ASRCP[4] Max Load	10000.000	10000.000
ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Max Load 10000.000 10000.000 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000	ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load 10000.000 10000.000 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Imput Cap. 0.0100 0.0100 ASRCPCORE[1] Imput Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100	ASRCP[5] Max Load	10000.000	10000.000
ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Imput Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Imput Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100		0.0100	0.0100
ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Imput Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Imput Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100	ASRCP[6] Max Load	10000.000	10000.000
ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Max Load 10000.000 10000.000 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000		0.0100	0.0100
ASRCPCORE[1] Max Load 10000.000 10000.000 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100	ASRCPCORE[0] Max Load	10000.000	10000.000
ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000	ASRCPCORE[1] Input Cap.	0.0100	0.0100
ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000	ASRCPCORE[1] Max Load	10000.000	10000.000
ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Max Load 10000.000 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100 REFIOB Max Load 10000.000 0.0100	ASRCPCORE[2] Input Cap.	0.0100	0.0100
ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Max Load 10000.000 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100 REFIOB Max Load 10000.000 0.0100 REFIOB Max Load 10000.000 0.0100	ASRCPCORE[2] Max Load	10000.000	10000.000
ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Max Load 10000.000 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 0.0100 REFIOB Max Load 10000.000 0.0100	ASRCPCORE[3] Input Cap.	0.0100	0.0100
ASRCPCORE[4] Max Load 10000.000 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100 REFIOB Max Load 10000.000 0.0100	ASRCPCORE[3] Max Load	10000.000	10000.000
ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100		0.0100	0.0100
ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100 REFIOB CORE Input Cap. 0.0100 0.0100	ASRCPCORE[4] Max Load	10000.000	10000.000
ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100			0.0100
ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB CORE Input Cap. 0.0100 0.0100		10000.000	10000.000
REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOBCORE Input Cap. 0.0100 0.0100			
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REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOBCORE Input Cap. 0.0100 0.0100			
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REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOBCORE Input Cap. 0.0100 0.0100	• • •		
REFIOB Max Load 10000.000 10000.000 REFIOBCORE Input Cap. 0.0100 0.0100			10000.000
REFIOBCORE Input Cap. 0.0100 0.0100			
REFIOBCORE Max Load 10000.000 10000.000			
	REFIOBCORE Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	1.012e-05
worst 0.90 -40	0.000e+00	5.445e-09



FILLCELL_END_RIGHT_EXT_CSF_FC_2ROWS

Cell Description

FILLCELL_END_RIGHT_EXT_CSF_FC_2ROWS

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

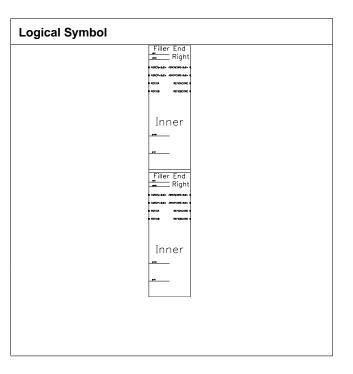
Physical Dimensions

Area(um2): 3876.600

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)		Va
Parameter	best 1.10 125	worst 0.90 -40	
ASRCN[0] Input Cap.	0.0100	0.0100	
ASRCN[0] Max Load	10000.000	10000.000	
ASRCN[1] Input Cap.	0.0100	0.0100	
ASRCN[1] Max Load	10000.000	10000.000	
ASRCN[2] Input Cap.	0.0100	0.0100	
ASRCN[2] Max Load	10000.000	10000.000	
ASRCN[3] Input Cap.	0.0100	0.0100	
ASRCN[3] Max Load	10000.000	10000.000	
ASRCN[4] Input Cap.	0.0100	0.0100	
ASRCN[4] Max Load	10000.000	10000.000	
ASRCN[5] Input Cap.	0.0100	0.0100	
ASRCN[5] Max Load	10000.000	10000.000	
ASRCN[6] Input Cap.	0.0100	0.0100	
ASRCN[6] Max Load	10000.000	10000.000	
ASRCNCORE[0] Input Cap.	0.0100	0.0100	
ASRCNCORE[0] Max Load	10000.000	10000.000	
ASRCNCORE[1] Input Cap.	0.0100	0.0100	
ASRCNCORE[1] Max Load	10000.000	10000.000	
ASRCNCORE[2] Input Cap.	0.0100	0.0100	
ASRCNCORE[2] Max Load	10000.000	10000.000	
ASRCNCORE[3] Input Cap.	0.0100	0.0100	
ASRCNCORE[3] Max Load	10000.000	10000.000	



ASRCNCORE[4] Input Cap.	0.0100	0.0100
ASRCNCORE[4] Max Load	10000.000	10000.000
ASRCNCORE[5] Input Cap.	0.0100	0.0100
ASRCNCORE[5] Max Load	10000.000	10000.000
ASRCNCORE[6] Input Cap.	0.0100	0.0100
ASRCNCORE[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000
ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
ASRCPCORE[0] Input Cap.	0.0100	0.0100
ASRCPCORE[0] Max Load	10000.000	10000.000
ASRCPCORE[1] Input Cap.	0.0100	0.0100
ASRCPCORE[1] Max Load	10000.000	10000.000
ASRCPCORE[2] Input Cap.	0.0100	0.0100
ASRCPCORE[2] Max Load	10000.000	10000.000
ASRCPCORE[3] Input Cap.	0.0100	0.0100
ASRCPCORE[3] Max Load	10000.000	10000.000
ASRCPCORE[4] Input Cap.	0.0100	0.0100
ASRCPCORE[4] Max Load	10000.000	10000.000
ASRCPCORE[5] Input Cap.	0.0100	0.0100
ASRCPCORE[5] Max Load	10000.000	10000.000
ASRCPCORE[6] Input Cap.	0.0100	0.0100
ASRCPCORE[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOACORE Input Cap.	0.0100	0.0100
REFIOACORE Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000
REFIOBCORE Input Cap.	0.0100	0.0100
REFIOBCORE Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	2.683e-05
worst 0.90 -40	0.000e+00	5.445e-09



FILLCELL_END_RIGHT_EXT_CSF_FC_INNER

Cell Description

FILLCELL_END_RIGHT_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

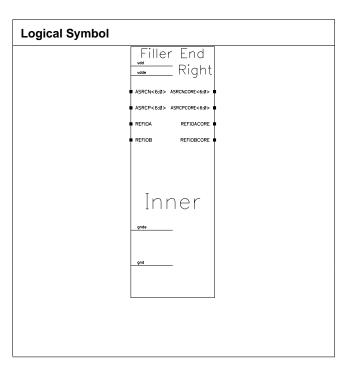
Physical Dimensions

Area(um2): 1543.600

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCNCORE[0] Input Cap.	0.0100	0.0100
ASRCNCORE[0] Max Load	10000.000	10000.000
ASRCNCORE[1] Input Cap.	0.0100	0.0100
ASRCNCORE[1] Max Load	10000.000	10000.000
ASRCNCORE[2] Input Cap.	0.0100	0.0100
ASRCNCORE[2] Max Load	10000.000	10000.000
ASRCNCORE[3] Input Cap.	0.0100	0.0100
ASRCNCORE[3] Max Load	10000.000	10000.000



ASRCNCORE[5] Input Cap. 0.0100 0.0000 ASRCNCORE[6] Input Cap. 0.0100 0.0100 ASRCNCORE[6] Input Cap. 0.0100 0.0100 ASRCNCORE[6] Imax Load 10000.000 10000.000 ASRCNCORE[6] Max Load 10000.000 10000.000 ASRCNCORE[6] Max Load 10000.000 10000.000 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0	ASRCNCORE[4] Input Cap.	0.0100	0.0100
ASRCNCORE[5] Max Load 10000.000 10000.000	ASRCNCORE[4] Max Load	10000.000	10000.000
ASRCNCORE[5] Max Load 10000.000 10000.000	ASRCNCORE[5] Input Cap.	0.0100	0.0100
ASRCNCORE[6] Max Load ASRCP[0] Input Cap. ASRCP[0] Max Load ASRCP[1] Input Cap. ASRCP[1] Imput Cap. ASRCP[1] Max Load ASRCP[1] Imput Cap. ASRCP[2] Imput Cap. ASRCP[2] Imput Cap. ASRCP[2] Imput Cap. ASRCP[2] Imput Cap. ASRCP[3] Imput Cap. ASRCP[4] Max Load ASRCP[4] Imput Cap. ASRCP[4] Max Load ASRCP[4] Imput Cap. ASRCP[4] Imput Cap. ASRCP[4] Imput Cap. ASRCP[6] Imput Cap. ASRCPCORE[0] Imput Cap. ASRCPCORE[1] Imput Cap. ASRCPCORE[2] Imput Cap. ASRCPCORE[3] Imput Cap. ASRCPCORE[4] Imput Cap. ASRCPCORE[3] Imput Cap. ASRCPCORE[4] Imput Cap. ASRCPCORE[6] Imput Cap. ASRCPCO			10000.000
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ASRCP[2] Max Load 10000.000 10000.000	ASRCP[1] Max Load	10000.000	10000.000
ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[4] Max Load 10000.000 10000.000 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100	ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load 10000.000 10000.000 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100	ASRCP[2] Max Load	10000.000	10000.000
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ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Max Load 10000.000 10000.000 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000	ASRCP[5] Input Cap.	0.0100	0.0100
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ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Max Load 10000.000 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100 REFIOB Max Load 10000.000 0.0100 REFIOB Max Load 10000.000 0.0100	ASRCPCORE[2] Max Load	10000.000	10000.000
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REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOBCORE Input Cap. 0.0100 0.0100			
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REFIOB Max Load 10000.000 10000.000 REFIOBCORE Input Cap. 0.0100 0.0100			
REFIOBCORE Input Cap. 0.0100 0.0100	• • • • • • • • • • • • • • • • • • • •		
REFIOBCORE Max Load 10000.000 10000.000			
	REFIOBCORE Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	9.261e-06
worst 0.90 -40	0.000e+00	5.445e-09



FILLCELL_END_RIGHT_EXT_CSF_FC_LIN

Cell Description

FILLCELL_END_RIGHT_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

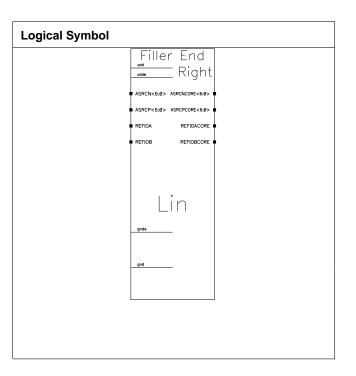
Physical Dimensions

Area(um2): 1543.600

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCNCORE[0] Input Cap.	0.0100	0.0100
ASRCNCORE[0] Max Load	10000.000	10000.000
ASRCNCORE[1] Input Cap.	0.0100	0.0100
ASRCNCORE[1] Max Load	10000.000	10000.000
ASRCNCORE[2] Input Cap.	0.0100	0.0100
ASRCNCORE[2] Max Load	10000.000	10000.000
ASRCNCORE[3] Input Cap.	0.0100	0.0100
ASRCNCORE[3] Max Load	10000.000	10000.000



ASRCNCORE[5] Input Cap. 0.0100 0.0000 ASRCNCORE[6] Input Cap. 0.0100 0.0100 ASRCNCORE[6] Input Cap. 0.0100 0.0100 ASRCNCORE[6] Imax Load 10000.000 10000.000 ASRCNCORE[6] Max Load 10000.000 10000.000 ASRCNCORE[6] Max Load 10000.000 10000.000 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Ca	ASRCNCORE[4] Input Cap.	0.0100	0.0100
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ASRCNCORE[5] Max Load 10000.000 10000.000 ASRCNCORE[6] Input Cap. 0.0100 0.0100 0.0100 ASRCNCORE[6] Max Load 10000.000 10000.000 10000.000 ASRCP[0] Input Cap. 0.0100 0.0100 0.0100 ASRCP[0] Input Cap. 0.0100 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[2] Max Load 10000.000 10000.000 10000.000 ASRCP[2] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 0.0100 0.0100 0.0000 ASRCP[4] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.05RCP[6] Input Cap. 0.0100 0.0100 0.0100 0.05RCP[6] Input Cap. 0.0100 0.0100 0.0100 0.05RCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0000 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0000 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0000 ASRCPCORE[0] Input Cap. 0.0100 0.0100 0.0100 0.0000 ASRCPCORE[0] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.010	ASRCNCORE[5] Input Cap.	0.0100	0.0100
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ASRCP(0) Input Cap. 0.0100 0.0100 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[3] Max Load 10000.000 10000.000 ASRCP[3] Max Load 10000.000 10000.000 ASRCP[3] Max Load 10000.000 10000.000 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap.		0.0100	0.0100
ASRCP[0] Max Load 10000.000 10000.000	ASRCNCORE[6] Max Load		10000.000
ASRCP[1] Input Cap. 0.0100 0.0100 10000.000 10000.000 ASRCP[2] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 0.0100 0.0100 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 0.0100 0.0100 0.0100 ASRCP[4] Input Cap. 0.01000 0.01000 0.01000 0.01000 0.01000 0.0100 0.0100 0.01000 0.01000 0.01000 0.	ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load 10000.000 10000.000	ASRCP[0] Max Load	10000.000	10000.000
ASRCP[2] Input Cap. 0.0100 0.0100 10000.000 10000.000 ASRCP[3] Input Cap. 0.0100 0.010	ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load 10000.000 10000.000 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[3] Max Load 10000.000 10000.000 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[4] Max Load 10000.000 10000.000 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100	ASRCP[1] Max Load	10000.000	10000.000
ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[4] Max Load 10000.000 10000.000 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100	ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load 10000.000 10000.000 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 10000.000 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 10000.000 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Cap. 0.0100 0.0100 REFIOA CORE Input Cap. 0.0100 0.0100 REFIOA CORE Input Cap. 0.0100 0.0100 REFIOA Dax Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOA Dax Load 10000.000 10000.000 REFIOA Core Input Cap. 0.0100 0.0100 REFIOA Dax Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100	ASRCP[2] Max Load	10000.000	10000.000
ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[4] Max Load 10000.000 10000.000 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100	ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load 10000.000 10000.000 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100	ASRCP[3] Max Load	10000.000	10000.000
ASRCP[5] Input Cap. 0.0100 0.0100 10000.000 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCPCORE[0] Imput Cap. 0.01000 0.01000 0.01000 0.01000 0.01000 0.0100 0.0100 0.0100 0.0100 0.0100 0.0100 0.0100 0.0100 0.0100 0.0100 0.0100 0.01000 0.0100 0.0100 0.0100 0.0100 0.0100 0.010	ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load 10000.000 10000.000 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100	ASRCP[4] Max Load	10000.000	10000.000
ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Max Load 10000.000 10000.000 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000	ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load 10000.000 10000.000 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Imput Cap. 0.0100 0.0100 ASRCPCORE[1] Imput Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100	ASRCP[5] Max Load	10000.000	10000.000
ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Imput Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Imput Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100		0.0100	0.0100
ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Imput Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Imput Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100	ASRCP[6] Max Load	10000.000	10000.000
ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Max Load 10000.000 10000.000 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000		0.0100	0.0100
ASRCPCORE[1] Max Load 10000.000 10000.000 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100	ASRCPCORE[0] Max Load	10000.000	10000.000
ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000	ASRCPCORE[1] Input Cap.	0.0100	0.0100
ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000	ASRCPCORE[1] Max Load	10000.000	10000.000
ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Max Load 10000.000 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100 REFIOB Max Load 10000.000 0.0100	ASRCPCORE[2] Input Cap.	0.0100	0.0100
ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Max Load 10000.000 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100 REFIOB Max Load 10000.000 0.0100 REFIOB Max Load 10000.000 0.0100	ASRCPCORE[2] Max Load	10000.000	10000.000
ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Max Load 10000.000 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 0.0100 REFIOB Max Load 10000.000 0.0100	ASRCPCORE[3] Input Cap.	0.0100	0.0100
ASRCPCORE[4] Max Load 10000.000 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100 REFIOB Max Load 10000.000 0.0100	ASRCPCORE[3] Max Load	10000.000	10000.000
ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100		0.0100	0.0100
ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100 REFIOB CORE Input Cap. 0.0100 0.0100	ASRCPCORE[4] Max Load	10000.000	10000.000
ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100			0.0100
ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB CORE Input Cap. 0.0100 0.0100		10000.000	10000.000
REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOBCORE Input Cap. 0.0100 0.0100			
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REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOBCORE Input Cap. 0.0100 0.0100			
REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOBCORE Input Cap. 0.0100 0.0100	• • •		
REFIOB Max Load 10000.000 10000.000 REFIOBCORE Input Cap. 0.0100 0.0100			10000.000
REFIOBCORE Input Cap. 0.0100 0.0100			
REFIOBCORE Max Load 10000.000 10000.000			
	REFIOBCORE Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	9.261e-06
worst 0.90 -40	0.000e+00	5.445e-09



FILLCELL_FEEDTHROUGH_40UM_EXT_CSF_CL_LIN

Cell Description

FILLCELL_FEEDTHROUGH_40UM_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

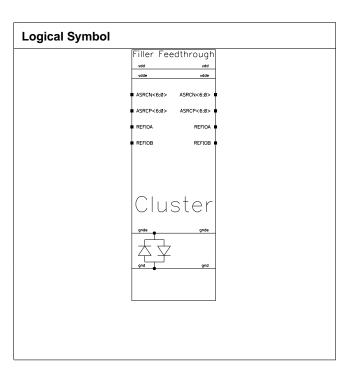
Physical Dimensions

Area(um2): 4252.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Valu	e(pF)
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	1.450e-05
worst 0.90 -40	0.000e+00	2.335e-08



FILLCELL_REFASRC_EXT_CSF_CL_LIN

Cell Description

FILLCELL_REFASRC_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

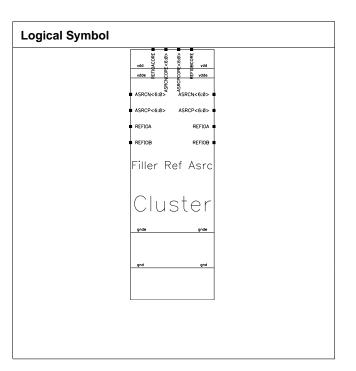
Physical Dimensions

Area(um2): 903.550

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCNCORE[0] Input Cap.	0.0100	0.0100
ASRCNCORE[0] Max Load	10000.000	10000.000
ASRCNCORE[1] Input Cap.	0.0100	0.0100
ASRCNCORE[1] Max Load	10000.000	10000.000
ASRCNCORE[2] Input Cap.	0.0100	0.0100
ASRCNCORE[2] Max Load	10000.000	10000.000
ASRCNCORE[3] Input Cap.	0.0100	0.0100
ASRCNCORE[3] Max Load	10000.000	10000.000



ASRCNCORE[5] Input Cap. 0.0100 0.0000 ASRCNCORE[6] Input Cap. 0.0100 0.0100 ASRCNCORE[6] Input Cap. 0.0100 0.0100 ASRCNCORE[6] Imax Load 10000.000 10000.000 ASRCNCORE[6] Max Load 10000.000 10000.000 ASRCNCORE[6] Max Load 10000.000 10000.000 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0	ASRCNCORE[4] Input Cap.	0.0100	0.0100
ASRCNCORE[5] Max Load 10000.000 10000.000	ASRCNCORE[4] Max Load	10000.000	10000.000
ASRCNCORE[5] Max Load 10000.000 10000.000	ASRCNCORE[5] Input Cap.	0.0100	0.0100
ASRCNCORE[6] Max Load ASRCP[0] Input Cap. ASRCP[0] Max Load ASRCP[1] Input Cap. ASRCP[1] Imput Cap. ASRCP[1] Max Load ASRCP[1] Imput Cap. ASRCP[2] Imput Cap. ASRCP[2] Imput Cap. ASRCP[2] Imput Cap. ASRCP[2] Imput Cap. ASRCP[3] Imput Cap. ASRCP[4] Max Load ASRCP[4] Imput Cap. ASRCP[4] Max Load ASRCP[4] Imput Cap. ASRCP[4] Imput Cap. ASRCP[4] Imput Cap. ASRCP[6] Imput Cap. ASRCPCORE[0] Imput Cap. ASRCPCORE[1] Imput Cap. ASRCPCORE[2] Imput Cap. ASRCPCORE[3] Imput Cap. ASRCPCORE[4] Imput Cap. ASRCPCORE[3] Imput Cap. ASRCPCORE[4] Imput Cap. ASRCPCORE[6] Imput Cap. ASRCPCO		10000.000	10000.000
ASRCP(0) Input Cap. 0.0100 0.0100 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[3] Max Load 10000.000 10000.000 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0		0.0100	0.0100
ASRCP[0] Max Load 10000.000 10000.000	ASRCNCORE[6] Max Load		10000.000
ASRCP[1] Input Cap. 0.0100 0.0100 10000.000 10000.000 ASRCP[2] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 0.0100 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 0.0100 0.0100 0.050	ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[2] Input Cap. 0.0100 0.0000 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.01000 ASRCPCORE[6] Input Cap. 0.0100 0.0100	ASRCP[0] Max Load	10000.000	10000.000
ASRCP[2] Input Cap. 0.0100 0.0100 10000.000 10000.000 ASRCP[3] Input Cap. 0.0100 0.010	ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load 10000.000 10000.000	ASRCP[1] Max Load	10000.000	10000.000
ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[4] Max Load 10000.000 10000.000 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100	ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load 10000.000 10000.000 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100	ASRCP[2] Max Load	10000.000	10000.000
ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[4] Max Load 10000.000 10000.000 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100	ASRCP[3] Input Cap.	0.0100	0.0100
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ASRCP[4] Max Load 10000.000 10000.000 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100	ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load 10000.000 10000.000 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[3] Max Load 10000.000 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100	ASRCP[4] Max Load	10000.000	10000.000
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ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Max Load 10000.000 10000.000 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000		0.0100	0.0100
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ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Max Load 10000.000 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000		0.0100	0.0100
ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000	ASRCPCORE[1] Max Load	10000.000	10000.000
ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Max Load 10000.000 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100	ASRCPCORE[2] Input Cap.	0.0100	0.0100
ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Max Load 10000.000 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100 REFIOB Max Load 10000.000 0.0100 REFIOB Max Load 10000.000 0.0100	ASRCPCORE[2] Max Load	10000.000	10000.000
ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Max Load 10000.000 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 0.0100 REFIOB Max Load 10000.000 0.0100	ASRCPCORE[3] Input Cap.	0.0100	0.0100
ASRCPCORE[4] Max Load 10000.000 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100 REFIOB Max Load 10000.000 0.0100	ASRCPCORE[3] Max Load	10000.000	10000.000
ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100		0.0100	0.0100
ASRCPCORE[5] Max Load 10000.000 10000.000 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100 REFIOB CORE Input Cap. 0.0100 0.0100	ASRCPCORE[4] Max Load	10000.000	10000.000
ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 0.0100			0.0100
ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB CORE Input Cap. 0.0100 0.0100		10000.000	10000.000
REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOBCORE Input Cap. 0.0100 0.0100			
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REFIOB Max Load 10000.000 10000.000 REFIOBCORE Input Cap. 0.0100 0.0100			
REFIOBCORE Input Cap. 0.0100 0.0100	• • • • • • • • • • • • • • • • • • • •		
REFIOBCORE Max Load 10000.000 10000.000			
	REFIOBCORE Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	9.008e-07
worst 0.90 -40	0.000e+00	5.445e-09



FILLCELL_REFASRC_EXT_CSF_FC_2ROWS

Cell Description

FILLCELL_REFASRC_EXT_CSF_FC_2ROWS

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

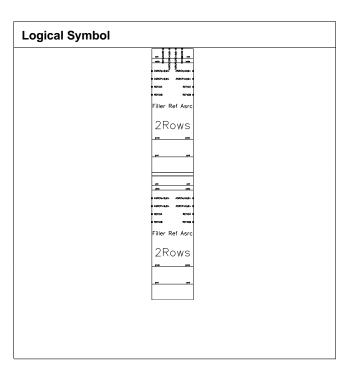
Physical Dimensions

Area(um2): 1569.100

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Farameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCNCORE[0] Input Cap.	0.0100	0.0100
ASRCNCORE[0] Max Load	10000.000	10000.000
ASRCNCORE[1] Input Cap.	0.0100	0.0100
ASRCNCORE[1] Max Load	10000.000	10000.000
ASRCNCORE[2] Input Cap.	0.0100	0.0100
ASRCNCORE[2] Max Load	10000.000	10000.000
ASRCNCORE[3] Input Cap.	0.0100	0.0100
ASRCNCORE[3] Max Load	10000.000	10000.000



ASRCNCORE[4] Input Cap.	0.0100	0.0100
ASRCNCORE[4] Max Load	10000.000	10000.000
ASRCNCORE[5] Input Cap.	0.0100	0.0100
ASRCNCORE[5] Max Load	10000.000	10000.000
ASRCNCORE[6] Input Cap.	0.0100	0.0100
ASRCNCORE[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000
ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
ASRCPCORE[0] Input Cap.	0.0100	0.0100
ASRCPCORE[0] Max Load	10000.000	10000.000
ASRCPCORE[1] Input Cap.	0.0100	0.0100
ASRCPCORE[1] Max Load	10000.000	10000.000
ASRCPCORE[2] Input Cap.	0.0100	0.0100
ASRCPCORE[2] Max Load	10000.000	10000.000
ASRCPCORE[3] Input Cap.	0.0100	0.0100
ASRCPCORE[3] Max Load	10000.000	10000.000
ASRCPCORE[4] Input Cap.	0.0100	0.0100
ASRCPCORE[4] Max Load	10000.000	10000.000
ASRCPCORE[5] Input Cap.	0.0100	0.0100
ASRCPCORE[5] Max Load	10000.000	10000.000
ASRCPCORE[6] Input Cap.	0.0100	0.0100
ASRCPCORE[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOACORE Input Cap.	0.0100	0.0100
REFIOACORE Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000
REFIOBCORE Input Cap.	0.0100	0.0100
REFIOBCORE Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_REFASRC_EXT_CSF_FC_LIN

Cell Description

FILLCELL_REFASRC_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

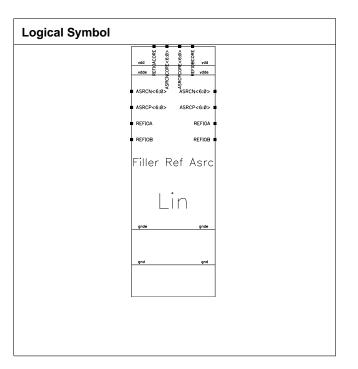
Physical Dimensions

Area(um2): 771.800

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCNCORE[0] Input Cap.	0.0100	0.0100
ASRCNCORE[0] Max Load	10000.000	10000.000
ASRCNCORE[1] Input Cap.	0.0100	0.0100
ASRCNCORE[1] Max Load	10000.000	10000.000
ASRCNCORE[2] Input Cap.	0.0100	0.0100
ASRCNCORE[2] Max Load	10000.000	10000.000
ASRCNCORE[3] Input Cap.	0.0100	0.0100
ASRCNCORE[3] Max Load	10000.000	10000.000



ASRCNCORE[5] Input Cap. 0.0100 0.0000 ASRCNCORE[6] Input Cap. 0.0100 0.0100 ASRCNCORE[6] Input Cap. 0.0100 0.0100 ASRCNCORE[6] Imax Load 10000.000 10000.000 ASRCNCORE[6] Max Load 10000.000 10000.000 ASRCNCORE[6] Max Load 10000.000 10000.000 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0	ASRCNCORE[4] Input Cap.	0.0100	0.0100
ASRCNCORE[5] Max Load 10000.000 10000.000	ASRCNCORE[4] Max Load	10000.000	10000.000
ASRCNCORE[5] Max Load 10000.000 10000.000	ASRCNCORE[5] Input Cap.	0.0100	0.0100
ASRCNCORE[6] Max Load ASRCP[0] Input Cap. ASRCP[0] Max Load ASRCP[1] Input Cap. ASRCP[1] Imput Cap. ASRCP[1] Max Load ASRCP[1] Imput Cap. ASRCP[2] Imput Cap. ASRCP[2] Imput Cap. ASRCP[2] Imput Cap. ASRCP[2] Imput Cap. ASRCP[3] Imput Cap. ASRCP[4] Max Load ASRCP[4] Imput Cap. ASRCP[4] Max Load ASRCP[4] Imput Cap. ASRCP[4] Imput Cap. ASRCP[4] Imput Cap. ASRCP[6] Imput Cap. ASRCPCORE[0] Imput Cap. ASRCPCORE[1] Imput Cap. ASRCPCORE[2] Imput Cap. ASRCPCORE[3] Imput Cap. ASRCPCORE[4] Imput Cap. ASRCPCORE[3] Imput Cap. ASRCPCORE[4] Imput Cap. ASRCPCORE[6] Imput Cap. ASRCPCO		10000.000	10000.000
ASRCP(0) Input Cap. 0.0100 0.0100 ASRCP[0] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[1] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 ASRCP[3] Max Load 10000.000 10000.000 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0		0.0100	0.0100
ASRCP[0] Max Load 10000.000 10000.000	ASRCNCORE[6] Max Load		10000.000
ASRCP[1] Input Cap. 0.0100 0.0100 10000.000 10000.000 ASRCP[2] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[2] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[3] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 0.0100 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 0.0100 0.0100 0.050	ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[2] Input Cap. 0.0100 0.0000 ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.01000 ASRCPCORE[6] Input Cap. 0.0100 0.0100	ASRCP[0] Max Load	10000.000	10000.000
ASRCP[2] Input Cap. 0.0100 0.0100 10000.000 10000.000 ASRCP[3] Input Cap. 0.0100 0.010	ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load 10000.000 10000.000	ASRCP[1] Max Load	10000.000	10000.000
ASRCP[3] Input Cap. 0.0100 0.0100 ASRCP[4] Max Load 10000.000 10000.000 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100	ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load 10000.000 10000.000 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100	ASRCP[2] Max Load	10000.000	10000.000
ASRCP[4] Input Cap. 0.0100 0.0100 ASRCP[4] Max Load 10000.000 10000.000 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100	ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load 10000.000 10000.000 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100	ASRCP[3] Max Load	10000.000	10000.000
ASRCP[4] Max Load 10000.000 10000.000 ASRCP[5] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100	ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load 10000.000 10000.000 ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[3] Max Load 10000.000 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100 REFIOBCORE Input Cap. 0.0100 0.0100	ASRCP[4] Max Load	10000.000	10000.000
ASRCP[6] Input Cap. 0.0100 0.0100 ASRCP[6] Max Load 10000.000 10000.000 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Max Load 10000.000 10000.000 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000	ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load 10000.000 10000.000 ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100	ASRCP[5] Max Load	10000.000	10000.000
ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Imput Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[3] Imput Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Imput Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Imput Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Imput Cap. 0.0100 0.0100 ASRCPCORE[6] Imput Cap. 0.0100 0.0100 REFIOA Imput Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Imput Cap. 0.0100 0.0100 REFIOACORE Imput Cap. 0.0100 0.0100 REFIOB Imput Cap. 0.0100 0.0100		0.0100	0.0100
ASRCPCORE[0] Input Cap. 0.0100 0.0100 ASRCPCORE[0] Max Load 10000.000 10000.000 ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Imput Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[3] Imput Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Imput Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Imput Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Imput Cap. 0.0100 0.0100 ASRCPCORE[6] Imput Cap. 0.0100 0.0100 REFIOA Imput Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Imput Cap. 0.0100 0.0100 REFIOACORE Imput Cap. 0.0100 0.0100 REFIOB Imput Cap. 0.0100 0.0100	ASRCP[6] Max Load	10000.000	10000.000
ASRCPCORE[1] Input Cap. 0.0100 0.0100 ASRCPCORE[1] Max Load 10000.000 10000.000 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000		0.0100	0.0100
ASRCPCORE[1] Max Load 10000.000 10000.000 ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[2] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000 REFIOB Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100	ASRCPCORE[0] Max Load	10000.000	10000.000
ASRCPCORE[2] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[3] Input Cap. 0.0100 0.0100 ASRCPCORE[3] Max Load 10000.000 10000.000 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Input Cap. 0.0100 0.0100 ASRCPCORE[4] Max Load 10000.000 10000.000 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[5] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Input Cap. 0.0100 0.0100 ASRCPCORE[6] Max Load 10000.000 10000.000 REFIOA Input Cap. 0.0100 0.0100 REFIOA Max Load 10000.000 10000.000 REFIOA Max Load 10000.000 10000.000 REFIOACORE Input Cap. 0.0100 0.0100 REFIOACORE Max Load 10000.000 10000.000 REFIOACORE Max Load 10000.000 10000.000 REFIOB Input Cap. 0.0100 0.0100 REFIOB Max Load 10000.000 10000.000		0.0100	0.0100
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REFIOB Max Load 10000.000 10000.000 REFIOBCORE Input Cap. 0.0100 0.0100			
REFIOBCORE Input Cap. 0.0100 0.0100	• • • • • • • • • • • • • • • • • • • •		
REFIOBCORE Max Load 10000.000 10000.000			
	REFIOBCORE Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_VDDE_GNDE_EXT_CSF_CL_LIN

Cell Description

FILLCELL_VDDE_GNDE_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

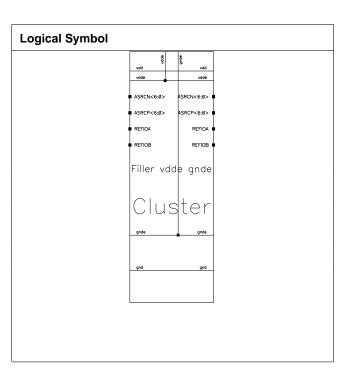
Physical Dimensions

Area(um2): 903.550

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_VDDE_GNDE_EXT_CSF_FC_2ROWS

Cell Description

FILLCELL_VDDE_GNDE_EXT_CSF_FC_2ROWS

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

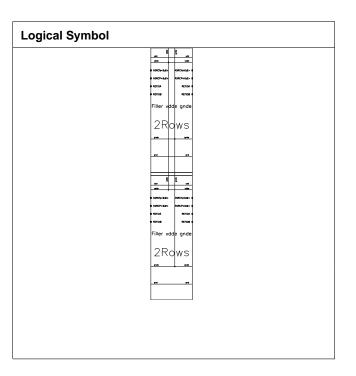
Physical Dimensions

Area(um2): 1569.100

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCELL_VDDE_GNDE_EXT_CSF_FC_LIN

Cell Description

FILLCELL_VDDE_GNDE_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

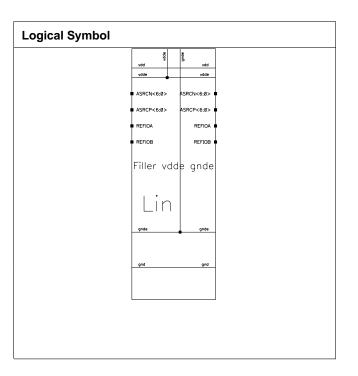
Physical Dimensions

Area(um2): 771.800

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCUTCELL_ALL_EXT_CSF_FC_2ROWS

Cell Description

FILLCUTCELL_ALL_EXT_CSF_FC_2ROWS

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

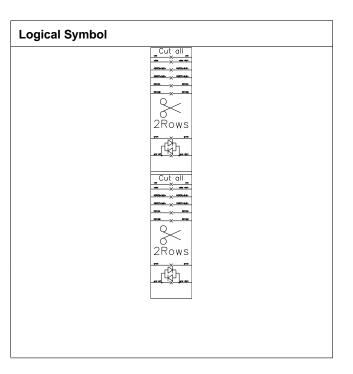
Physical Dimensions

Area(um2): 2584.400

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Default Leakage Power (mW)	vdderight
best 1.10 125	1.496e-05
worst 0.90 -40	3.915e-08



FILLCUTCELL_ALL_EXT_CSF_FC_LIN

Cell Description

FILLCUTCELL_ALL_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

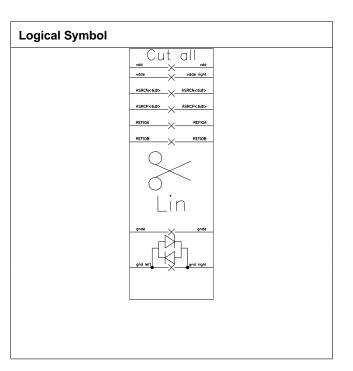
Physical Dimensions

Area(um2): 1271.200

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Default Leakage Power (mW)	vdderight
best 1.10 125	7.482e-06
worst 0.90 -40	2.230e-08



FILLCUTCELL_GNDE_EXT_CSF_CL_LIN

Cell Description

FILLCUTCELL_GNDE_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

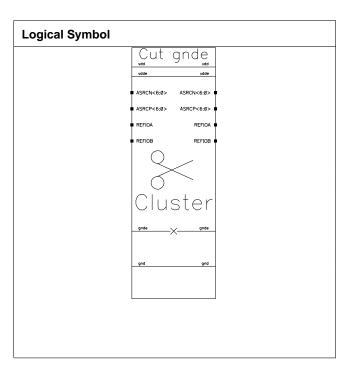
Physical Dimensions

Area(um2): 170.080

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCUTCELL_GNDE_EXT_CSF_FC_2ROWS

Cell Description

FILLCUTCELL_GNDE_EXT_CSF_FC_2ROWS

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

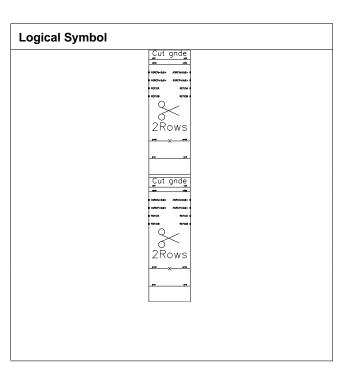
Physical Dimensions

Area(um2): 295.360

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCUTCELL_GNDE_EXT_CSF_FC_LIN

Cell Description

FILLCUTCELL_GNDE_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

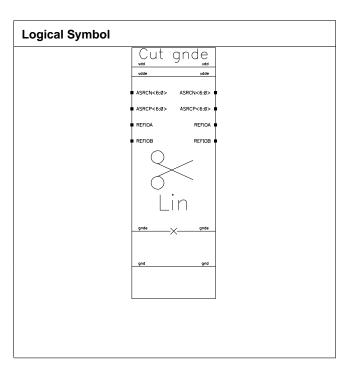
Physical Dimensions

Area(um2): 145.280

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCUTCELL_REFASRC_EXT_CSF_CL_LIN

Cell Description

FILLCUTCELL_REFASRC_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

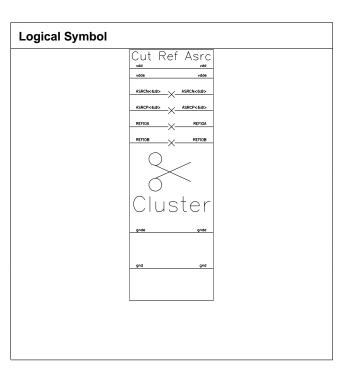
Physical Dimensions

Area(um2): 170.080

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCUTCELL_REFASRC_EXT_CSF_FC_2ROWS

Cell Description

FILLCUTCELL_REFASRC_EXT_CSF_FC_2ROWS

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

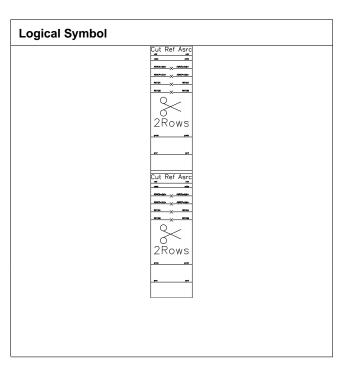
Physical Dimensions

Area(um2): 295.360

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCUTCELL_REFASRC_EXT_CSF_FC_LIN

Cell Description

FILLCUTCELL_REFASRC_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

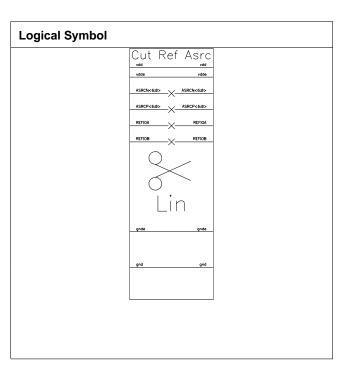
Physical Dimensions

Area(um2): 145.280

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	0.000e+00
worst 0.90 -40	0.000e+00	0.000e+00



FILLCUTCELL_VDD_EXT_CSF_CL_LIN

Cell Description

FILLCUTCELL_VDD_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

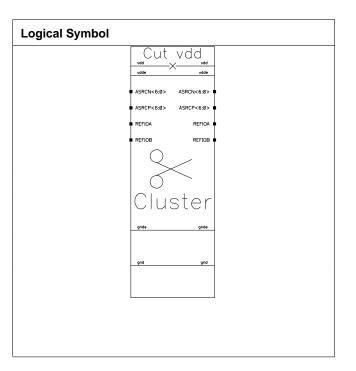
Physical Dimensions

Area(um2): 170.080

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdde
best 1.10 125	0.000e+00
worst 0.90 -40	0.000e+00



FILLCUTCELL_VDD_EXT_CSF_FC_2ROWS

Cell Description

FILLCUTCELL_VDD_EXT_CSF_FC_2ROWS

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

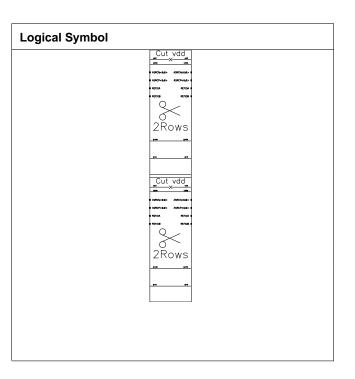
Physical Dimensions

Area(um2): 295.360

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Davamatas	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdde
best 1.10 125	0.000e+00
worst 0.90 -40	0.000e+00



FILLCUTCELL_VDD_EXT_CSF_FC_LIN

Cell Description

FILLCUTCELL_VDD_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

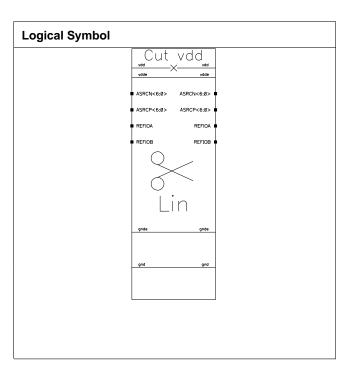
Physical Dimensions

Area(um2): 145.280

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Downston	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdde
best 1.10 125	0.000e+00
worst 0.90 -40	0.000e+00



FILLCUTCELL_VDDE_EXT_CSF_CL_LIN

Cell Description

FILLCUTCELL_VDDE_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

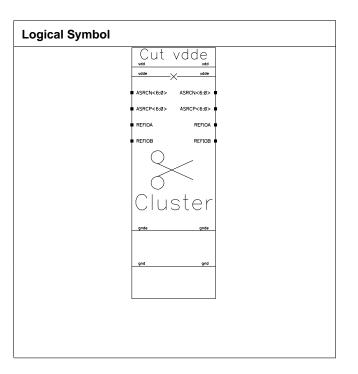
Physical Dimensions

Area(um2): 372.050

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Valu	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40	
ASRCN[0] Input Cap.	0.0100	0.0100	
ASRCN[0] Max Load	10000.000	10000.000	
ASRCN[1] Input Cap.	0.0100	0.0100	
ASRCN[1] Max Load	10000.000	10000.000	
ASRCN[2] Input Cap.	0.0100	0.0100	
ASRCN[2] Max Load	10000.000	10000.000	
ASRCN[3] Input Cap.	0.0100	0.0100	
ASRCN[3] Max Load	10000.000	10000.000	
ASRCN[4] Input Cap.	0.0100	0.0100	
ASRCN[4] Max Load	10000.000	10000.000	
ASRCN[5] Input Cap.	0.0100	0.0100	
ASRCN[5] Max Load	10000.000	10000.000	
ASRCN[6] Input Cap.	0.0100	0.0100	
ASRCN[6] Max Load	10000.000	10000.000	
ASRCP[0] Input Cap.	0.0100	0.0100	
ASRCP[0] Max Load	10000.000	10000.000	
ASRCP[1] Input Cap.	0.0100	0.0100	
ASRCP[1] Max Load	10000.000	10000.000	
ASRCP[2] Input Cap.	0.0100	0.0100	
ASRCP[2] Max Load	10000.000	10000.000	
ASRCP[3] Input Cap.	0.0100	0.0100	
ASRCP[3] Max Load	10000.000	10000.000	



0.0100	0.0100
10000.000	10000.000
0.0100	0.0100
10000.000	10000.000
0.0100	0.0100
10000.000	10000.000
0.0100	0.0100
10000.000	10000.000
0.0100	0.0100
10000.000	10000.000
	10000.000 0.0100 10000.000 0.0100 10000.000 0.0100 10000.000 0.0100

Default Leakage Power (mW)	vdd
best 1.10 125	0.000e+00
worst 0.90 -40	0.000e+00



FILLCUTCELL_VDDE_EXT_CSF_FC_2ROWS

Cell Description

FILLCUTCELL_VDDE_EXT_CSF_FC_2ROWS

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

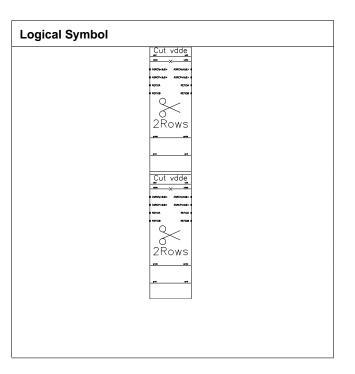
Physical Dimensions

Area(um2): 295.360

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Val	ue(pF)
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd
best 1.10 125	0.000e+00
worst 0.90 -40	0.000e+00



FILLCUTCELL_VDDE_EXT_CSF_FC_LIN

Cell Description

FILLCUTCELL_VDDE_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

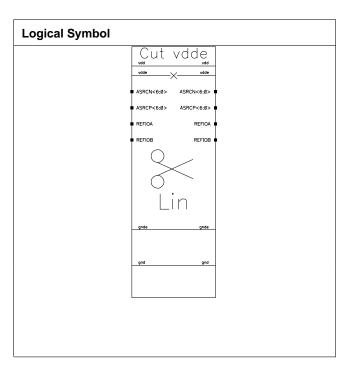
Physical Dimensions

Area(um2): 145.280

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Valu	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40	
ASRCN[0] Input Cap.	0.0100	0.0100	
ASRCN[0] Max Load	10000.000	10000.000	
ASRCN[1] Input Cap.	0.0100	0.0100	
ASRCN[1] Max Load	10000.000	10000.000	
ASRCN[2] Input Cap.	0.0100	0.0100	
ASRCN[2] Max Load	10000.000	10000.000	
ASRCN[3] Input Cap.	0.0100	0.0100	
ASRCN[3] Max Load	10000.000	10000.000	
ASRCN[4] Input Cap.	0.0100	0.0100	
ASRCN[4] Max Load	10000.000	10000.000	
ASRCN[5] Input Cap.	0.0100	0.0100	
ASRCN[5] Max Load	10000.000	10000.000	
ASRCN[6] Input Cap.	0.0100	0.0100	
ASRCN[6] Max Load	10000.000	10000.000	
ASRCP[0] Input Cap.	0.0100	0.0100	
ASRCP[0] Max Load	10000.000	10000.000	
ASRCP[1] Input Cap.	0.0100	0.0100	
ASRCP[1] Max Load	10000.000	10000.000	
ASRCP[2] Input Cap.	0.0100	0.0100	
ASRCP[2] Max Load	10000.000	10000.000	
ASRCP[3] Input Cap.	0.0100	0.0100	
ASRCP[3] Max Load	10000.000	10000.000	



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd
best 1.10 125	0.000e+00
worst 0.90 -40	0.000e+00



FILLCUTCELL_VDDE_GNDE_EXT_CSF_CL_LIN

Cell Description

FILLCUTCELL_VDDE_GNDE_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

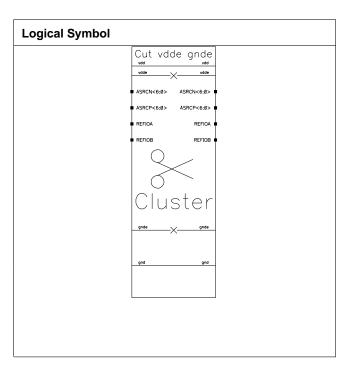
Physical Dimensions

Area(um2): 372.050

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Valu	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40	
ASRCN[0] Input Cap.	0.0100	0.0100	
ASRCN[0] Max Load	10000.000	10000.000	
ASRCN[1] Input Cap.	0.0100	0.0100	
ASRCN[1] Max Load	10000.000	10000.000	
ASRCN[2] Input Cap.	0.0100	0.0100	
ASRCN[2] Max Load	10000.000	10000.000	
ASRCN[3] Input Cap.	0.0100	0.0100	
ASRCN[3] Max Load	10000.000	10000.000	
ASRCN[4] Input Cap.	0.0100	0.0100	
ASRCN[4] Max Load	10000.000	10000.000	
ASRCN[5] Input Cap.	0.0100	0.0100	
ASRCN[5] Max Load	10000.000	10000.000	
ASRCN[6] Input Cap.	0.0100	0.0100	
ASRCN[6] Max Load	10000.000	10000.000	
ASRCP[0] Input Cap.	0.0100	0.0100	
ASRCP[0] Max Load	10000.000	10000.000	
ASRCP[1] Input Cap.	0.0100	0.0100	
ASRCP[1] Max Load	10000.000	10000.000	
ASRCP[2] Input Cap.	0.0100	0.0100	
ASRCP[2] Max Load	10000.000	10000.000	
ASRCP[3] Input Cap.	0.0100	0.0100	
ASRCP[3] Max Load	10000.000	10000.000	



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd
best 1.10 125	0.000e+00
worst 0.90 -40	0.000e+00



FILLCUTCELL_VDDE_GNDE_EXT_CSF_FC_2ROWS

Cell Description

FILLCUTCELL_VDDE_GNDE_EXT_CSF_FC_2ROWS

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

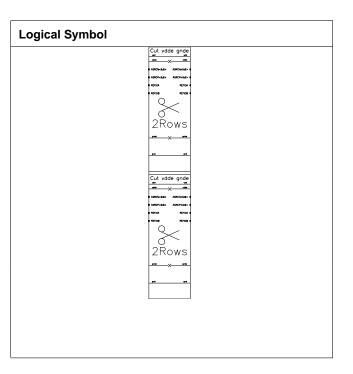
Physical Dimensions

Area(um2): 295.360

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Val	ue(pF)
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd
best 1.10 125	0.000e+00
worst 0.90 -40	0.000e+00



FILLCUTCELL_VDDE_GNDE_EXT_CSF_FC_LIN

Cell Description

FILLCUTCELL_VDDE_GNDE_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

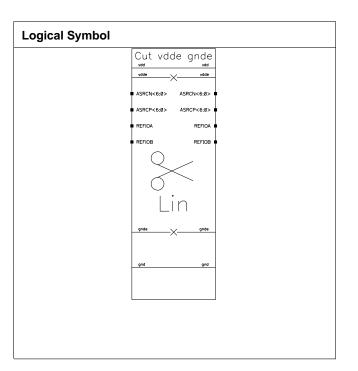
Physical Dimensions

Area(um2): 145.280

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Davamatas	Valu	e(pF)
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd
best 1.10 125	0.000e+00
worst 0.90 -40	0.000e+00



GND_EXT_CSF_CL_LIN

Cell Description

GND_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

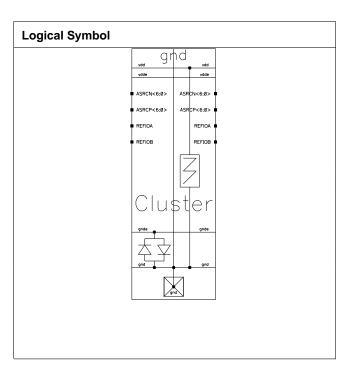
Physical Dimensions

Area(um2): 4252.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	4.227e-03	0.000e+00
worst 0.90 -40	4.949e-07	0.000e+00



GND_EXT_CSF_FC_LIN

Cell Description

GND_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

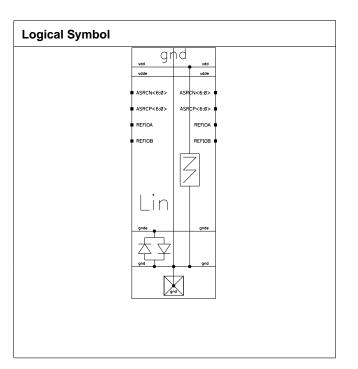
Physical Dimensions

Area(um2): 3632.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Val	ue(pF)
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	4.227e-03	0.000e+00
worst 0.90 -40	4.941e-07	0.000e+00



GND_VDD_EXT_CSF_FC_2ROWS

Cell Description

GND_VDD_EXT_CSF_FC_2ROWS

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

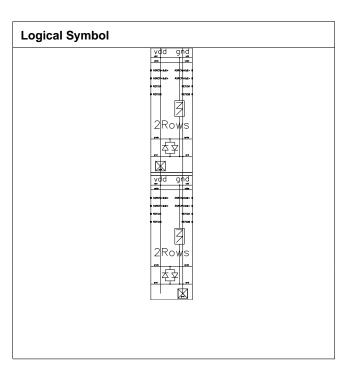
Physical Dimensions

Area(um2): 8307.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	8.454e-03	0.000e+00
worst 0.90 -40	9.882e-07	0.000e+00



GNDE_EXT_CSF_CL_LIN

Cell Description

GNDE_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STE.
- The cell has "dont_touch" attribute set in the Synopsys STF.

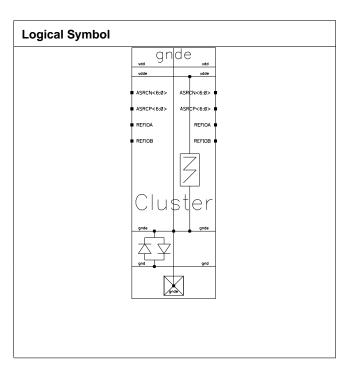
Physical Dimensions

Area(um2): 4252.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Devenuetes	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	4.799e-03
worst 0.90 -40	0.000e+00	3.306e-06



GNDE_EXT_CSF_FC_INNER

Cell Description

GNDE_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

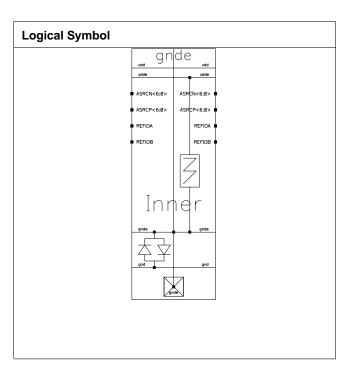
Physical Dimensions

Area(um2): 3632.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	4.795e-03
worst 0.90 -40	0.000e+00	3.306e-06



GNDE_EXT_CSF_FC_LIN

Cell Description

GNDE_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF
- The cell has "dont_touch" attribute set in the Synopsys STF.

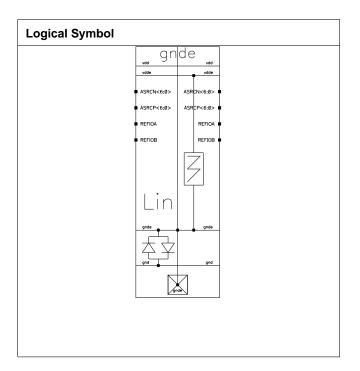
Physical Dimensions

Area(um2): 3632.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	4.795e-03
worst 0.90 -40	0.000e+00	3.306e-06



GNDE_EXT_CSF_FC_OUTER

Cell Description

GNDE_EXT_CSF_FC_OUTER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

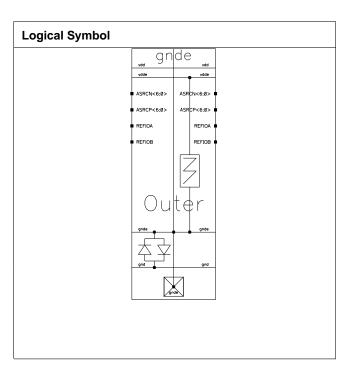
Physical Dimensions

Area(um2): 8307.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	2.512e-29	4.795e-03
worst 0.90 -40	0.000e+00	3.303e-06



LBCORNERCELL_EXT_CSF_FC_2ROWS

Cell Description

LBCORNERCELL_EXT_CSF_FC_2ROWS

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

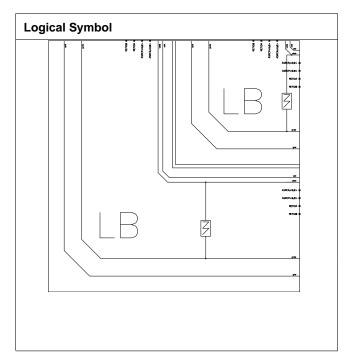
Physical Dimensions

Area(um2): 34077.200

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	9.840e-03
worst 0.90 -40	4.953e-29	6.752e-06



LBCORNERCELL_EXT_CSF_FC_LIN

Cell Description

LBCORNERCELL_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

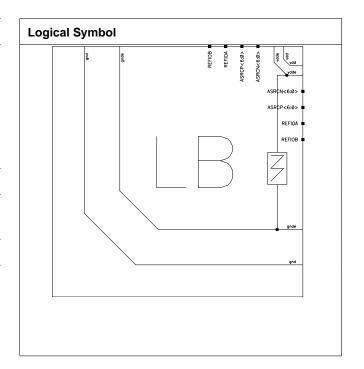
Physical Dimensions

Area(um2): 8244.640

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	4.920e-03
worst 0.90 -40	0.000e+00	3.382e-06



LTCORNERCELL_EXT_CSF_FC_2ROWS

Cell Description

LTCORNERCELL_EXT_CSF_FC_2ROWS

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

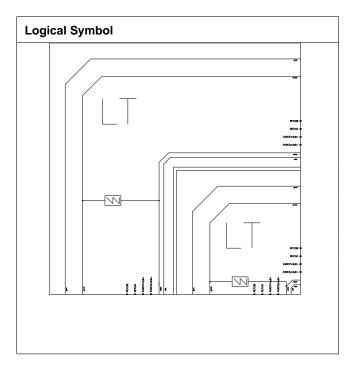
Physical Dimensions

Area(um2): 34077.200

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Downston	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	9.840e-03
worst 0.90 -40	0.000e+00	6.752e-06



LTCORNERCELL_EXT_CSF_FC_LIN

Cell Description

LTCORNERCELL_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

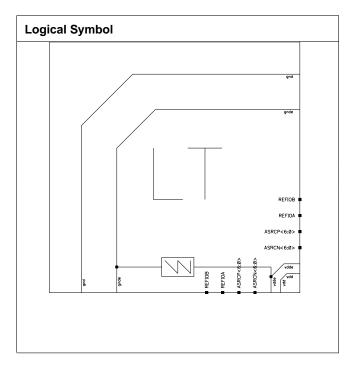
Physical Dimensions

Area(um2): 8244.640

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



0.0100	0.0100
10000.000	10000.000
0.0100	0.0100
10000.000	10000.000
0.0100	0.0100
10000.000	10000.000
0.0100	0.0100
10000.000	10000.000
0.0100	0.0100
10000.000	10000.000
	10000.000 0.0100 10000.000 0.0100 10000.000 0.0100 10000.000 0.0100

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	4.920e-03
worst 0.90 -40	0.000e+00	3.382e-06



RBCORNERCELL_EXT_CSF_FC_2ROWS

Cell Description

RBCORNERCELL_EXT_CSF_FC_2ROWS

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

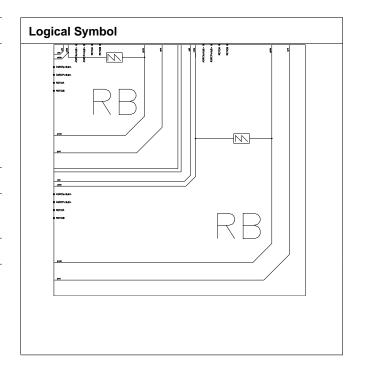
Physical Dimensions

Area(um2): 34077.200

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	9.840e-03
worst 0.90 -40	4.738e-29	6.752e-06



RBCORNERCELL_EXT_CSF_FC_LIN

Cell Description

RBCORNERCELL_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

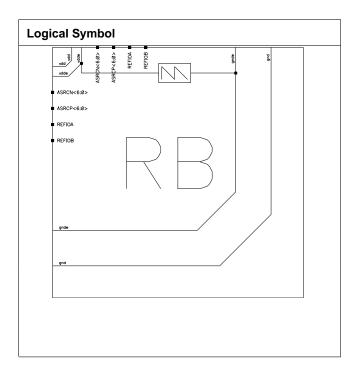
Physical Dimensions

Area(um2): 8244.640

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	4.920e-03
worst 0.90 -40	0.000e+00	3.382e-06



RTCORNERCELL_EXT_CSF_FC_2ROWS

Cell Description

RTCORNERCELL_EXT_CSF_FC_2ROWS

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

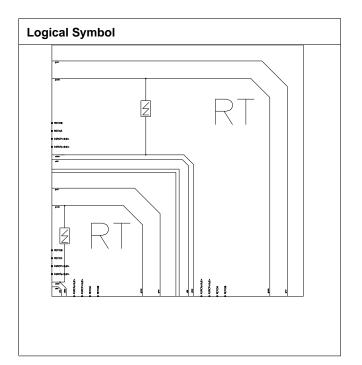
Physical Dimensions

Area(um2): 34077.200

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Б ,	Valu	e(pF)
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	1.199e-28	9.840e-03
worst 0.90 -40	0.000e+00	6.752e-06



RTCORNERCELL_EXT_CSF_FC_LIN

Cell Description

RTCORNERCELL_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

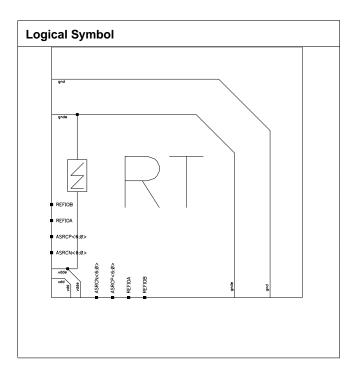
Physical Dimensions

Area(um2): 8244.640

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Downston	Valu	e(pF)
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	4.920e-03
worst 0.90 -40	0.000e+00	3.382e-06



VDD_EXT_CSF_CL_LIN

Cell Description

VDD_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STE.
- The cell has "dont_touch" attribute set in the Synopsys STF.

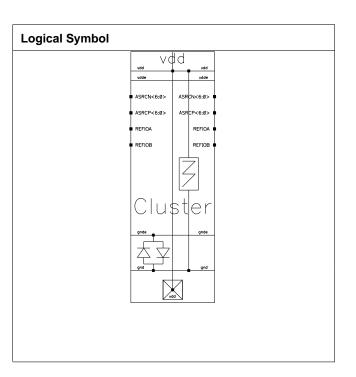
Physical Dimensions

Area(um2): 4252.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	4.227e-03	0.000e+00
worst 0.90 -40	4.949e-07	0.000e+00



VDD_EXT_CSF_FC_LIN

Cell Description

VDD_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

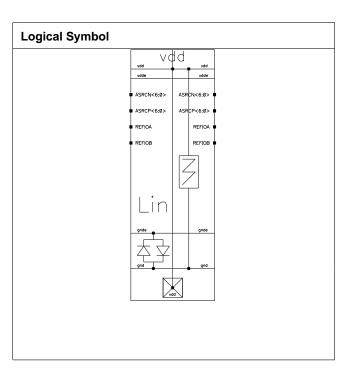
Physical Dimensions

Area(um2): 3632.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	4.227e-03	0.000e+00
worst 0.90 -40	4.941e-07	0.000e+00



VDD_GND_EXT_CSF_FC_2ROWS

Cell Description

VDD_GND_EXT_CSF_FC_2ROWS

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

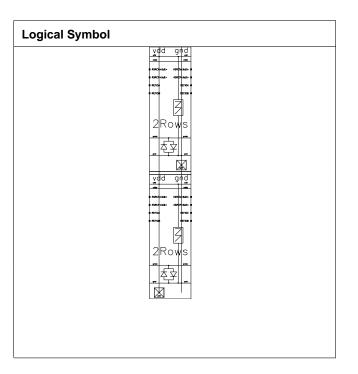
Physical Dimensions

Area(um2): 8307.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	8.454e-03	0.000e+00
worst 0.90 -40	9.882e-07	0.000e+00



VDDE_EXT_CSF_CL_LIN

Cell Description

VDDE_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STE.
- The cell has "dont_touch" attribute set in the Synopsys STF.

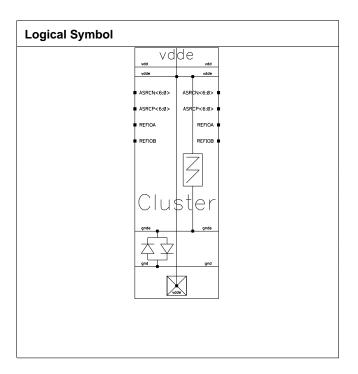
Physical Dimensions

Area(um2): 4252.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	4.799e-03
worst 0.90 -40	0.000e+00	3.306e-06



VDDE_EXT_CSF_FC_INNER

Cell Description

VDDE_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

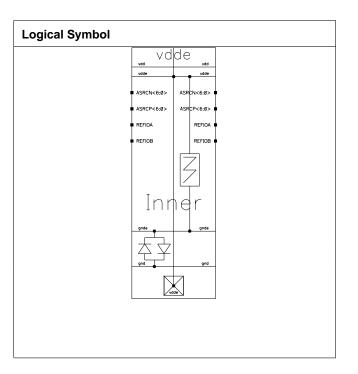
Physical Dimensions

Area(um2): 3632.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter -	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	4.795e-03
worst 0.90 -40	0.000e+00	3.306e-06



VDDE_EXT_CSF_FC_LIN

Cell Description

VDDE_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

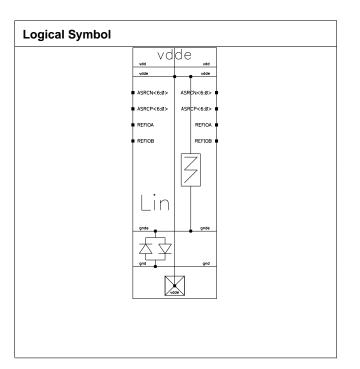
Physical Dimensions

Area(um2): 3632.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	4.795e-03
worst 0.90 -40	0.000e+00	3.306e-06



VDDE_EXT_CSF_FC_OUTER

Cell Description

VDDE_EXT_CSF_FC_OUTER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

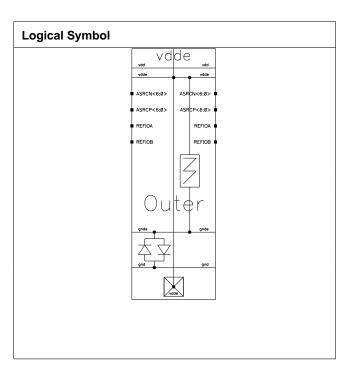
Physical Dimensions

Area(um2): 8307.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Val	ue(pF)
Parameter	best 1.10 125	worst 0.90 -40
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000



ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	2.516e-29	4.795e-03
worst 0.90 -40	0.000e+00	3.303e-06



WIRECELL_50OHMS_EXT_CSF_CL_LIN

Cell Description

WIRECELL_50OHMS_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

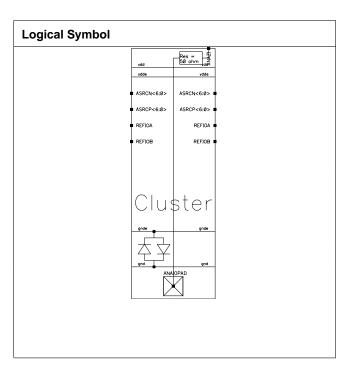
Physical Dimensions

Area(um2): 4252.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Val	ue(pF)
Farameter	best 1.10 125	worst 0.90 -40
ANAIOPAD Input Cap.	1.0000	1.0000
ANAIOPAD Max Load	10000.000	10000.000
ANAZI Input Cap.	0.0100	0.0100
ANAZI Max Load	10000.000	10000.000
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000



ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000
ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Pin	Parameter -	Value	
PIII		best 1.10	worst 0.90
ANAIOPAD (Input)	Min Transition (ns)	0.002	0.002
ANAIOPAD (Input)	Max Transition (ns)	0.675	0.675
ANAIOPAD (Input)	Swing (V)	0.0 - 1.95	0.0 - 1.65
ANAIOPAD (Input)	Delay thres. rising (V)	0.975	0.825
ANAIOPAD (Input)	Delay thres. falling (V)	0.975	0.825
ANAIOPAD (Input)	Slope thres. low (V)	-	-
ANAIOPAD (Input)	Slope thres. high (V)	-	-
ANAIOPAD (Output)	Min Transition (ns)	-	-
ANAIOPAD (Output)	Max Transition (ns)	-	-
ANAIOPAD (Output)	Swing (V)	0.0 - 1.95	0.0 - 1.65
ANAIOPAD (Output)	Delay thres. rising (V)	0.975	0.825
ANAIOPAD (Output)	Delay thres. falling (V)	0.975	0.825
ANAIOPAD (Output)	Slope thres. low (V)	0.585	0.495
ANAIOPAD (Output)	Slope thres. high (V)	1.365	1.155

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	1.429e-05
worst 0.90 -40	0.000e+00	4.128e-08



WIRECELL_50OHMS_EXT_CSF_FC_INNER

Cell Description

WIRECELL_50OHMS_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

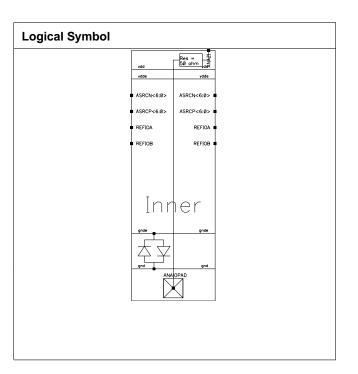
Physical Dimensions

Area(um2): 3632.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Val	ue(pF)
Farameter	best 1.10 125	worst 0.90 -40
ANAIOPAD Input Cap.	1.0000	1.0000
ANAIOPAD Max Load	10000.000	10000.000
ANAZI Input Cap.	0.0100	0.0100
ANAZI Max Load	10000.000	10000.000
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000



ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000
ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Pin	Parameter -	Value	
PIII		best 1.10	worst 0.90
ANAIOPAD (Input)	Min Transition (ns)	0.002	0.002
ANAIOPAD (Input)	Max Transition (ns)	0.675	0.675
ANAIOPAD (Input)	Swing (V)	0.0 - 1.95	0.0 - 1.65
ANAIOPAD (Input)	Delay thres. rising (V)	0.975	0.825
ANAIOPAD (Input)	Delay thres. falling (V)	0.975	0.825
ANAIOPAD (Input)	Slope thres. low (V)	-	-
ANAIOPAD (Input)	Slope thres. high (V)	-	-
ANAIOPAD (Output)	Min Transition (ns)	-	-
ANAIOPAD (Output)	Max Transition (ns)	-	-
ANAIOPAD (Output)	Swing (V)	0.0 - 1.95	0.0 - 1.65
ANAIOPAD (Output)	Delay thres. rising (V)	0.975	0.825
ANAIOPAD (Output)	Delay thres. falling (V)	0.975	0.825
ANAIOPAD (Output)	Slope thres. low (V)	0.585	0.495
ANAIOPAD (Output)	Slope thres. high (V)	1.365	1.155

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	1.071e-05
worst 0.90 -40	0.000e+00	3.584e-08



WIRECELL_50OHMS_EXT_CSF_FC_LIN

Cell Description

WIRECELL_50OHMS_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

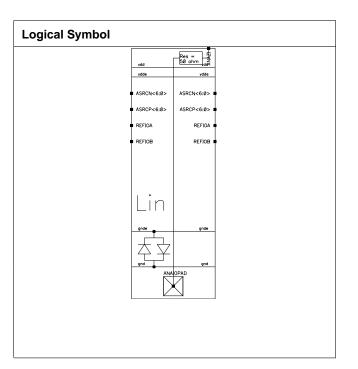
Physical Dimensions

Area(um2): 3632.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Val	ue(pF)
Farameter	best 1.10 125	worst 0.90 -40
ANAIOPAD Input Cap.	1.0000	1.0000
ANAIOPAD Max Load	10000.000	10000.000
ANAZI Input Cap.	0.0100	0.0100
ANAZI Max Load	10000.000	10000.000
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000
ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000



ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000
ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Pin	Parameter	Value	
PIII		best 1.10	worst 0.90
ANAIOPAD (Input)	Min Transition (ns)	0.002	0.002
ANAIOPAD (Input)	Max Transition (ns)	0.675	0.675
ANAIOPAD (Input)	Swing (V)	0.0 - 1.95	0.0 - 1.65
ANAIOPAD (Input)	Delay thres. rising (V)	0.975	0.825
ANAIOPAD (Input)	Delay thres. falling (V)	0.975	0.825
ANAIOPAD (Input)	Slope thres. low (V)	-	-
ANAIOPAD (Input)	Slope thres. high (V)	-	-
ANAIOPAD (Output)	Min Transition (ns)	-	-
ANAIOPAD (Output)	Max Transition (ns)	-	-
ANAIOPAD (Output)	Swing (V)	0.0 - 1.95	0.0 - 1.65
ANAIOPAD (Output)	Delay thres. rising (V)	0.975	0.825
ANAIOPAD (Output)	Delay thres. falling (V)	0.975	0.825
ANAIOPAD (Output)	Slope thres. low (V)	0.585	0.495
ANAIOPAD (Output)	Slope thres. high (V)	1.365	1.155

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	1.071e-05
worst 0.90 -40	0.000e+00	3.584e-08



WIRECELL_EXT_CSF_CL_LIN

Cell Description

WIRECELL_EXT_CSF_CL_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

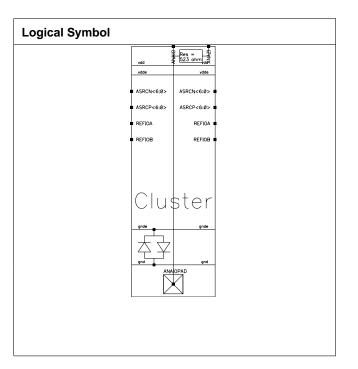
Physical Dimensions

Area(um2): 4252.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Developator	Valu	e(pF)
Parameter —	best 1.10 125	worst 0.90 -40
ANAIO Input Cap.	0.0100	0.0100
ANAIO Max Load	10000.000	10000.000
ANAIOPAD Input Cap.	1.0000	1.0000
ANAIOPAD Max Load	10000.000	10000.000
ANAZI Input Cap.	0.0100	0.0100
ANAZI Max Load	10000.000	10000.000
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000



ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000
ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Pin	Parameter	Value	
PIII		best 1.10	worst 0.90
ANAIOPAD (Input)	Min Transition (ns)	0.002	0.002
ANAIOPAD (Input)	Max Transition (ns)	0.675	0.675
ANAIOPAD (Input)	Swing (V)	0.0 - 1.95	0.0 - 1.65
ANAIOPAD (Input)	Delay thres. rising (V)	0.975	0.825
ANAIOPAD (Input)	Delay thres. falling (V)	0.975	0.825
ANAIOPAD (Input)	Slope thres. low (V)	-	-
ANAIOPAD (Input)	Slope thres. high (V)	-	-
ANAIOPAD (Output)	Min Transition (ns)	-	-
ANAIOPAD (Output)	Max Transition (ns)	-	-
ANAIOPAD (Output)	Swing (V)	0.0 - 1.95	0.0 - 1.65
ANAIOPAD (Output)	Delay thres. rising (V)	0.975	0.825
ANAIOPAD (Output)	Delay thres. falling (V)	0.975	0.825
ANAIOPAD (Output)	Slope thres. low (V)	0.585	0.495
ANAIOPAD (Output)	Slope thres. high (V)	1.365	1.155

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	1.429e-05
worst 0.90 -40	0.000e+00	4.128e-08



WIRECELL_EXT_CSF_FC_INNER

Cell Description

WIRECELL_EXT_CSF_FC_INNER

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

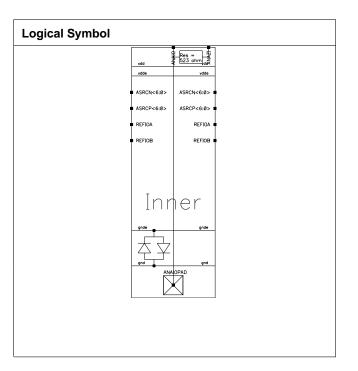
Physical Dimensions

Area(um2): 3632.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Valu	ue(pF)
Parameter	best 1.10 125	worst 0.90 -40
ANAIO Input Cap.	0.0100	0.0100
ANAIO Max Load	10000.000	10000.000
ANAIOPAD Input Cap.	1.0000	1.0000
ANAIOPAD Max Load	10000.000	10000.000
ANAZI Input Cap.	0.0100	0.0100
ANAZI Max Load	10000.000	10000.000
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000



ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000
ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Pin	Parameter	Value	
PIII		best 1.10	worst 0.90
ANAIOPAD (Input)	Min Transition (ns)	0.002	0.002
ANAIOPAD (Input)	Max Transition (ns)	0.675	0.675
ANAIOPAD (Input)	Swing (V)	0.0 - 1.95	0.0 - 1.65
ANAIOPAD (Input)	Delay thres. rising (V)	0.975	0.825
ANAIOPAD (Input)	Delay thres. falling (V)	0.975	0.825
ANAIOPAD (Input)	Slope thres. low (V)	-	-
ANAIOPAD (Input)	Slope thres. high (V)	-	-
ANAIOPAD (Output)	Min Transition (ns)	-	-
ANAIOPAD (Output)	Max Transition (ns)	-	-
ANAIOPAD (Output)	Swing (V)	0.0 - 1.95	0.0 - 1.65
ANAIOPAD (Output)	Delay thres. rising (V)	0.975	0.825
ANAIOPAD (Output)	Delay thres. falling (V)	0.975	0.825
ANAIOPAD (Output)	Slope thres. low (V)	0.585	0.495
ANAIOPAD (Output)	Slope thres. high (V)	1.365	1.155

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	1.071e-05
worst 0.90 -40	0.000e+00	3.584e-08



WIRECELL_EXT_CSF_FC_LIN

Cell Description

WIRECELL_EXT_CSF_FC_LIN

- The cell has "dont_use" attribute set in the Synopsys STF.
- The cell has "dont_touch" attribute set in the Synopsys STF.

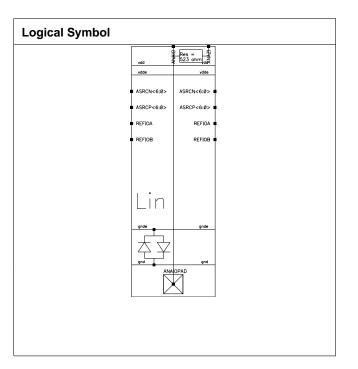
Physical Dimensions

Area(um2): 3632.000

Glossary

Tr : Input Transition time C : Output (capacitive) load

R : Rising edge F : Falling edge



Parameter	Value(pF)	
	best 1.10 125	worst 0.90 -40
ANAIO Input Cap.	0.0100	0.0100
ANAIO Max Load	10000.000	10000.000
ANAIOPAD Input Cap.	1.0000	1.0000
ANAIOPAD Max Load	10000.000	10000.000
ANAZI Input Cap.	0.0100	0.0100
ANAZI Max Load	10000.000	10000.000
ASRCN[0] Input Cap.	0.0100	0.0100
ASRCN[0] Max Load	10000.000	10000.000
ASRCN[1] Input Cap.	0.0100	0.0100
ASRCN[1] Max Load	10000.000	10000.000
ASRCN[2] Input Cap.	0.0100	0.0100
ASRCN[2] Max Load	10000.000	10000.000
ASRCN[3] Input Cap.	0.0100	0.0100
ASRCN[3] Max Load	10000.000	10000.000
ASRCN[4] Input Cap.	0.0100	0.0100
ASRCN[4] Max Load	10000.000	10000.000
ASRCN[5] Input Cap.	0.0100	0.0100
ASRCN[5] Max Load	10000.000	10000.000
ASRCN[6] Input Cap.	0.0100	0.0100
ASRCN[6] Max Load	10000.000	10000.000
ASRCP[0] Input Cap.	0.0100	0.0100
ASRCP[0] Max Load	10000.000	10000.000



ASRCP[1] Input Cap.	0.0100	0.0100
ASRCP[1] Max Load	10000.000	10000.000
ASRCP[2] Input Cap.	0.0100	0.0100
ASRCP[2] Max Load	10000.000	10000.000
ASRCP[3] Input Cap.	0.0100	0.0100
ASRCP[3] Max Load	10000.000	10000.000
ASRCP[4] Input Cap.	0.0100	0.0100
ASRCP[4] Max Load	10000.000	10000.000
ASRCP[5] Input Cap.	0.0100	0.0100
ASRCP[5] Max Load	10000.000	10000.000
ASRCP[6] Input Cap.	0.0100	0.0100
ASRCP[6] Max Load	10000.000	10000.000
REFIOA Input Cap.	0.0100	0.0100
REFIOA Max Load	10000.000	10000.000
REFIOB Input Cap.	0.0100	0.0100
REFIOB Max Load	10000.000	10000.000

Pin Parameter		Value	
rif1	Parameter	best 1.10	worst 0.90
ANAIOPAD (Input)	Min Transition (ns)	0.002	0.002
ANAIOPAD (Input)	Max Transition (ns)	0.675	0.675
ANAIOPAD (Input)	Swing (V)	0.0 - 1.95	0.0 - 1.65
ANAIOPAD (Input)	Delay thres. rising (V)	0.975	0.825
ANAIOPAD (Input)	Delay thres. falling (V)	0.975	0.825
ANAIOPAD (Input)	Slope thres. low (V)	-	-
ANAIOPAD (Input)	Slope thres. high (V)	-	-
ANAIOPAD (Output)	Min Transition (ns)	-	-
ANAIOPAD (Output)	Max Transition (ns)	-	-
ANAIOPAD (Output)	Swing (V)	0.0 - 1.95	0.0 - 1.65
ANAIOPAD (Output)	Delay thres. rising (V)	0.975	0.825
ANAIOPAD (Output)	Delay thres. falling (V)	0.975	0.825
ANAIOPAD (Output)	Slope thres. low (V)	0.585	0.495
ANAIOPAD (Output)	Slope thres. high (V)	1.365	1.155

Default Leakage Power (mW)	vdd	vdde
best 1.10 125	0.000e+00	1.071e-05
worst 0.90 -40	0.000e+00	3.584e-08





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