cmos028fdsoi Technology

SG LLE WPE models

DK1.2_RF_mmW

Comparison with DK1.1_RF_mmW model(s)

LLE - SG WPE

Please use the bookmark to navigate

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General information on SG LLE WPE models

- Maximum supply voltage is - V.
- Validity domain is defined as follows:
  - ✔ Drawn gate length varies from 30nm to 10um.
  - ✔ Drawn transistor width varies from 80nm to 10um.
  - ✔ Device temperature varies from -40 °C to 125 °C.

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Output parameters definitions

- Model(s): lvtnfet_acc, lvtpfet_acc, nfet_acc, pfet_acc
  - ✔ Vt_lin : Threshold voltage defined as Vgs value for which drain current is ivt*M*1*W/(1*L+0+1*p_la) at Vds = 0.05V.
  - ✔ Isat : Drain current at Vgs = 1V, Vds = VddV.
  - ✔ Vt_sat : Threshold voltage defined as Vgs value for which drain current is ivt*M*1*W/(1*L+0+1*p_la) at Vds = vds_satV.
  - ✔ Ilin : Drain current at Vgs = 1V, Vds = 0.05V.
  - ✔ Logioff : log10(Ioffsat).

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lvtnfet_acc
Electrical characteristics scaling

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- WPE effect w.r.t d0 - Lscaling at W=1u

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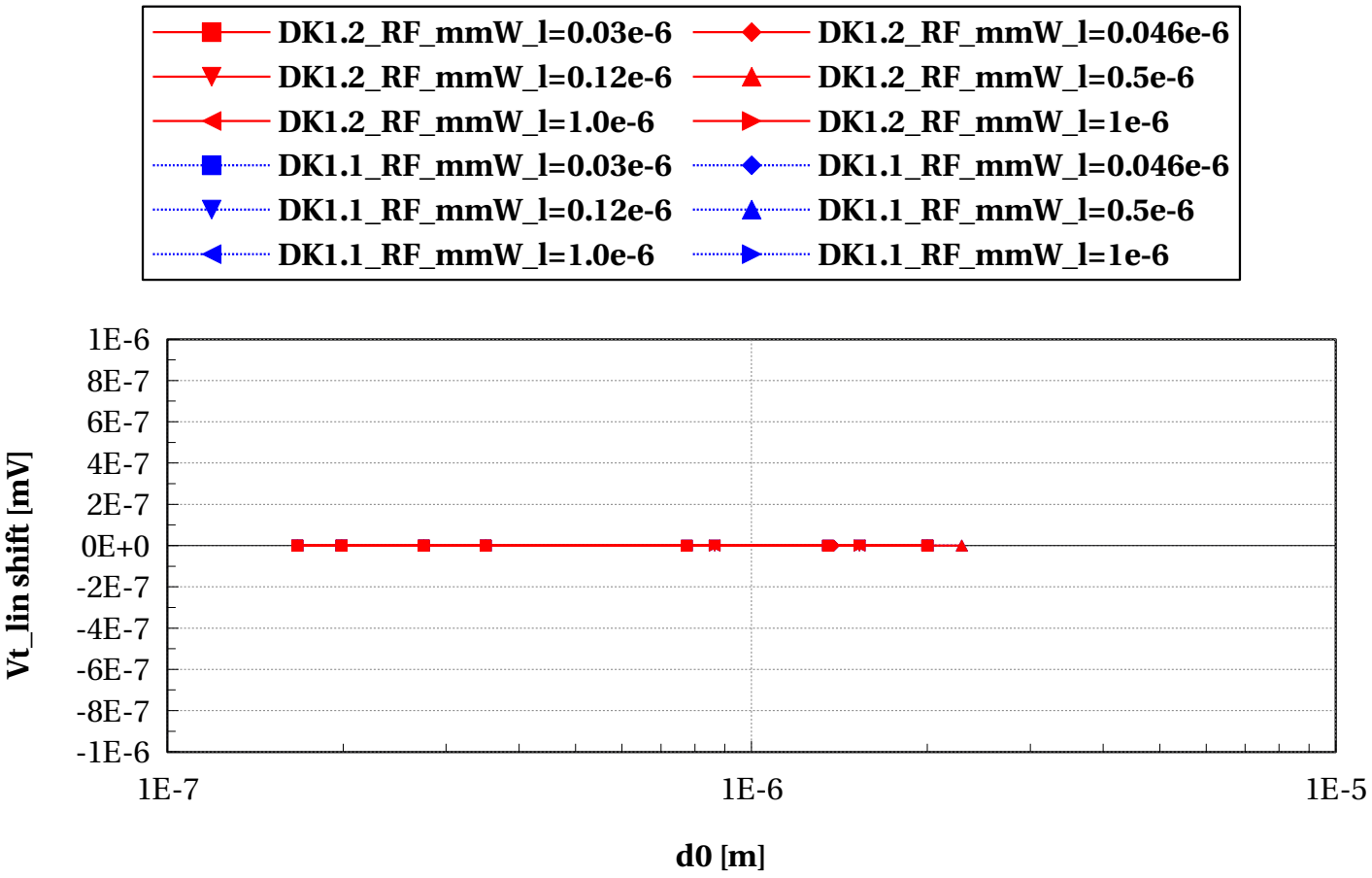
lvtnfet_acc, Vt_lin shift [mV] vs d0 [m]

temp==25 and d1==10e-6 and W==1e-6



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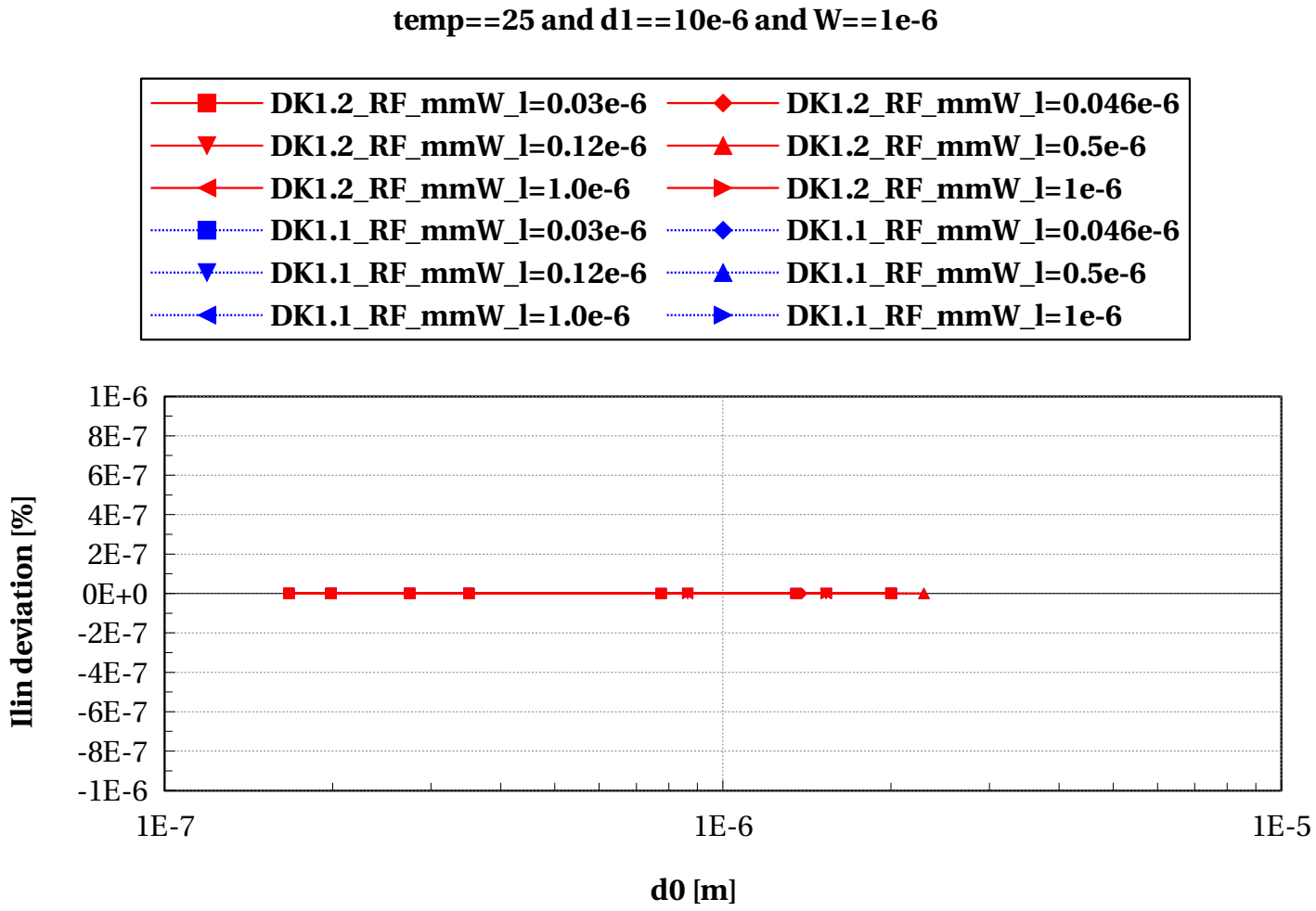
lvtnfet_acc, Ilin deviation [%] vs d0 [m]



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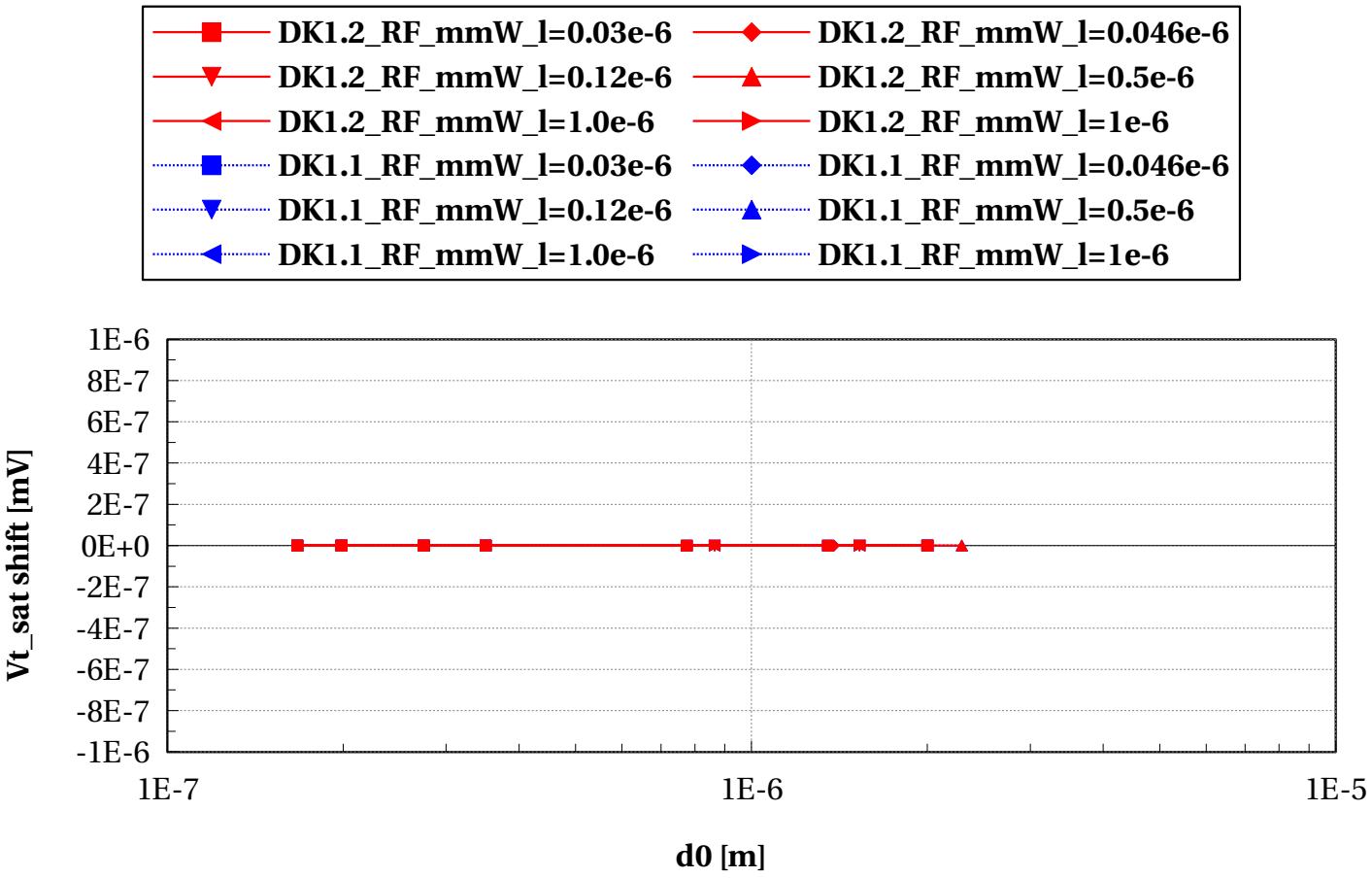
lvtnfet_acc, Vt_sat shift [mV] vs d0 [m]

temp==25 and d1==10e-6 and W==1e-6



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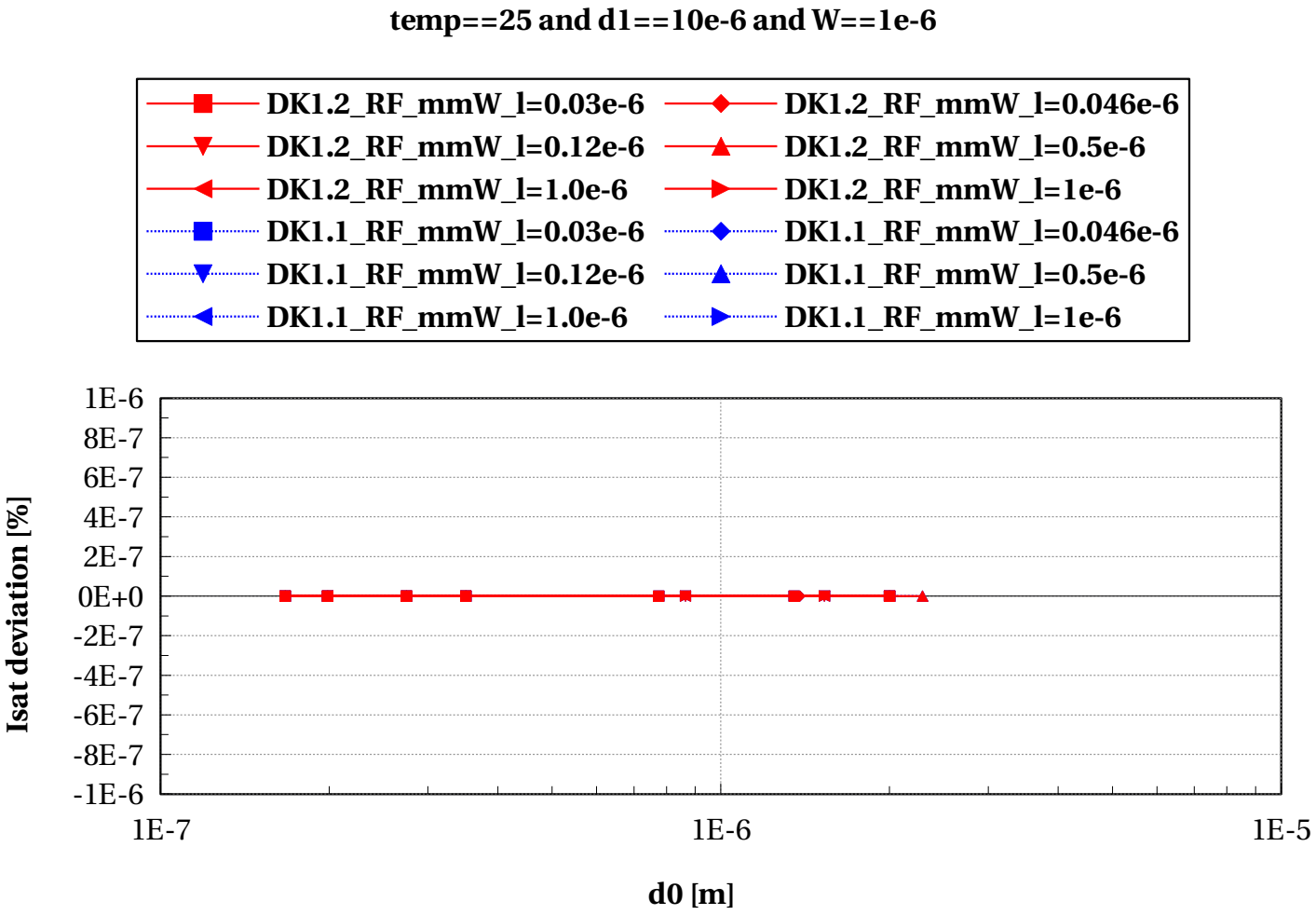
lvtnfet_acc, Isat deviation [%] vs d0 [m]



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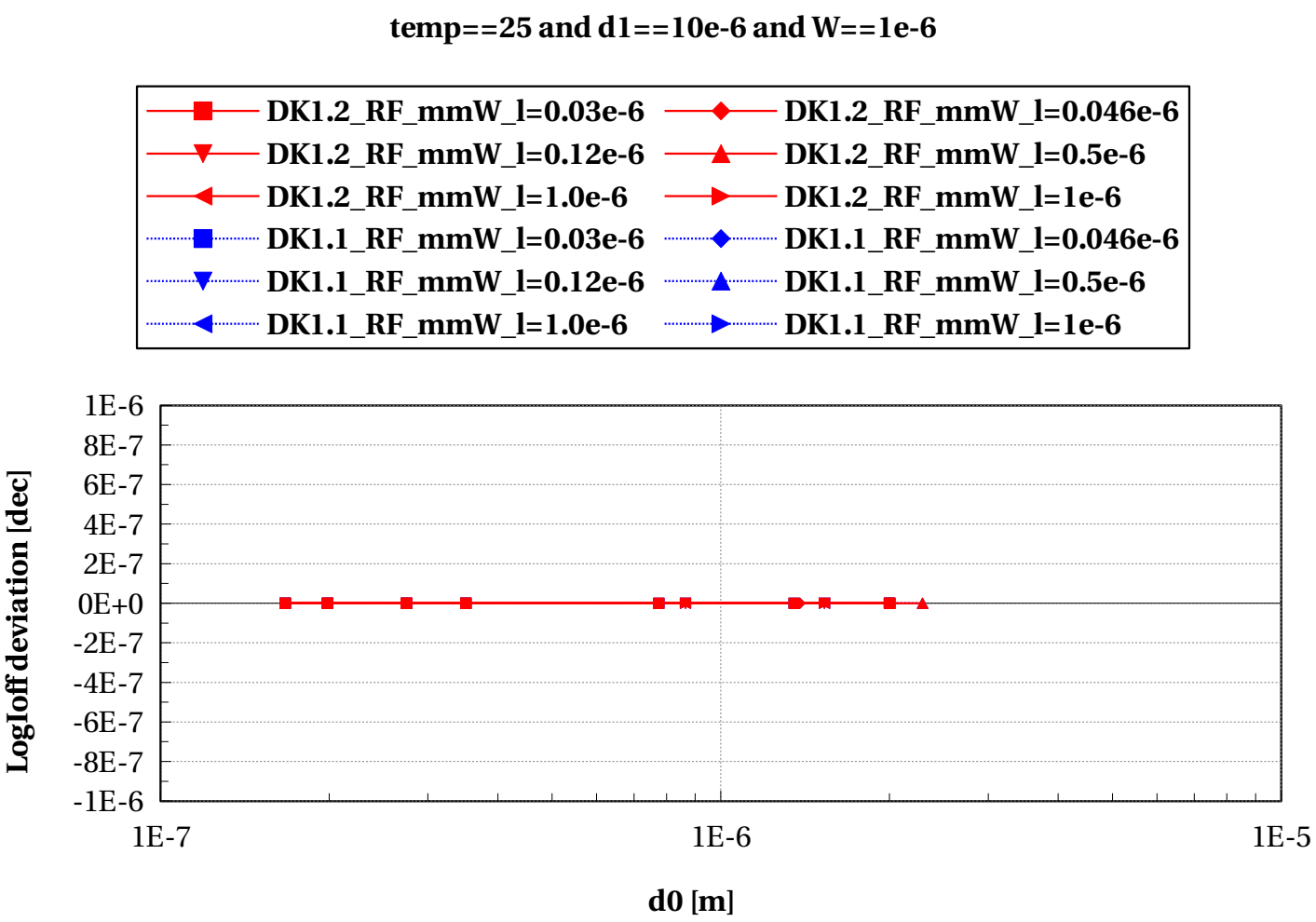
lvtnfet_acc, LogIoff deviation [dec] vs d0 [m]



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- WPE effect w.r.t d0 - Lscaling at W=0.34u

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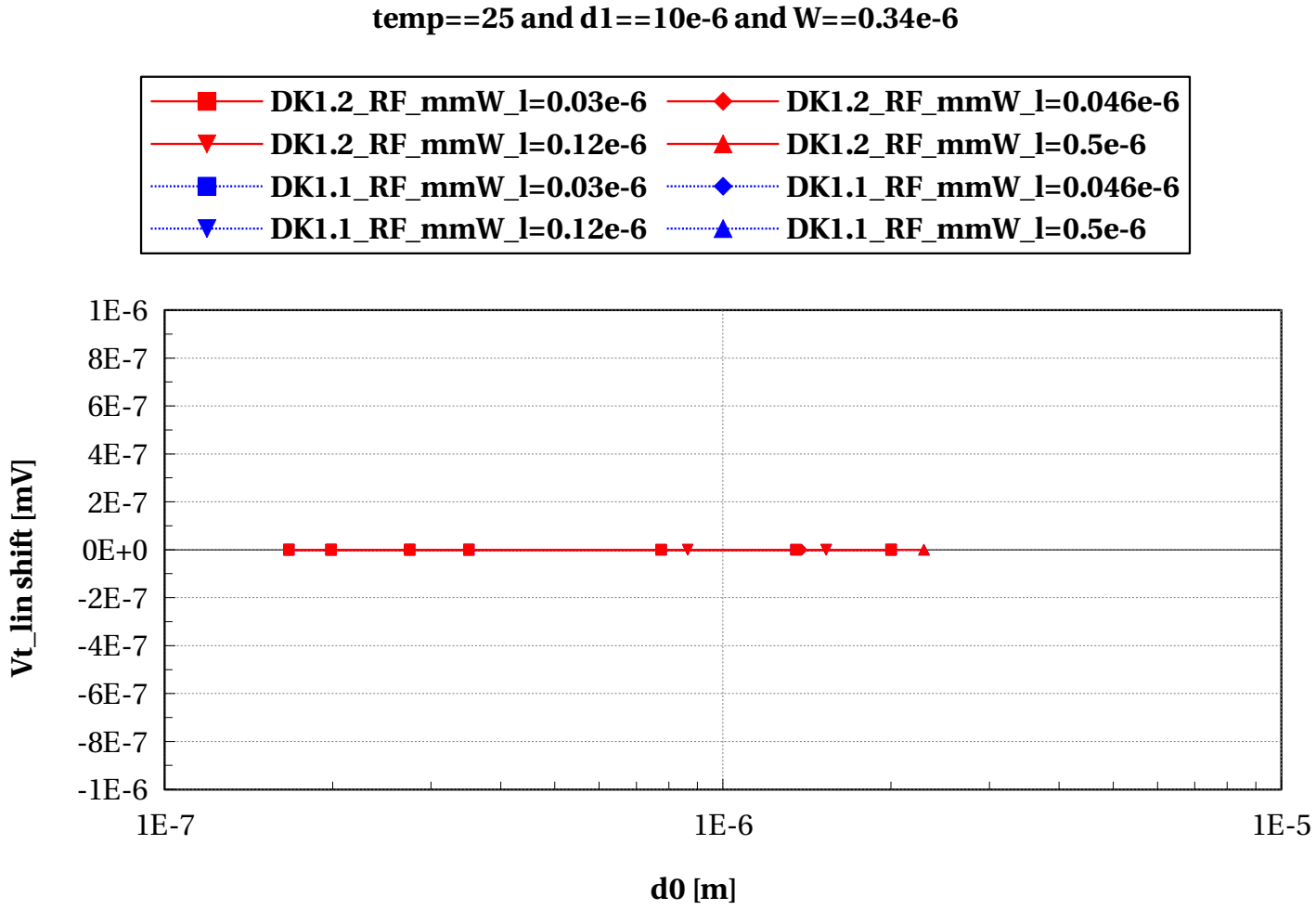
lvtnfet_acc, Vt_lin shift [mV] vs d0 [m]



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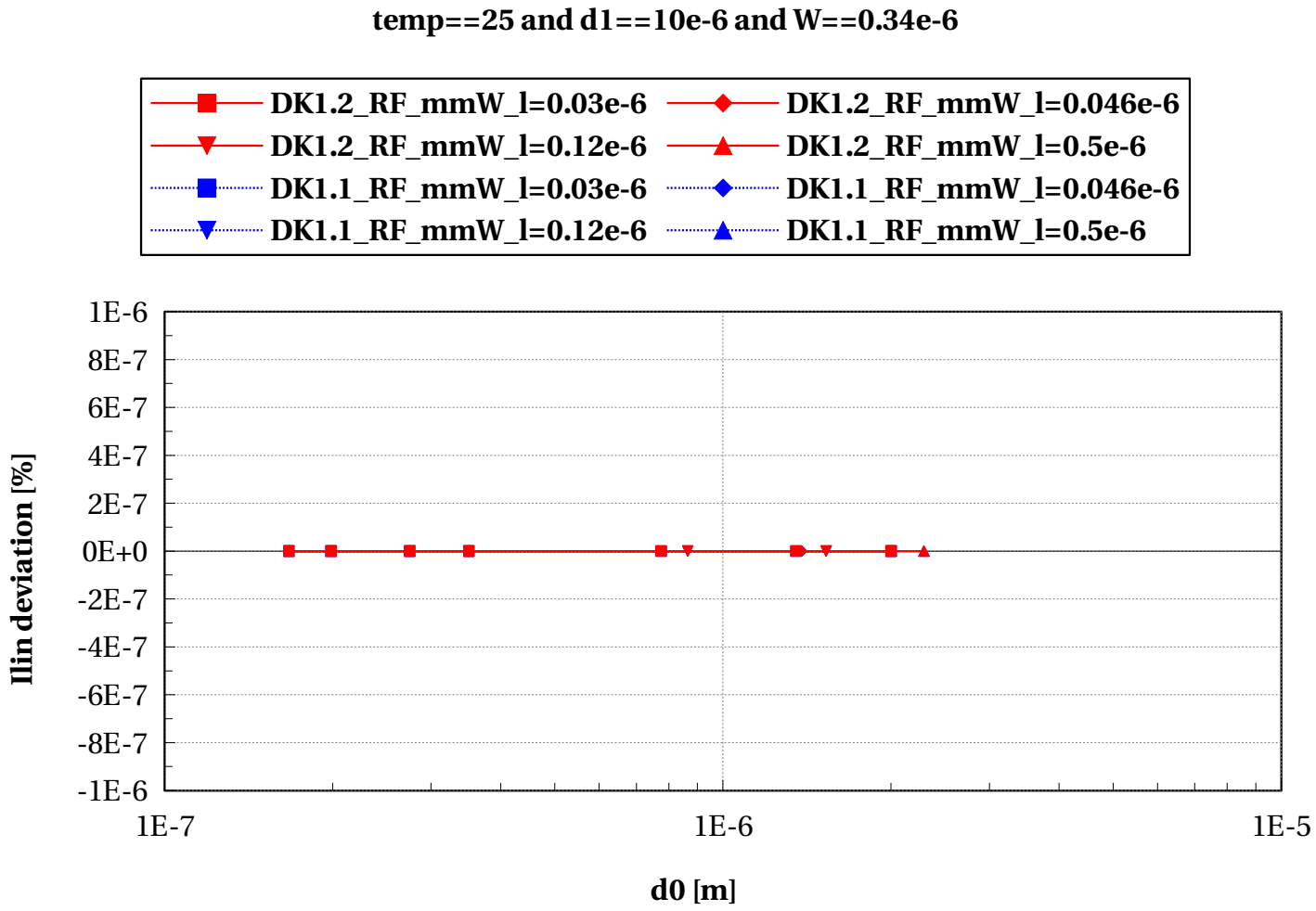
lvtnfet_acc, Ilin deviation [%] vs d0 [m]



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lvtnfet_acc, Vt_sat shift [mV] vs d0 [m]

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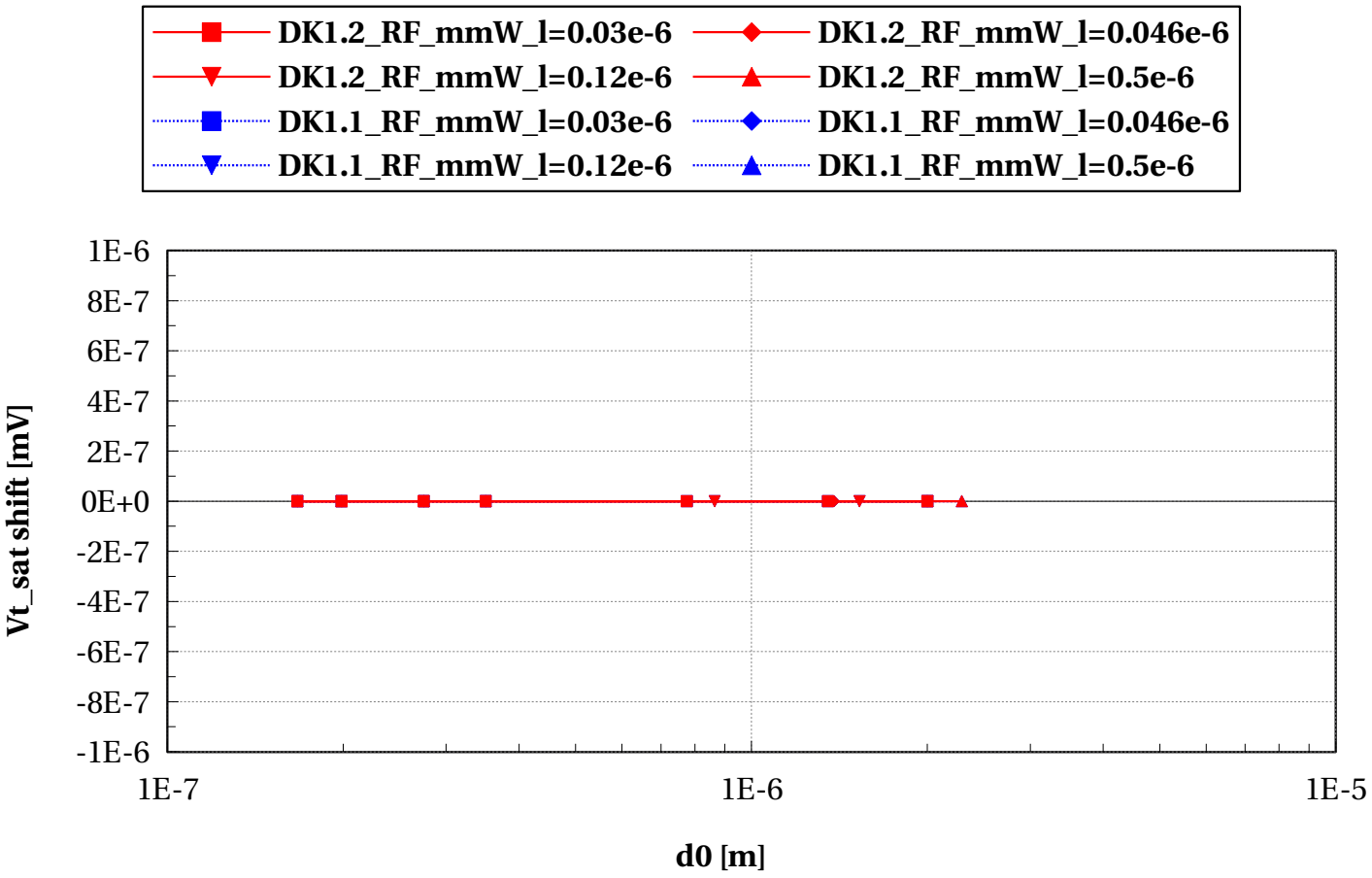

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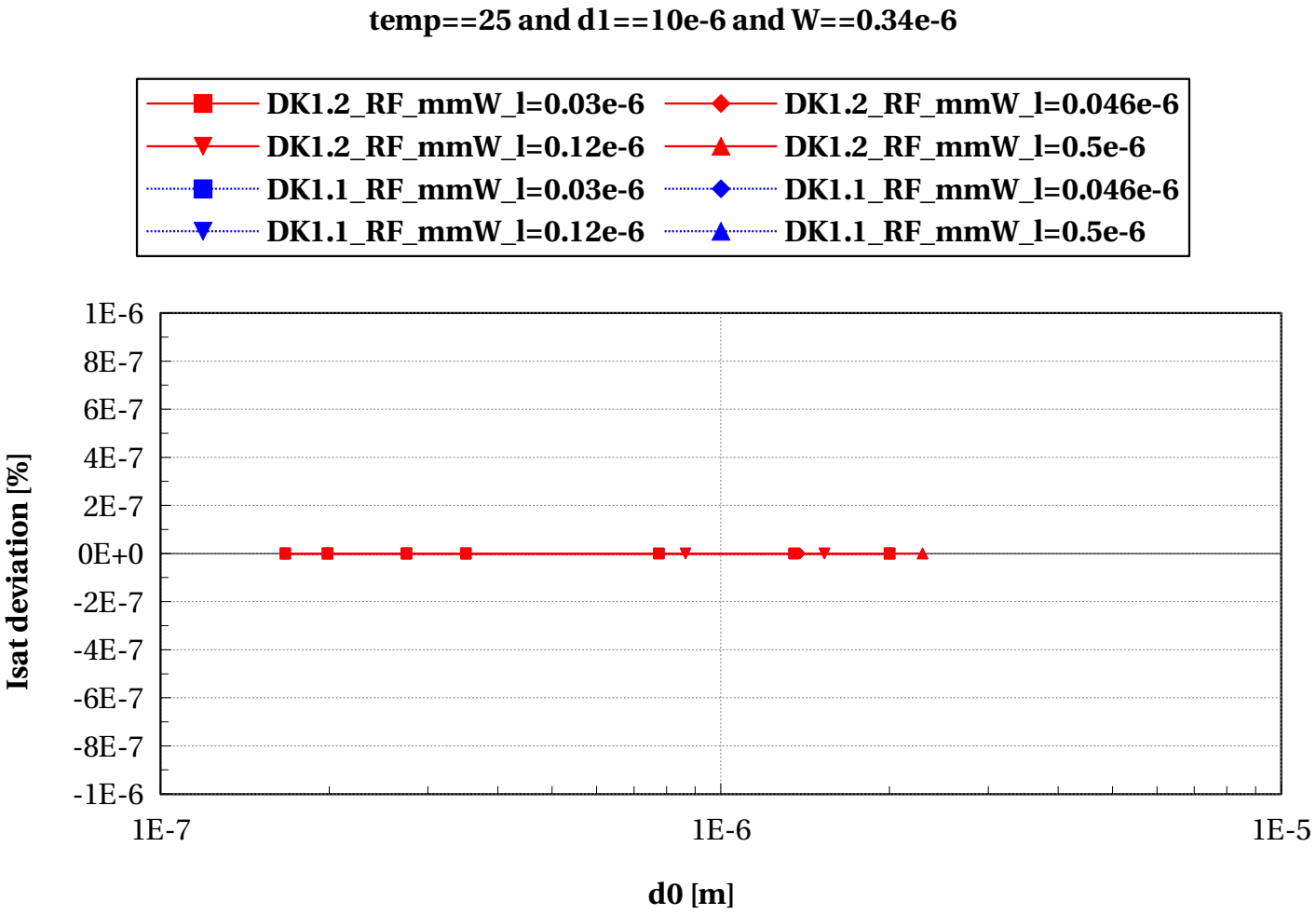
lvtnfet_acc, Isat deviation [%] vs d0 [m]



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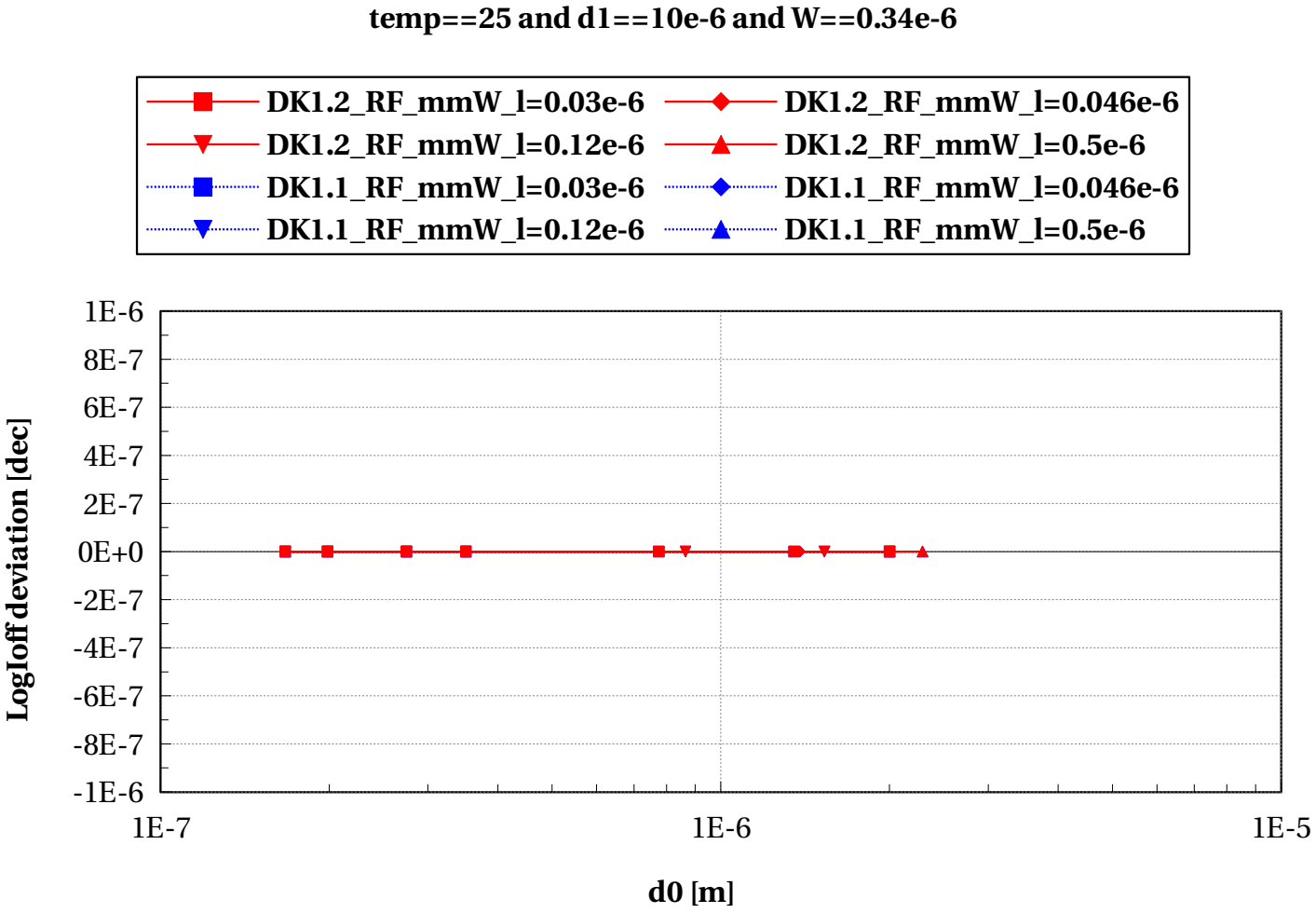
lvtnfet_acc, LogIoff deviation [dec] vs d0 [m]



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- WPE effect w.r.t d0 - Lscaling at W=0.1u

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lvtnfet_acc, Vt_lin shift [mV] vs d0 [m]

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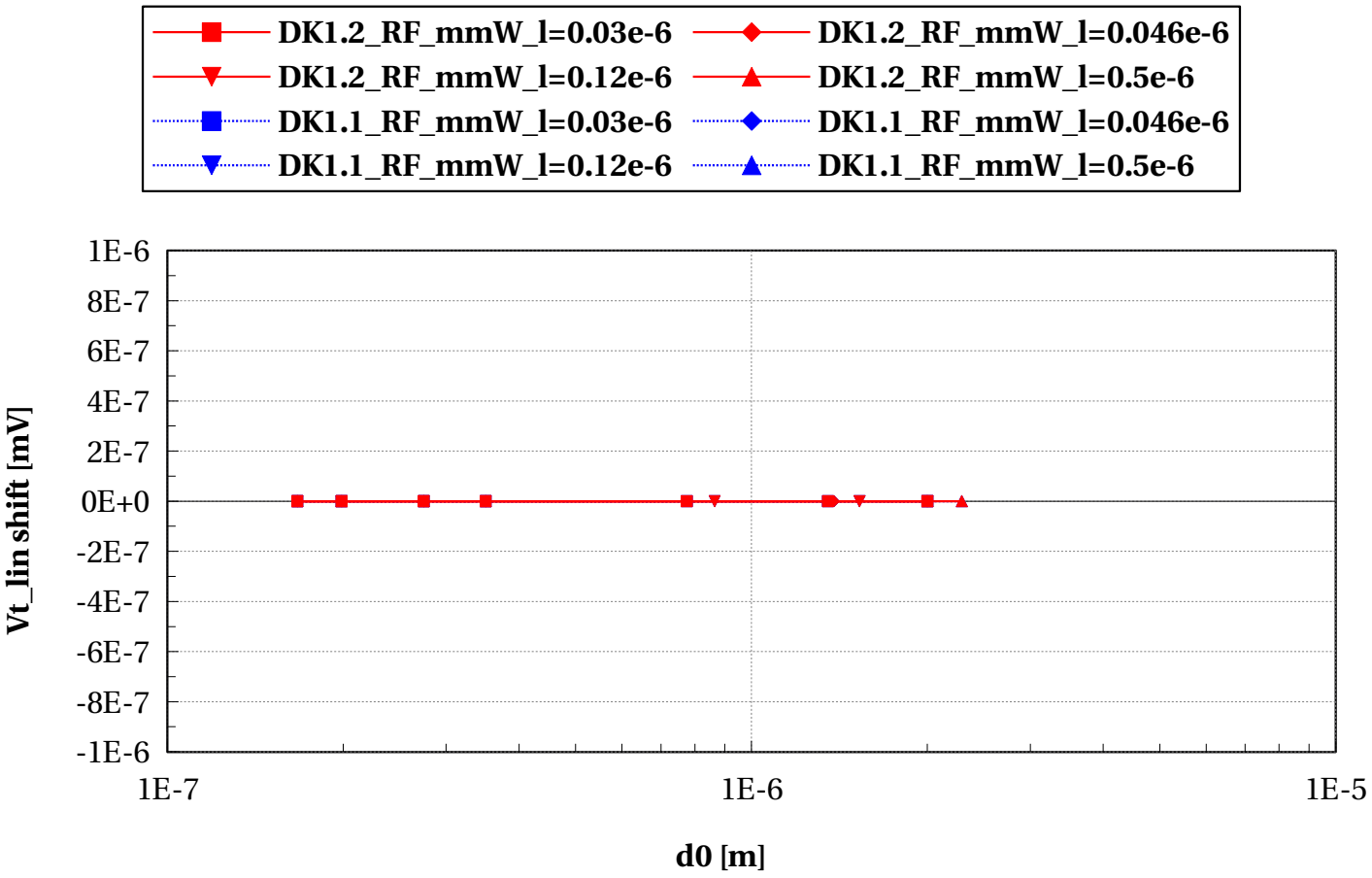

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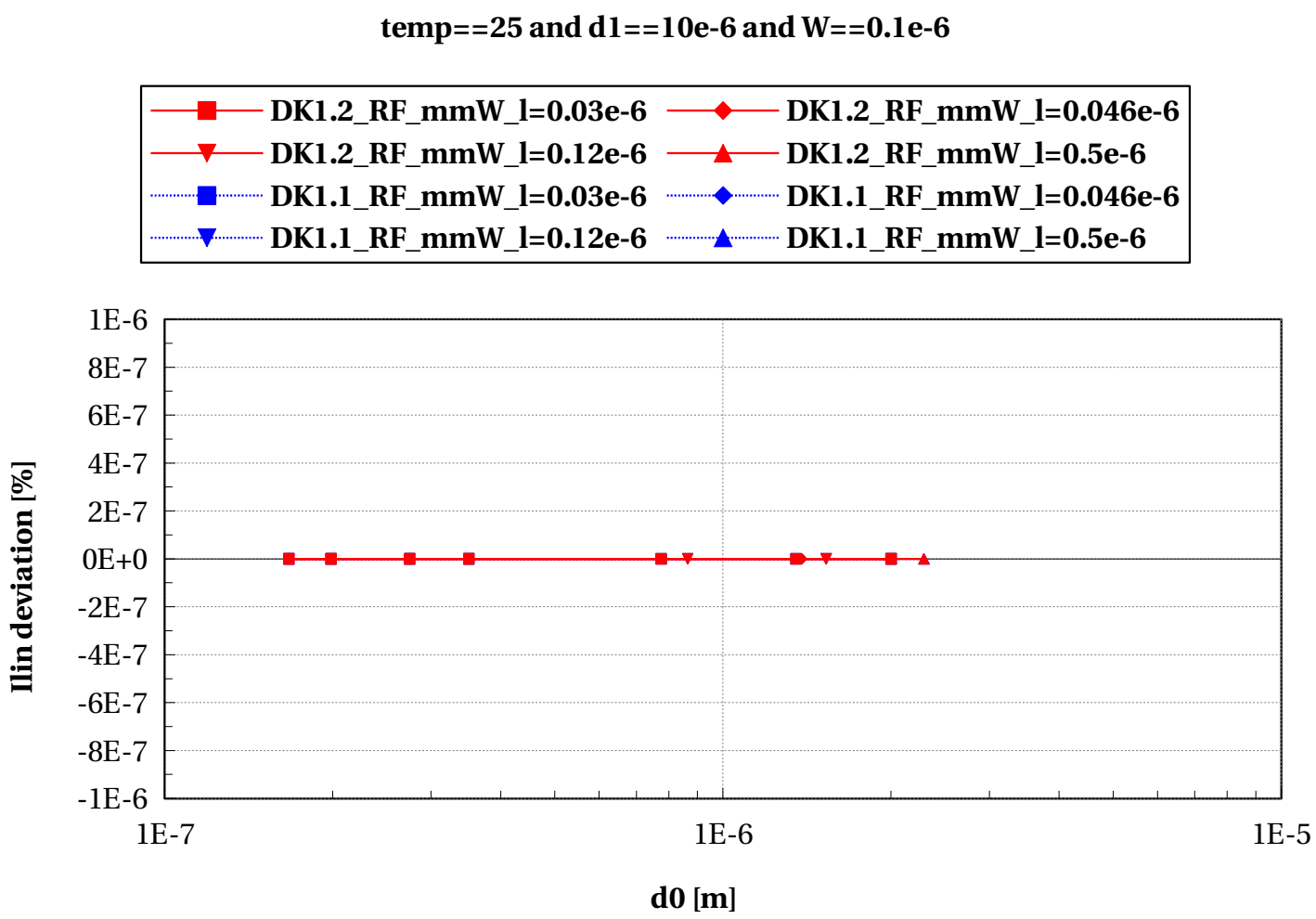
lvtnfet_acc, Ilin deviation [%] vs d0 [m]



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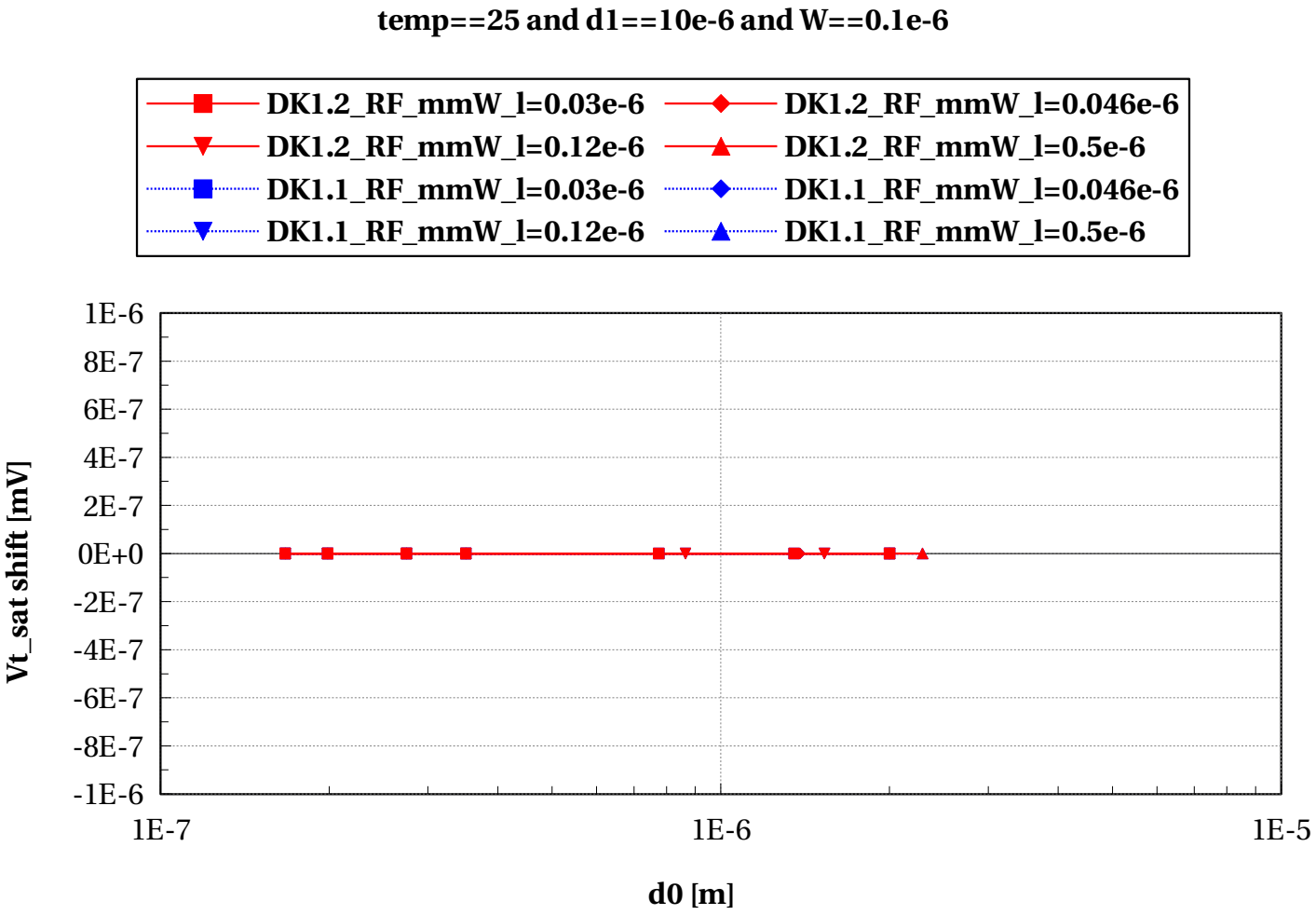
lvtnfet_acc, Vt_sat shift [mV] vs d0 [m]



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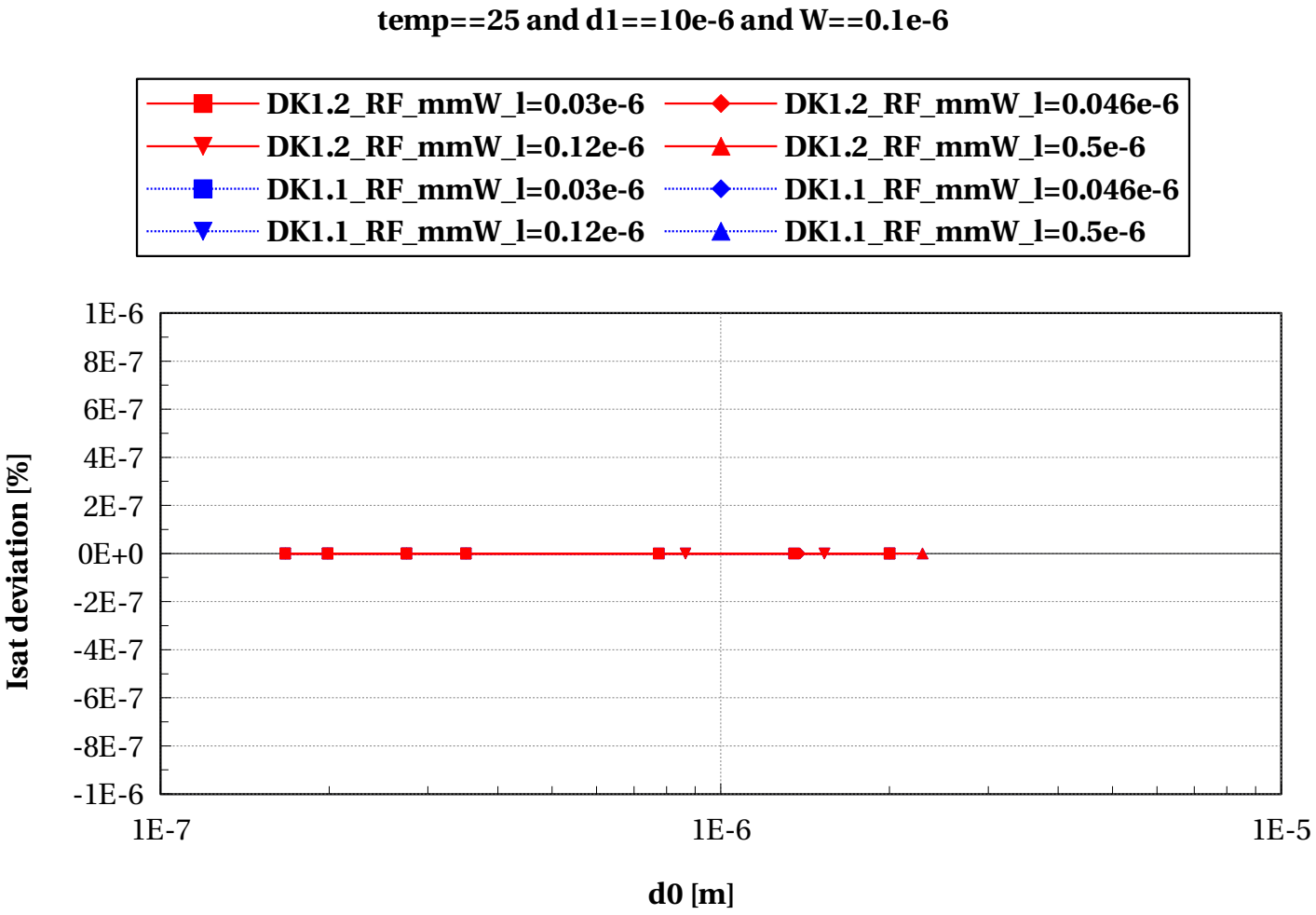
lvtnfet_acc, Isat deviation [%] vs d0 [m]



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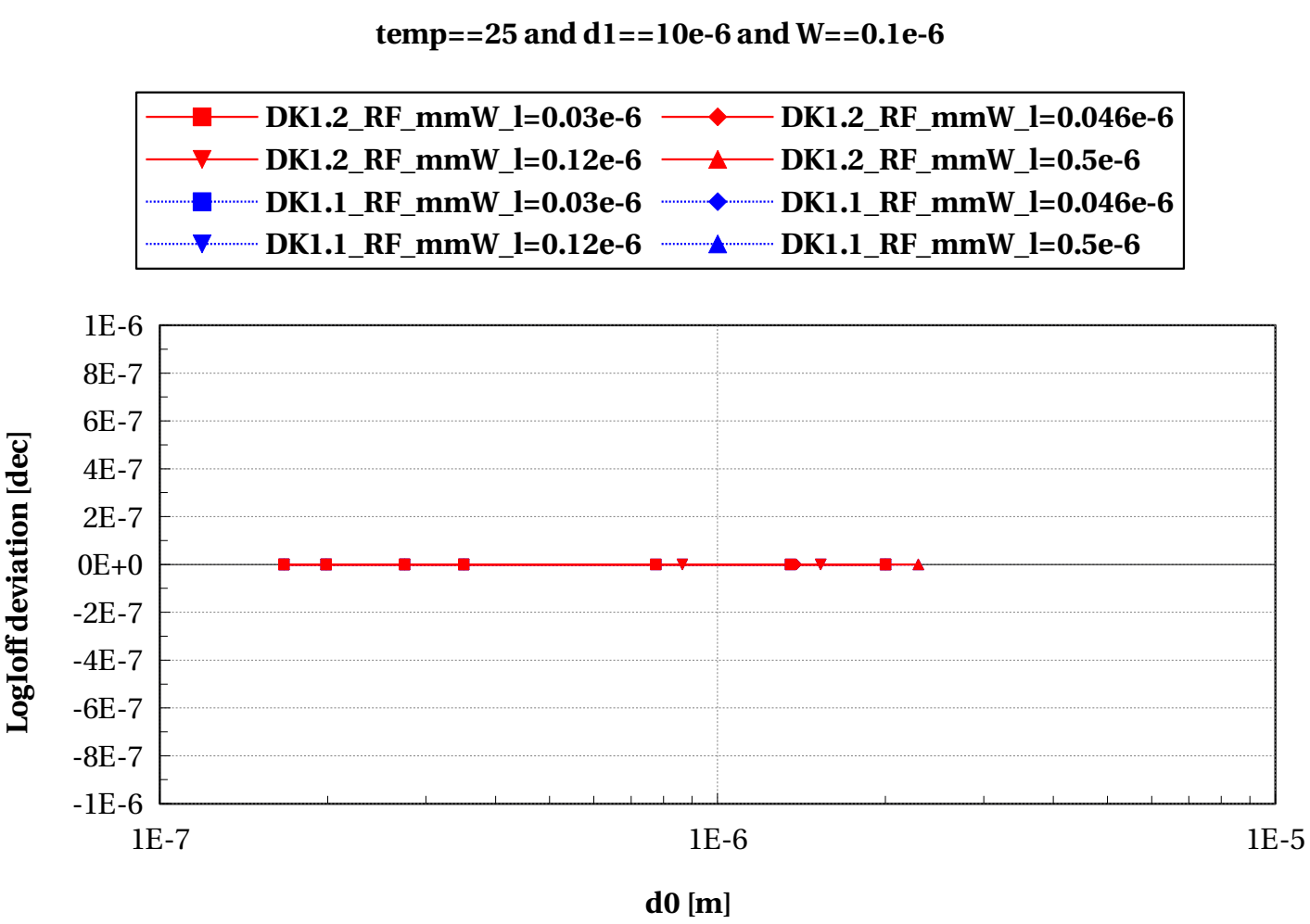
lvtnfet_acc, LogIoff deviation [dec] vs d0 [m]



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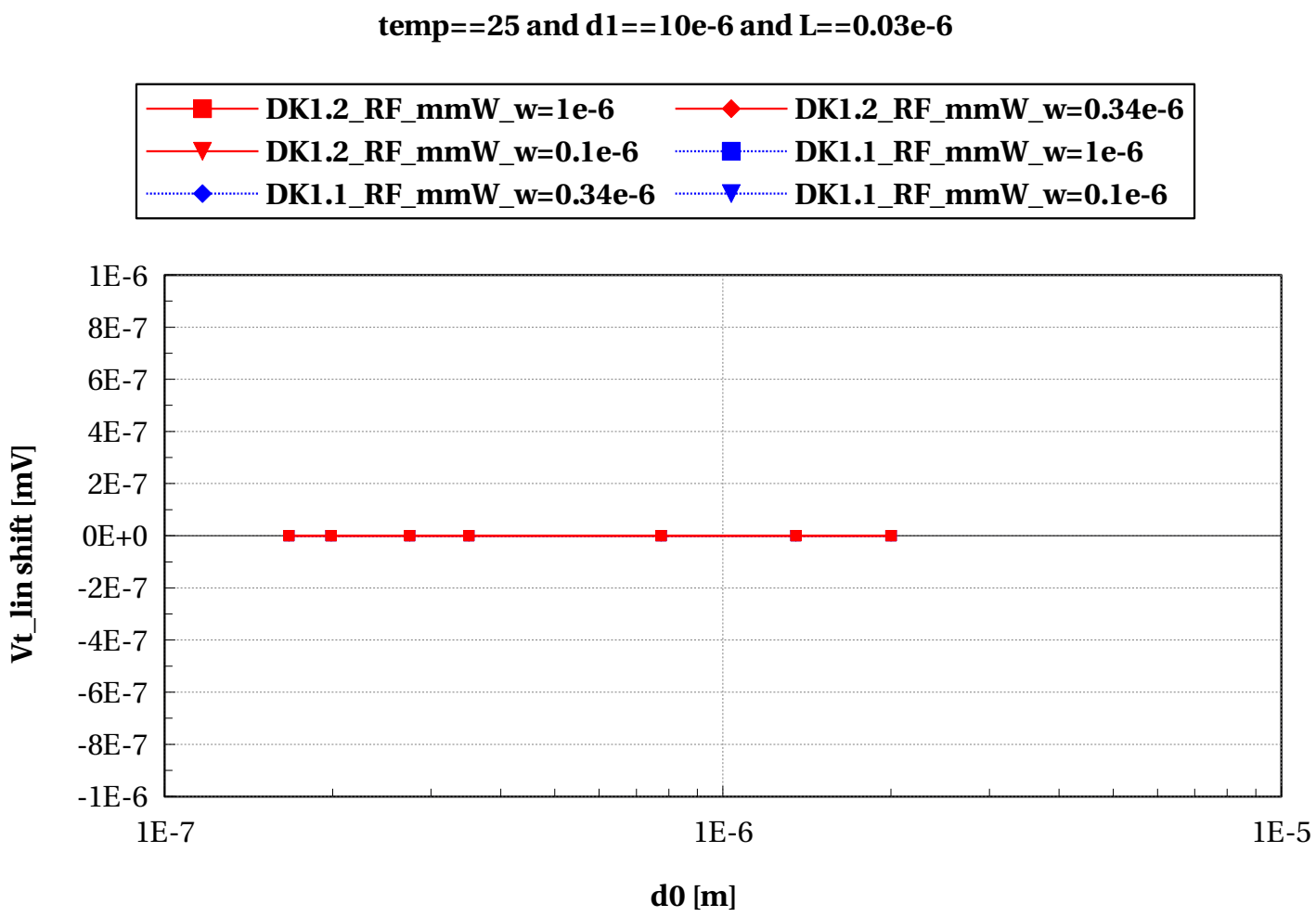
lvtnfet_acc, Vt_lin shift [mV] vs d0 [m]



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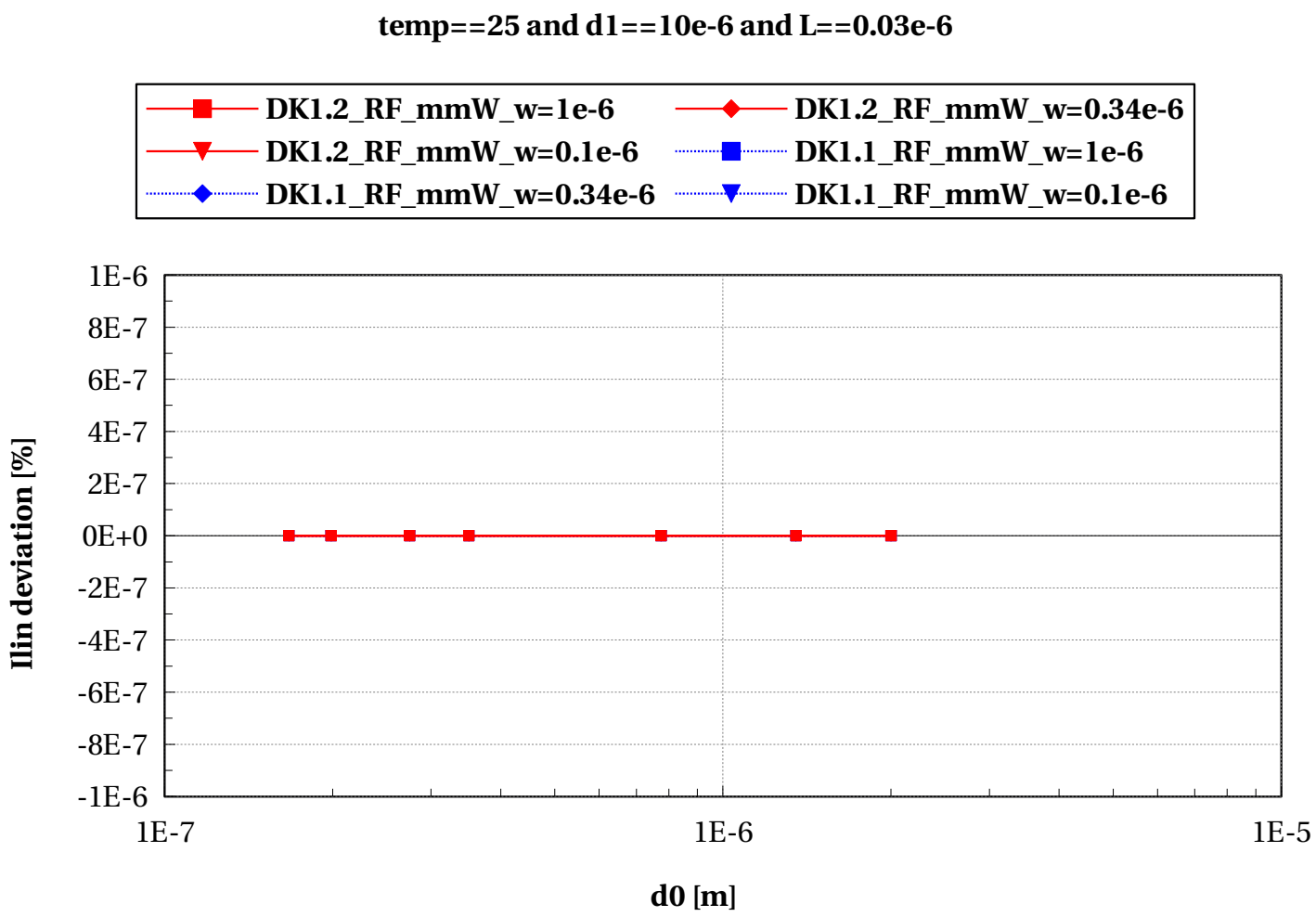
lvtnfet_acc, Ilin deviation [%] vs d0 [m]



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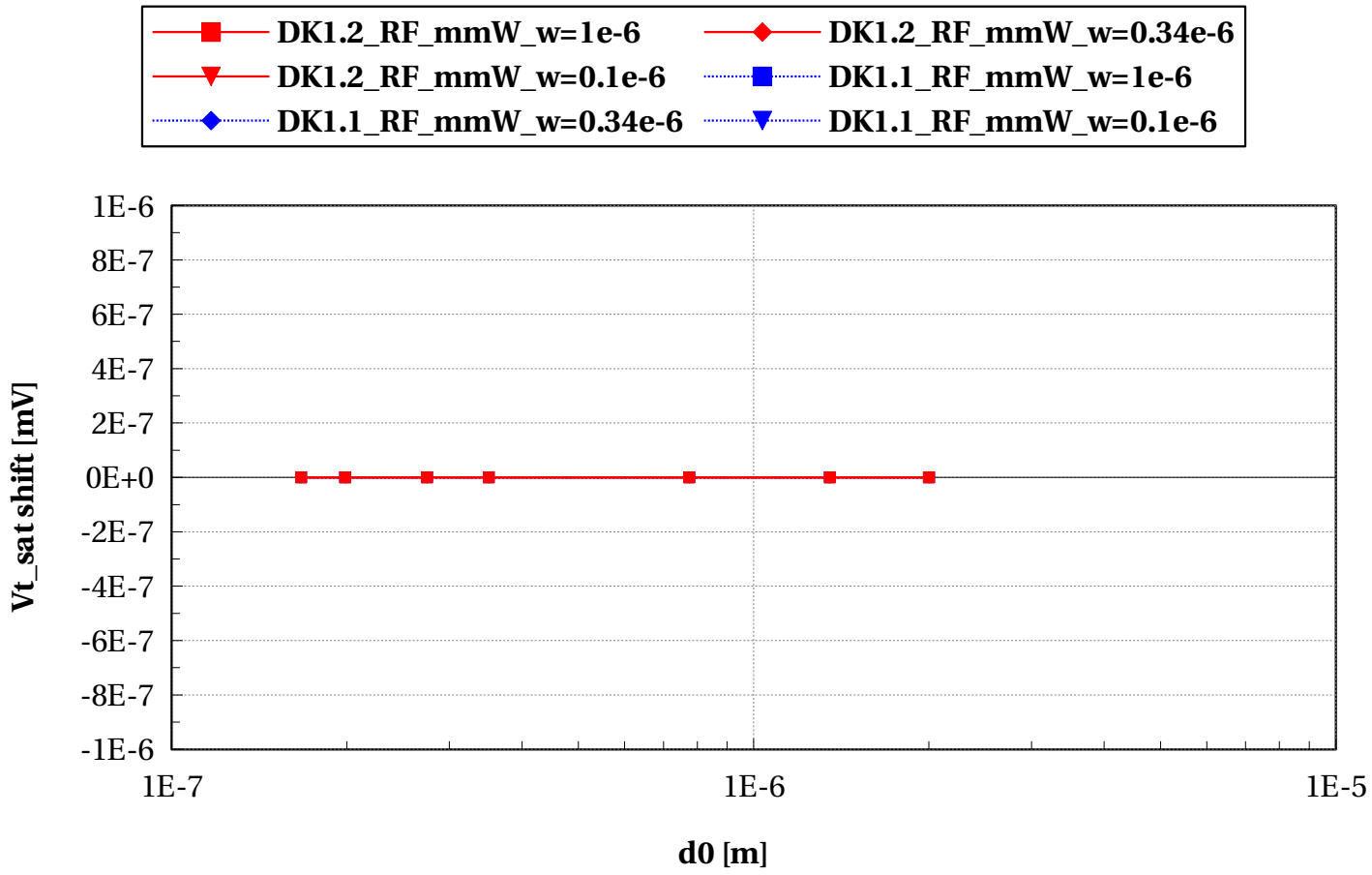
lvtnfet_acc, Vt_sat shift [mV] vs d0 [m]

temp==25 and d1==10e-6 and L==0.03e-6



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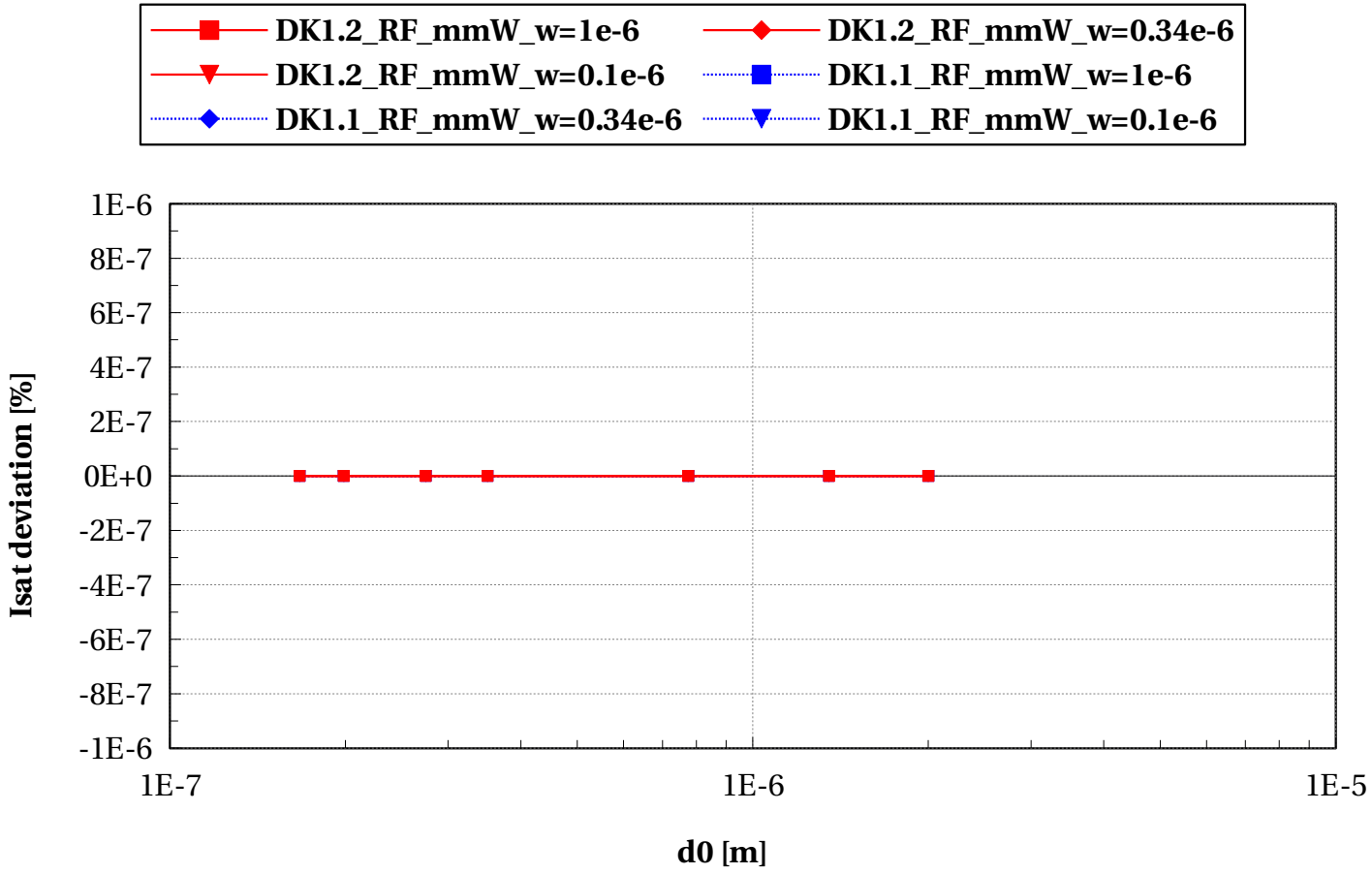
lvtnfet_acc, Isat deviation [%] vs d0 [m]

temp==25 and d1==10e-6 and L==0.03e-6



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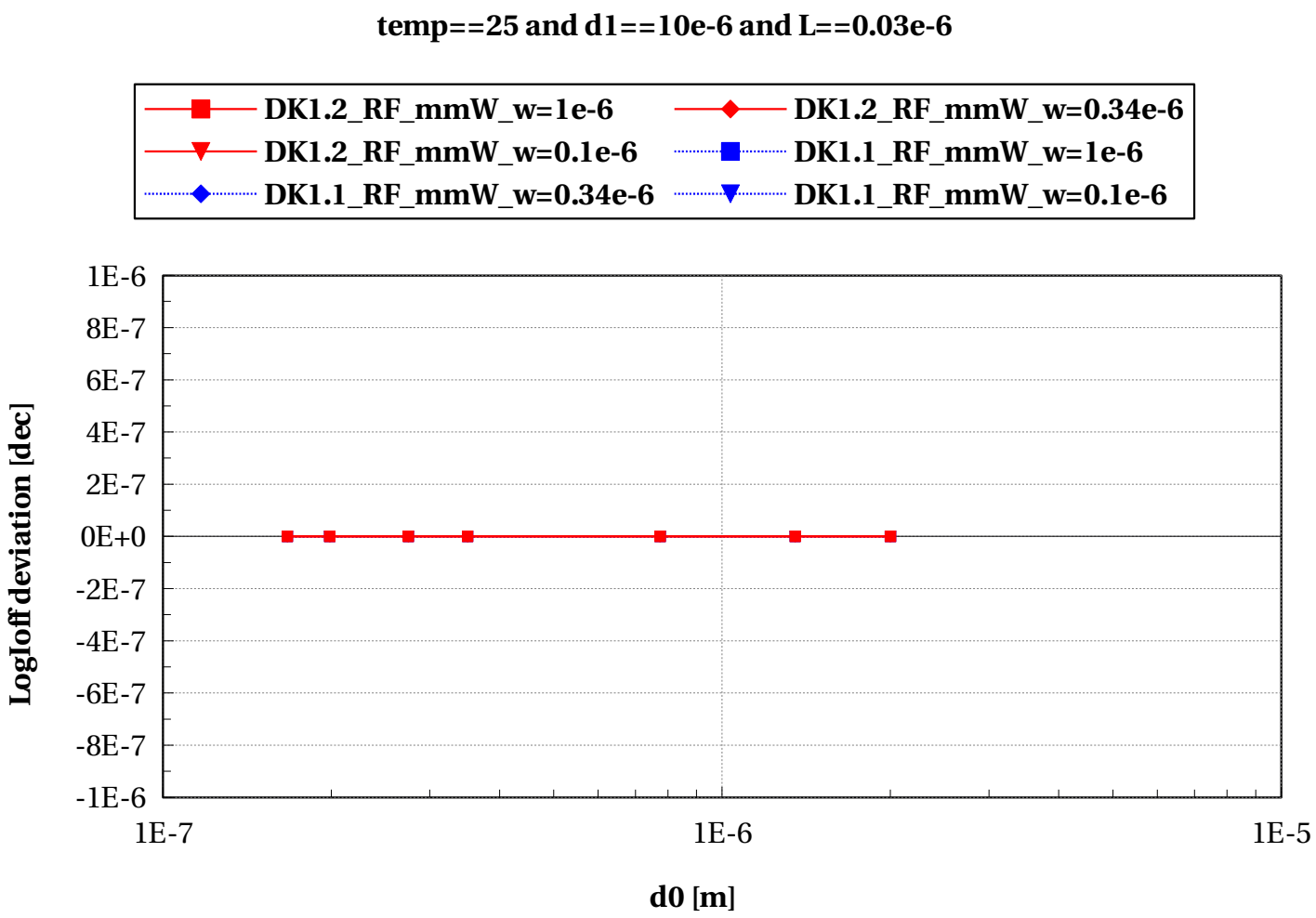
lvtnfet_acc, LogIoff deviation [dec] vs d0 [m]



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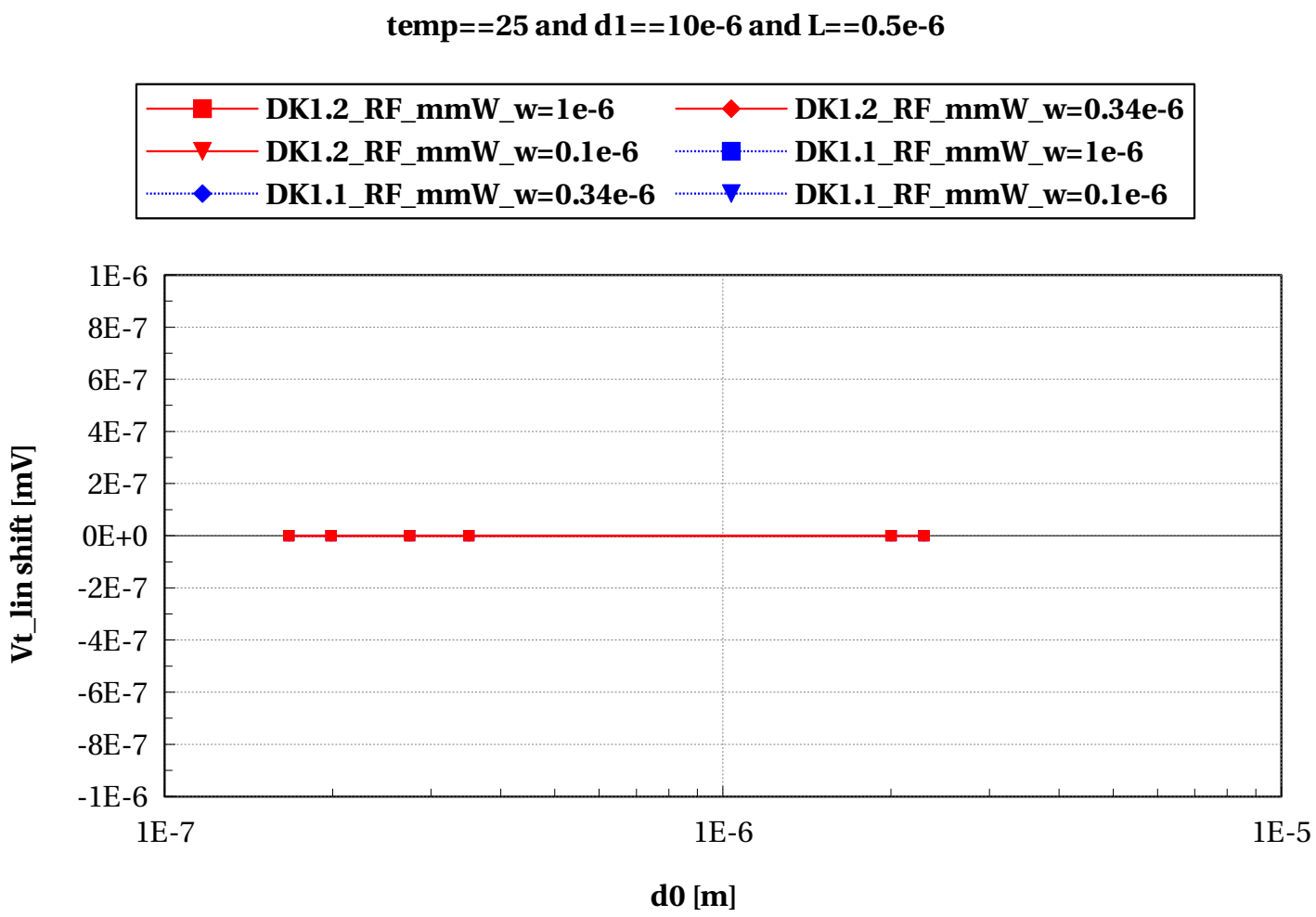
lvtnfet_acc, Vt_lin shift [mV] vs d0 [m]



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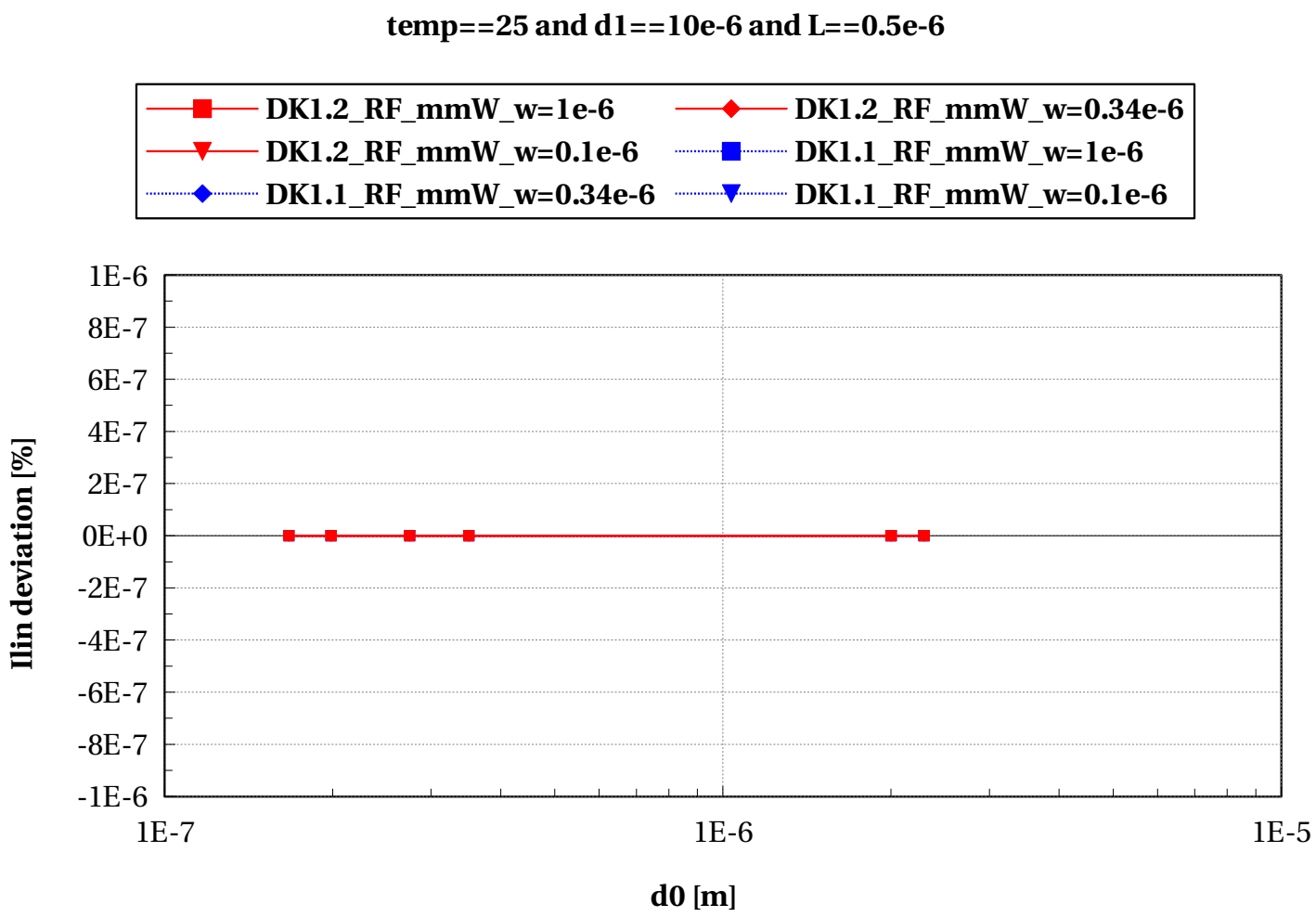
lvtnfet_acc, Ilin deviation [%] vs d0 [m]



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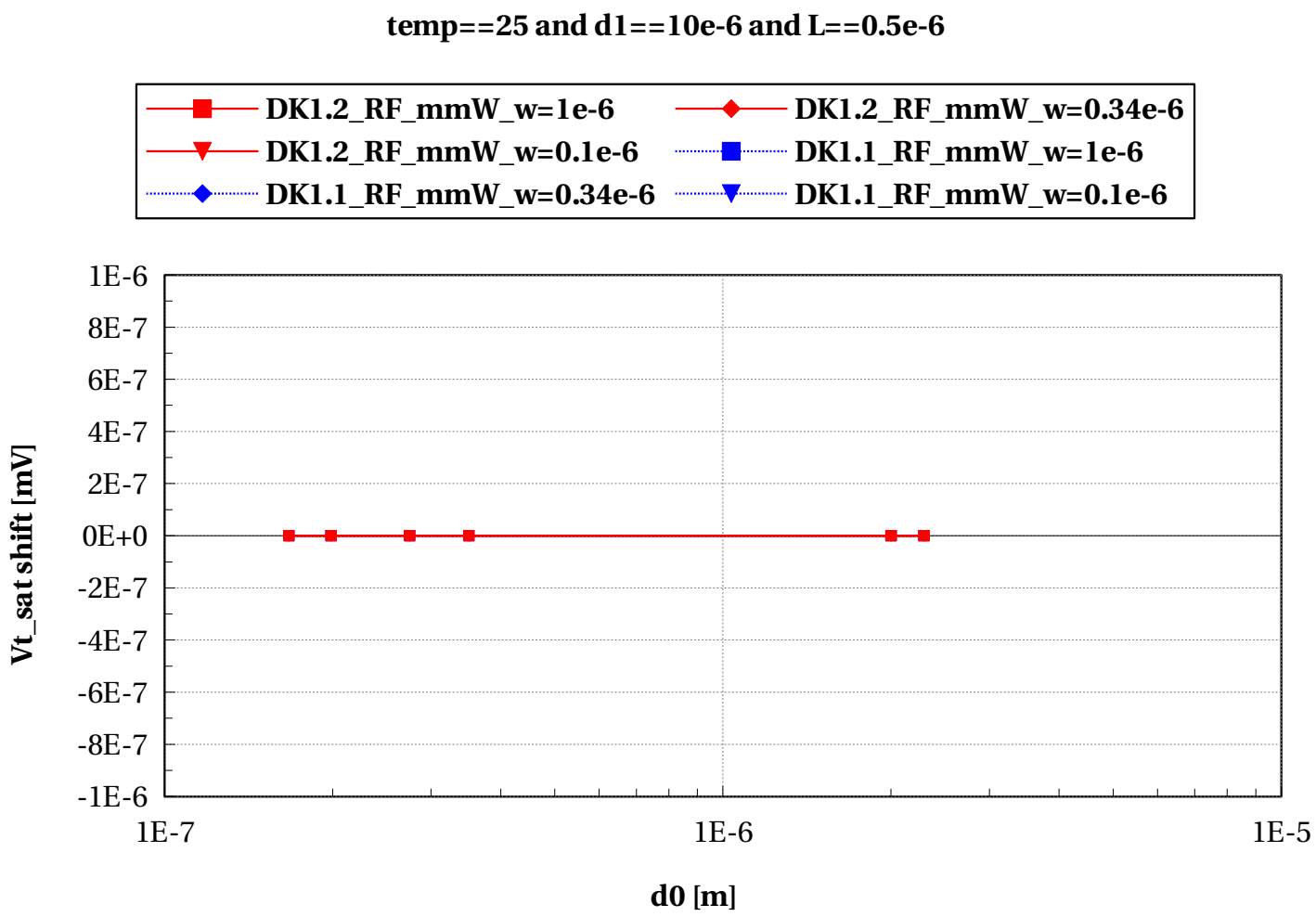
lvtnfet_acc, Vt_sat shift [mV] vs d0 [m]



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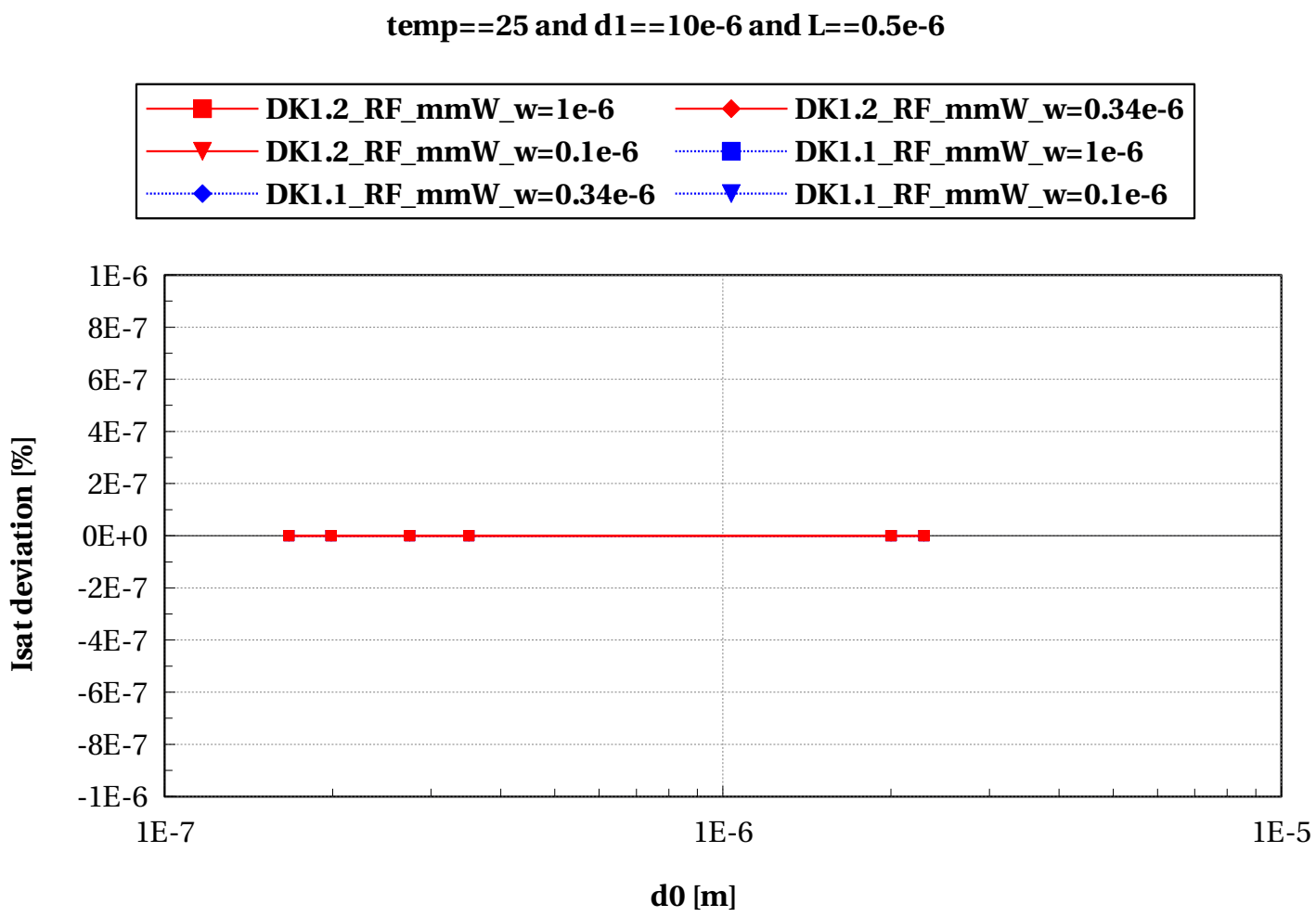
lvtnfet_acc, Isat deviation [%] vs d0 [m]



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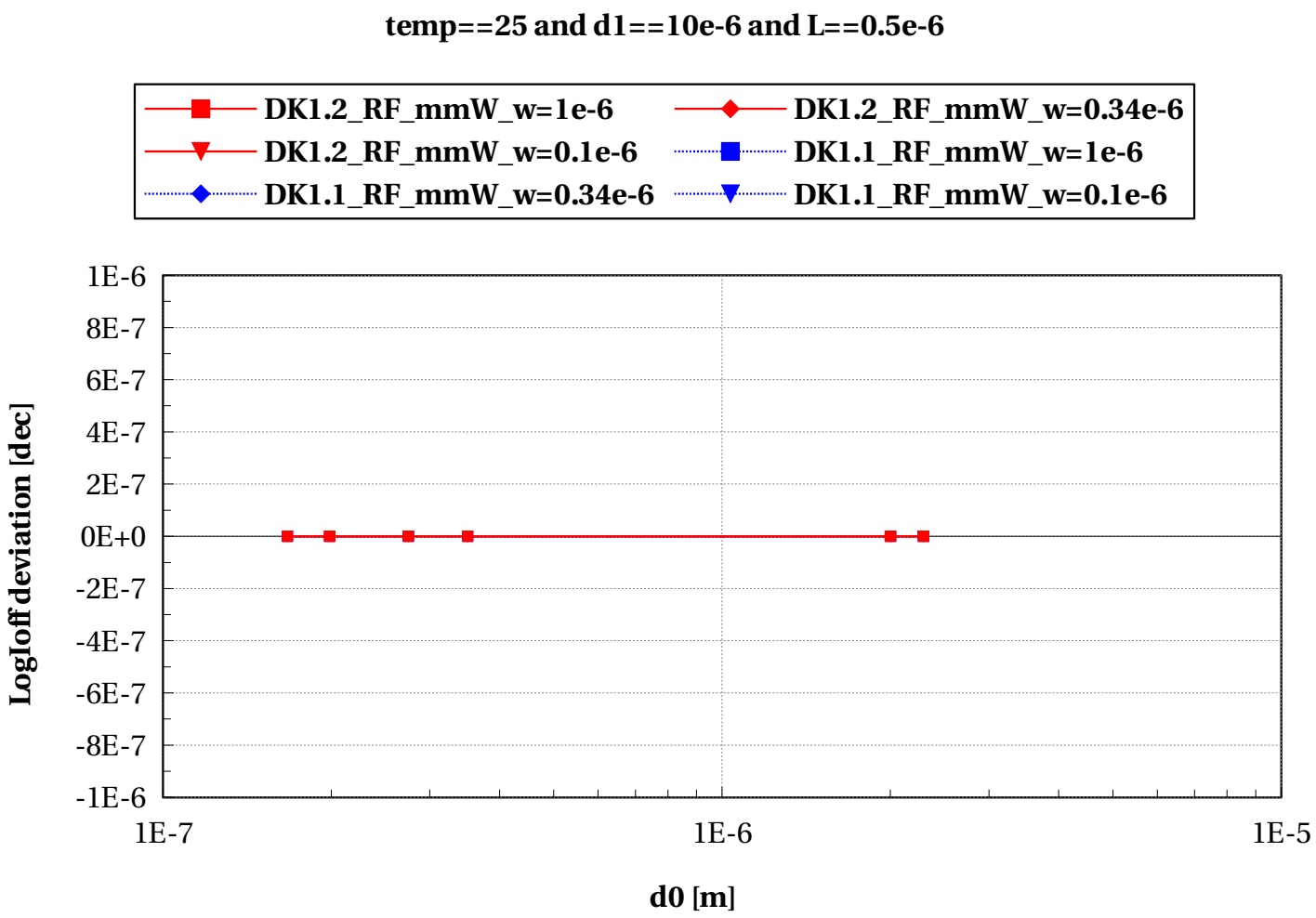
lvtnfet_acc, LogIoff deviation [dec] vs d0 [m]



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lvtnfet_acc, Vt_lin shift [mV] vs d1 [m]

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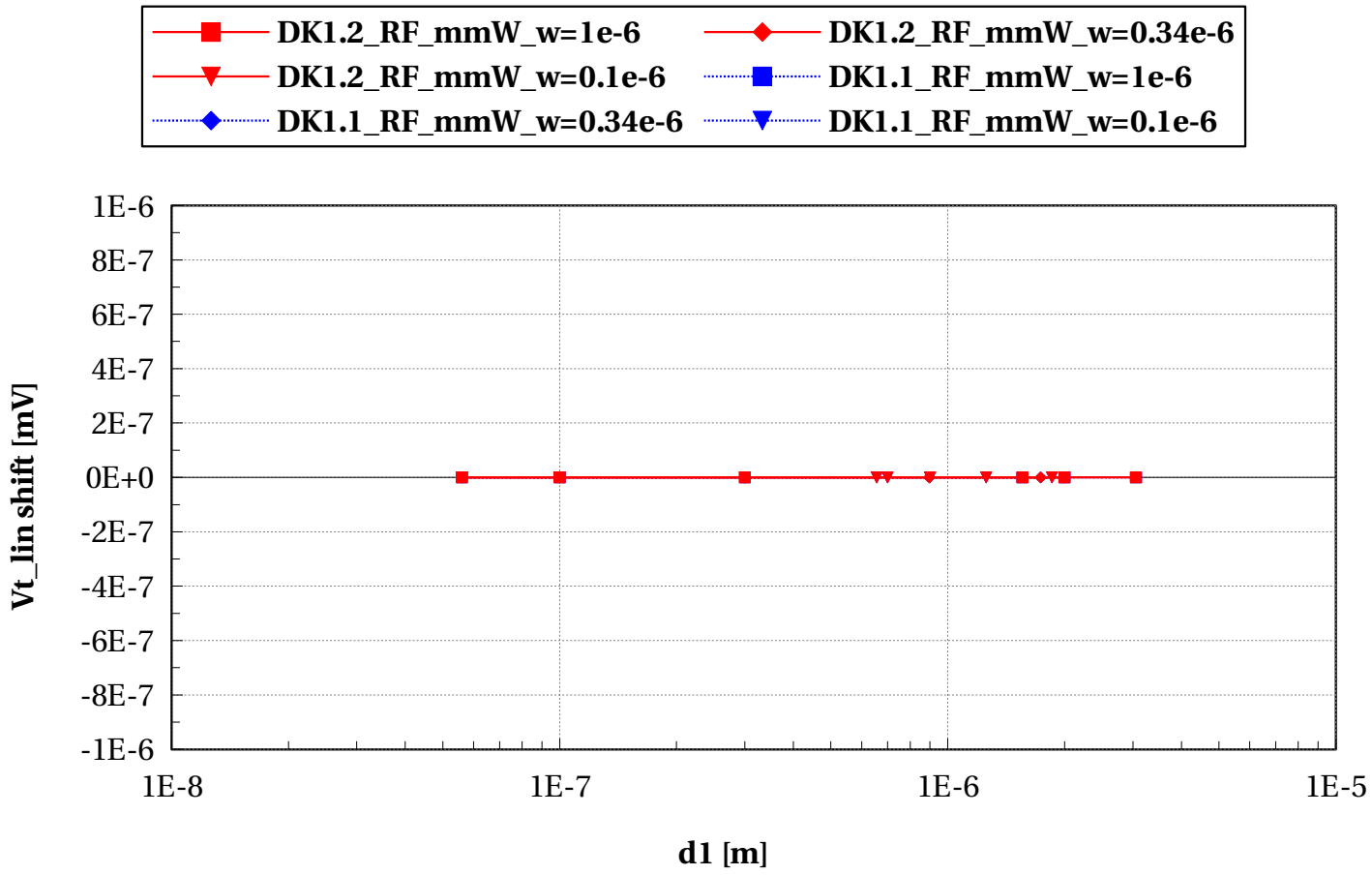

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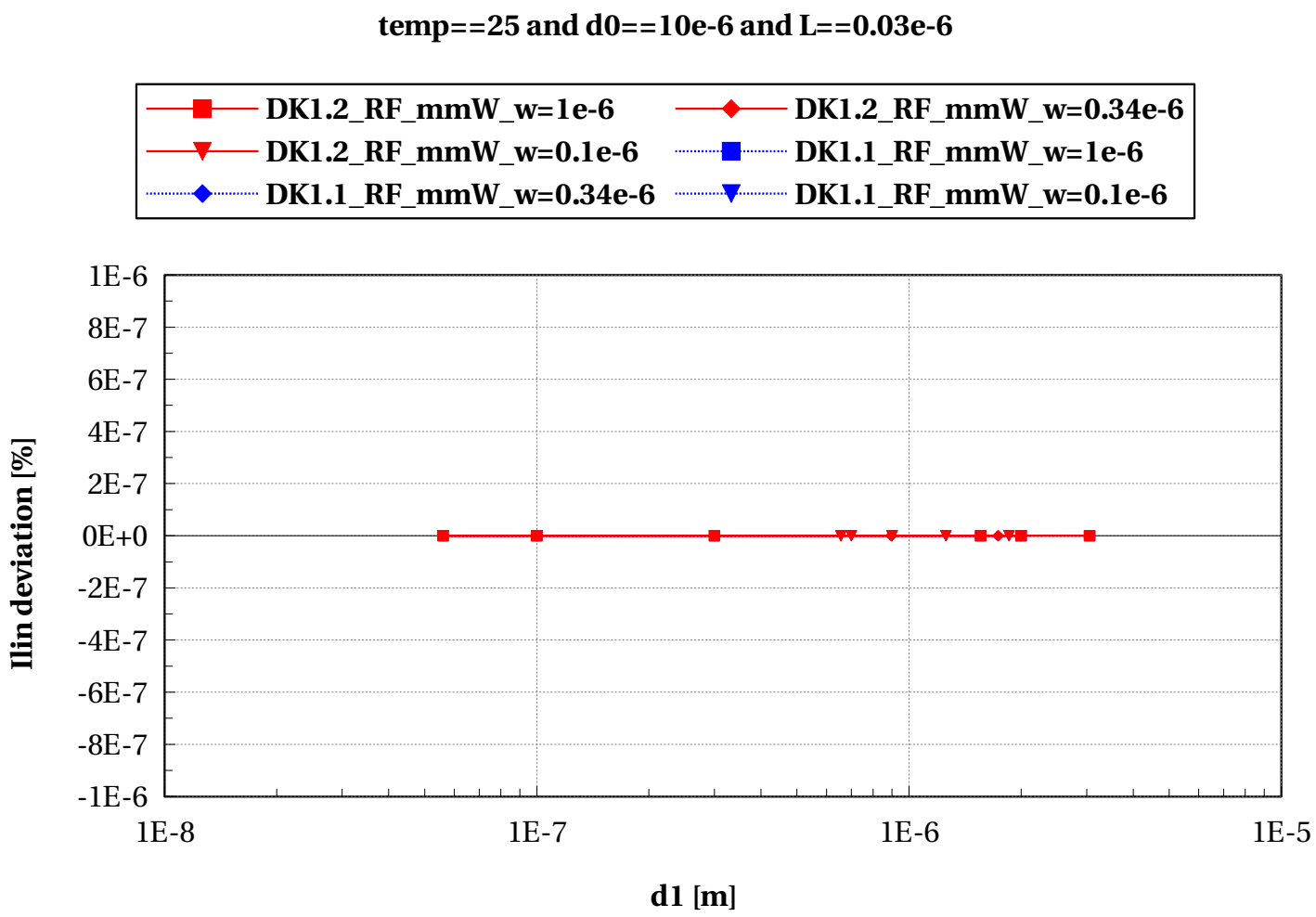
lvtnfet_acc, Ilin deviation [%] vs d1 [m]



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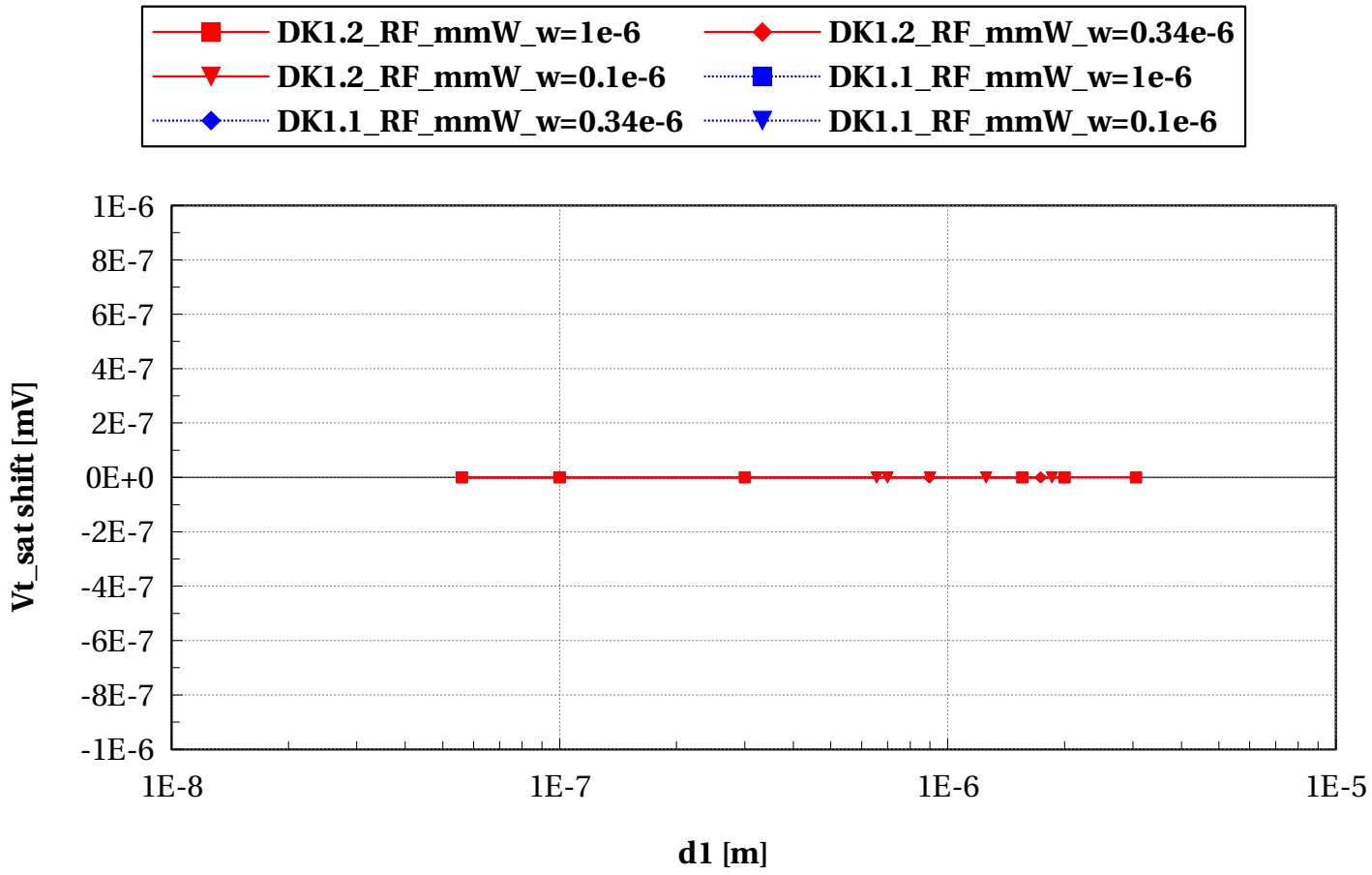
lvtnfet_acc, Vt_sat shift [mV] vs d1 [m]

temp==25 and d0==10e-6 and L==0.03e-6



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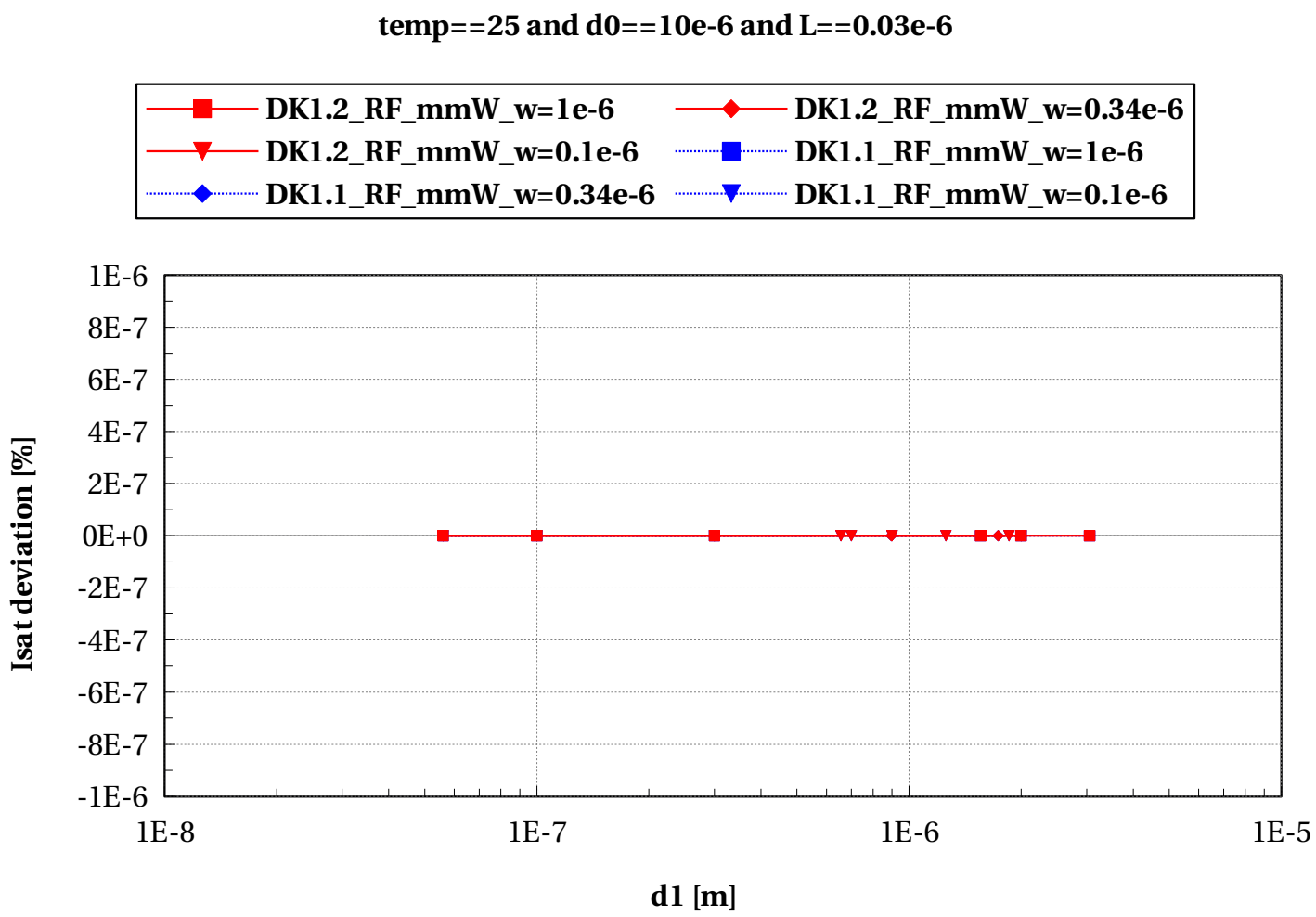
lvtnfet_acc, Isat deviation [%] vs d1 [m]



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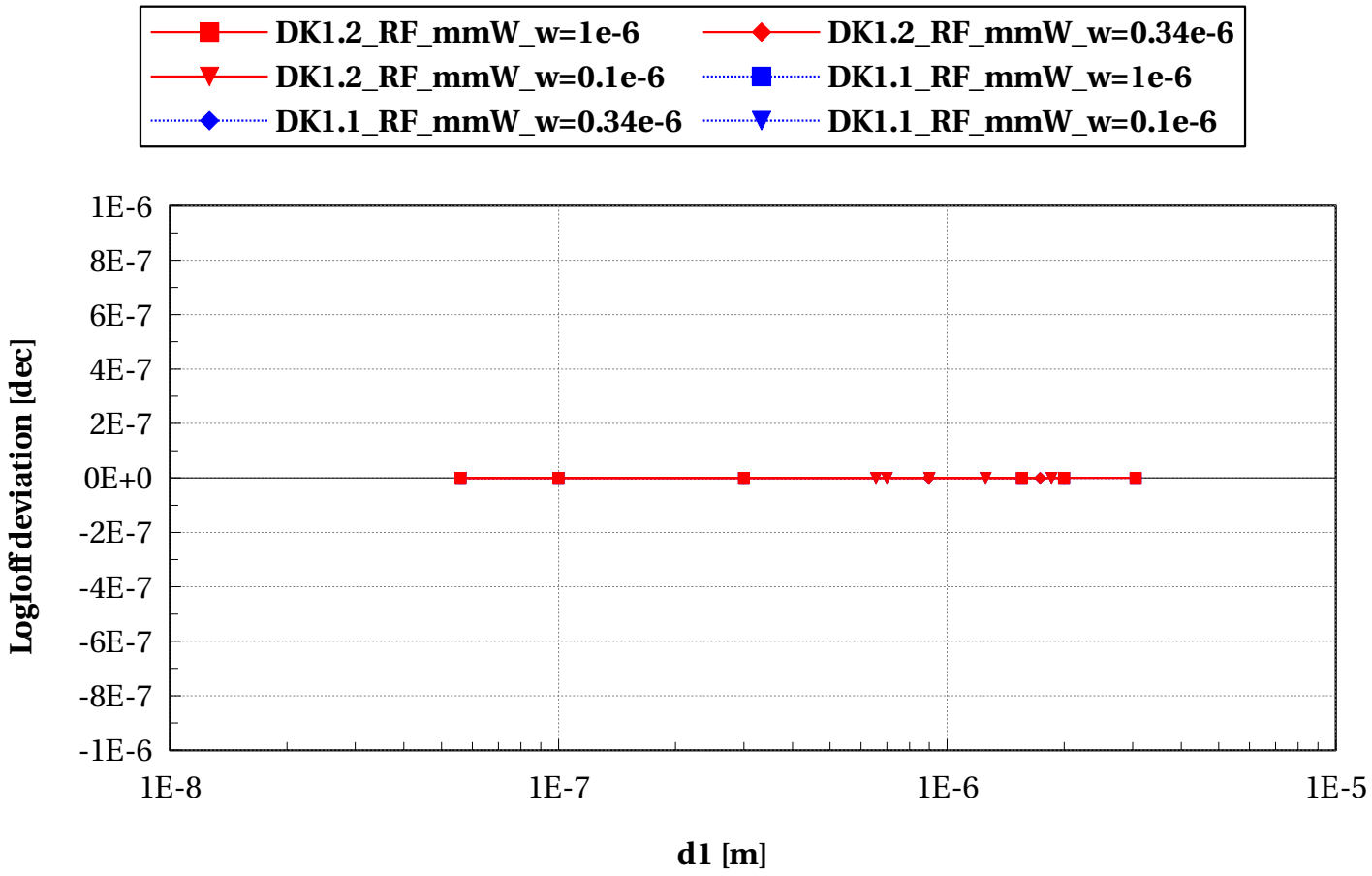
lvtnfet_acc, LogIoff deviation [dec] vs d1 [m]

temp==25 and d0==10e-6 and L==0.03e-6



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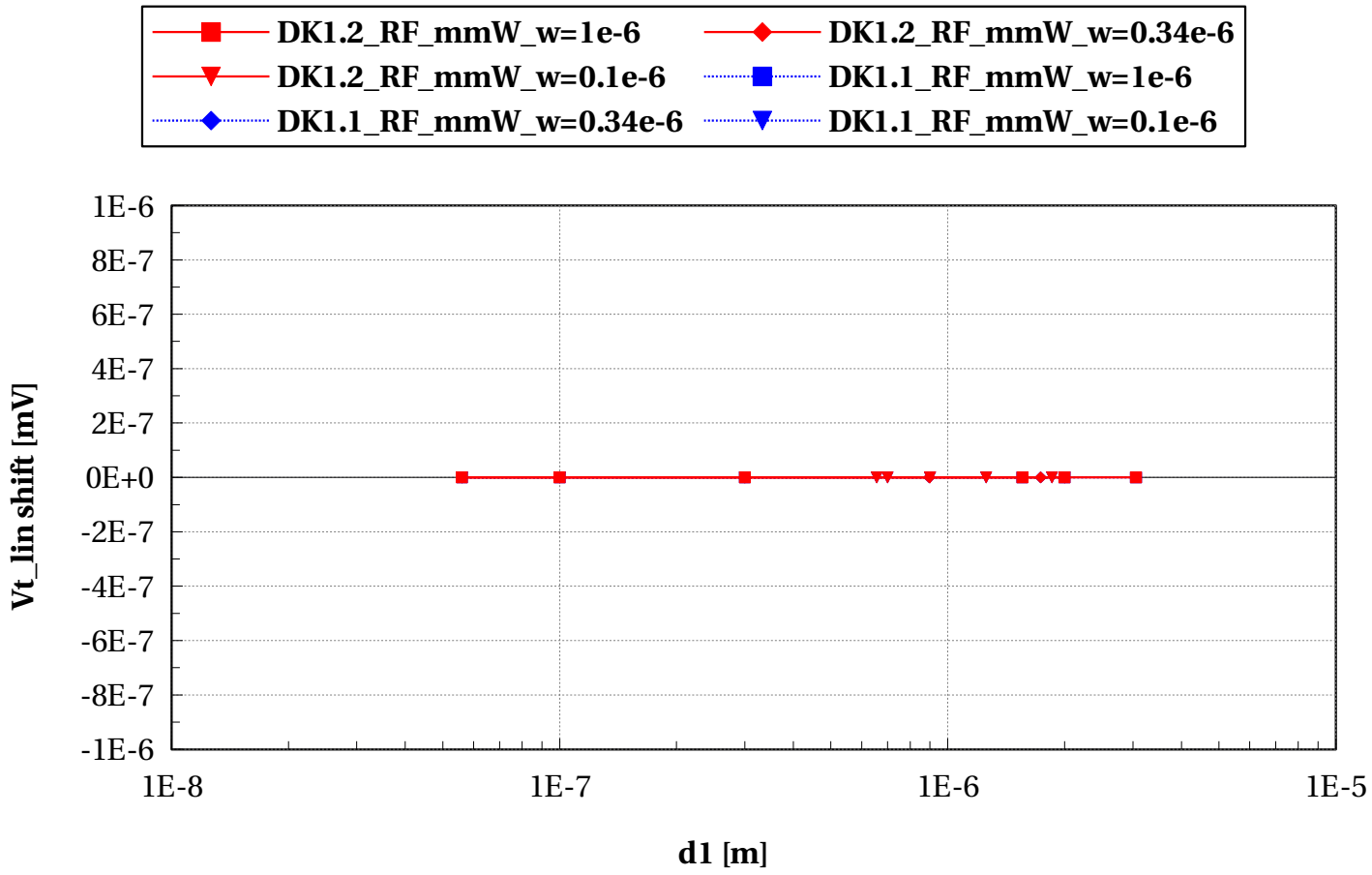
lvtnfet_acc, Vt_lin shift [mV] vs d1 [m]

temp==25 and d0==10e-6 and L==0.12e-6



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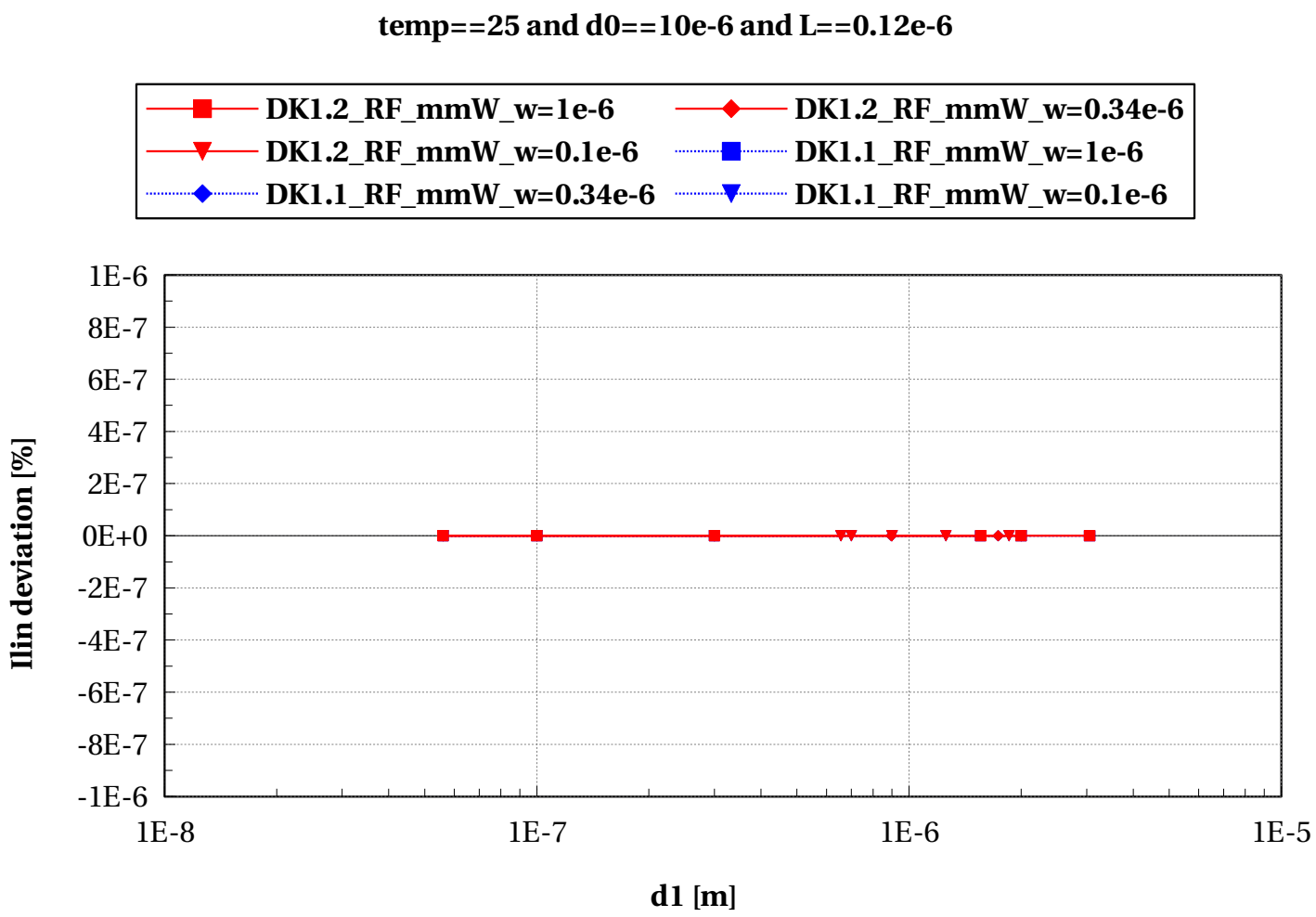
lvtnfet_acc, Ilin deviation [%] vs d1 [m]



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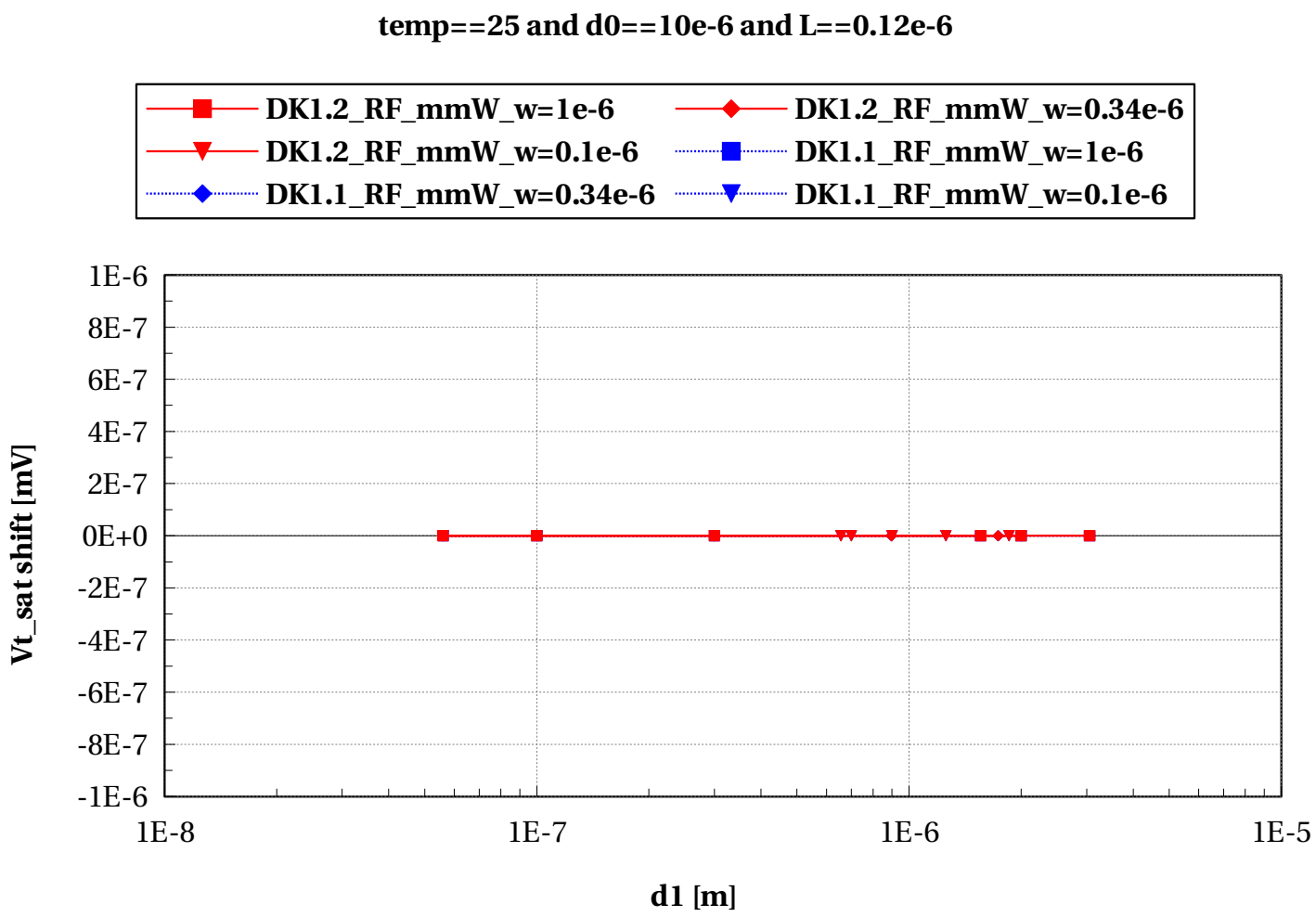
lvtnfet_acc, Vt_sat shift [mV] vs d1 [m]



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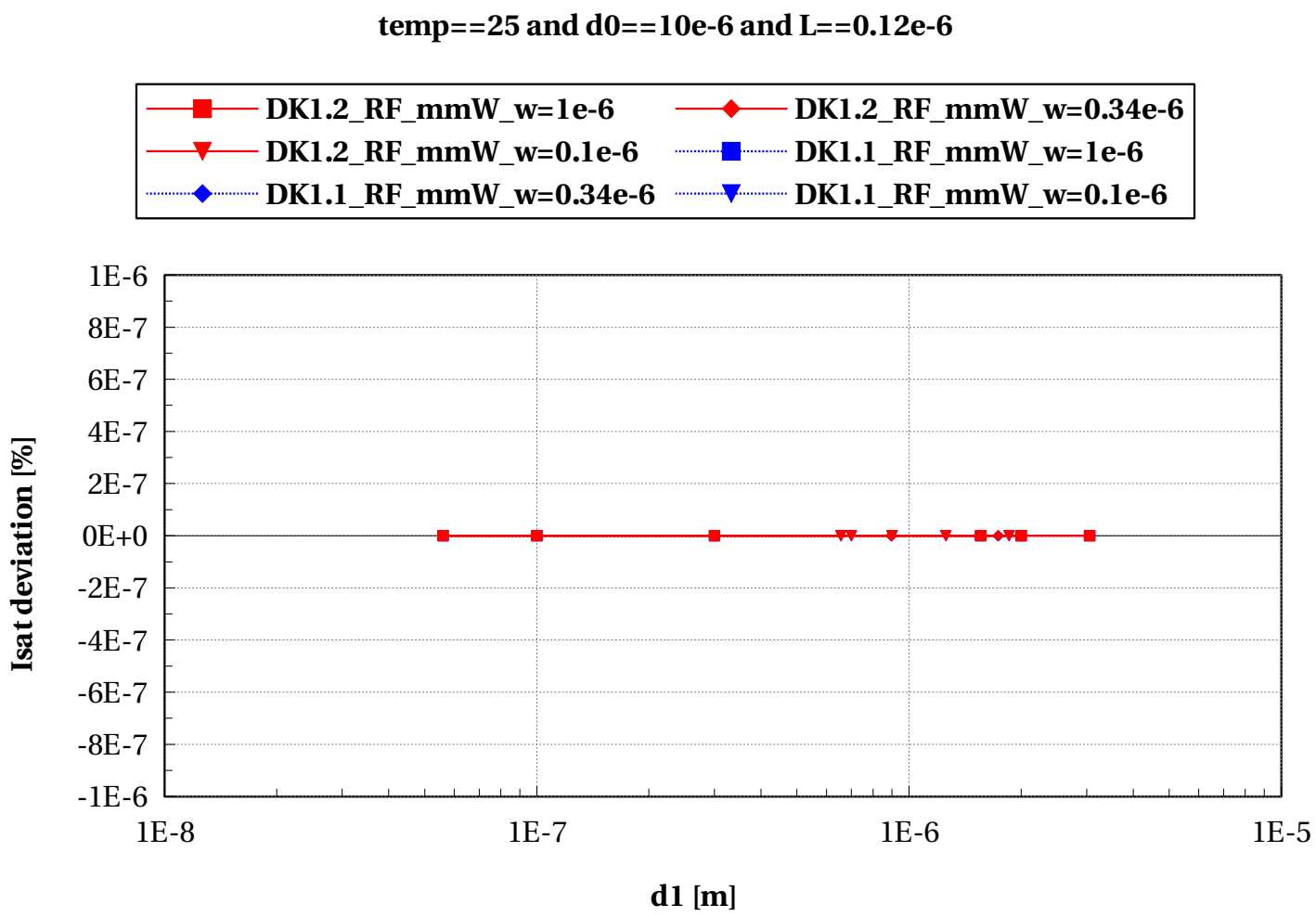
lvtnfet_acc, Isat deviation [%] vs d1 [m]



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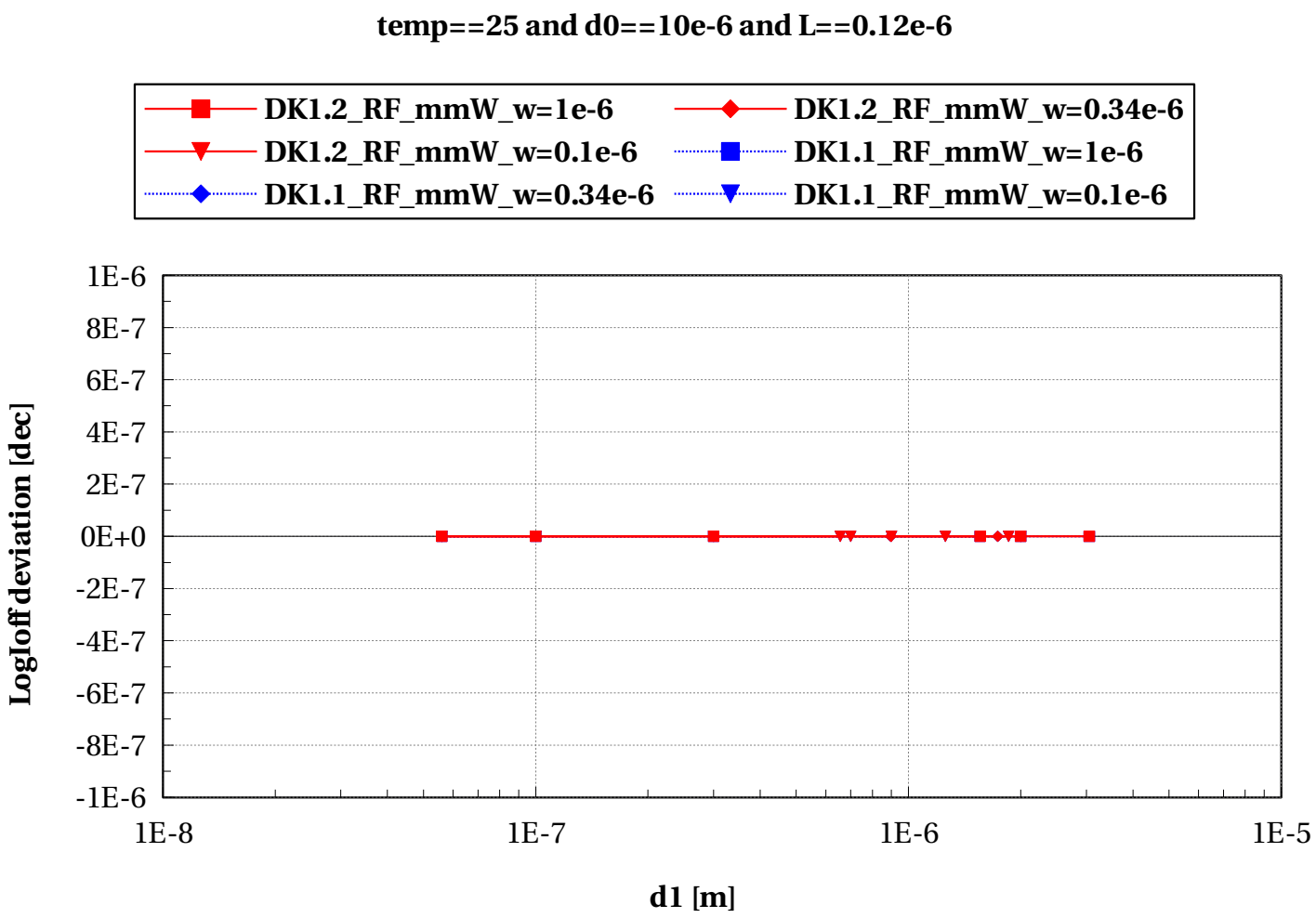
lvtnfet_acc, LogIoff deviation [dec] vs d1 [m]



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- WPE effect w.r.t d1 - Wscaling at L=1u

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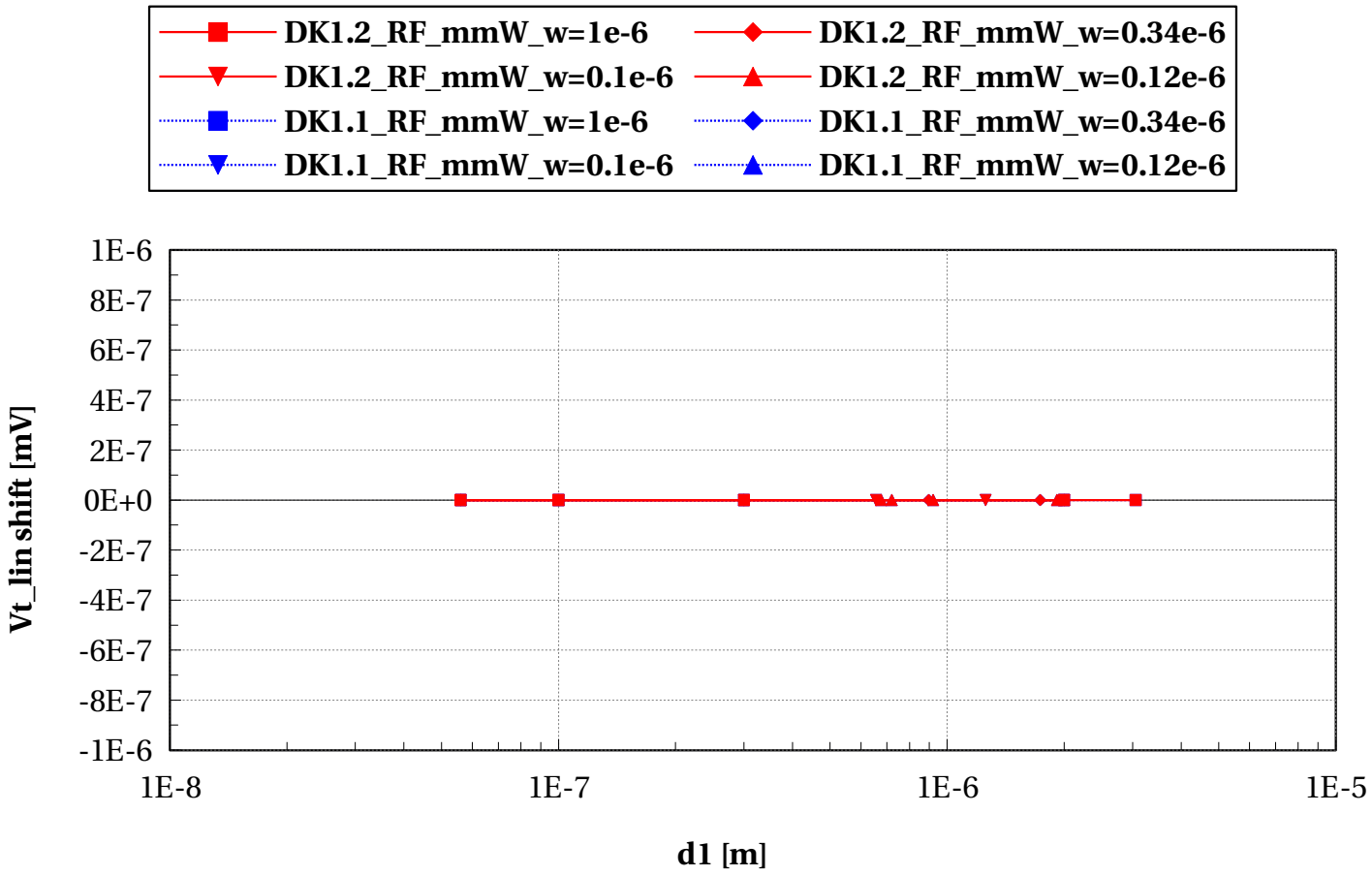
lvtnfet_acc, Vt_lin shift [mV] vs d1 [m]

temp==25 and d0==10e-6 and L==1e-6



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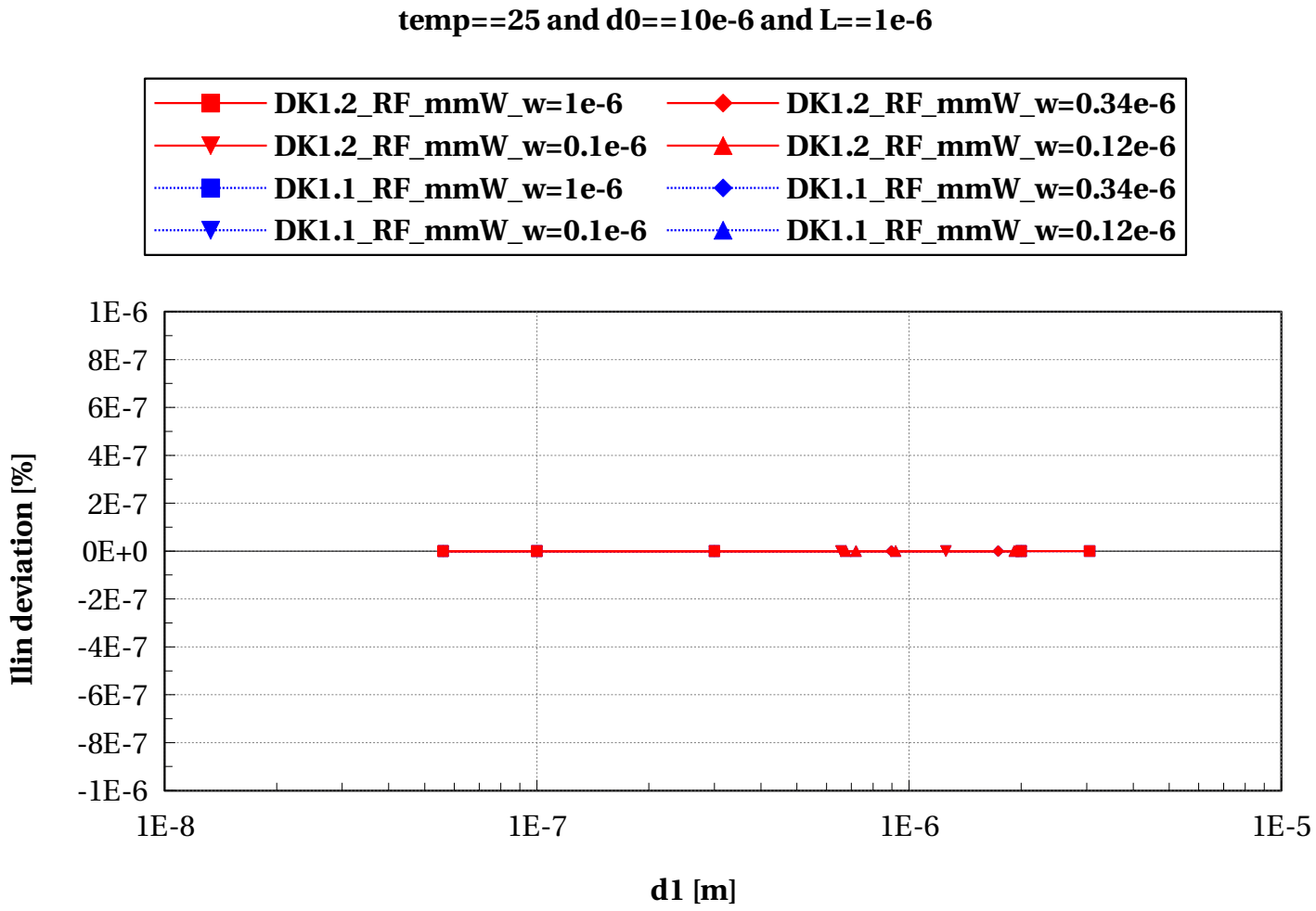
lvtnfet_acc, Ilin deviation [%] vs d1 [m]



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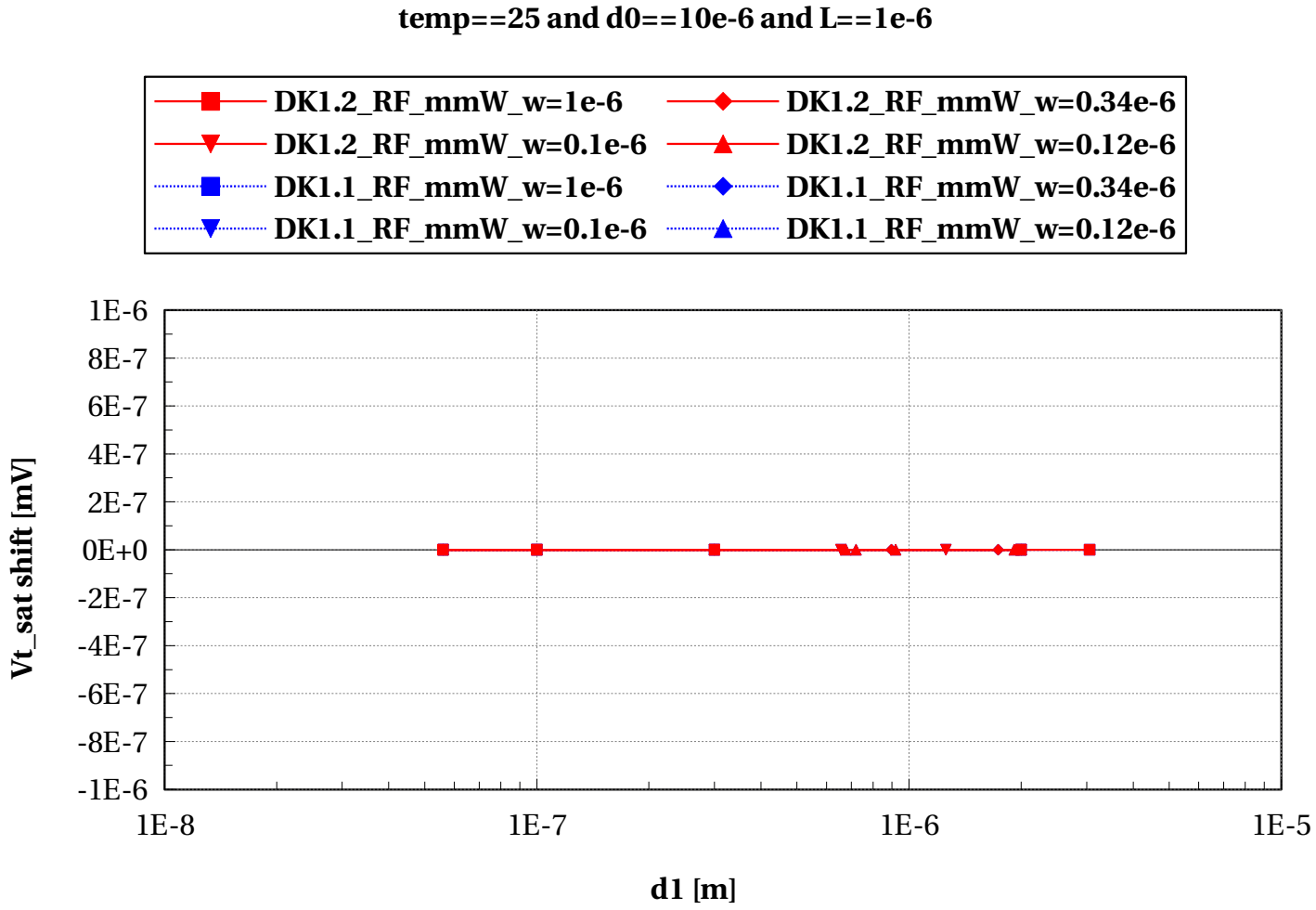
lvtnfet_acc, Vt_sat shift [mV] vs d1 [m]



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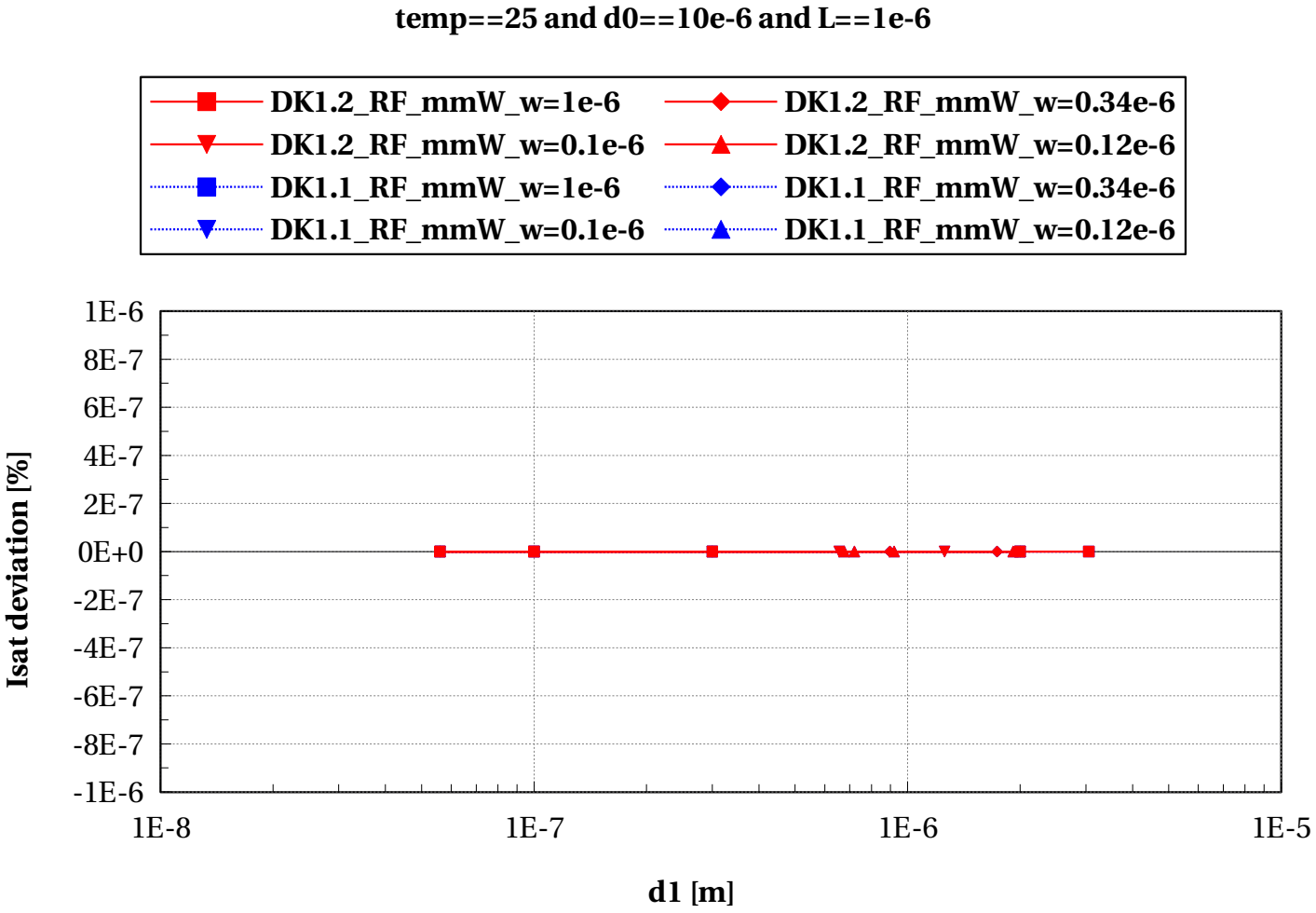
lvtnfet_acc, Isat deviation [%] vs d1 [m]



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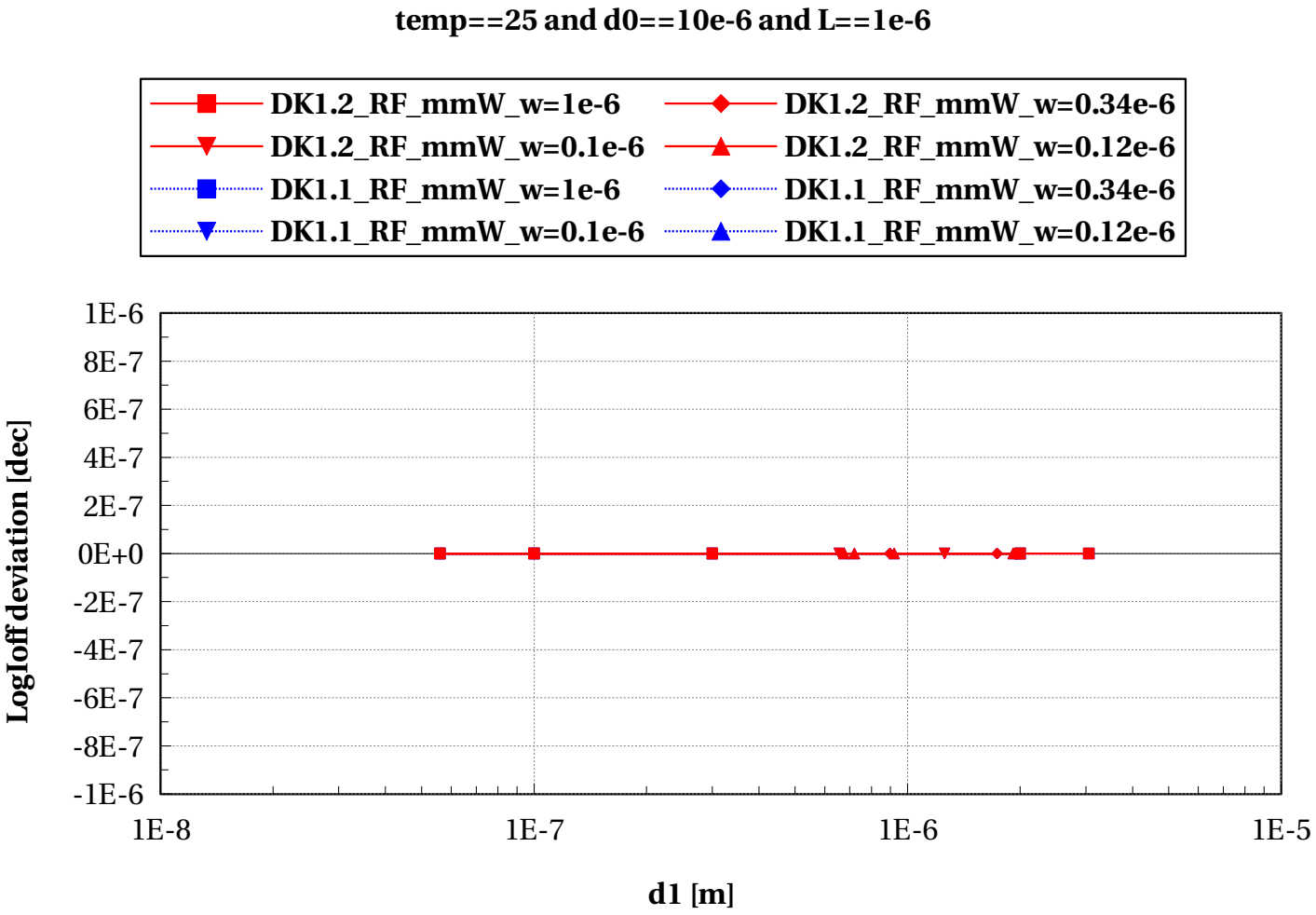
lvtnfet_acc, LogIoff deviation [dec] vs d1 [m]



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- WPE effect w.r.t d1 - Lscaling at W=0.1u

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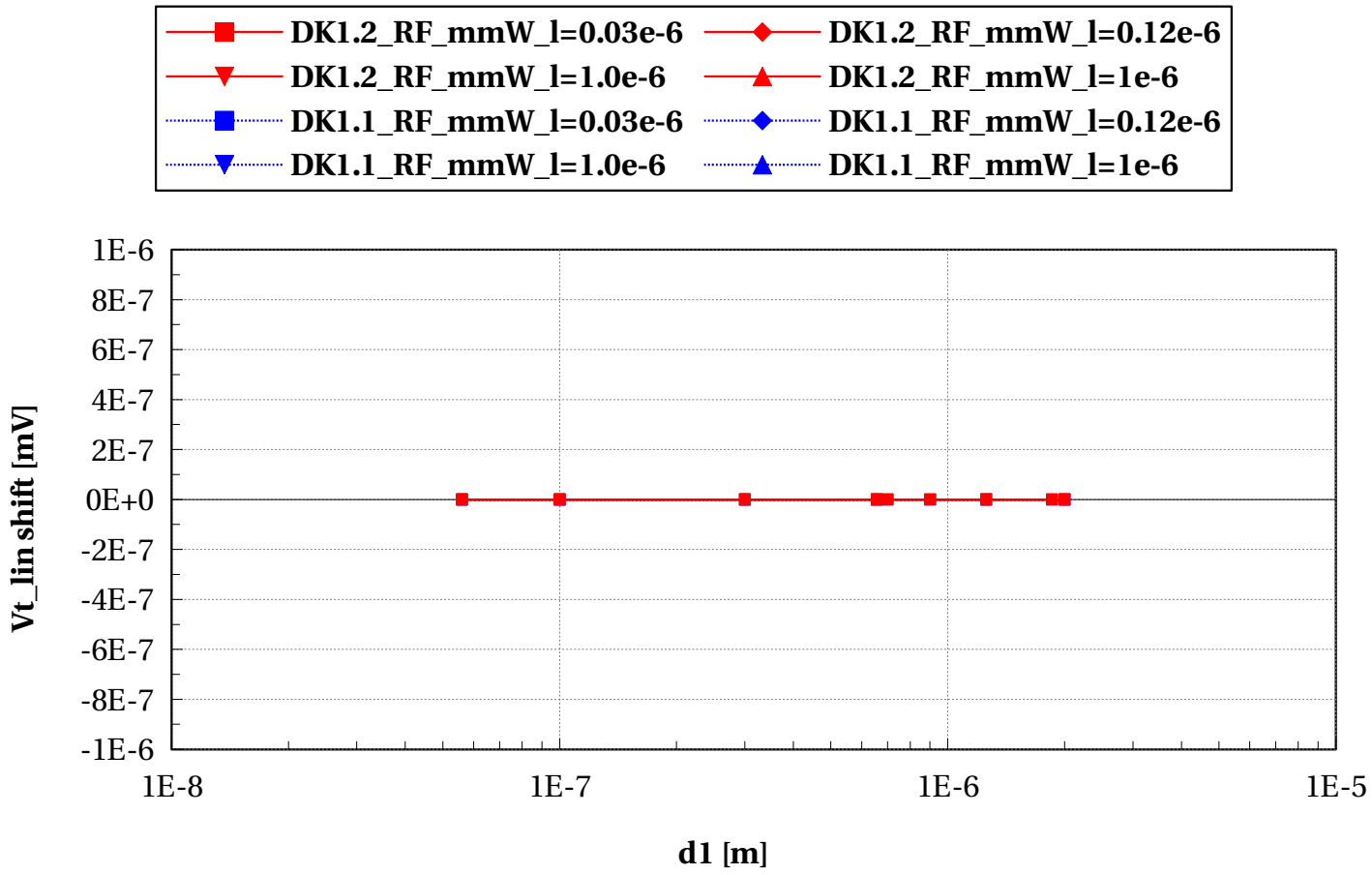
lvtnfet_acc, Vt_lin shift [mV] vs d1 [m]

temp==25 and d0==10e-6 and W==0.1e-6



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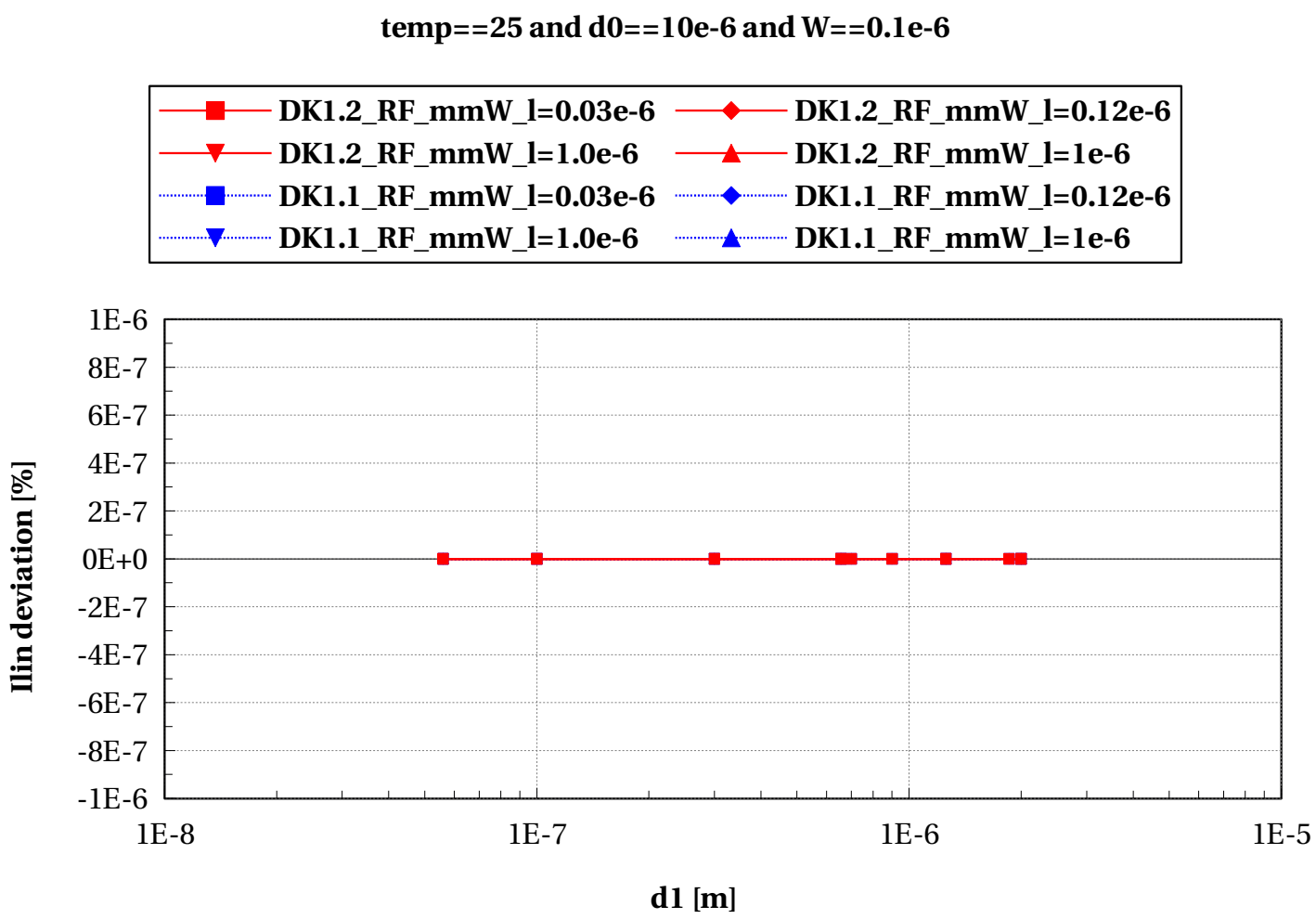
lvtnfet_acc, Ilin deviation [%] vs d1 [m]



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lvtnfet_acc, Vt_sat shift [mV] vs d1 [m]

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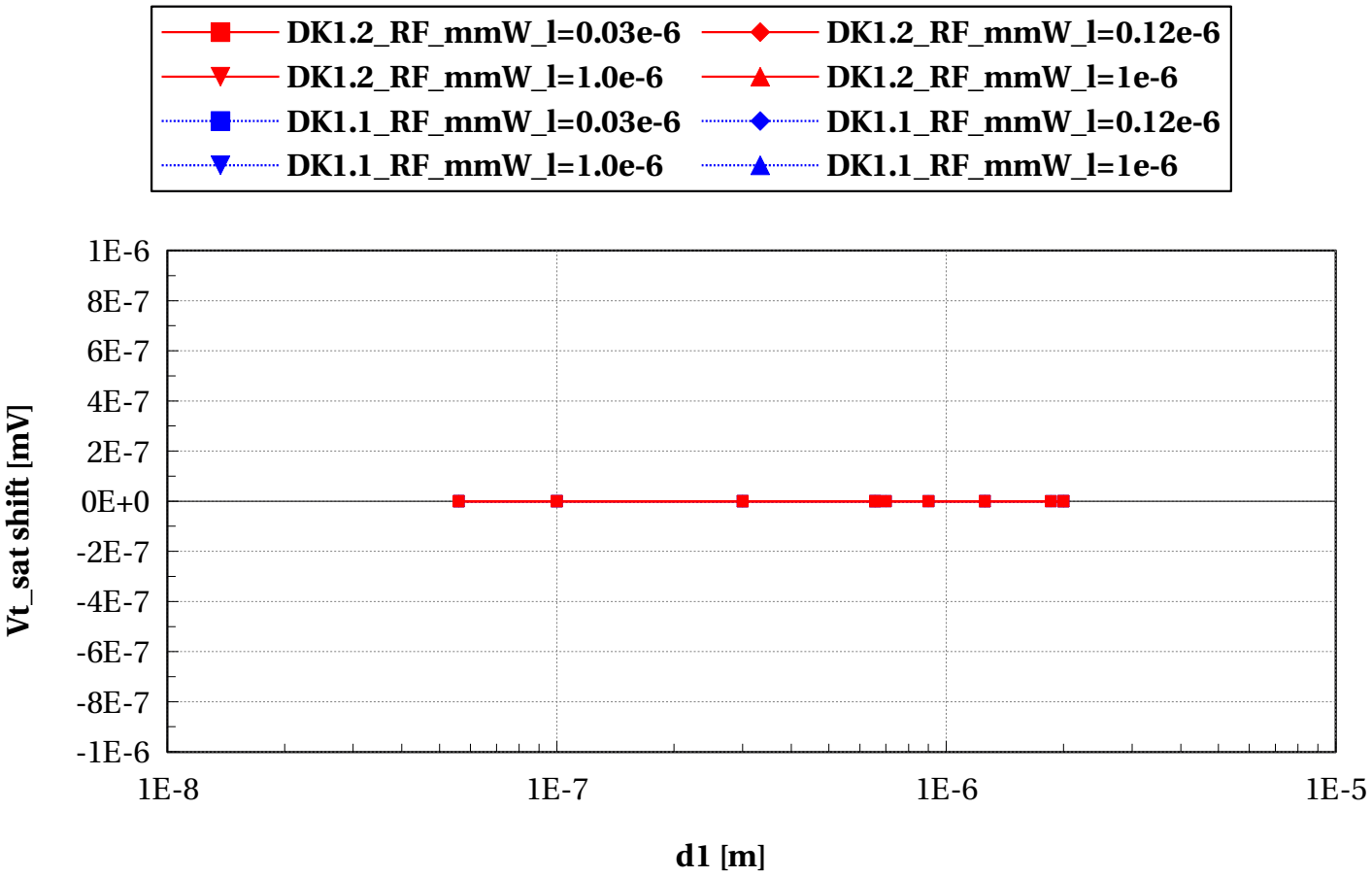

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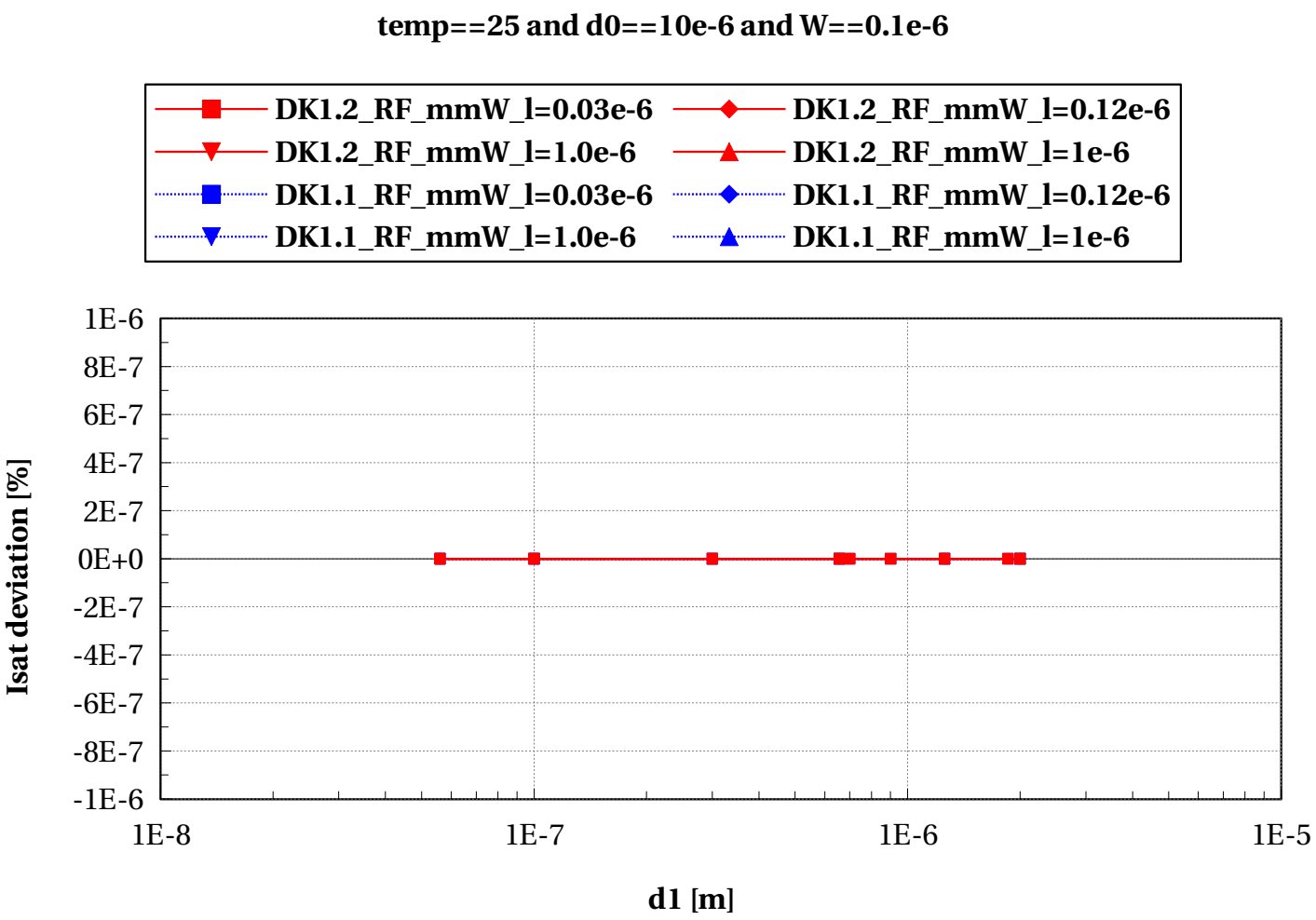
lvtnfet_acc, Isat deviation [%] vs d1 [m]



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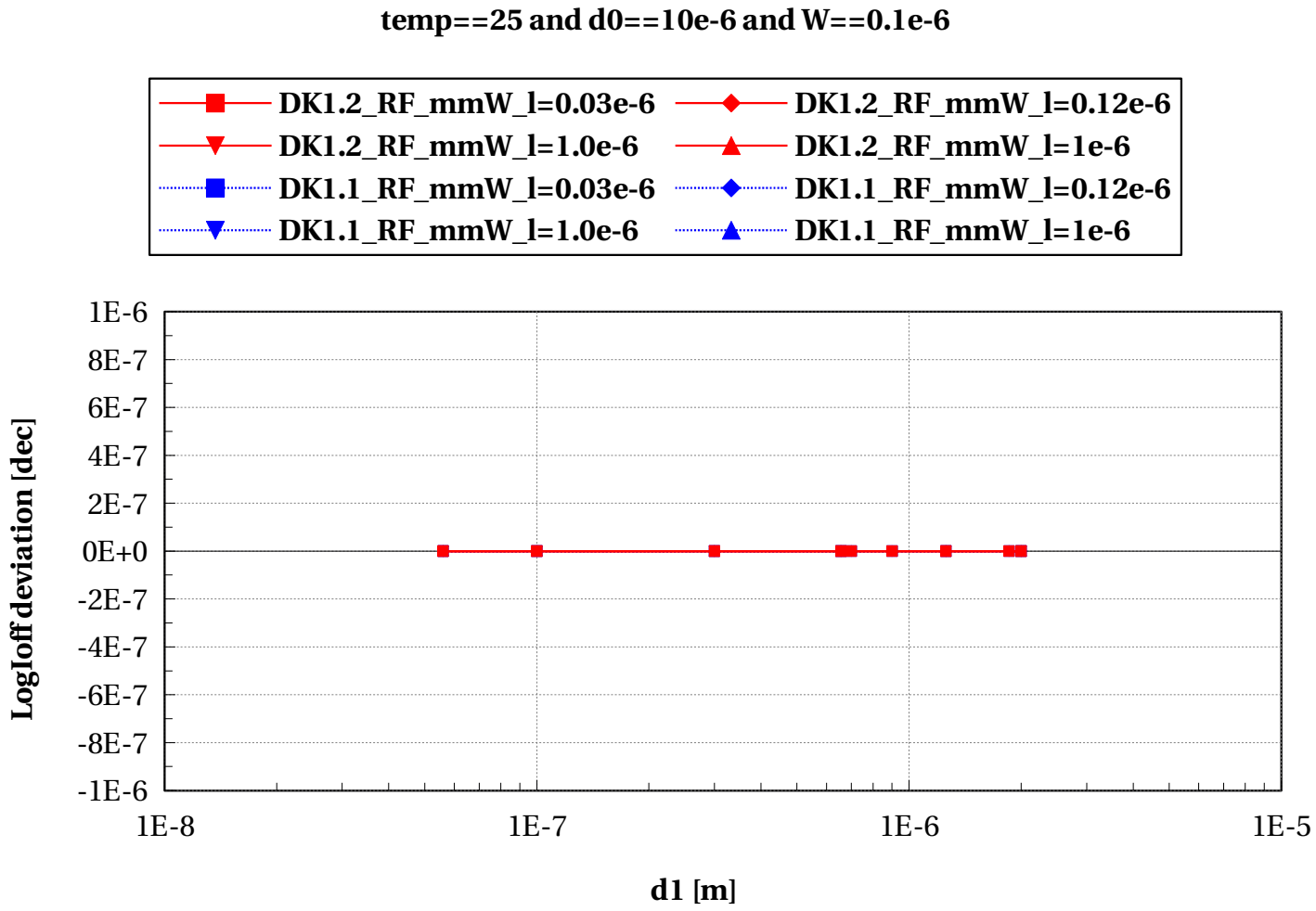
lvtnfet_acc, LogIoff deviation [dec] vs d1 [m]



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- WPE effect w.r.t d1 - Lscaling at W=1u

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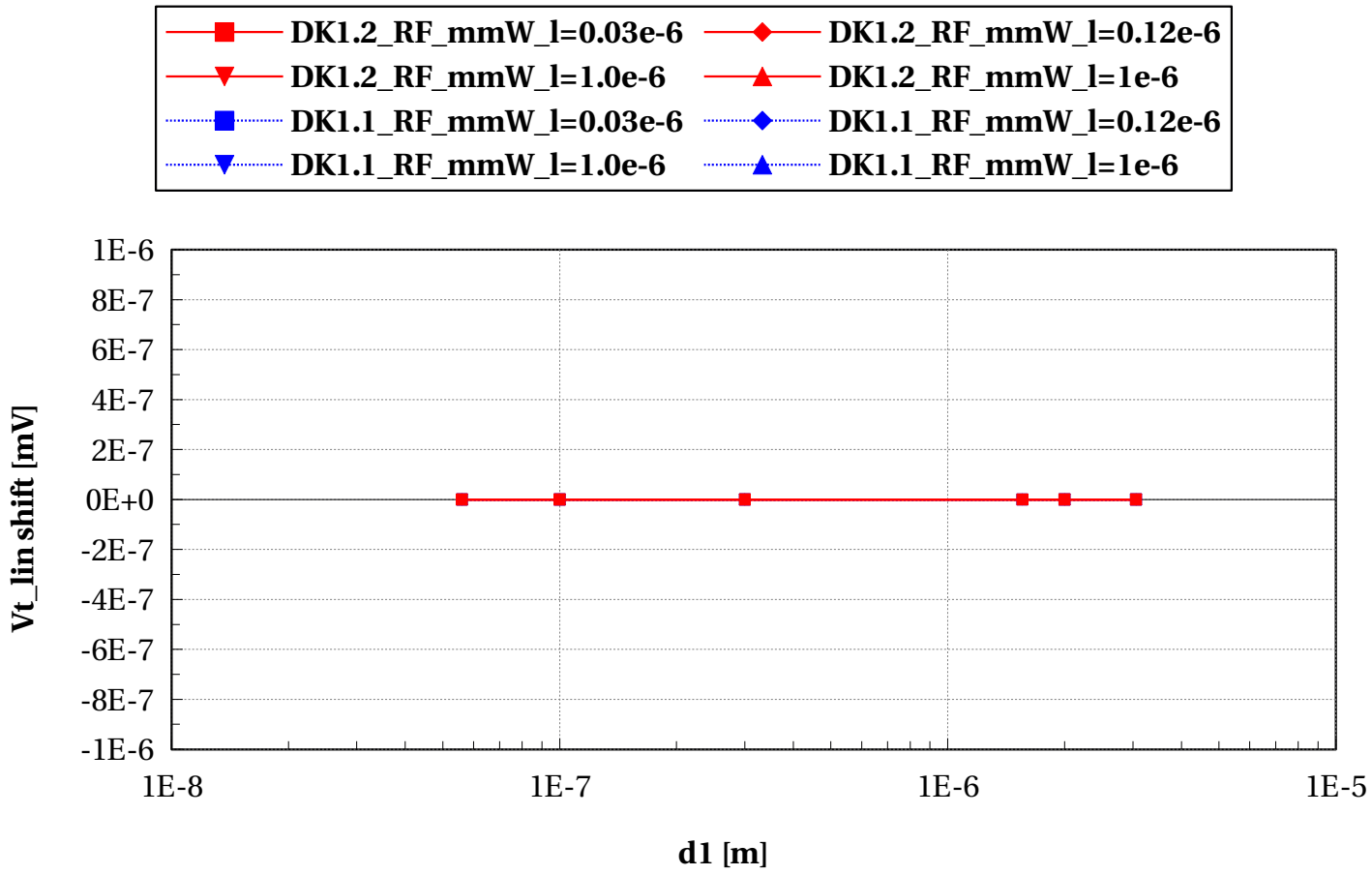
lvtnfet_acc, Vt_lin shift [mV] vs d1 [m]

temp==25 and d0==10e-6 and W==1e-6



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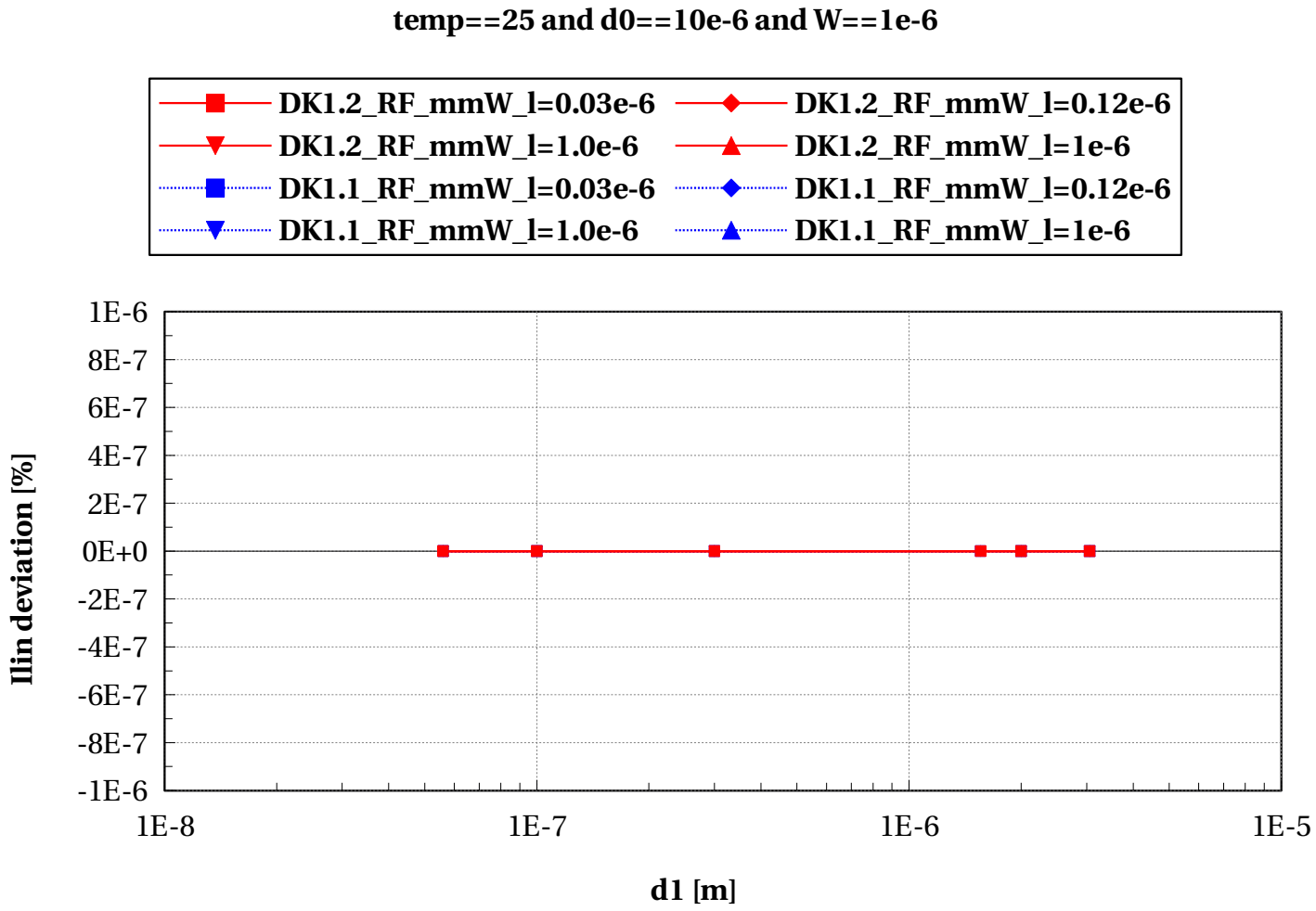
lvtnfet_acc, Ilin deviation [%] vs d1 [m]



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lvtnfet_acc, Vt_sat shift [mV] vs d1 [m]

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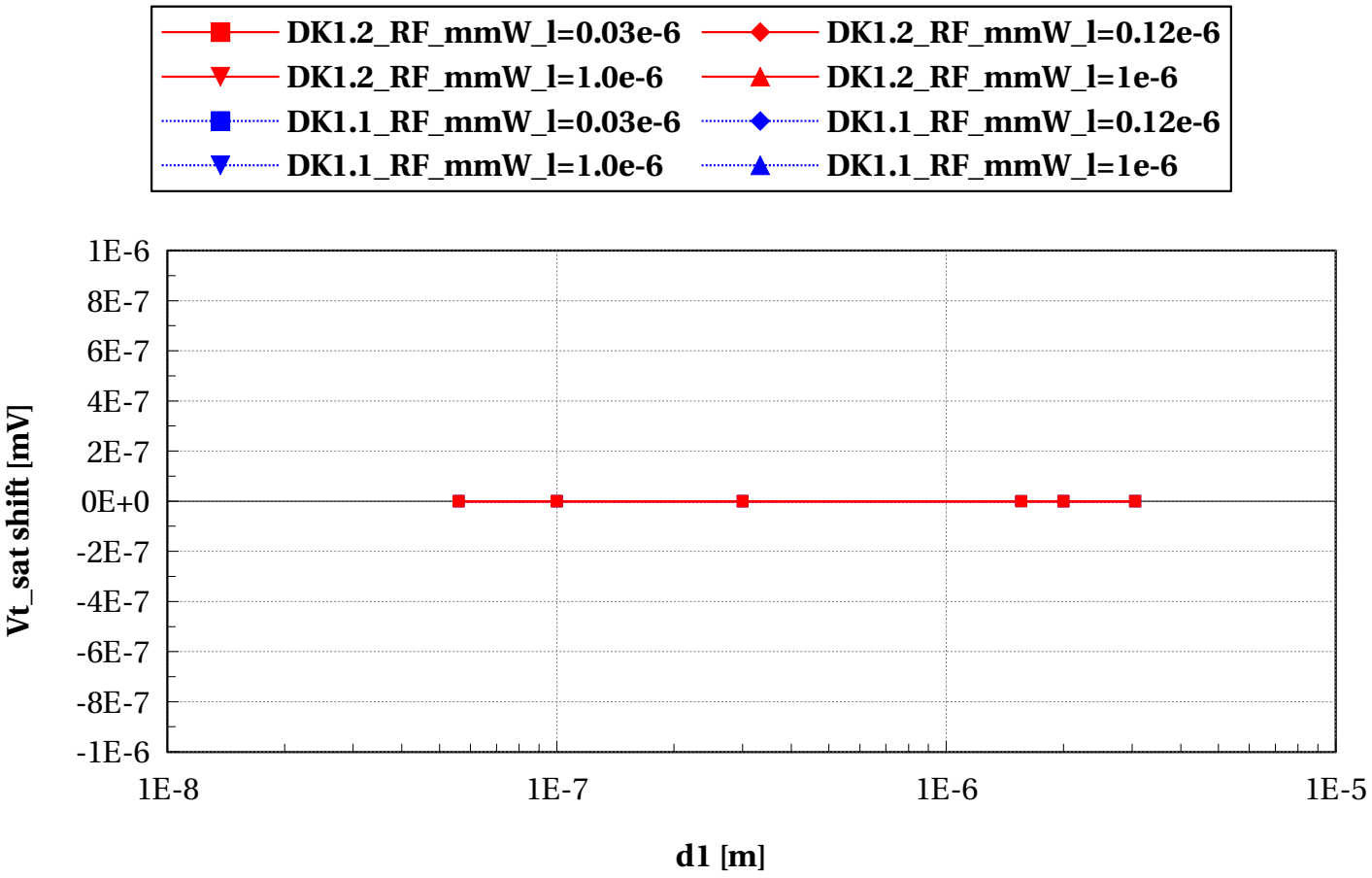

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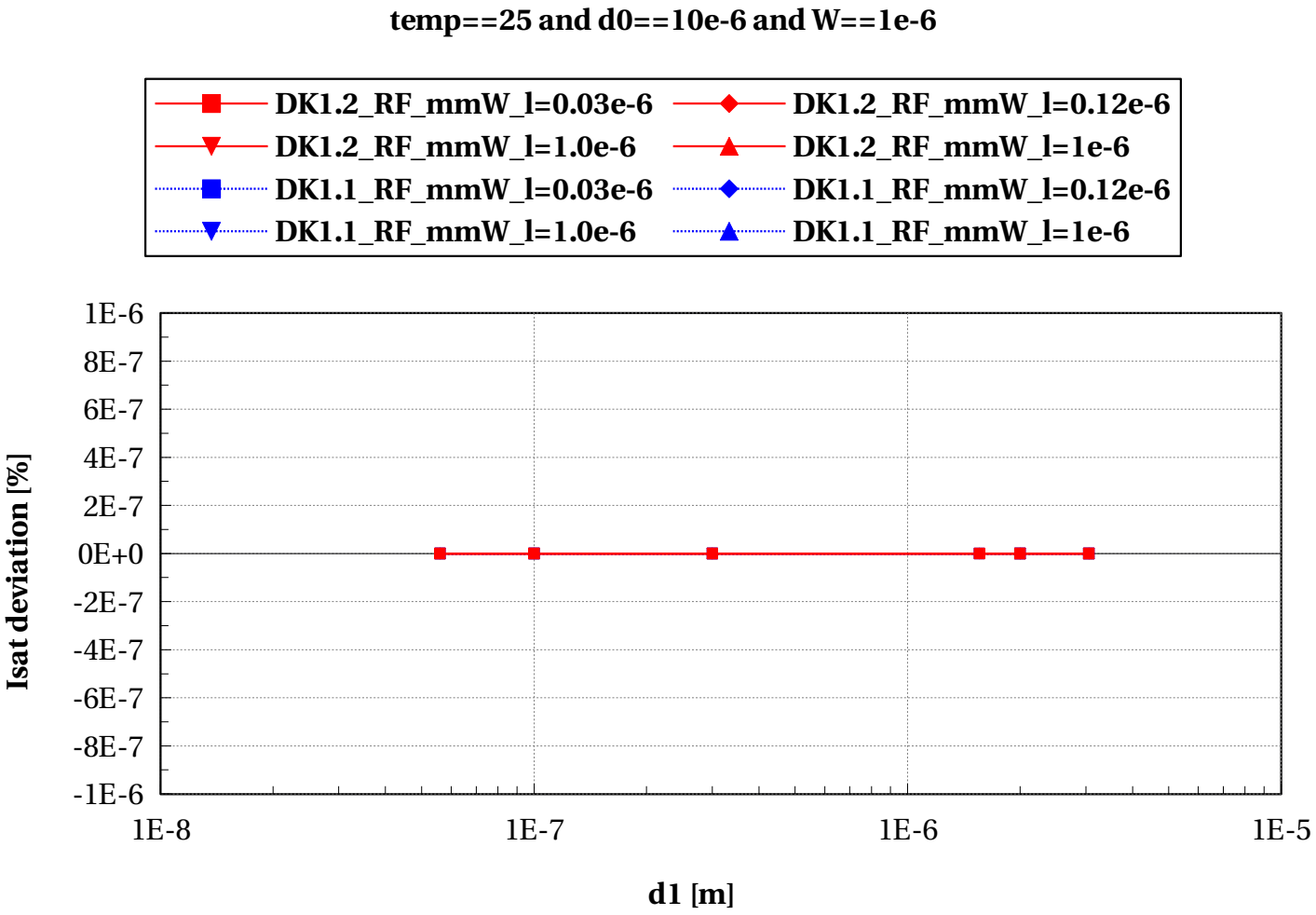
lvtnfet_acc, Isat deviation [%] vs d1 [m]



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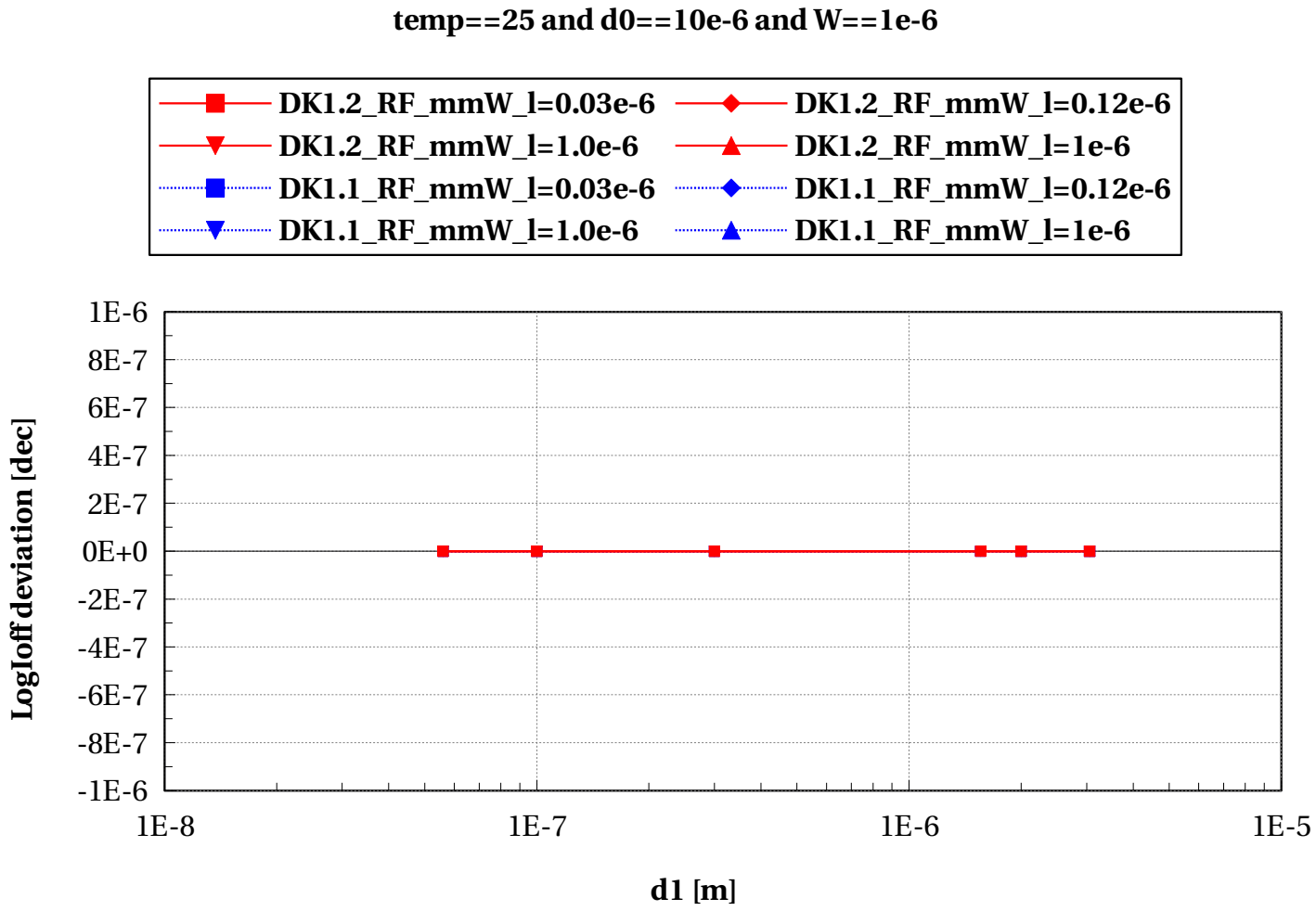
lvtnfet_acc, LogIoff deviation [dec] vs d1 [m]



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lvtpfet_acc
Electrical characteristics scaling

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- WPE effect w.r.t d0 - Lscaling at W=1u

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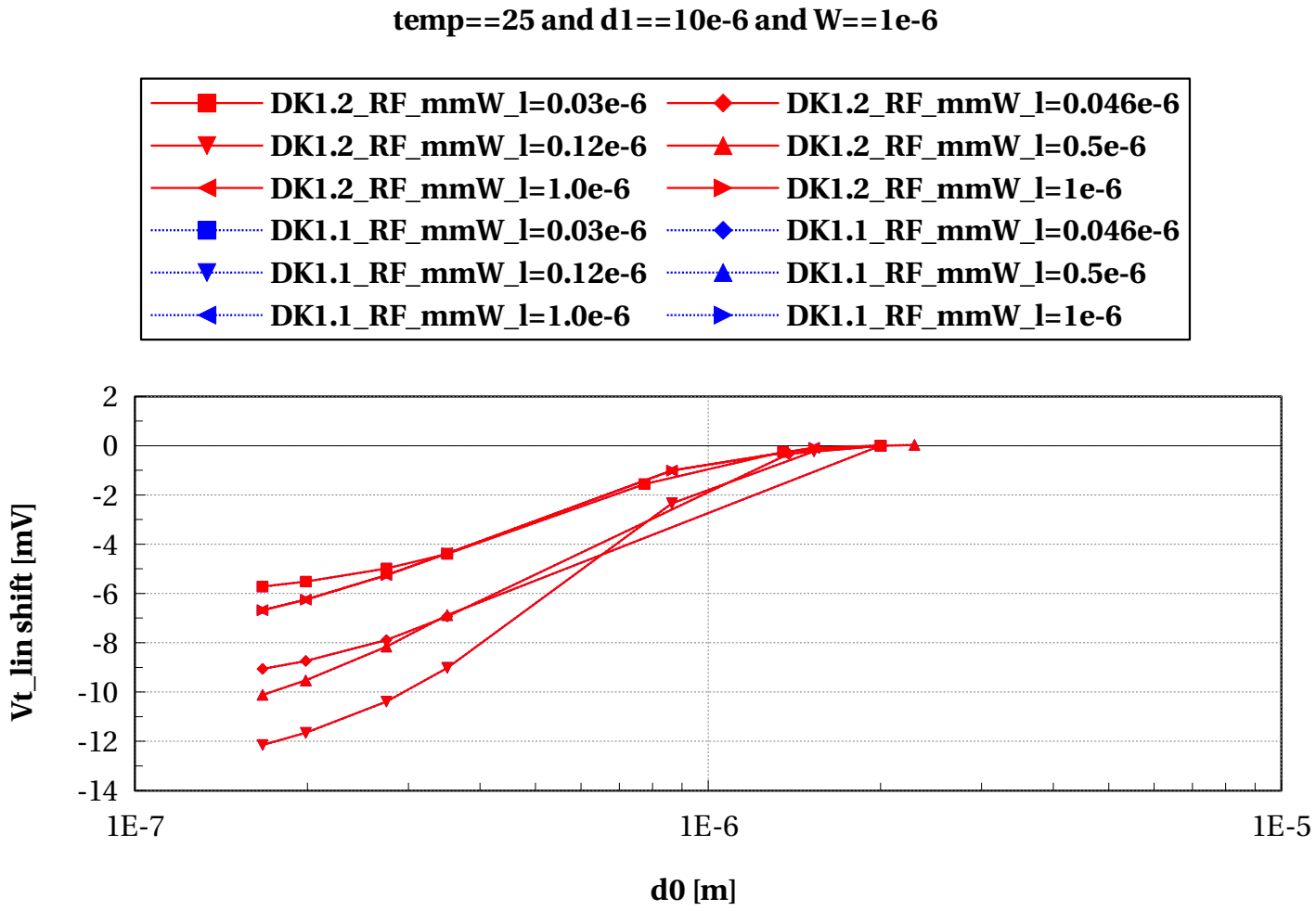
lvtpfet_acc, Vt_lin shift [mV] vs d0 [m]



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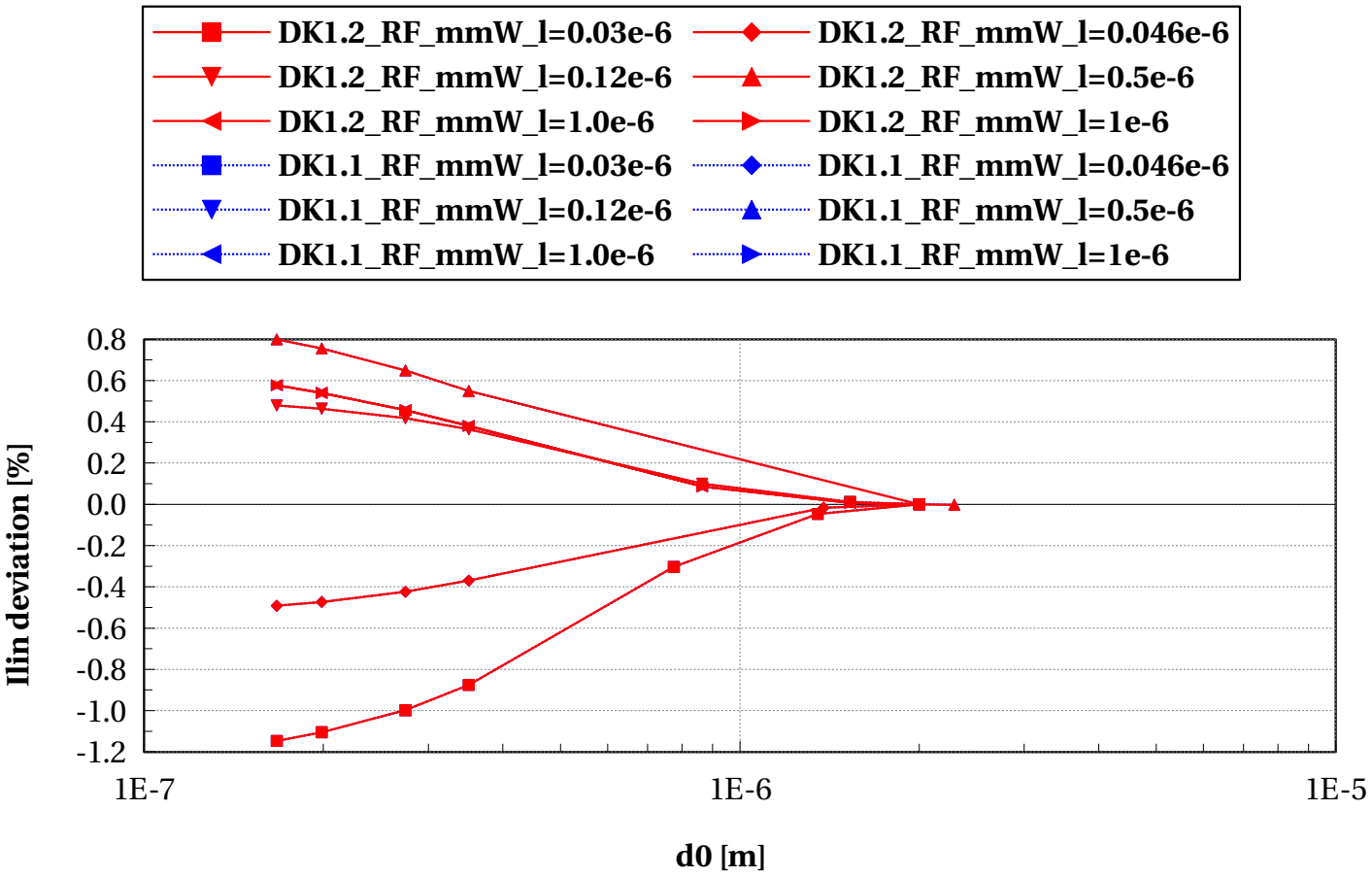
lvtpfet_acc, Ilin deviation [%] vs d0 [m]

temp==25 and d1==10e-6 and W==1e-6



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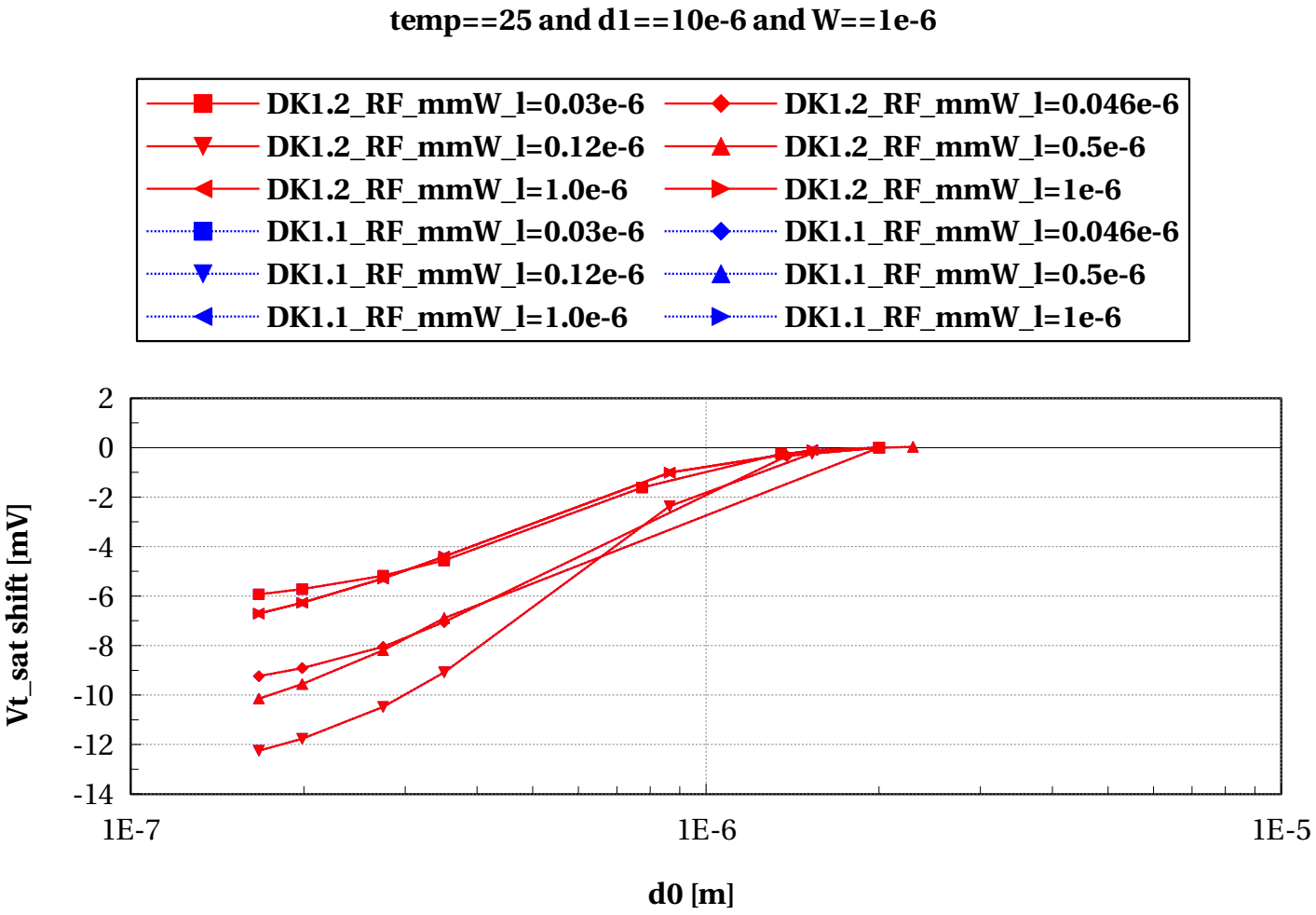
lvtpfet_acc, Vt_sat shift [mV] vs d0 [m]



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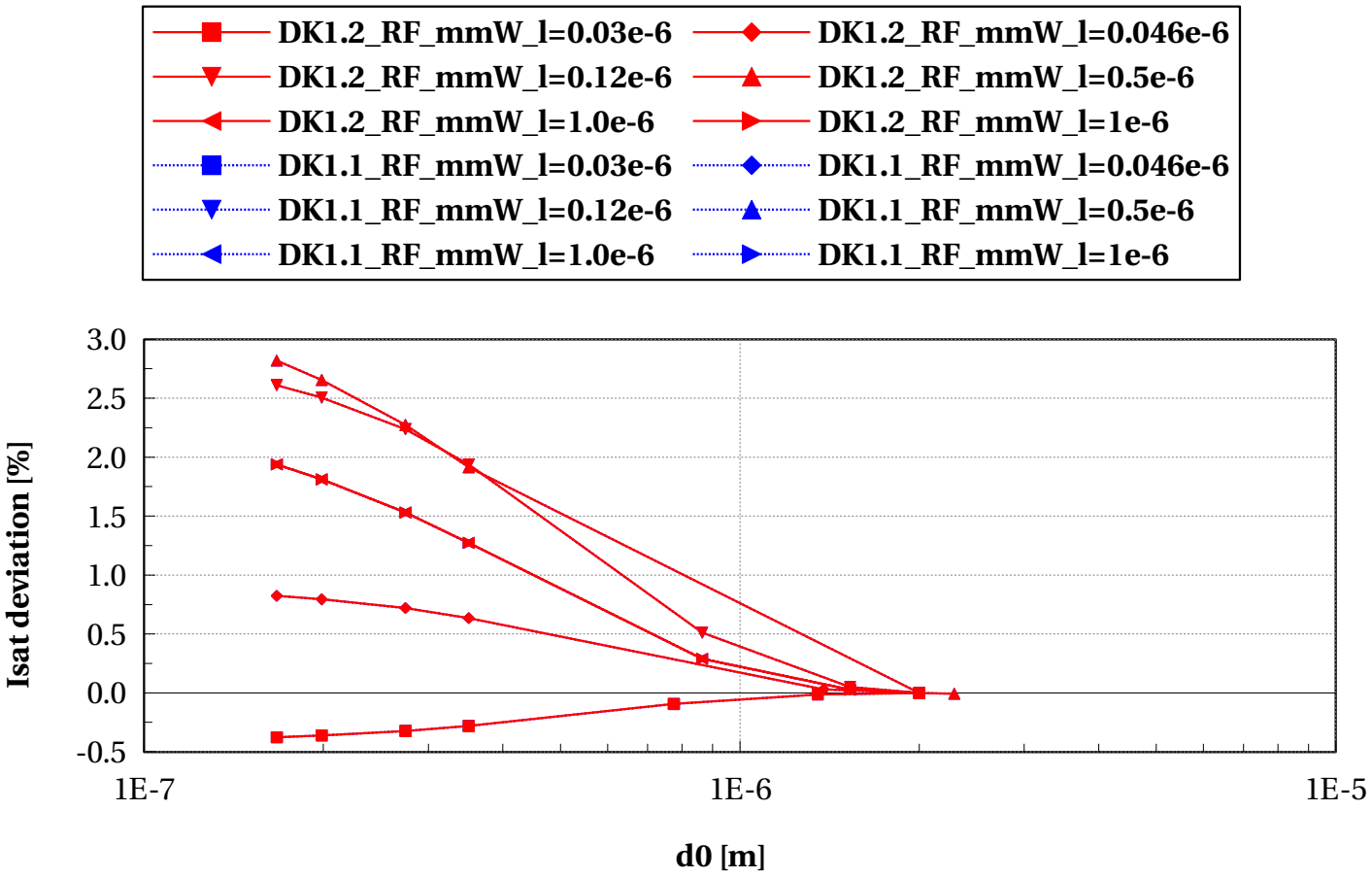
lvtpfet_acc, Isat deviation [%] vs d0 [m]

temp==25 and d1==10e-6 and W==1e-6



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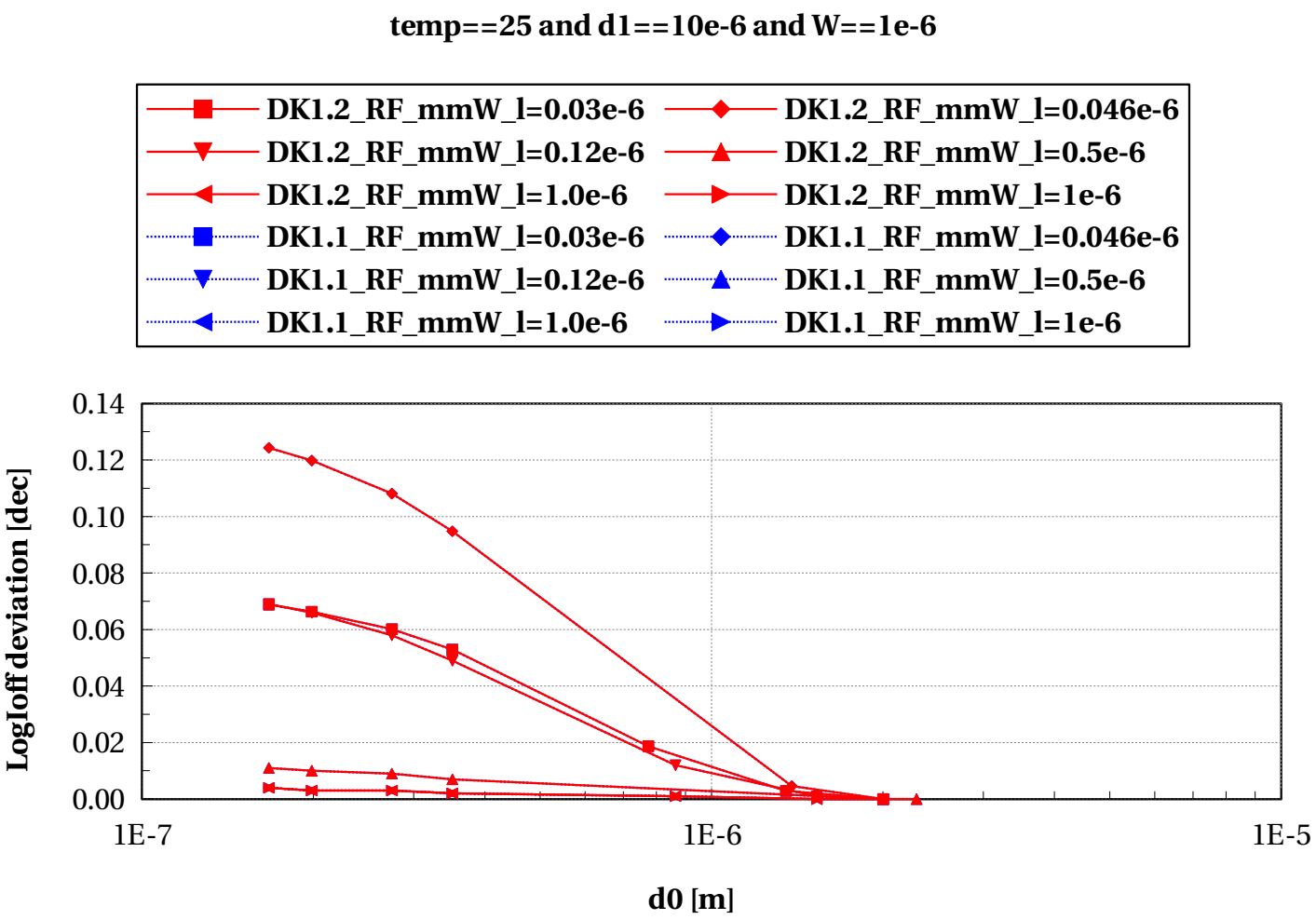
lvtpfet_acc, LogIoff deviation [dec] vs d0 [m]



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- WPE effect w.r.t d0 - Lscaling at W=0.34u

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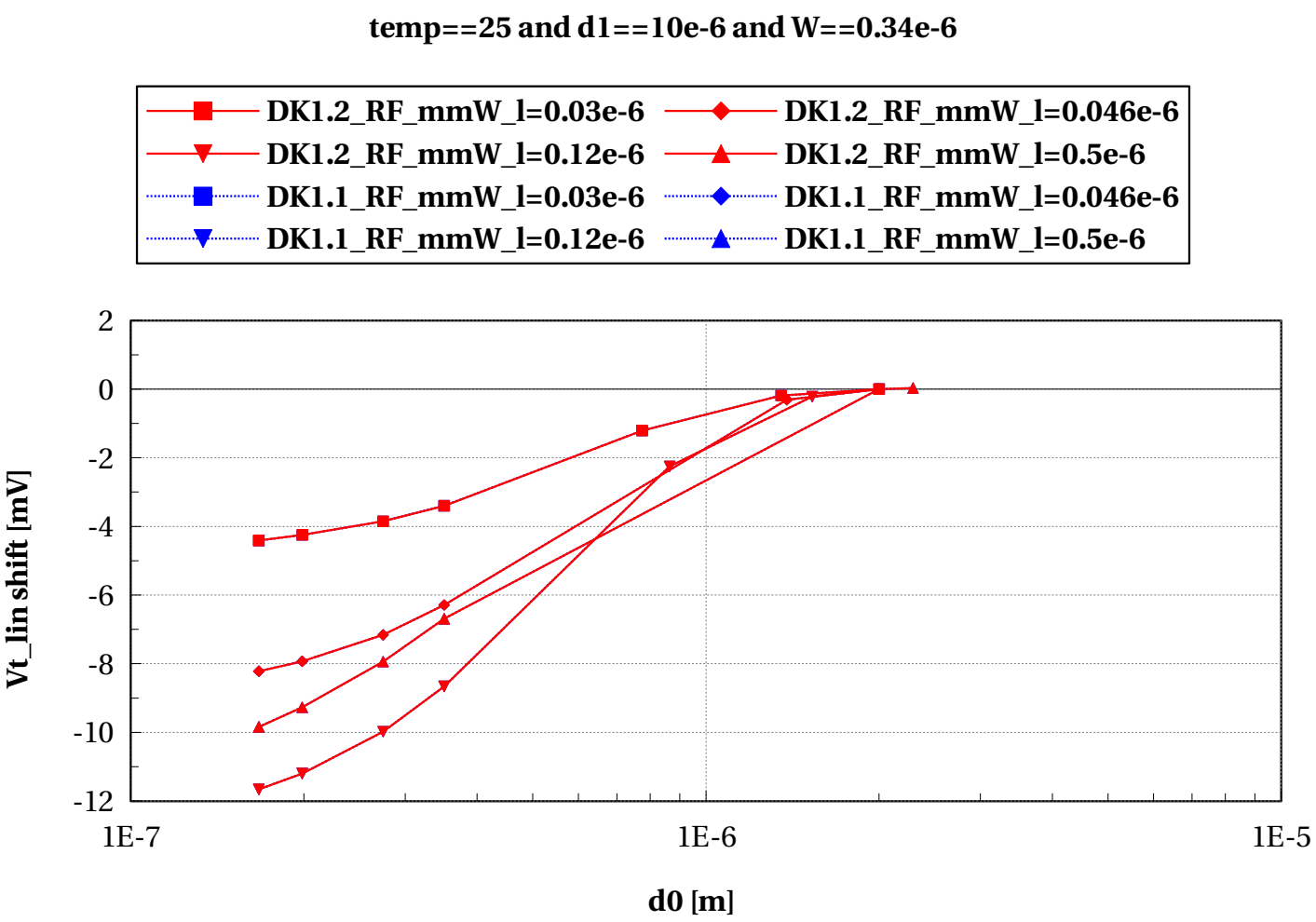
lvtpfet_acc, Vt_lin shift [mV] vs d0 [m]



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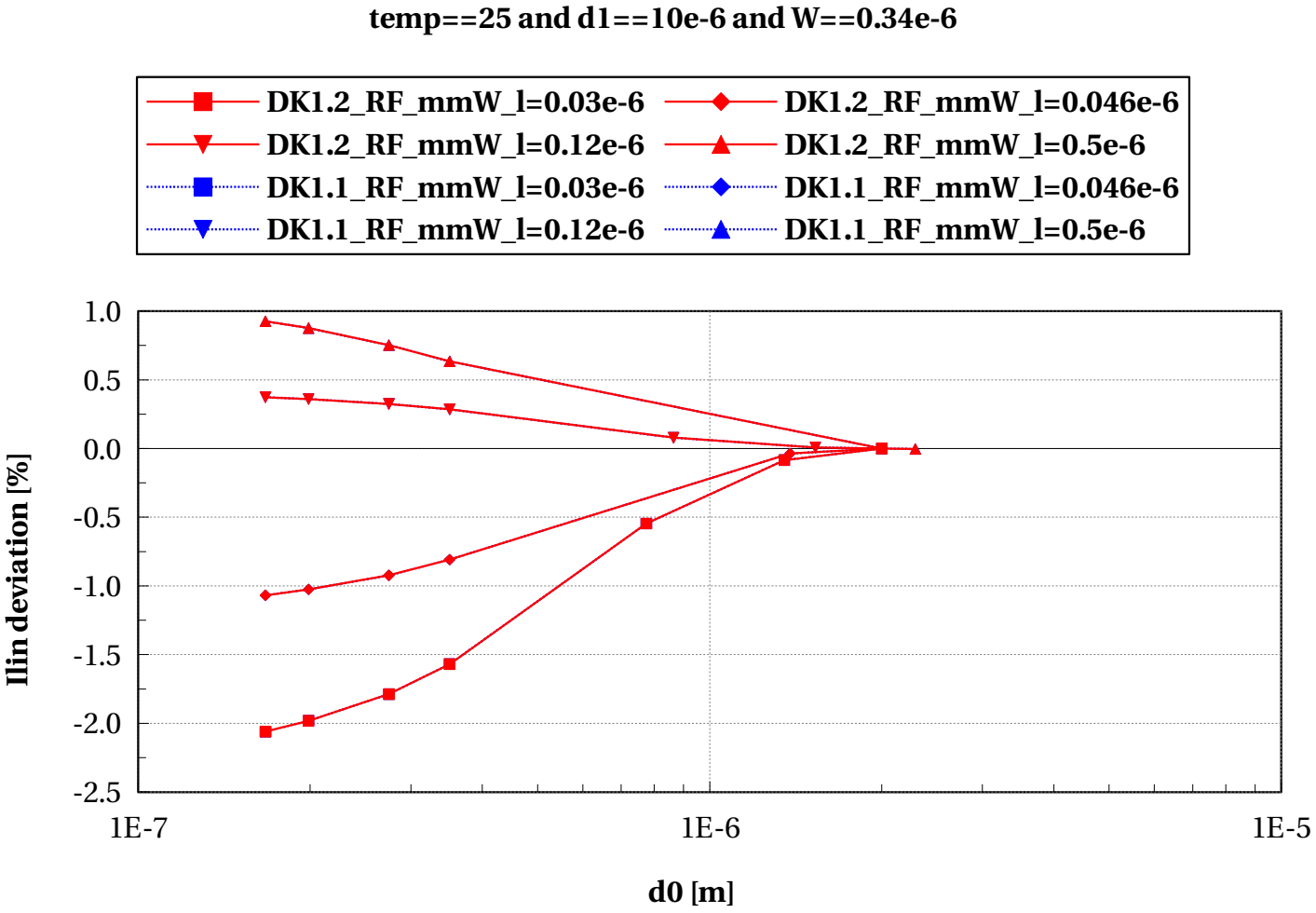
lvtpfet_acc, Ilin deviation [%] vs d0 [m]



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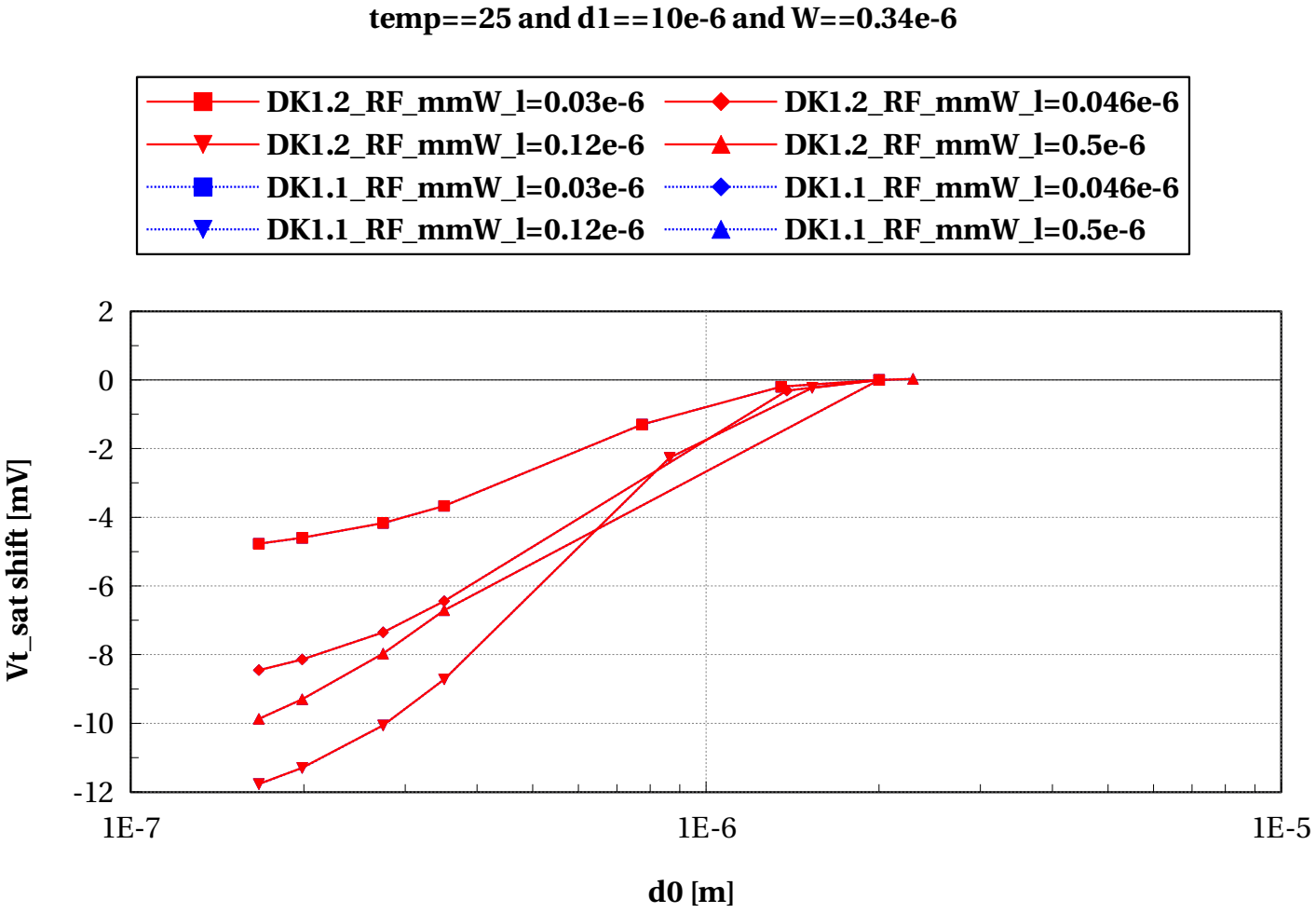
lvtpfet_acc, Vt_sat shift [mV] vs d0 [m]



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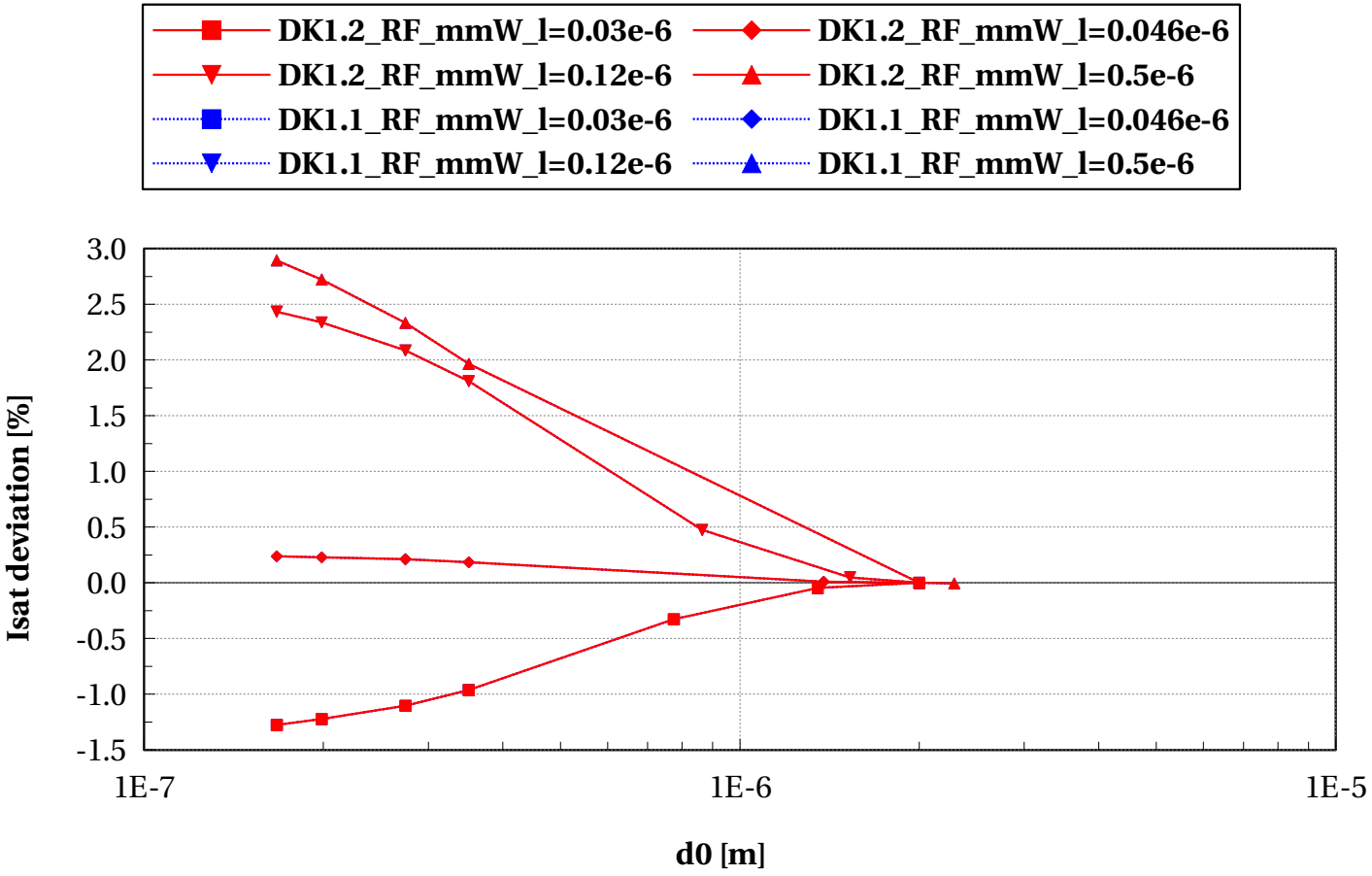
lvtpfet_acc, Isat deviation [%] vs d0 [m]

temp==25 and d1==10e-6 and W==0.34e-6



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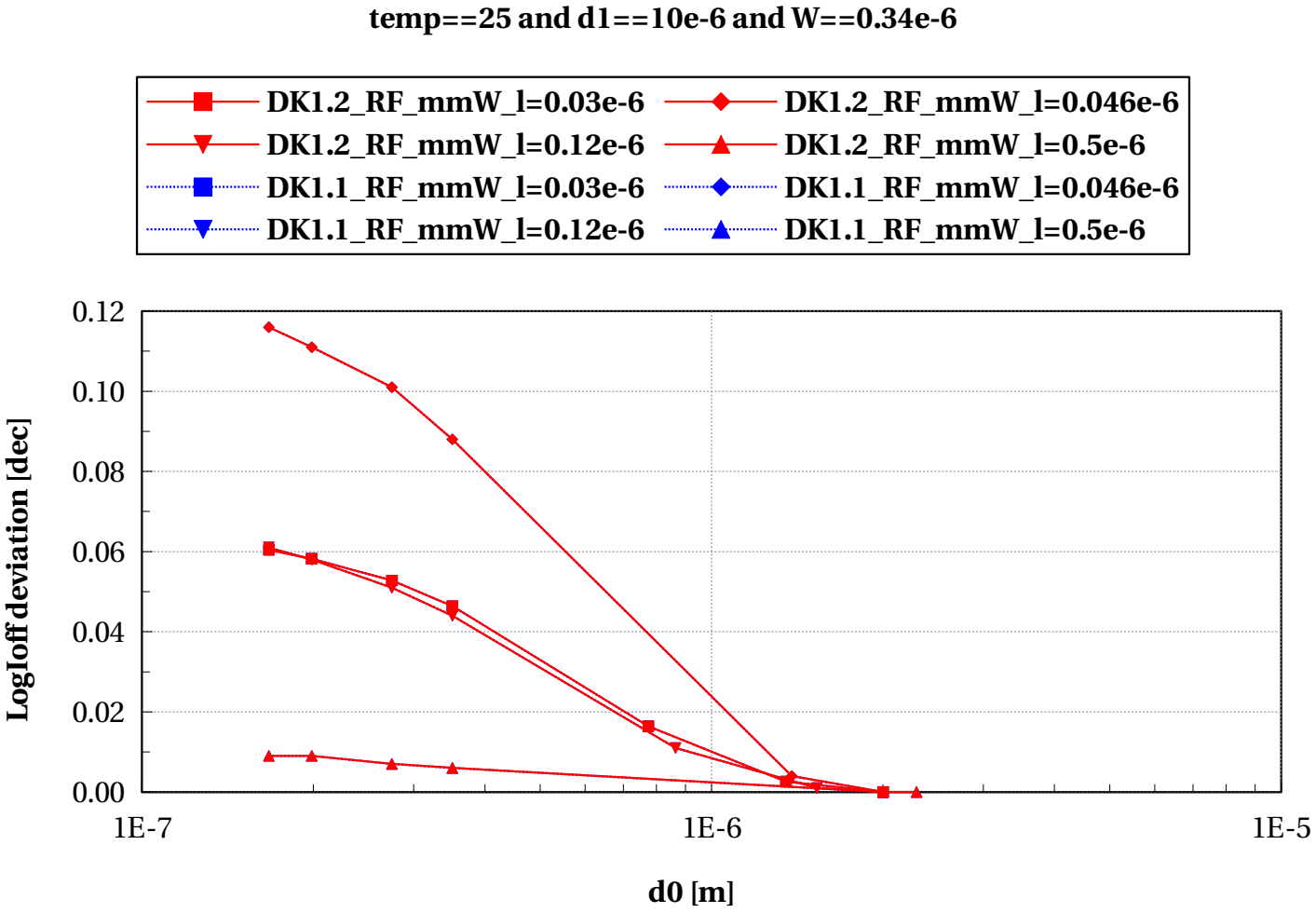
lvtpfet_acc, LogIoff deviation [dec] vs d0 [m]



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- WPE effect w.r.t d0 - Lscaling at W=0.1u

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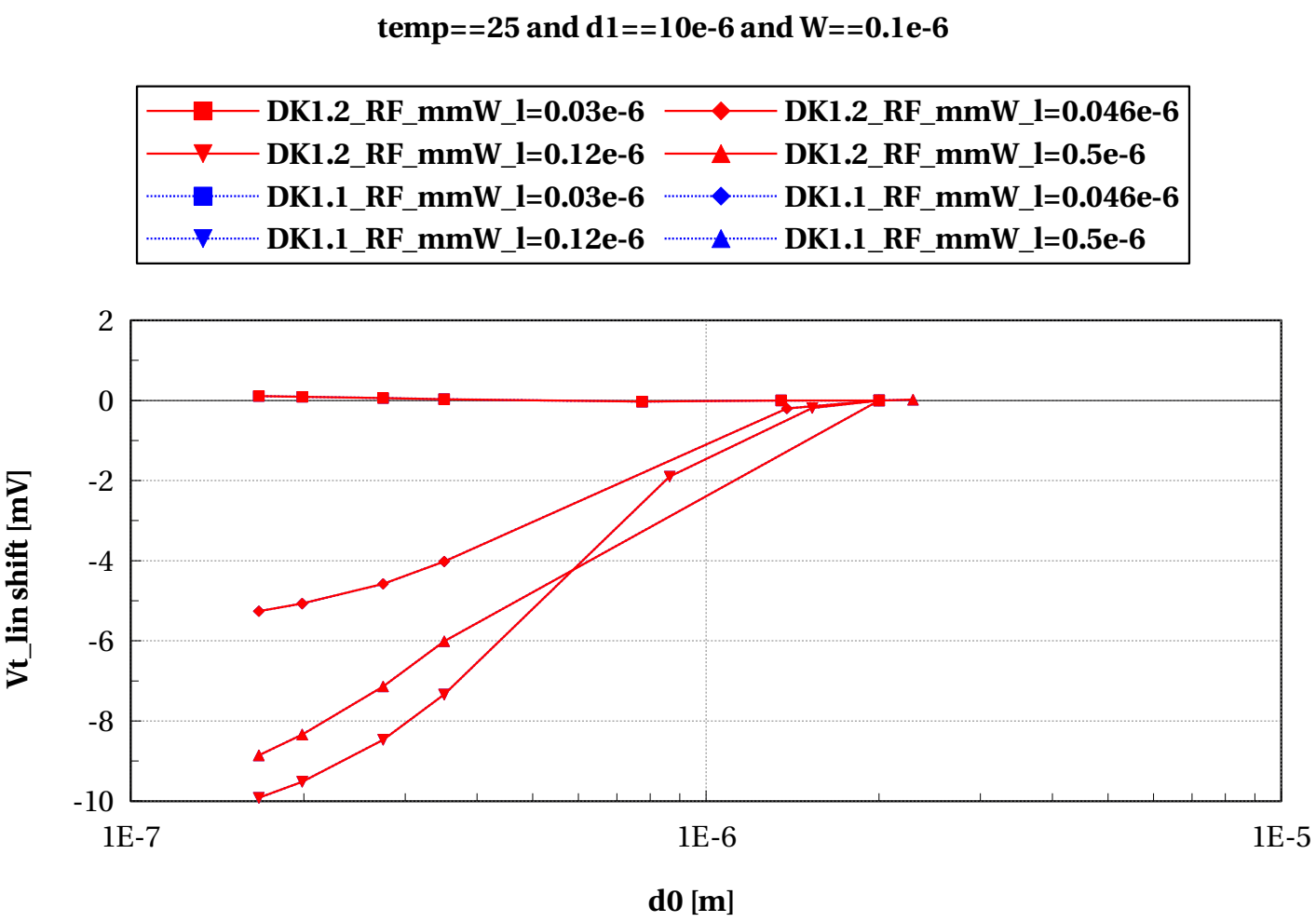
life.augmented

lvtpfet_acc, Vt_lin shift [mV] vs d0 [m]



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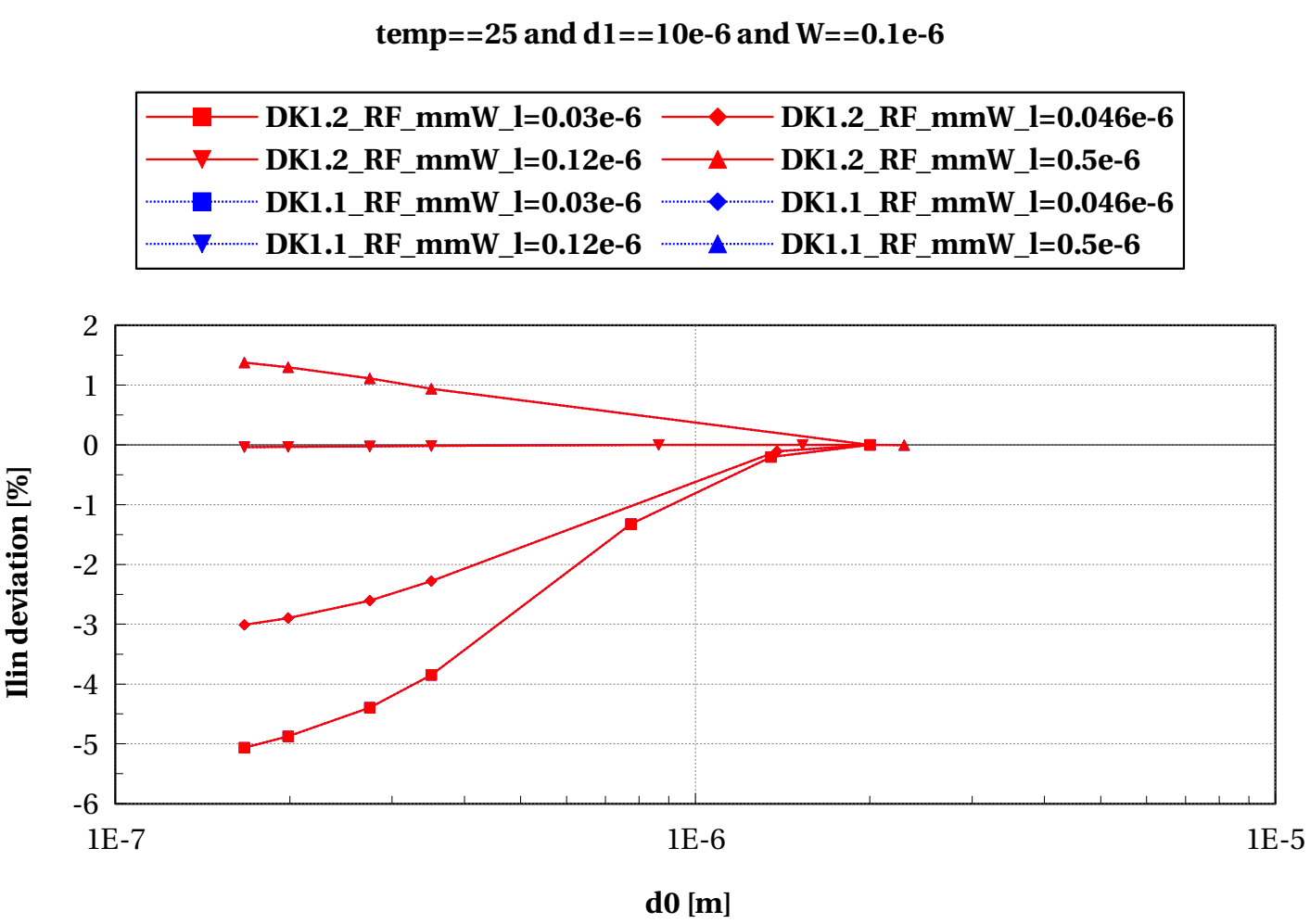
lvtpfet_acc, Ilin deviation [%] vs d0 [m]



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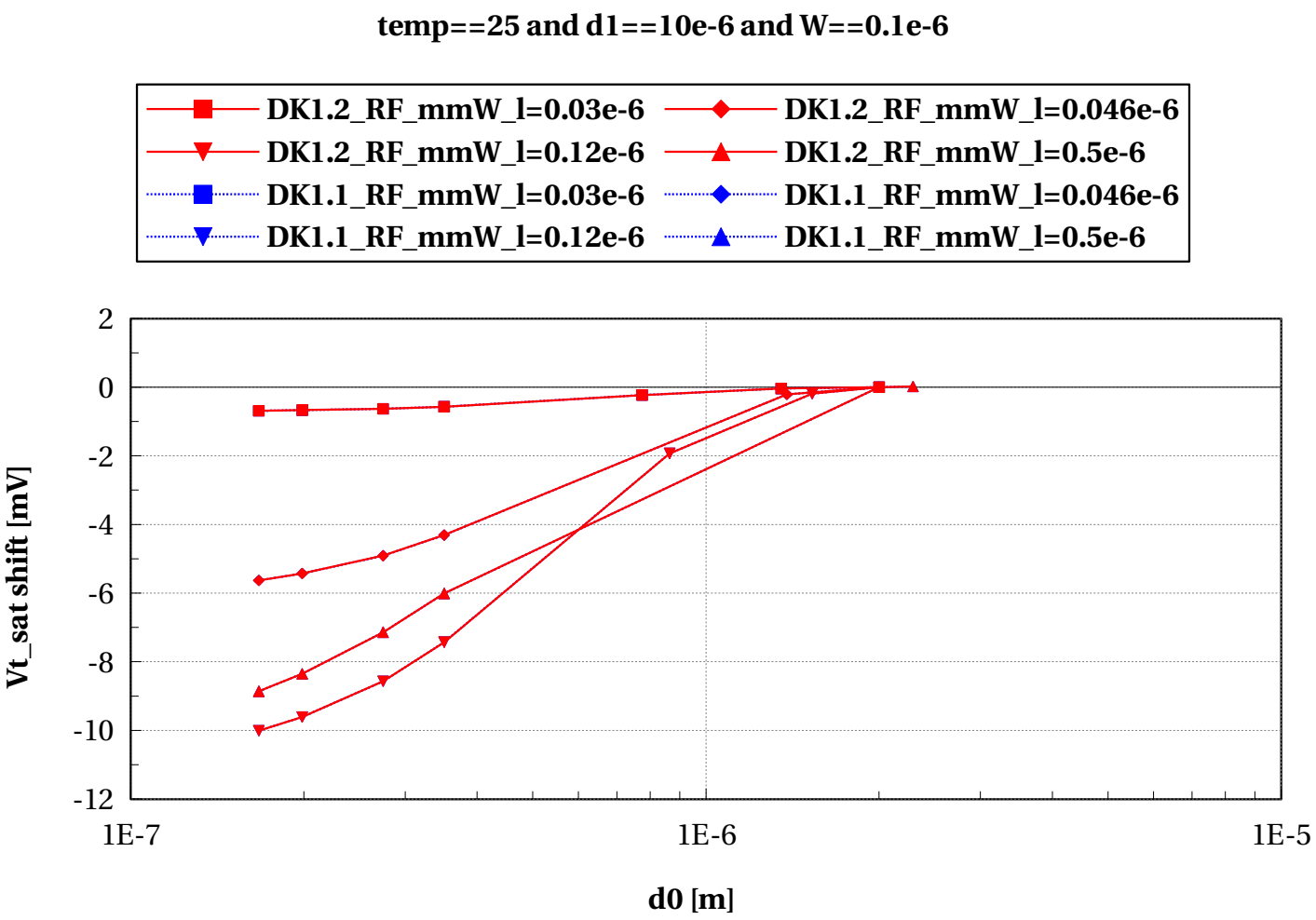
lvtpfet_acc, Vt_sat shift [mV] vs d0 [m]



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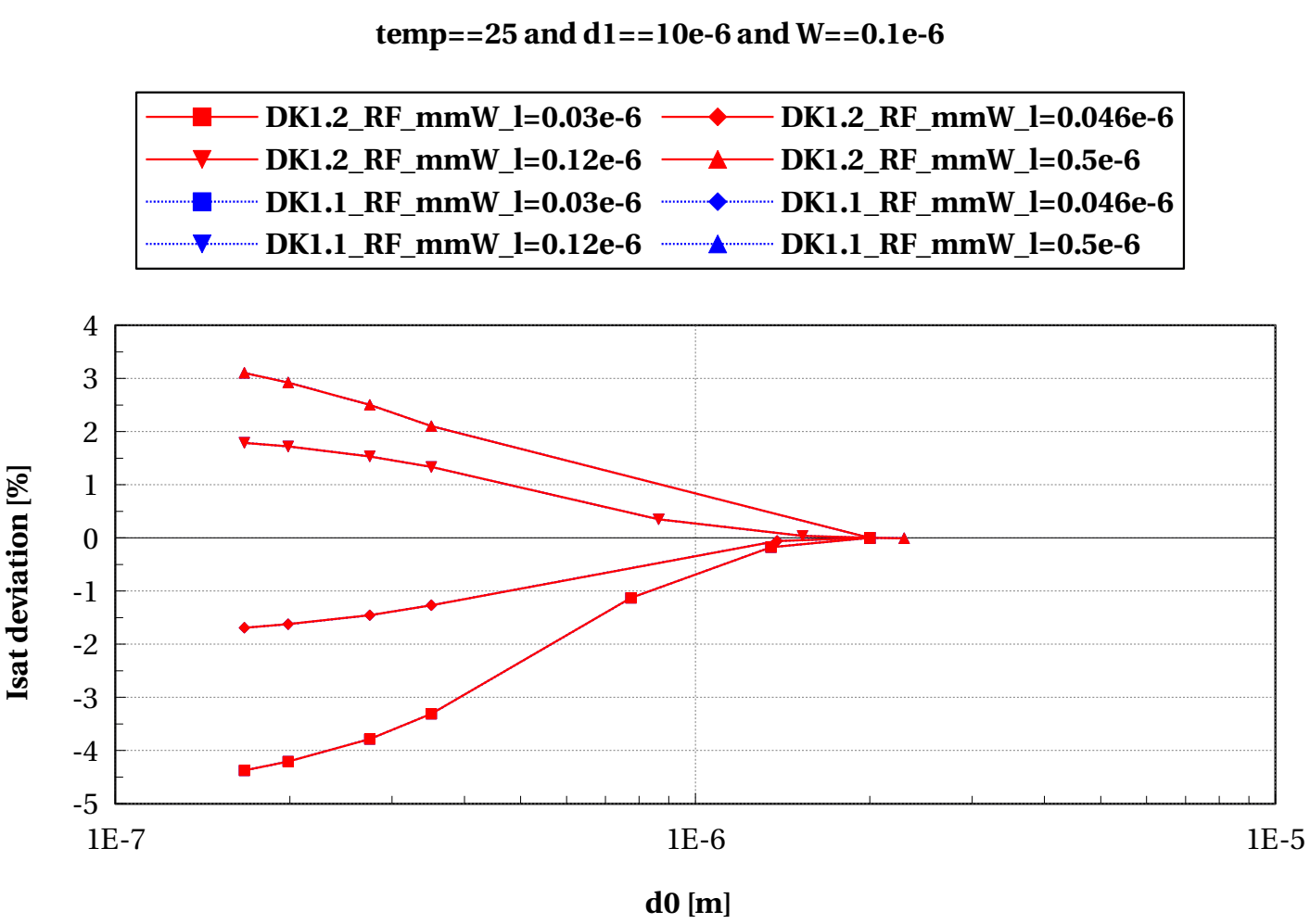
lvtpfet_acc, Isat deviation [%] vs d0 [m]



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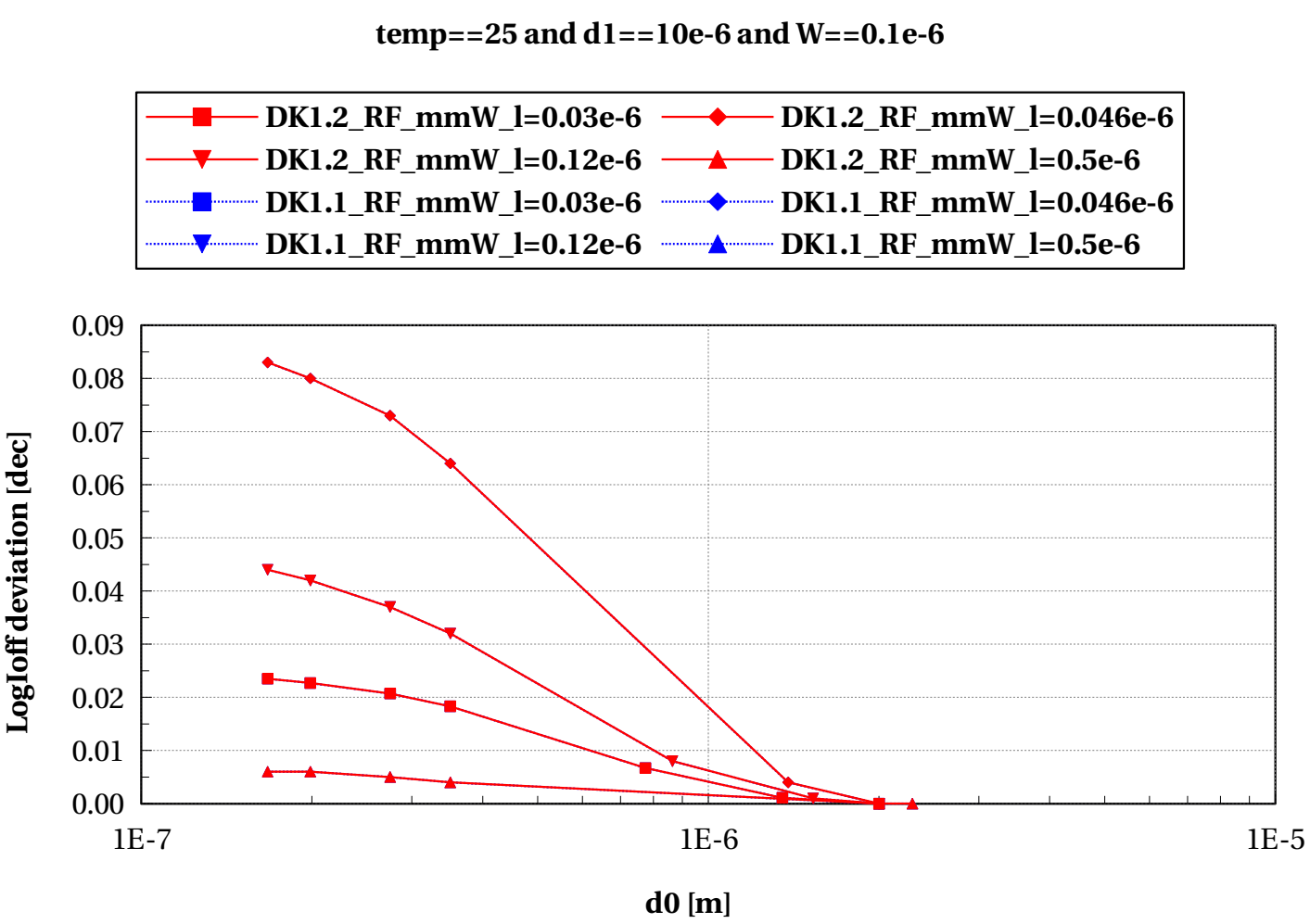
lvtpfet_acc, LogIoff deviation [dec] vs d0 [m]



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- WPE effect w.r.t d0 - Wscaling at L=0.03u

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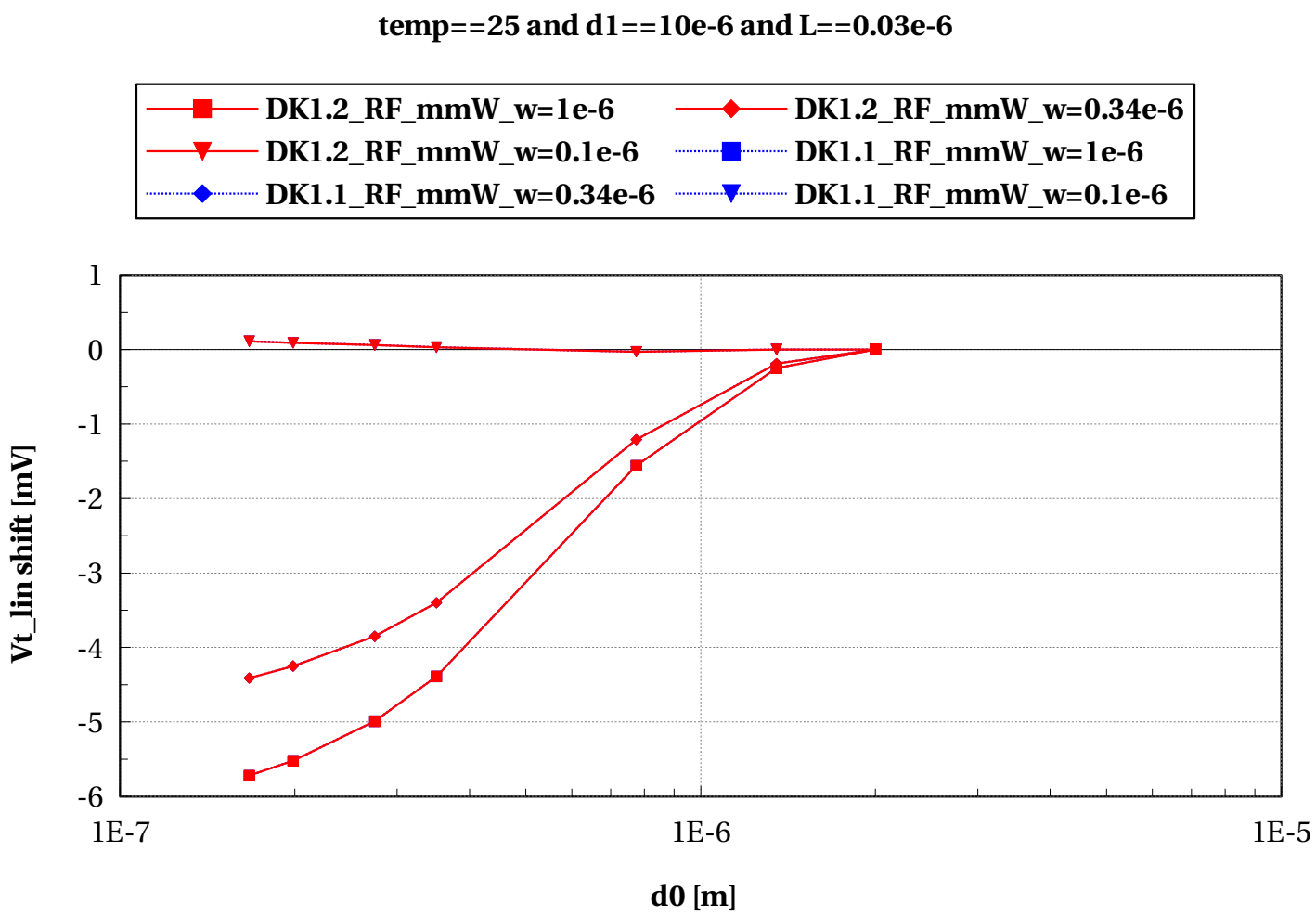
life.augmented

lvtpfet_acc, Vt_lin shift [mV] vs d0 [m]



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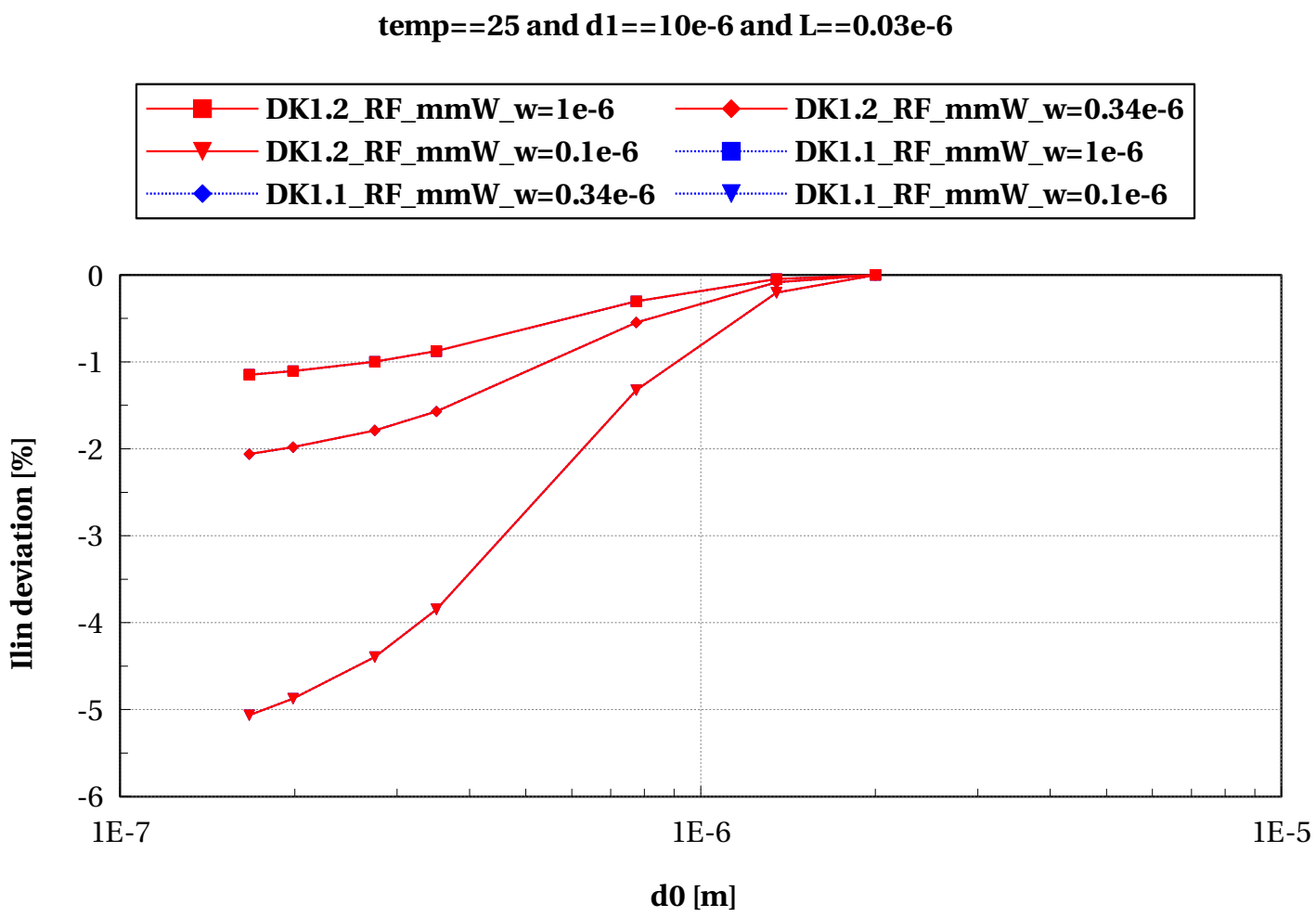
lvtpfet_acc, Ilin deviation [%] vs d0 [m]



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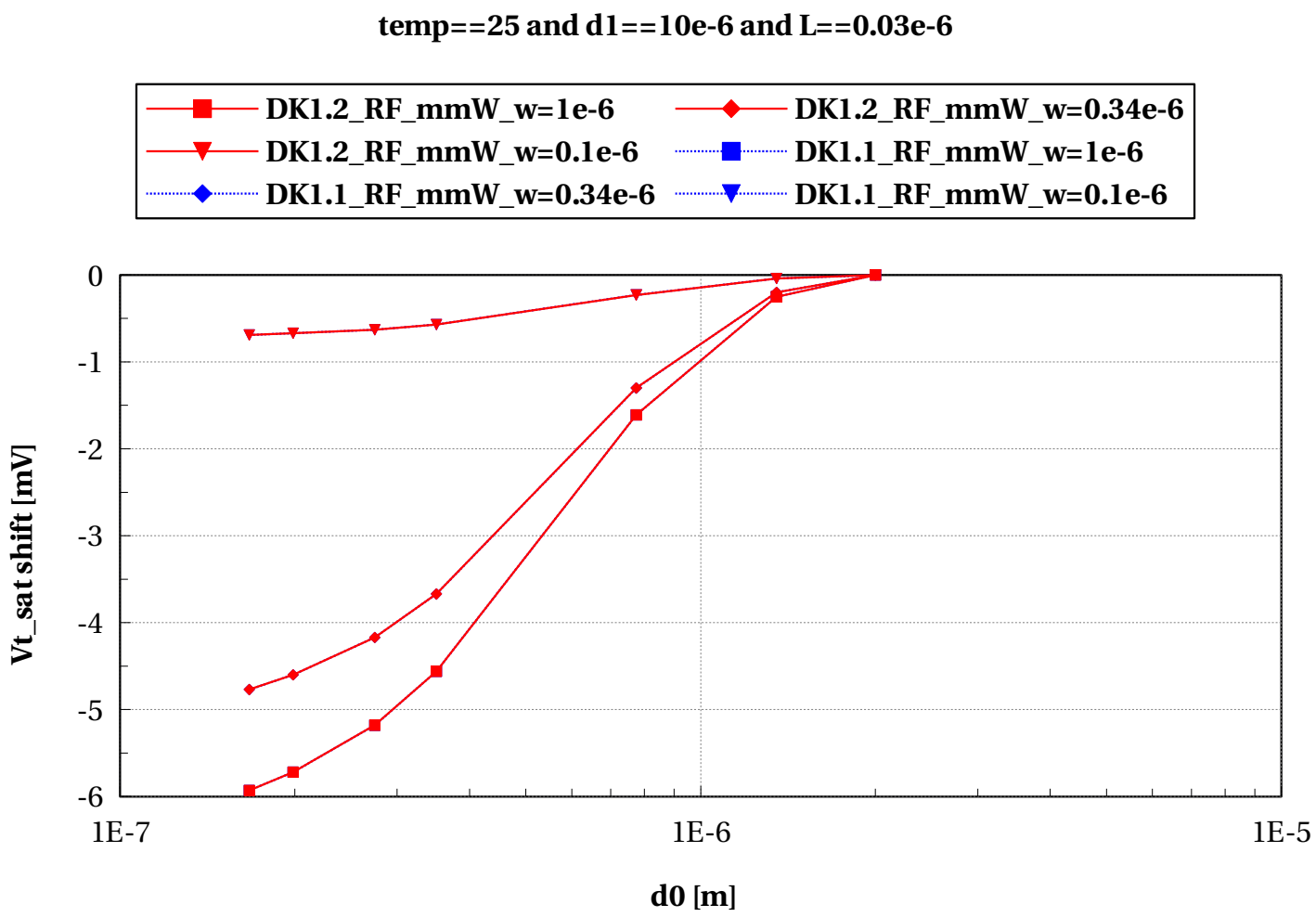
lvtpfet_acc, Vt_sat shift [mV] vs d0 [m]



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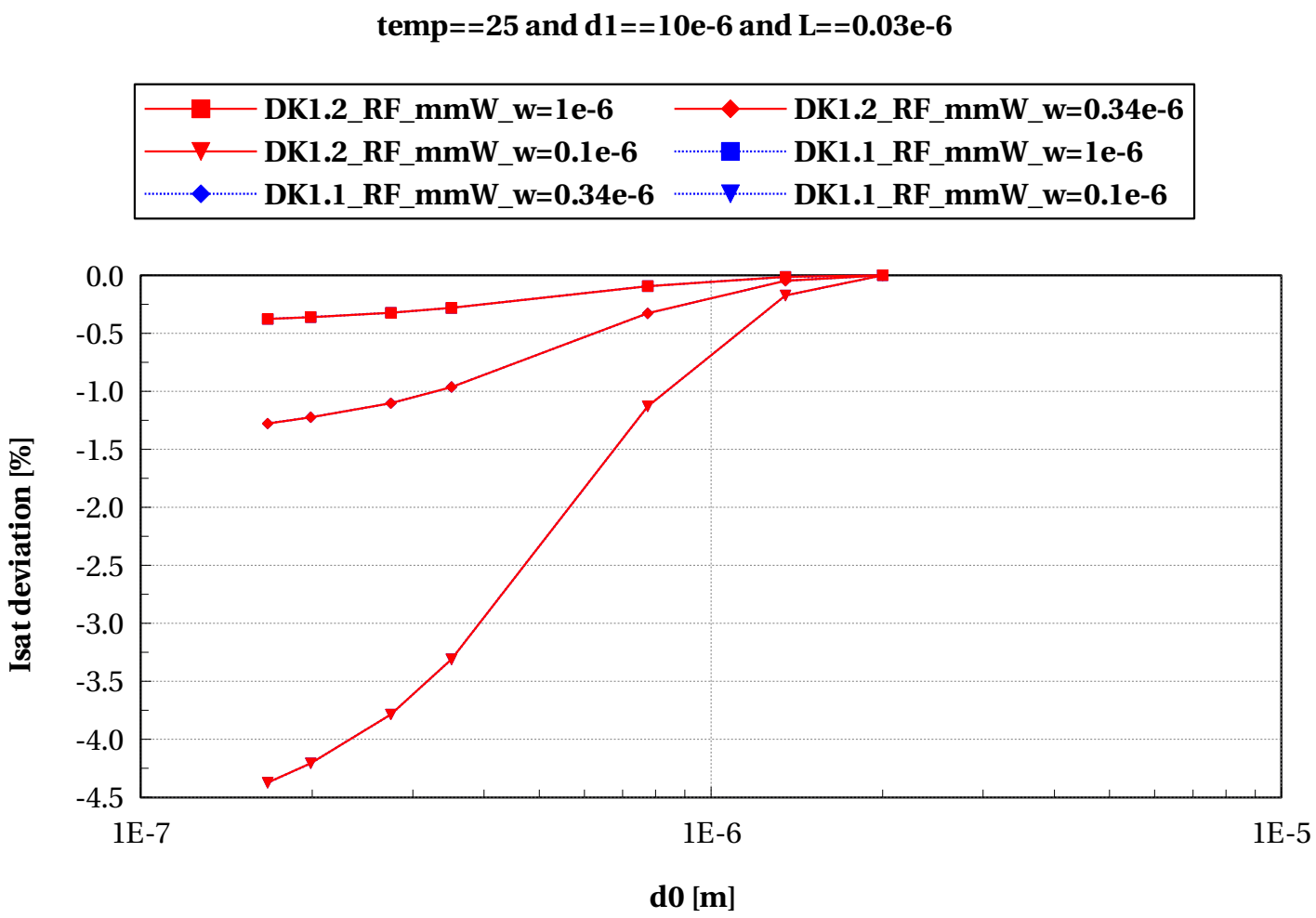
lvtpfet_acc, Isat deviation [%] vs d0 [m]



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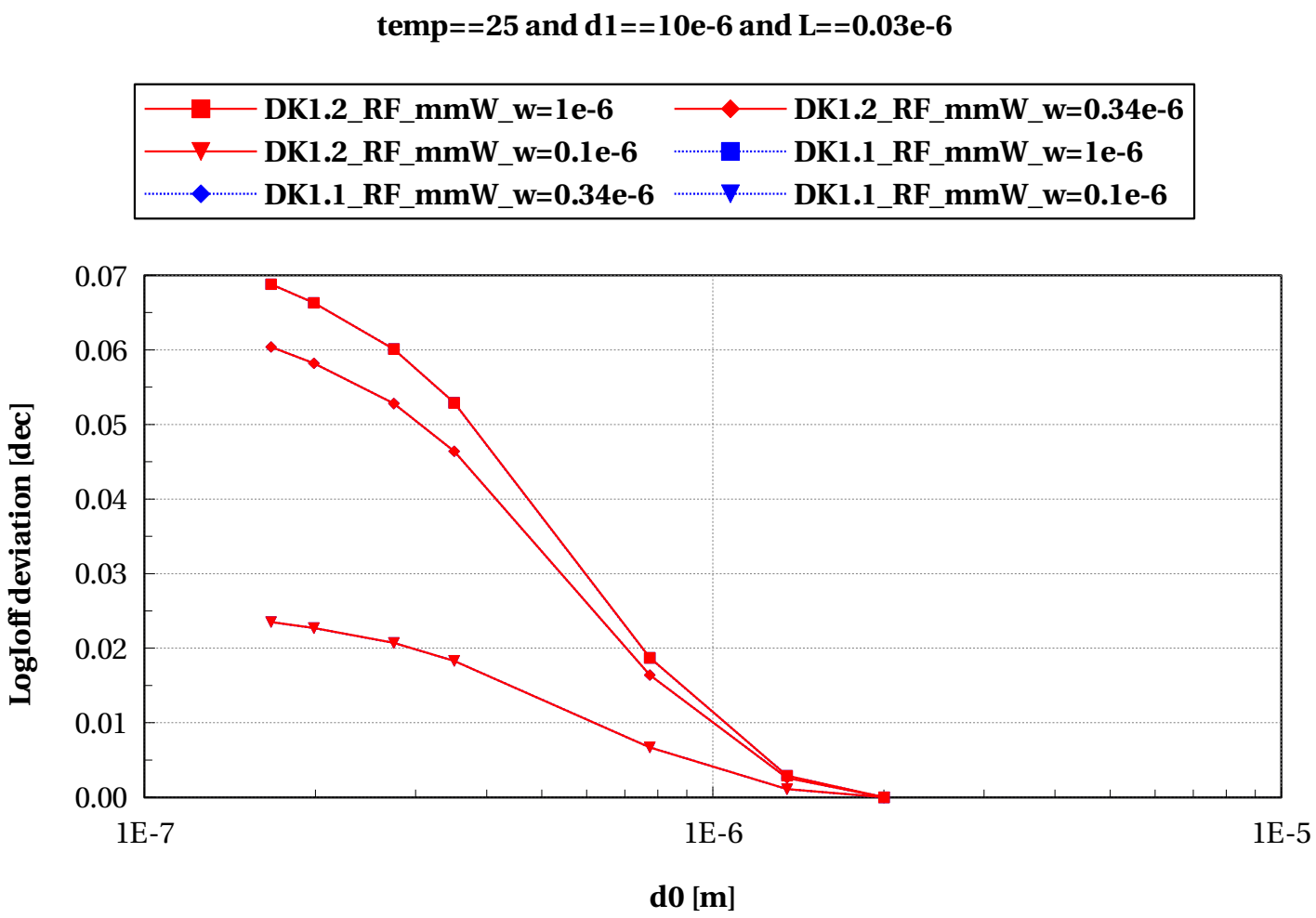
lvtpfet_acc, LogIoff deviation [dec] vs d0 [m]



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- WPE effect w.r.t d0 - Wscaling at L=0.5u

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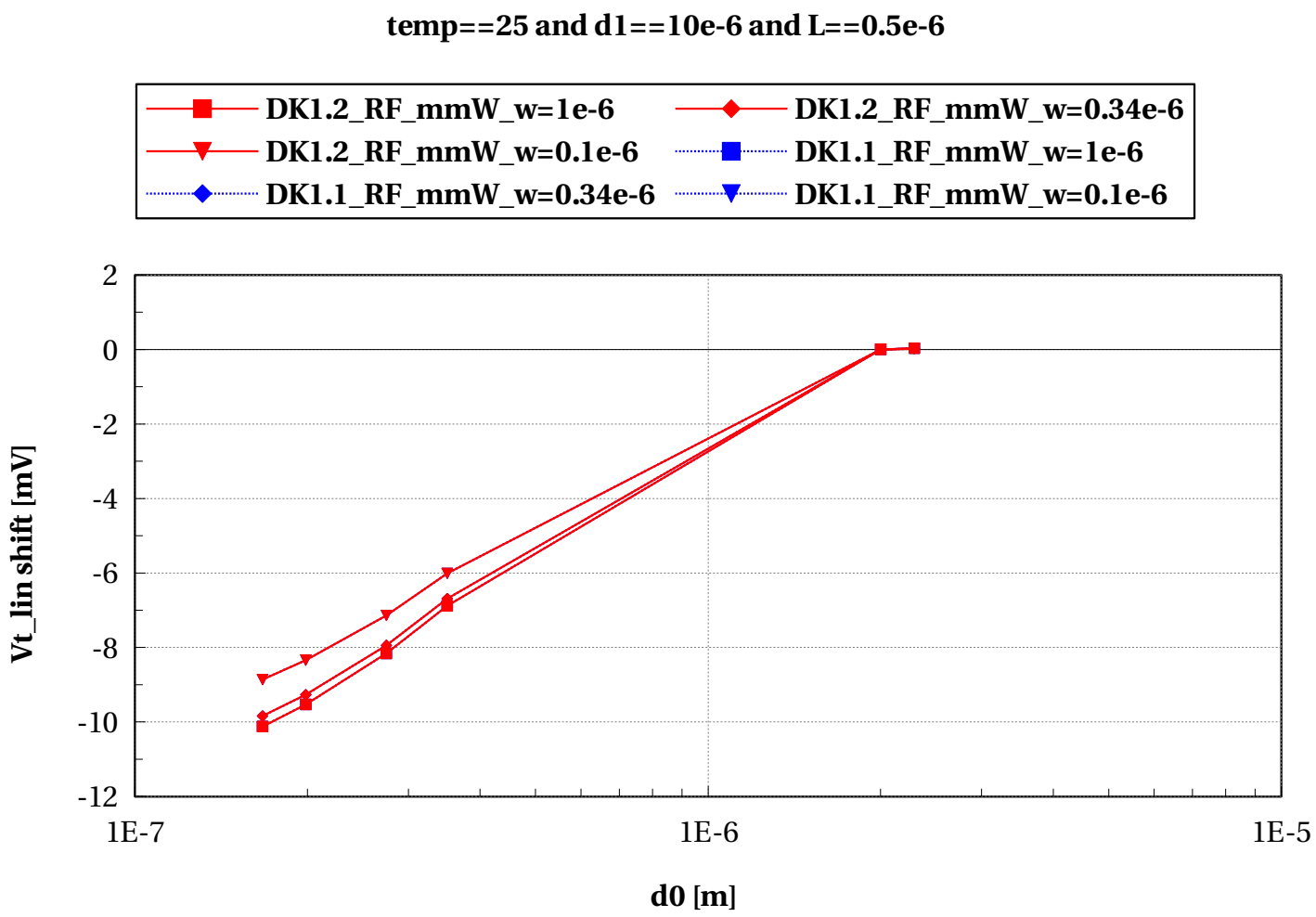
lvtpfet_acc, Vt_lin shift [mV] vs d0 [m]



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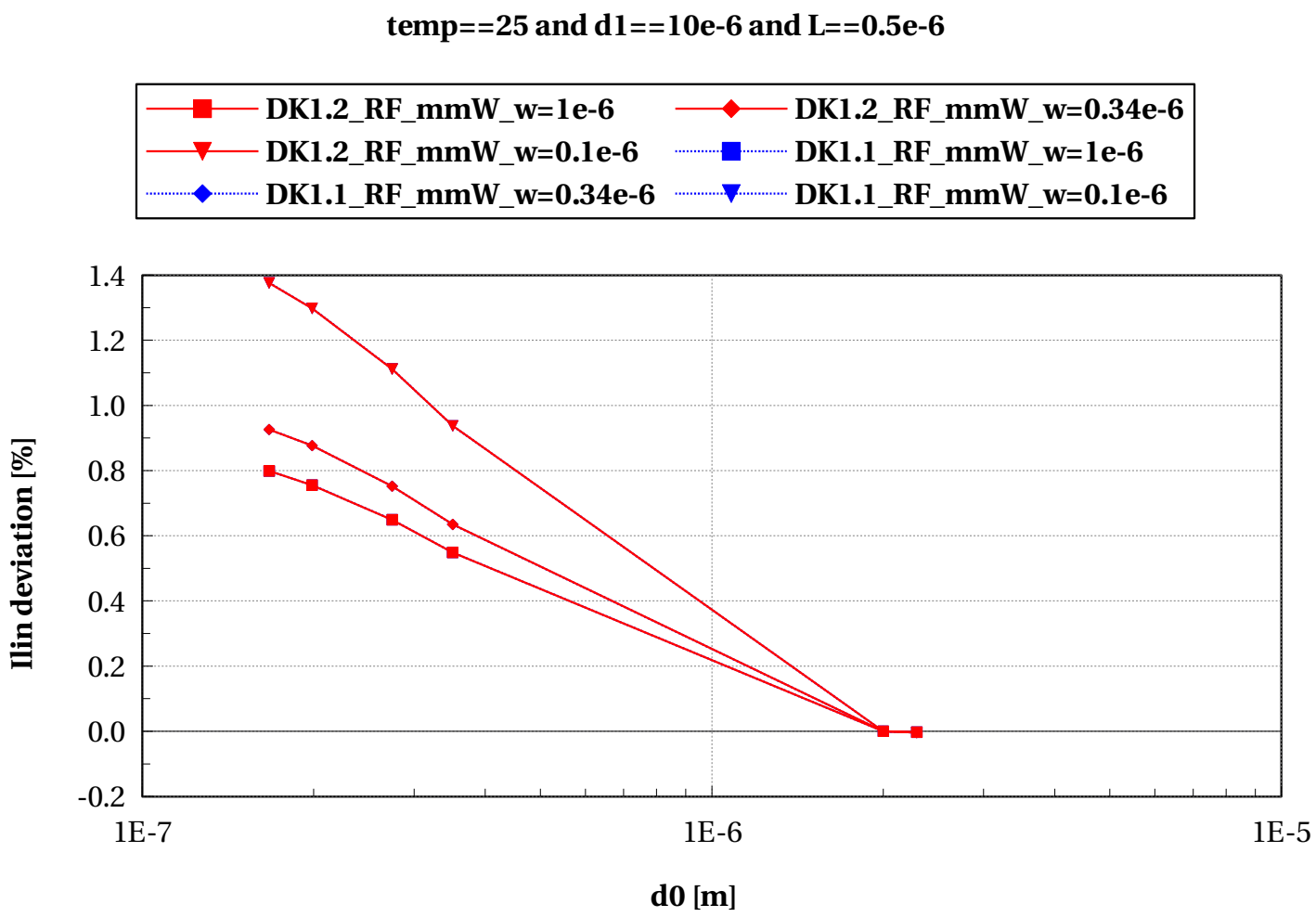
lvtpfet_acc, Ilin deviation [%] vs d0 [m]



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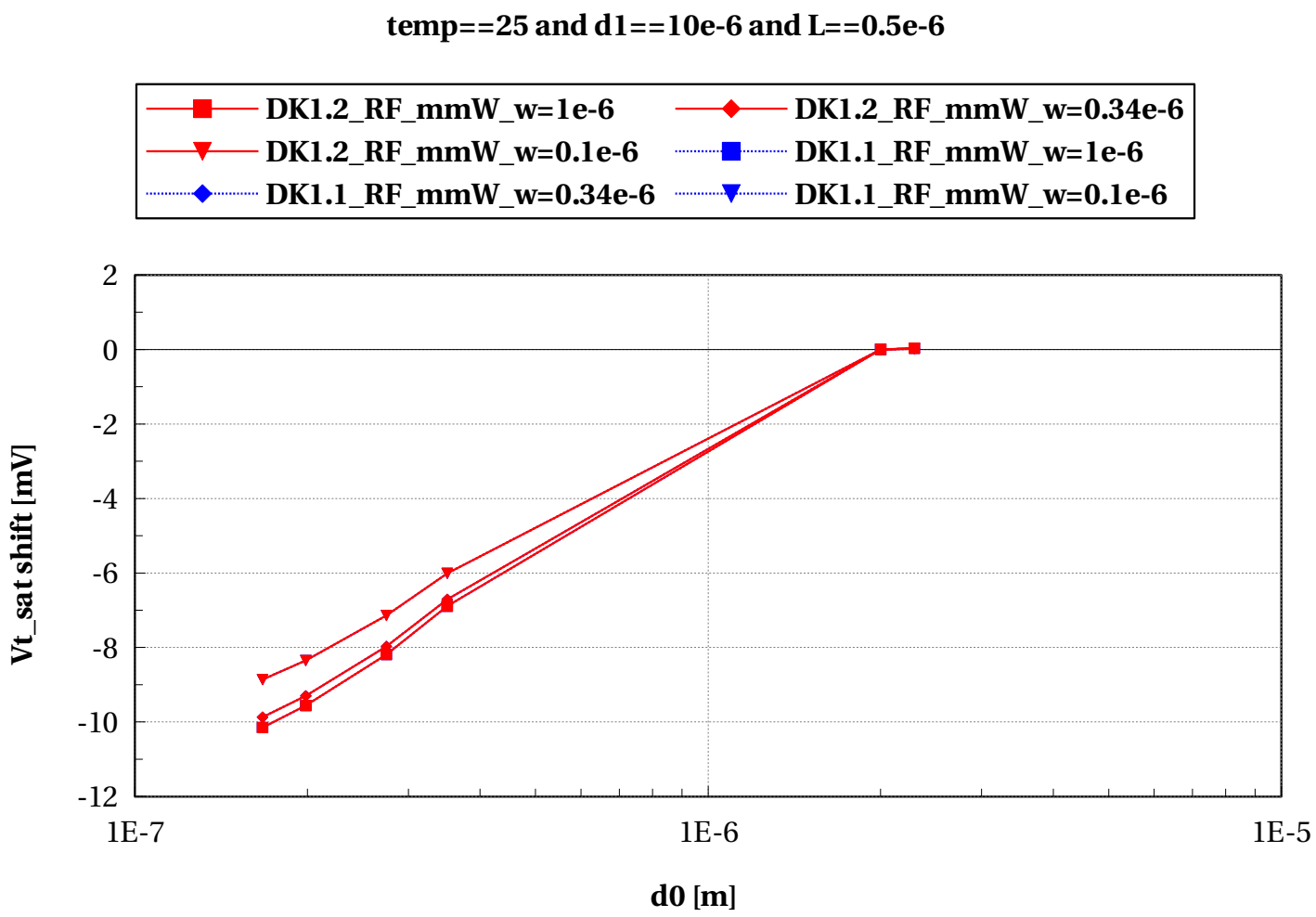
lvtpfet_acc, Vt_sat shift [mV] vs d0 [m]



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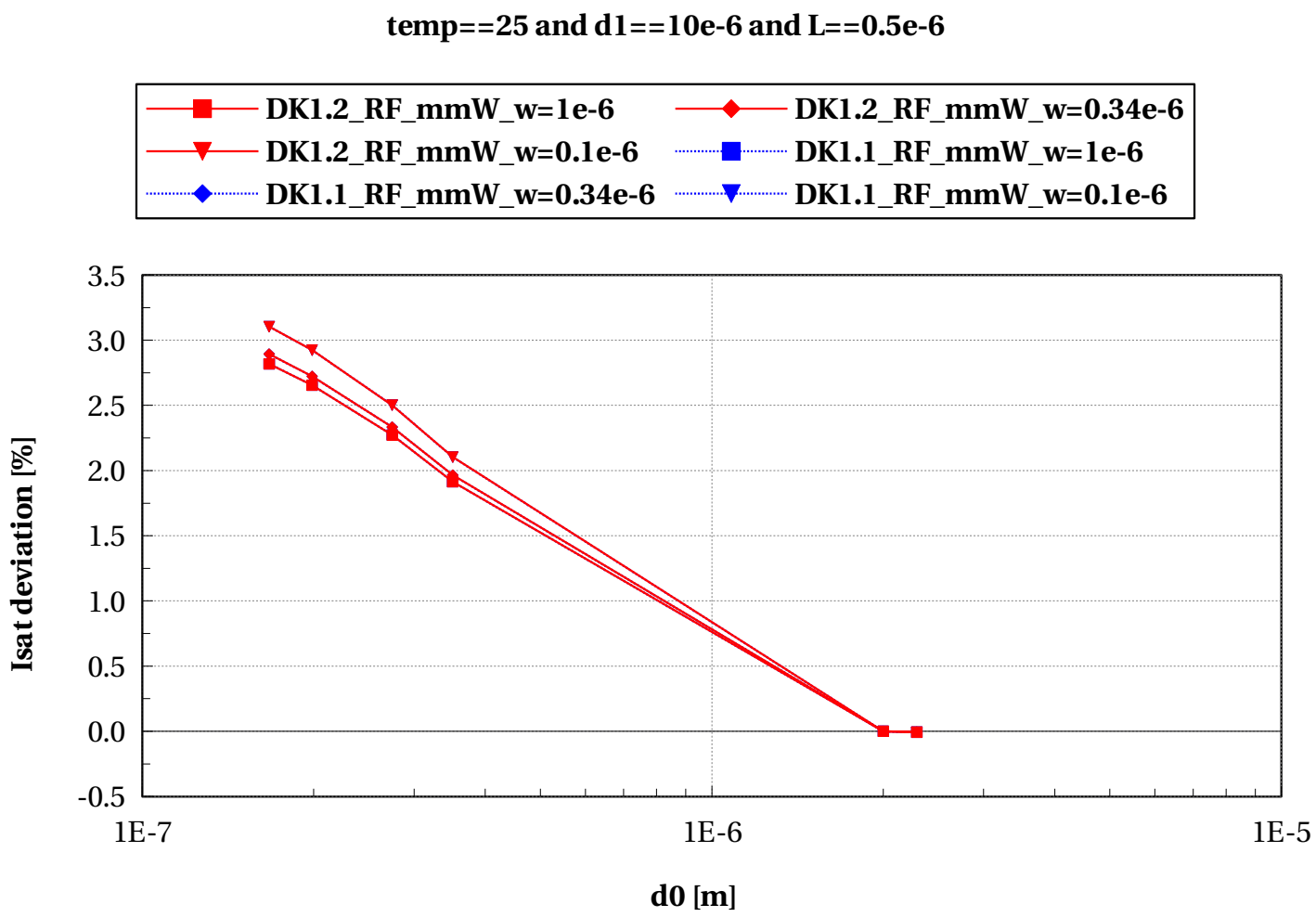
lvtpfet_acc, Isat deviation [%] vs d0 [m]



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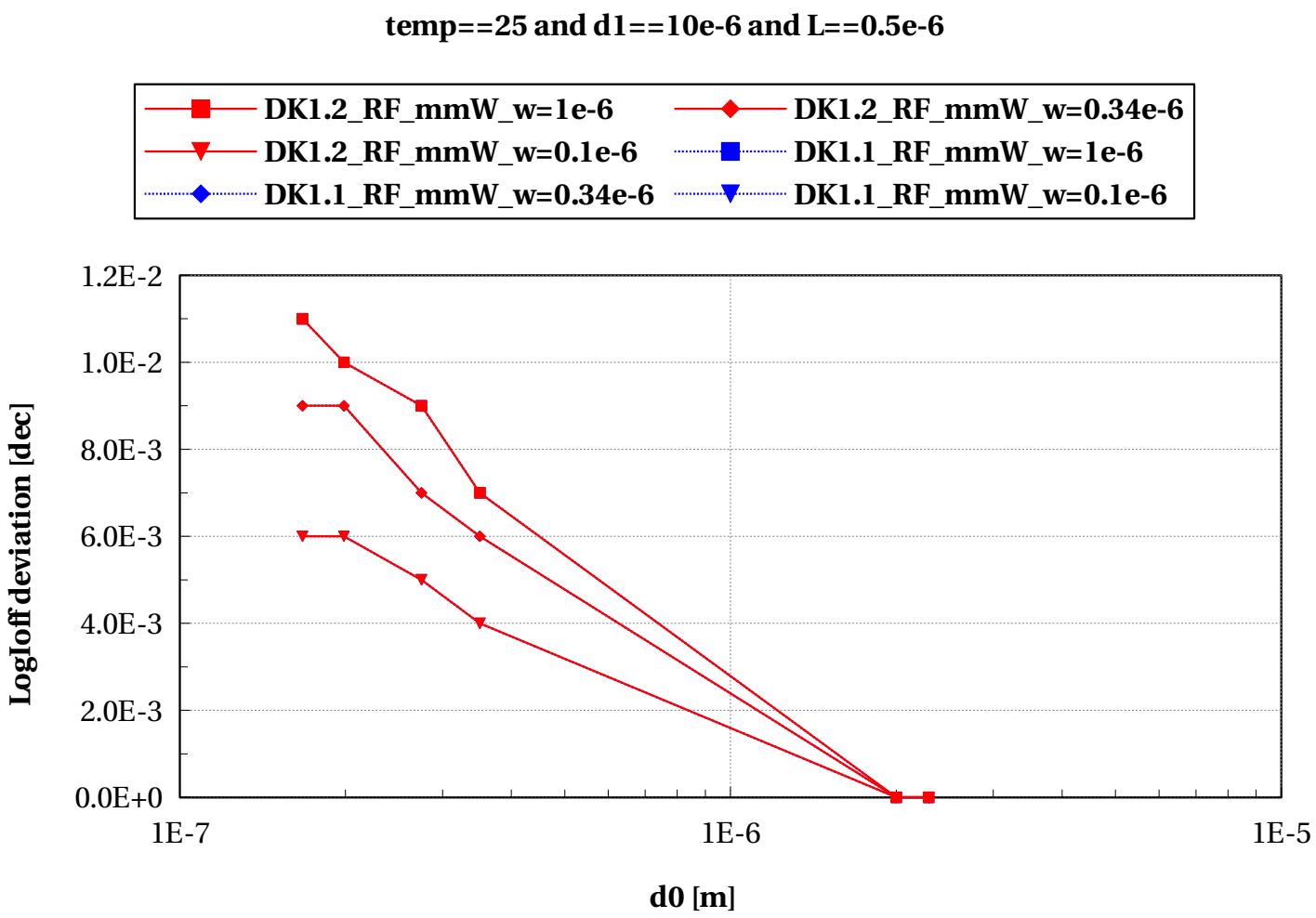
lvtpfet_acc, LogIoff deviation [dec] vs d0 [m]



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- WPE effect w.r.t d1 - Wscaling at L=0.03u

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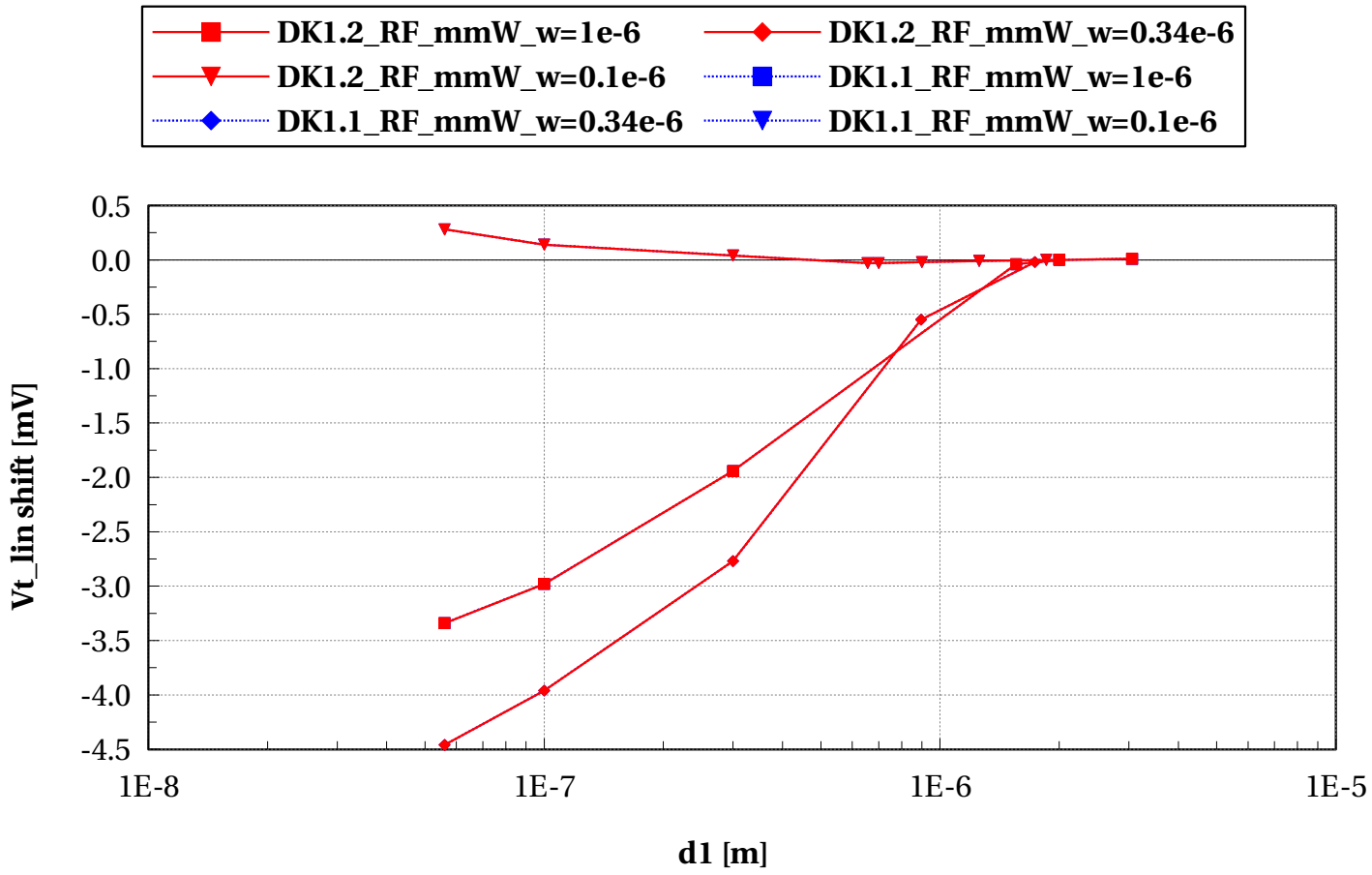
lvtpfet_acc, Vt_lin shift [mV] vs d1 [m]

temp==25 and d0==10e-6 and L==0.03e-6



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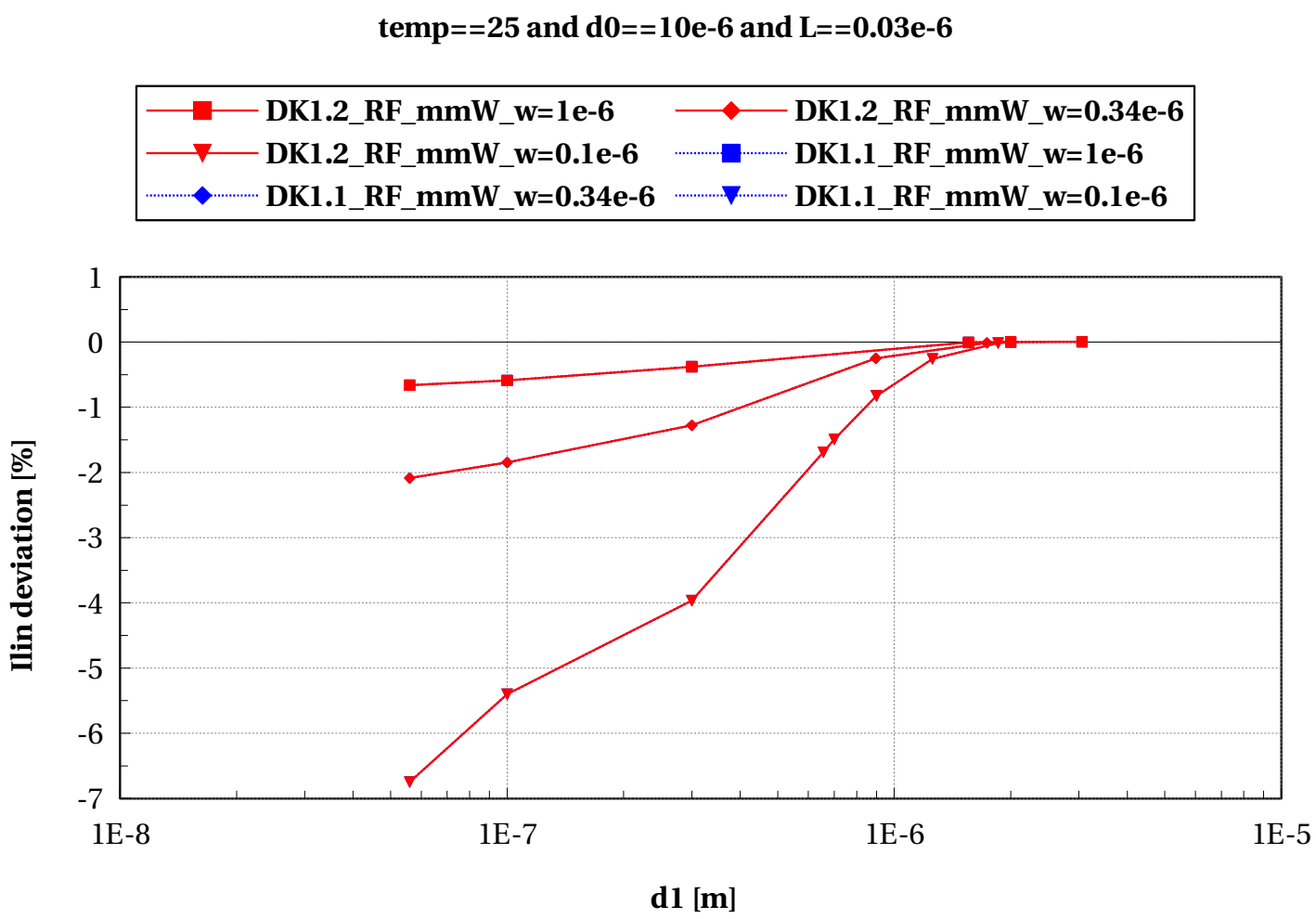
lvtpfet_acc, Ilin deviation [%] vs d1 [m]



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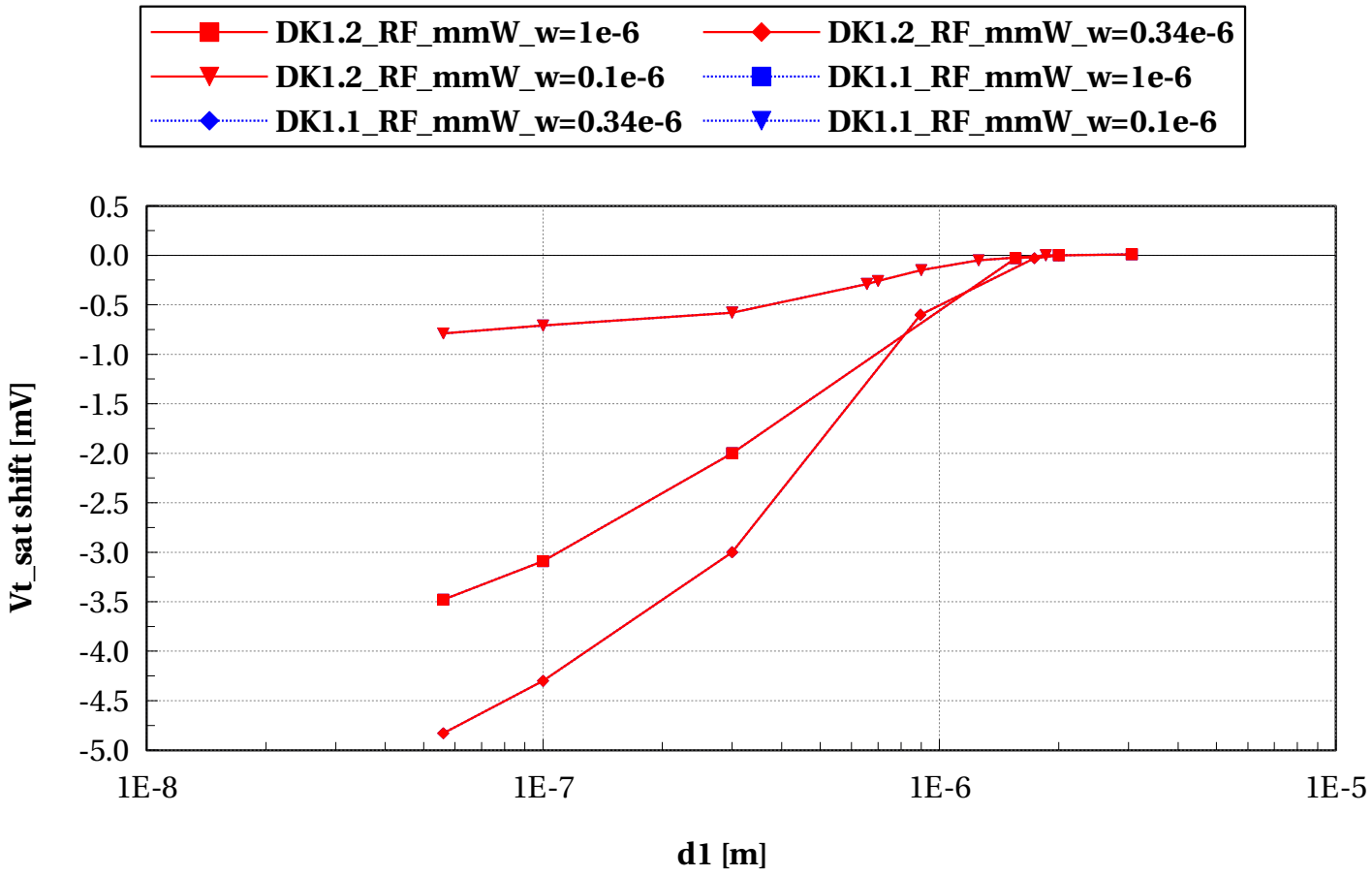
lvtpfet_acc, Vt_sat shift [mV] vs d1 [m]

temp==25 and d0==10e-6 and L==0.03e-6



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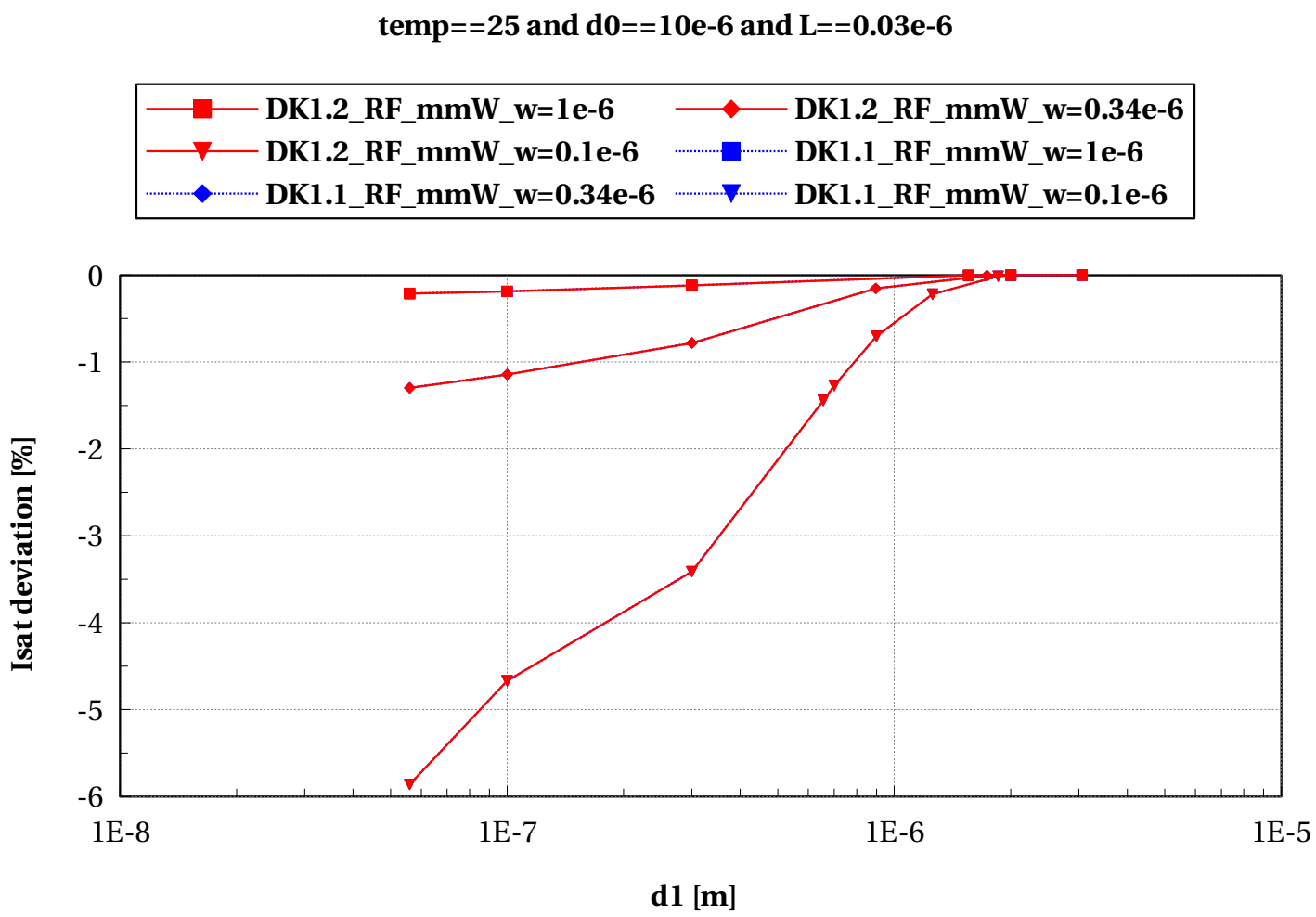
lvtpfet_acc, Isat deviation [%] vs d1 [m]



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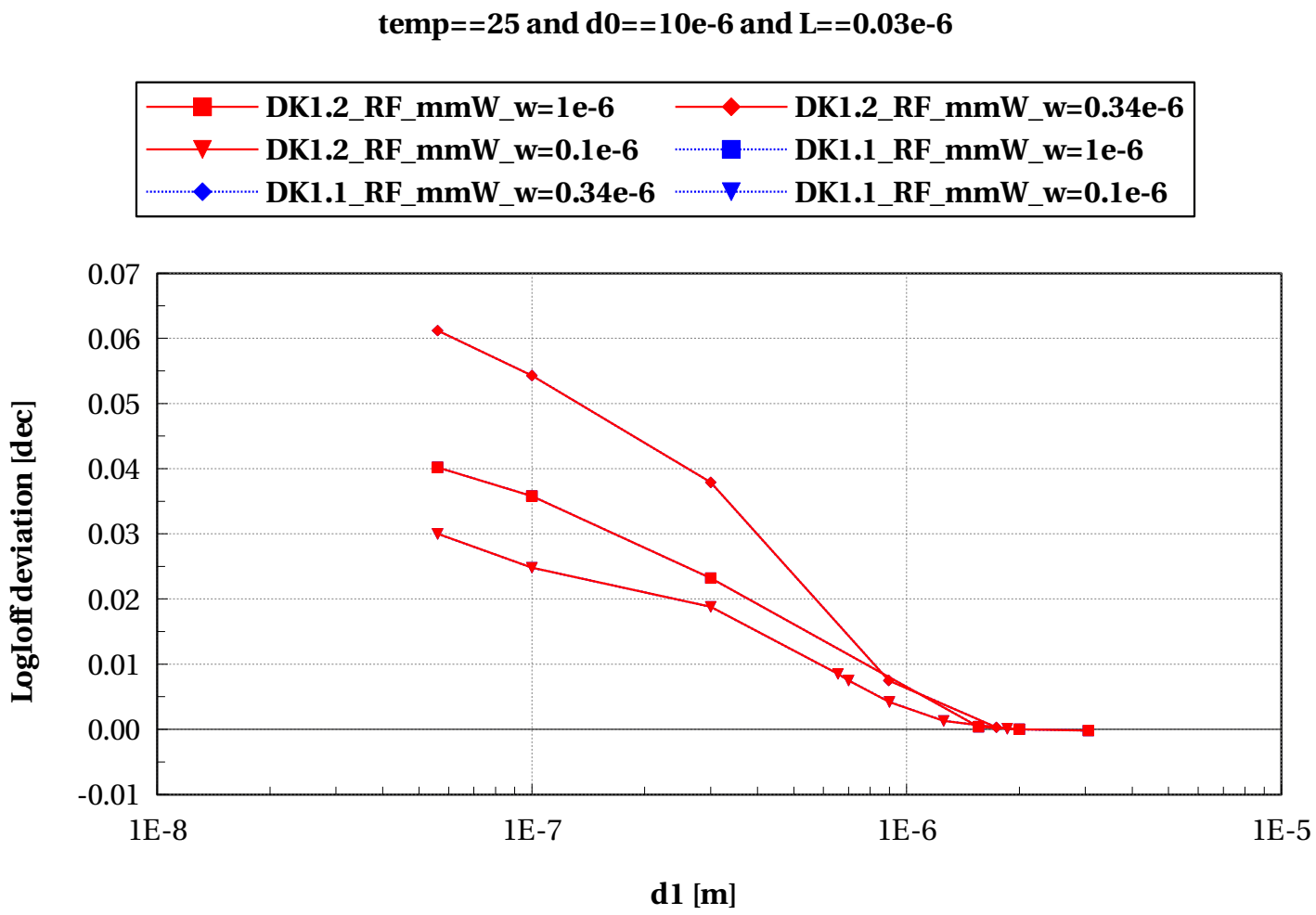
lvtpfet_acc, LogIoff deviation [dec] vs d1 [m]



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- WPE effect w.r.t d1 - Wscaling at L=0.12u

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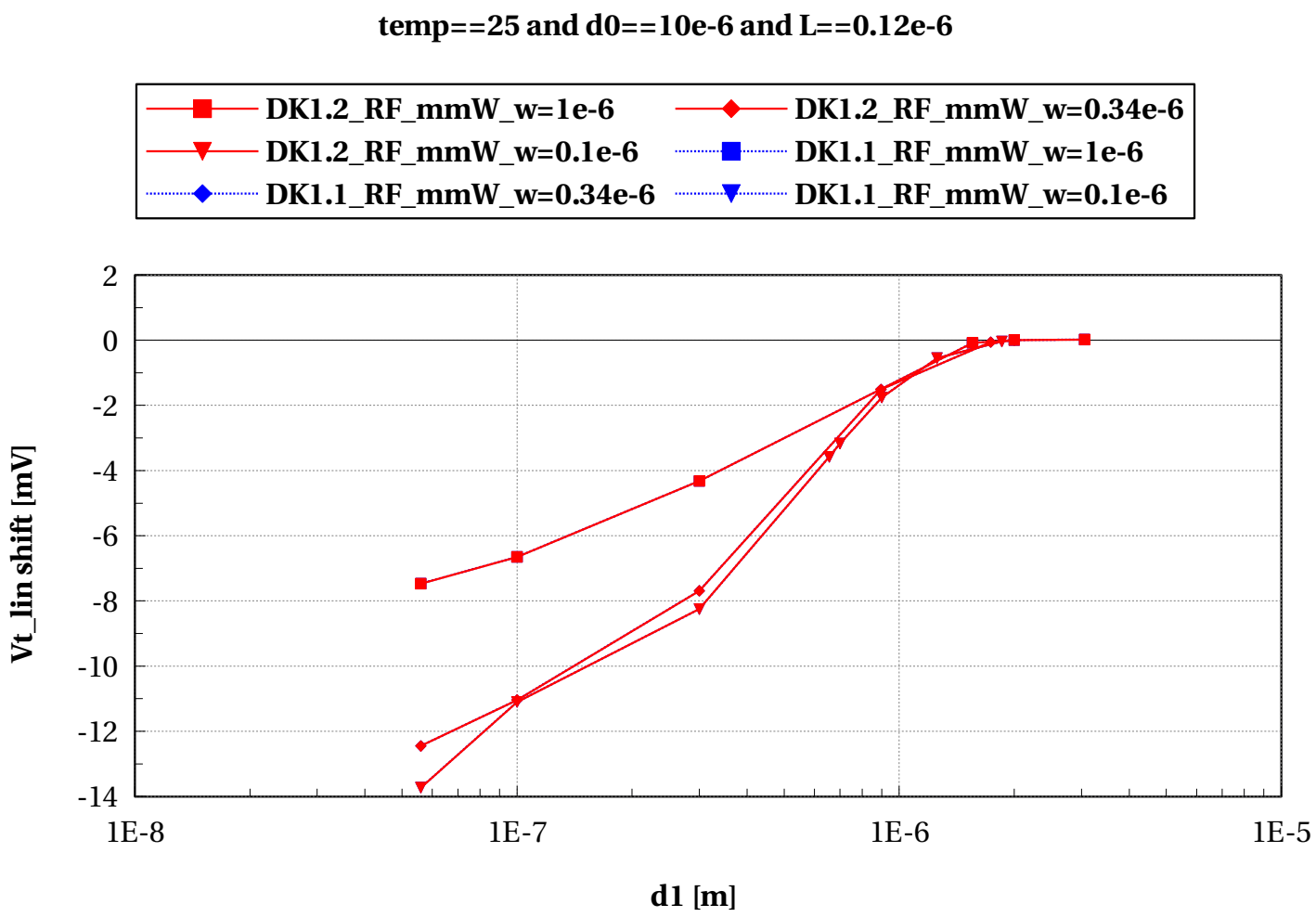
lvtpfet_acc, Vt_lin shift [mV] vs d1 [m]



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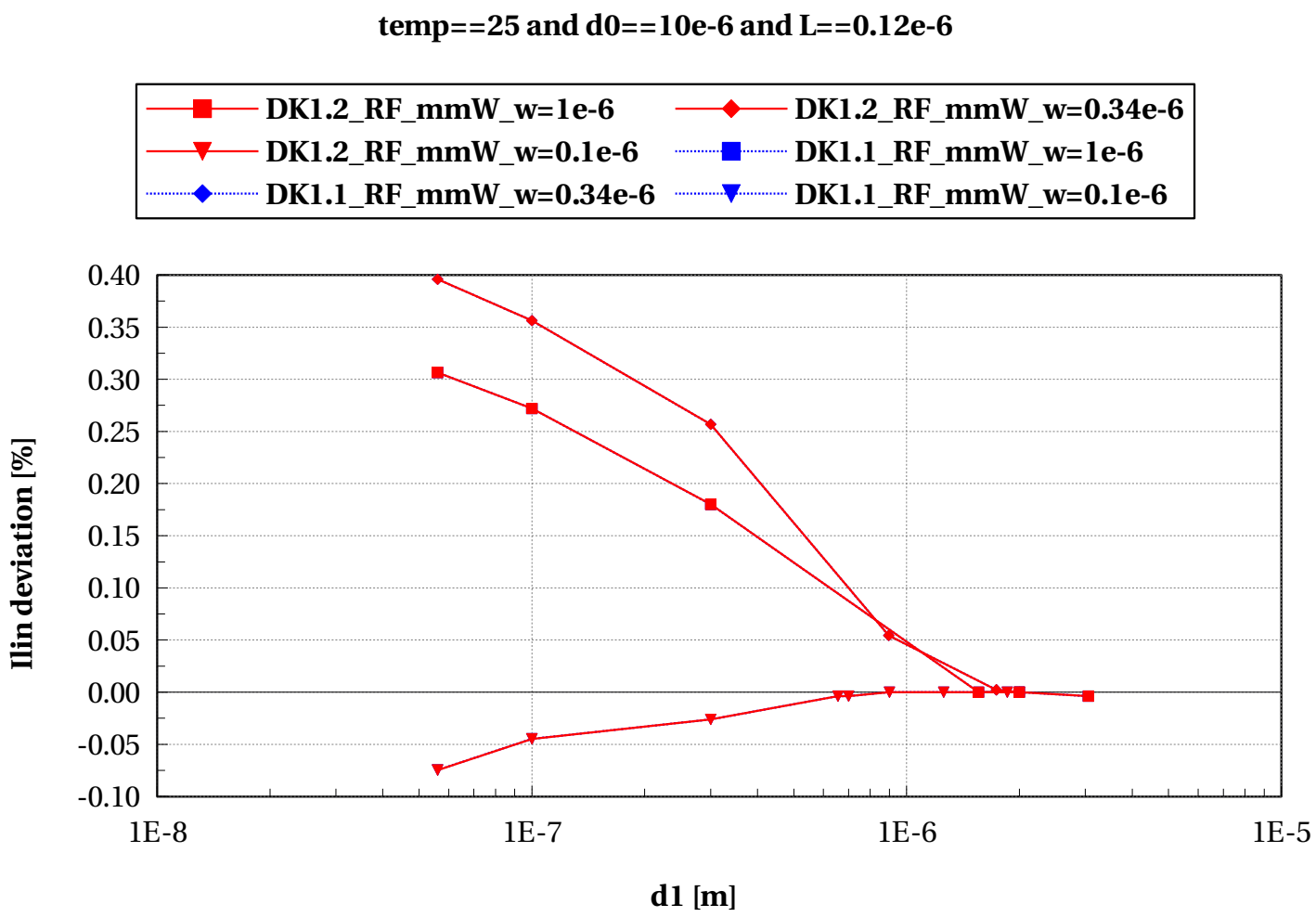
lvtpfet_acc, Ilin deviation [%] vs d1 [m]



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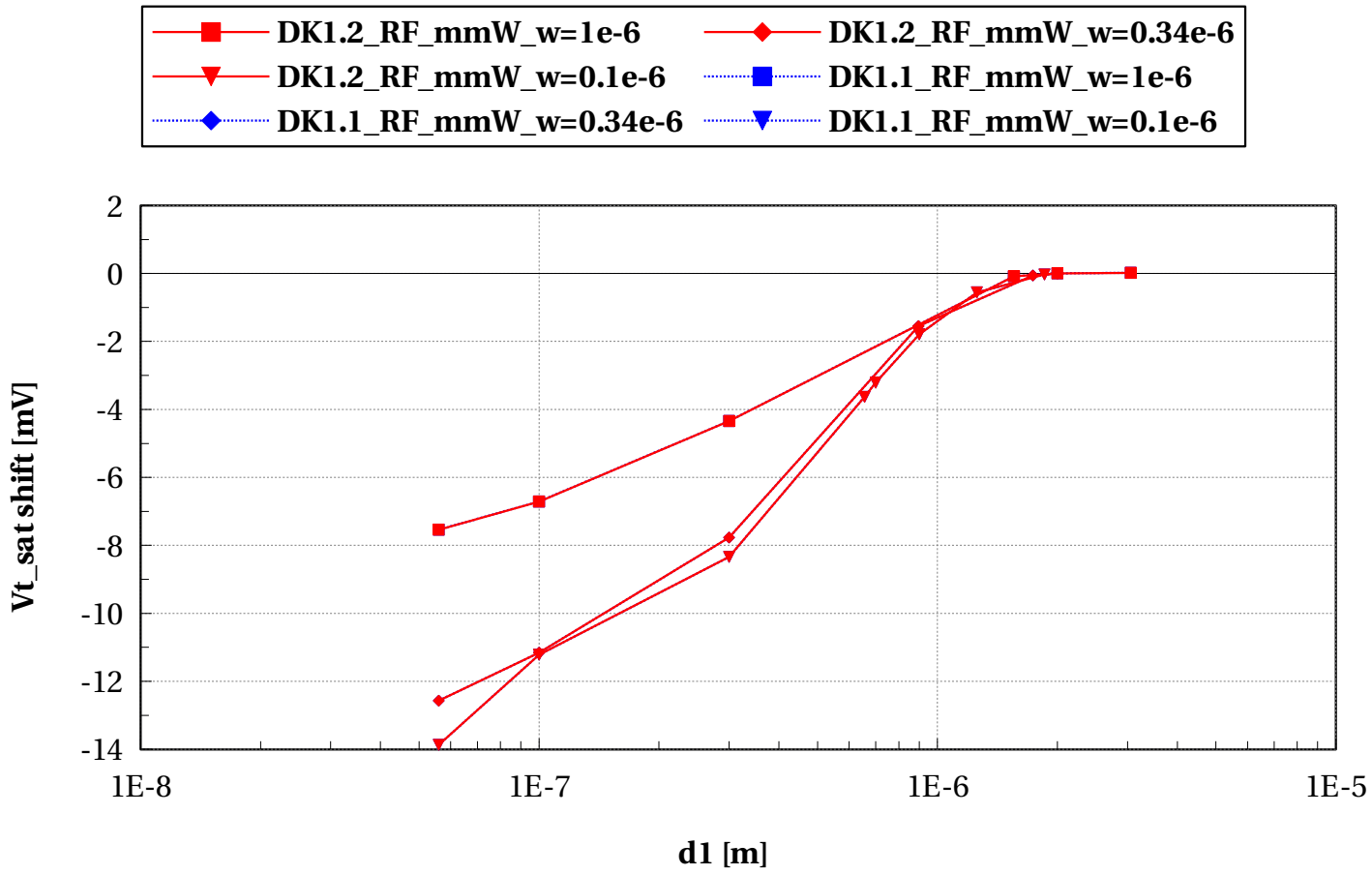
lvtpfet_acc, Vt_sat shift [mV] vs d1 [m]

temp==25 and d0==10e-6 and L==0.12e-6



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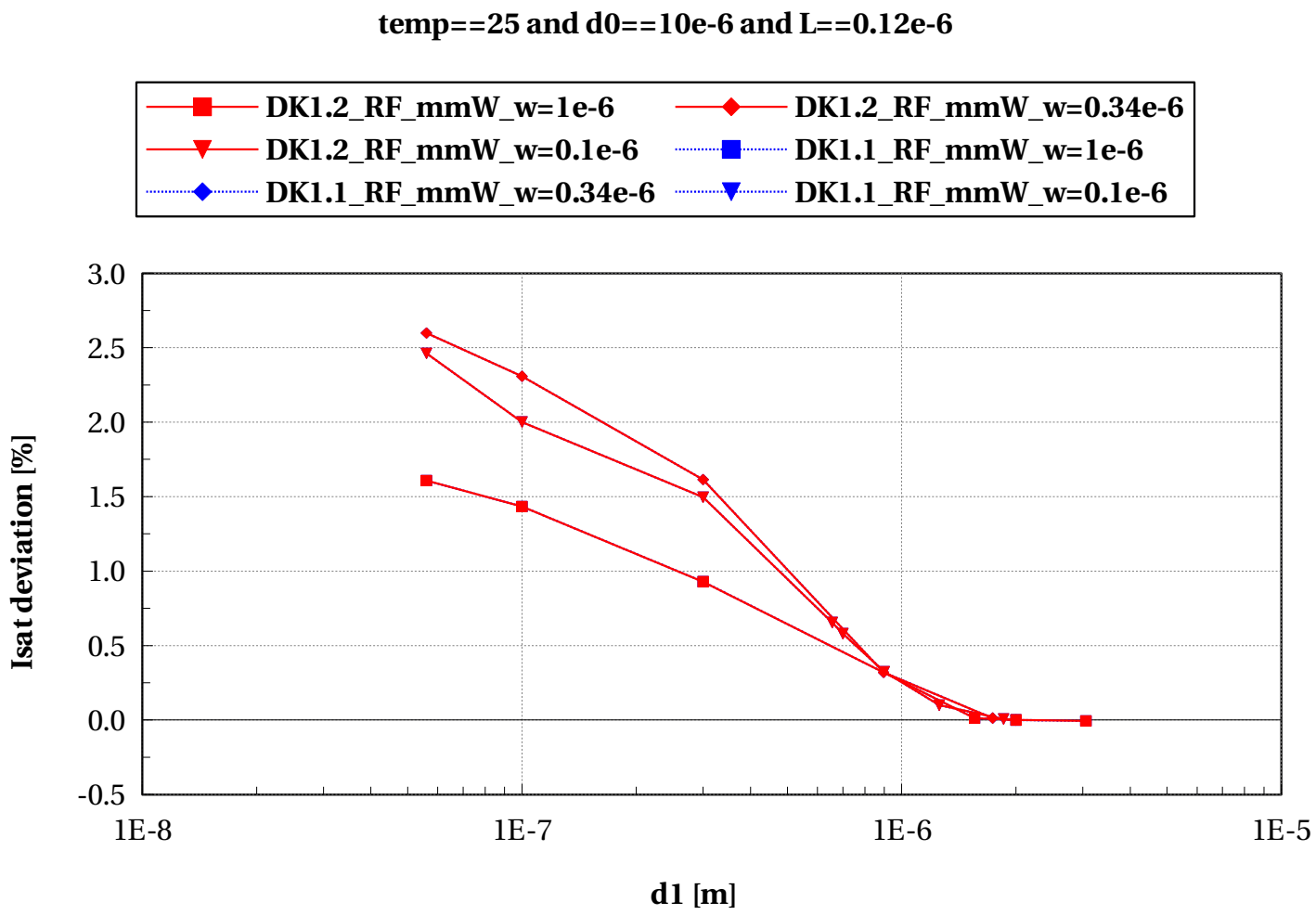
lvtpfet_acc, Isat deviation [%] vs d1 [m]



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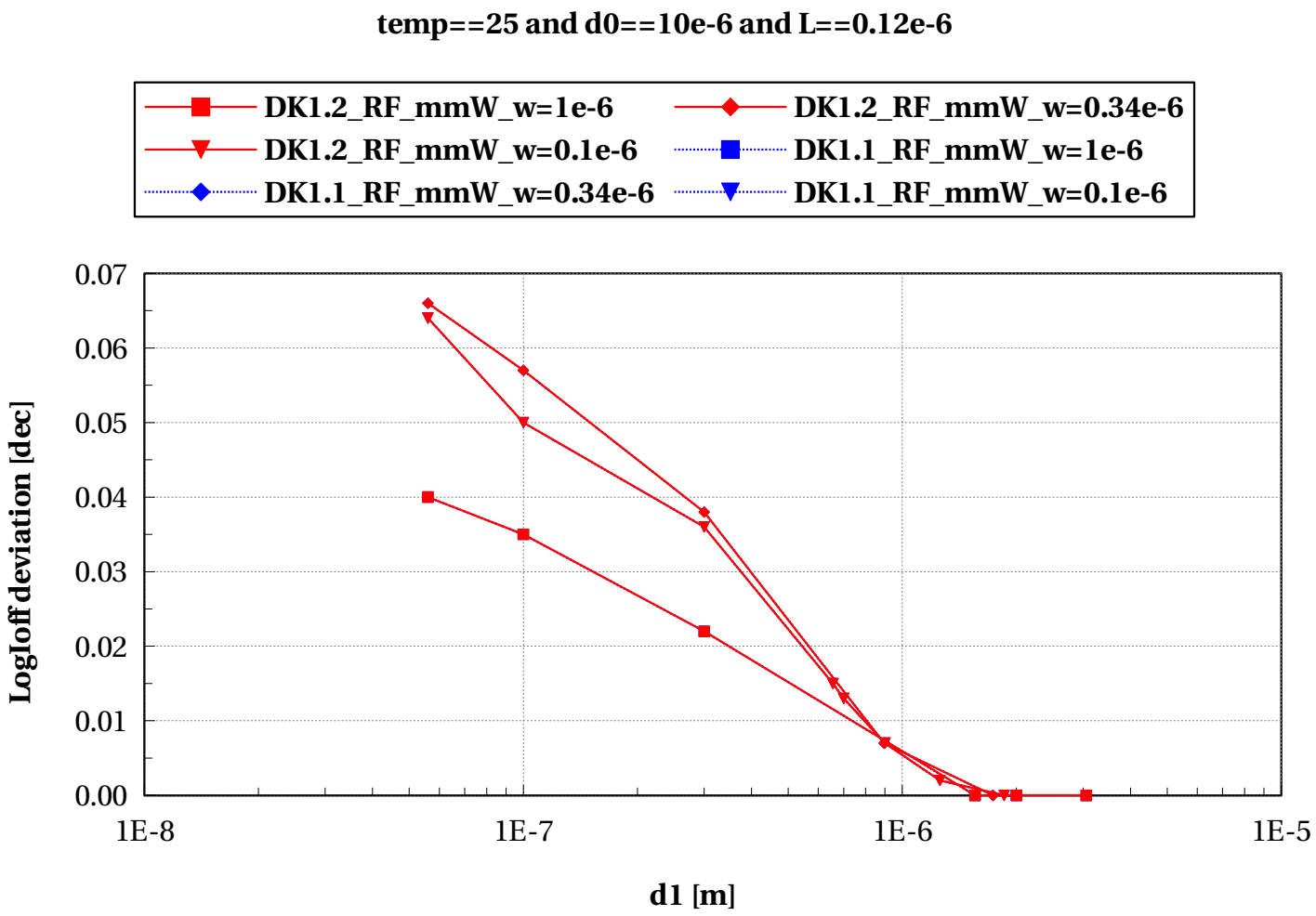
lvtpfet_acc, LogIoff deviation [dec] vs d1 [m]



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- WPE effect w.r.t d1 - Wscaling at L=1u

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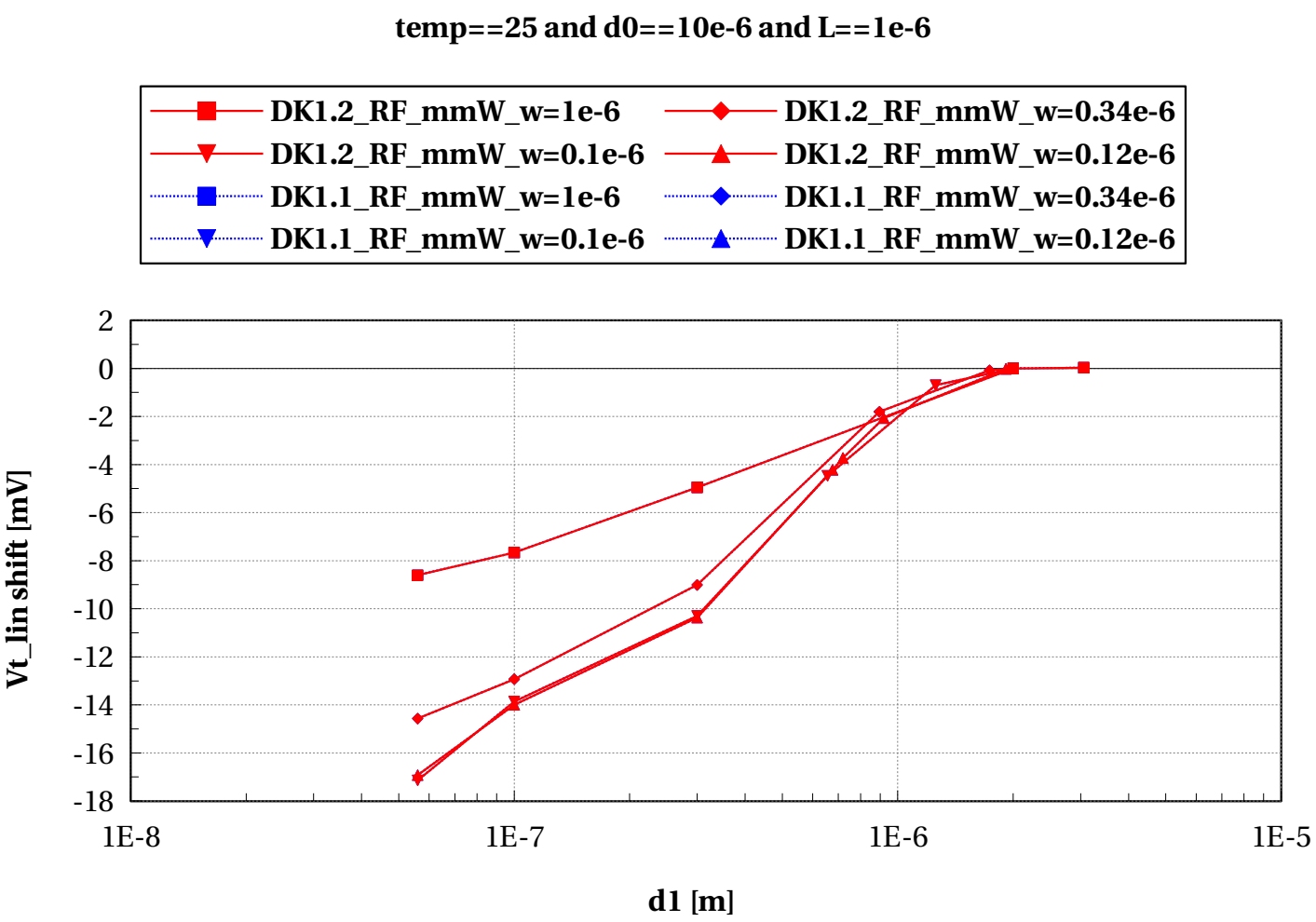
lvtpfet_acc, Vt_lin shift [mV] vs d1 [m]



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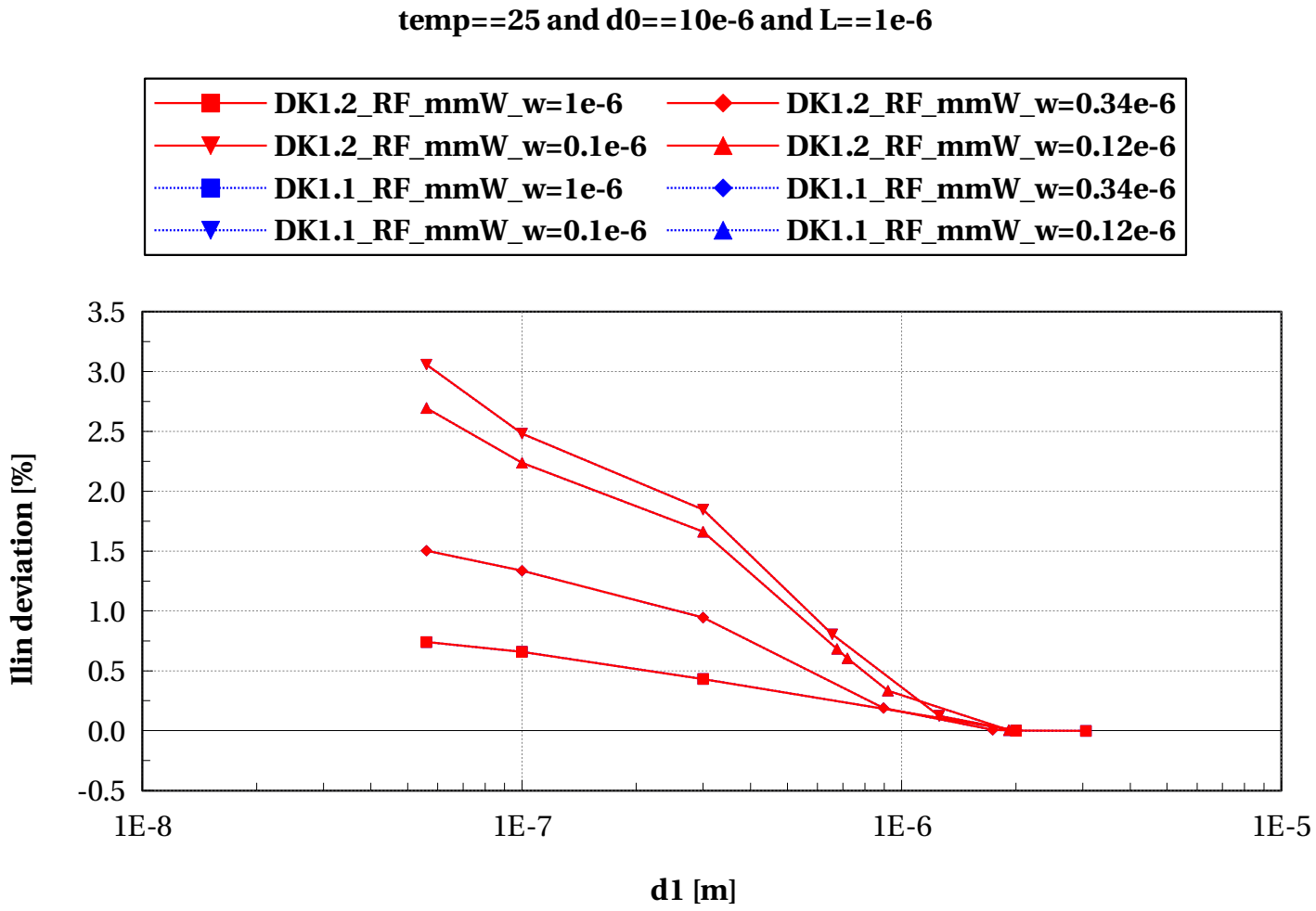
lvtpfet_acc, Ilin deviation [%] vs d1 [m]



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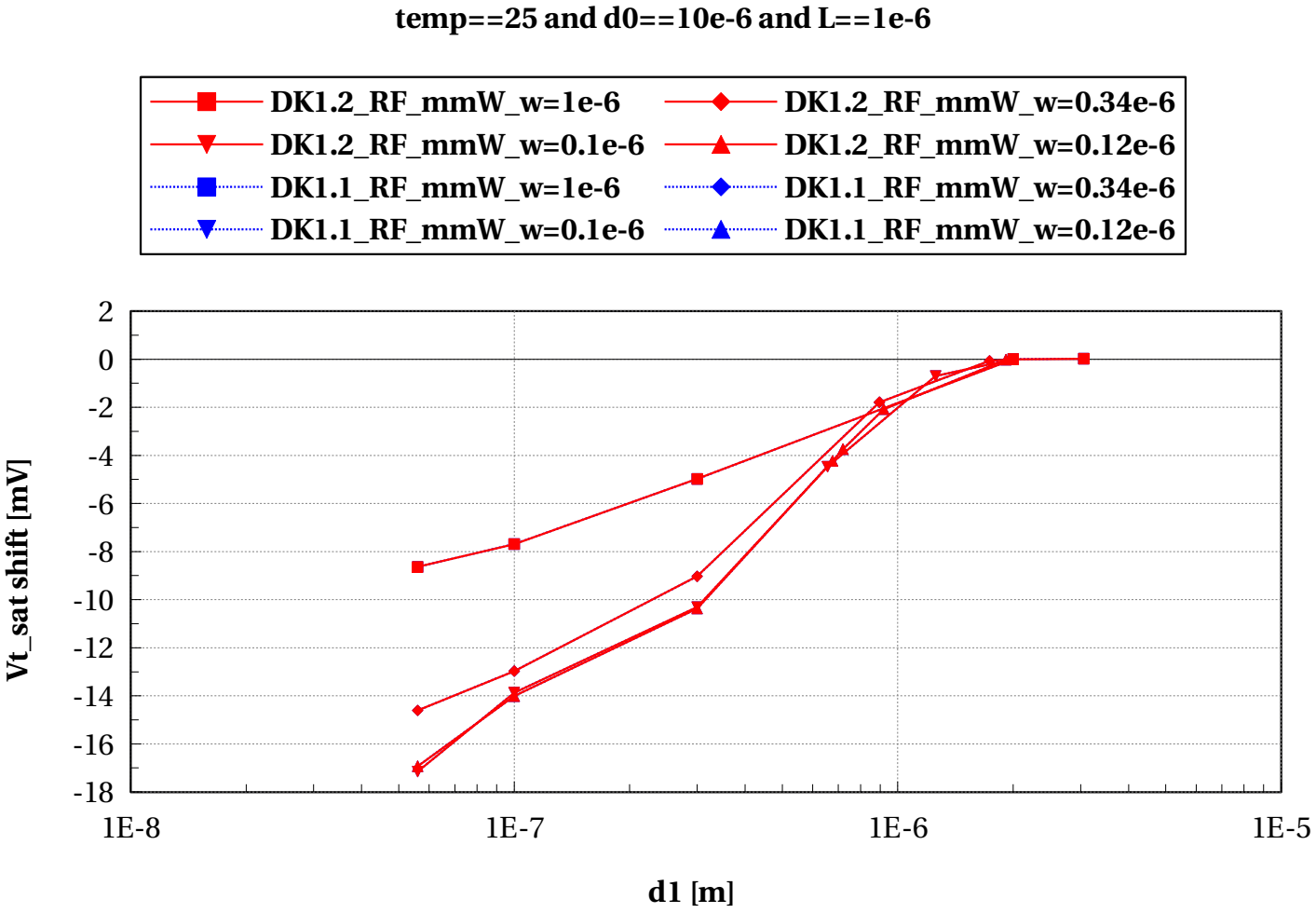
lvtpfet_acc, Vt_sat shift [mV] vs d1 [m]



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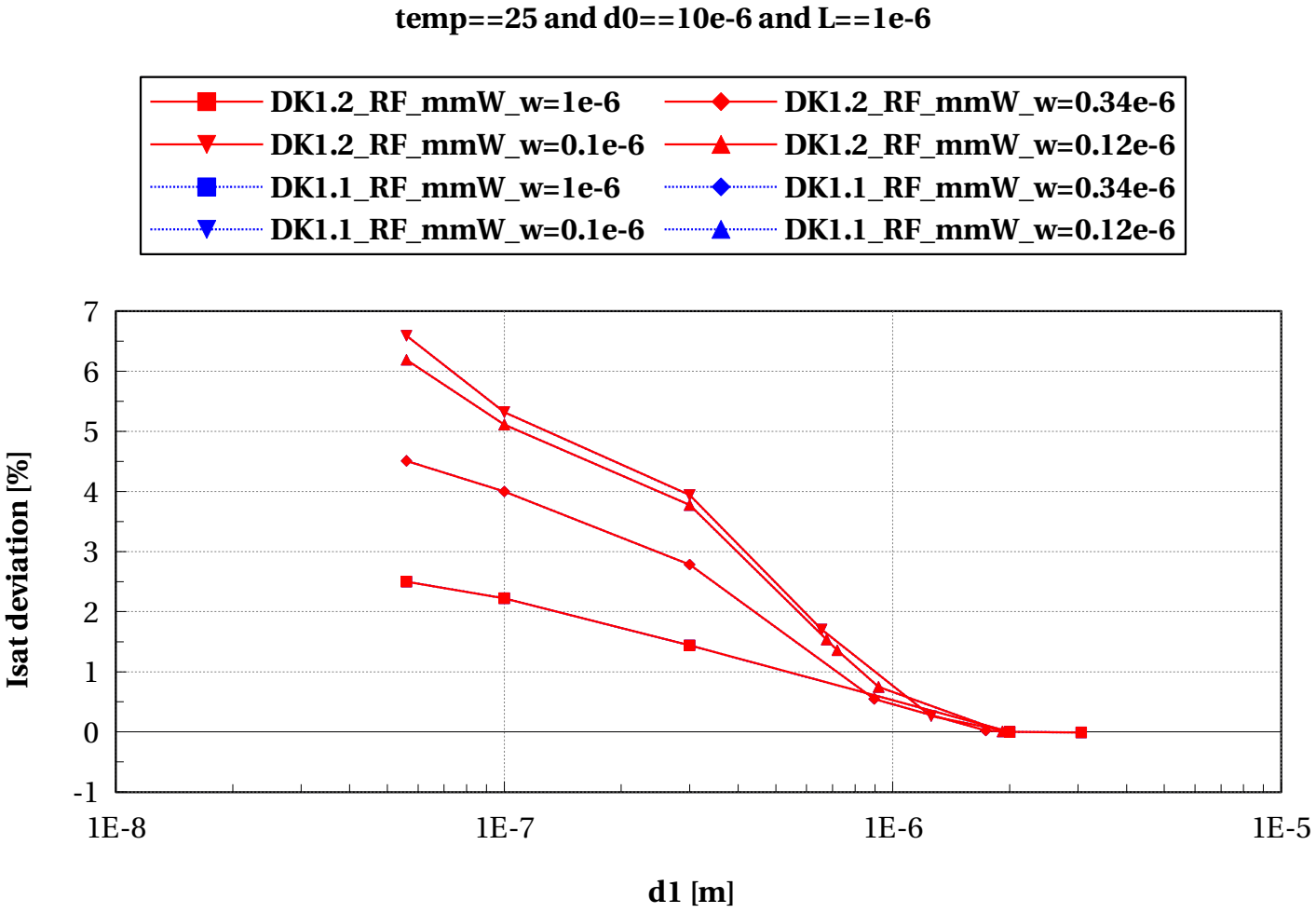
lvtpfet_acc, Isat deviation [%] vs d1 [m]



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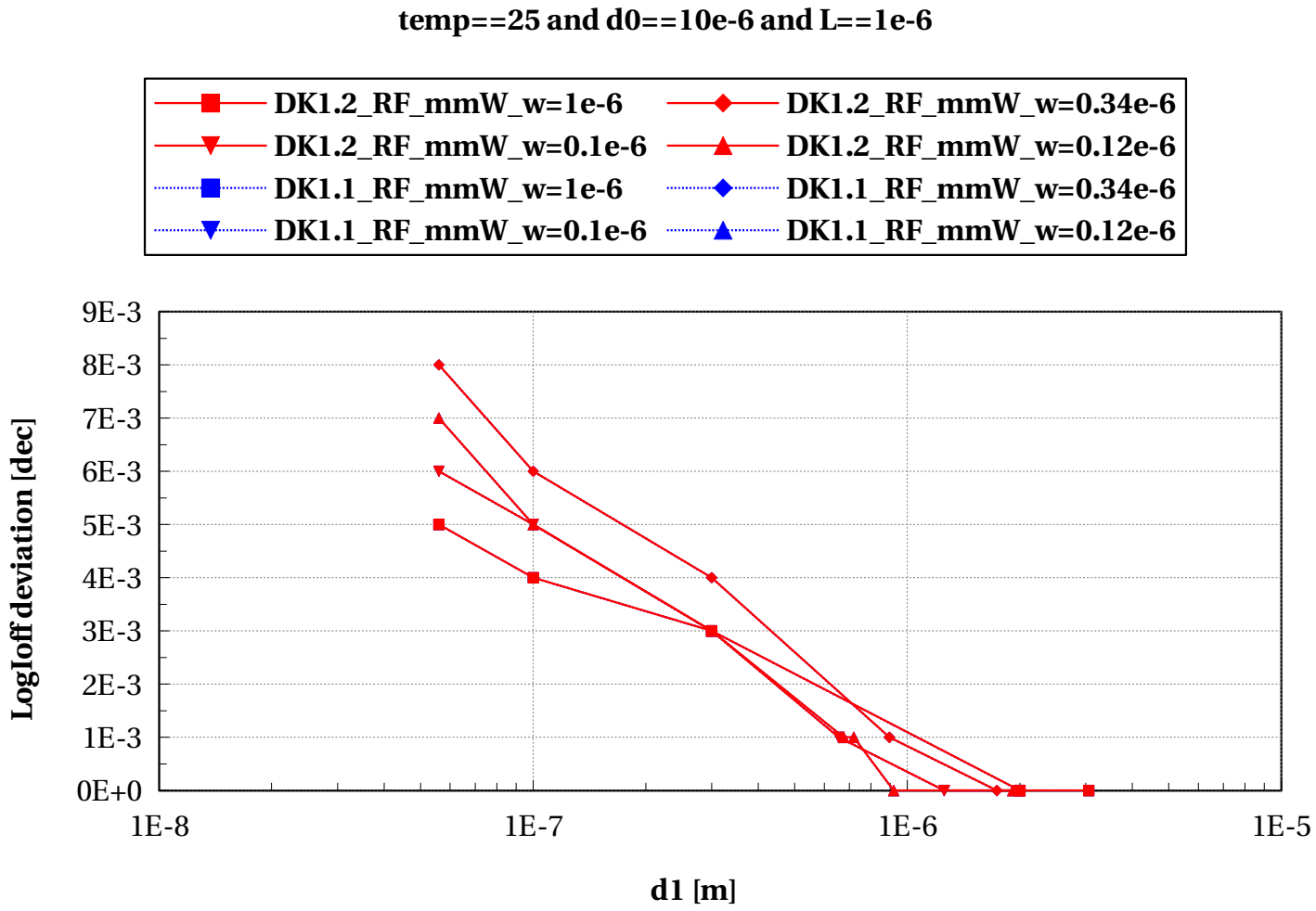
lvtpfet_acc, LogIoff deviation [dec] vs d1 [m]



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- WPE effect w.r.t d1 - Lscaling at W=0.1u

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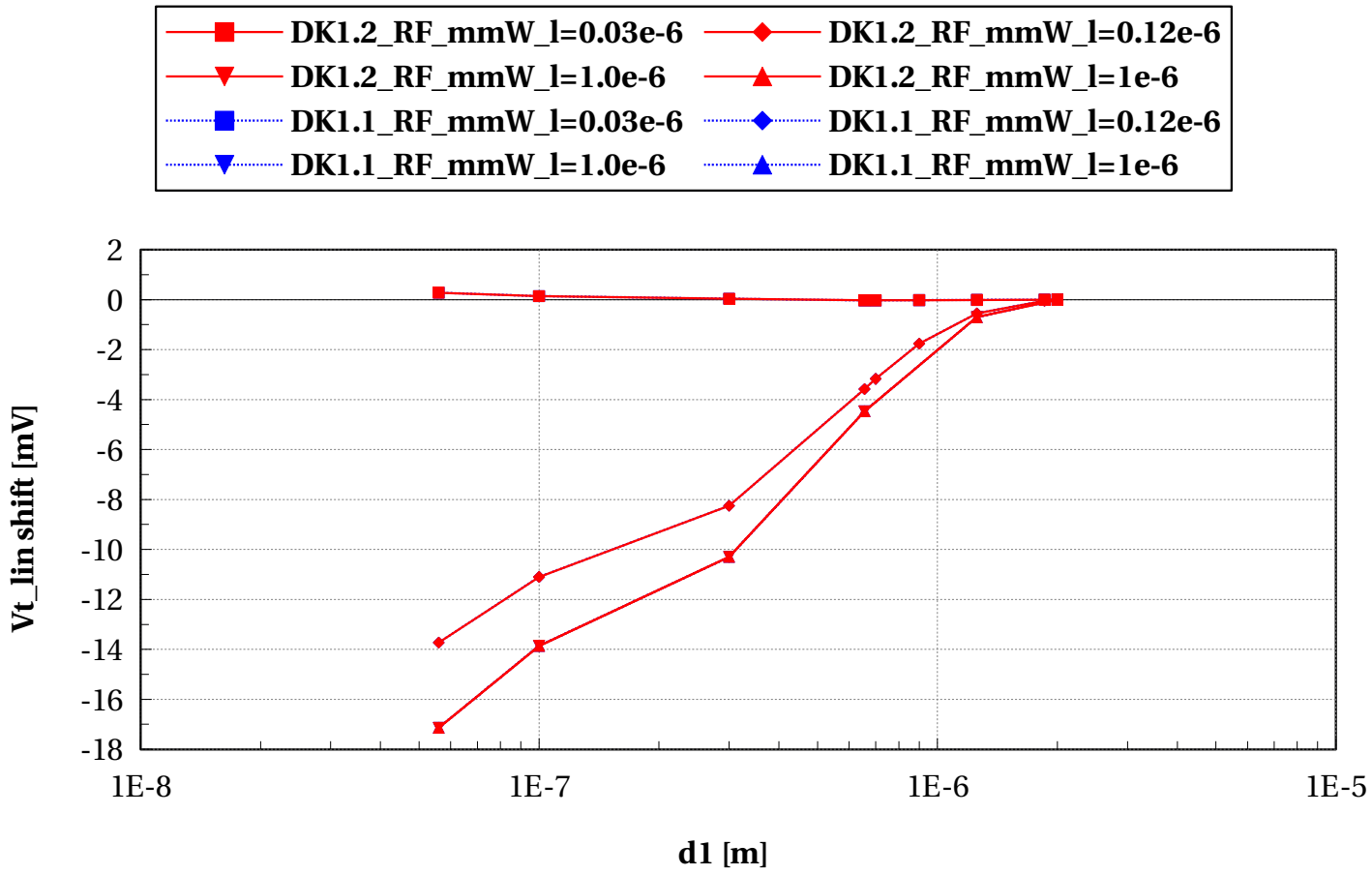
lvtpfet_acc, Vt_lin shift [mV] vs d1 [m]

temp==25 and d0==10e-6 and W==0.1e-6



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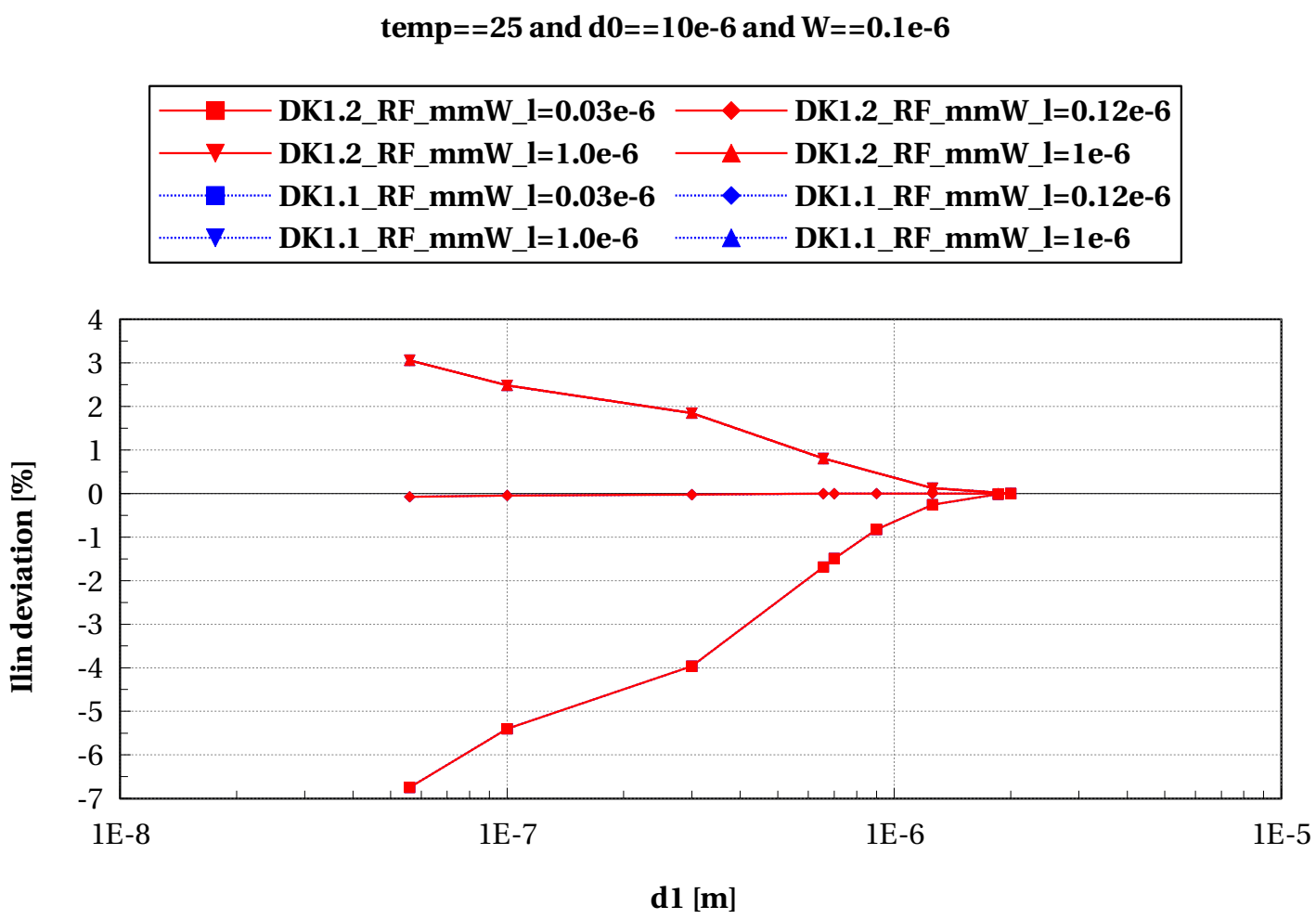
lvtpfet_acc, Ilin deviation [%] vs d1 [m]



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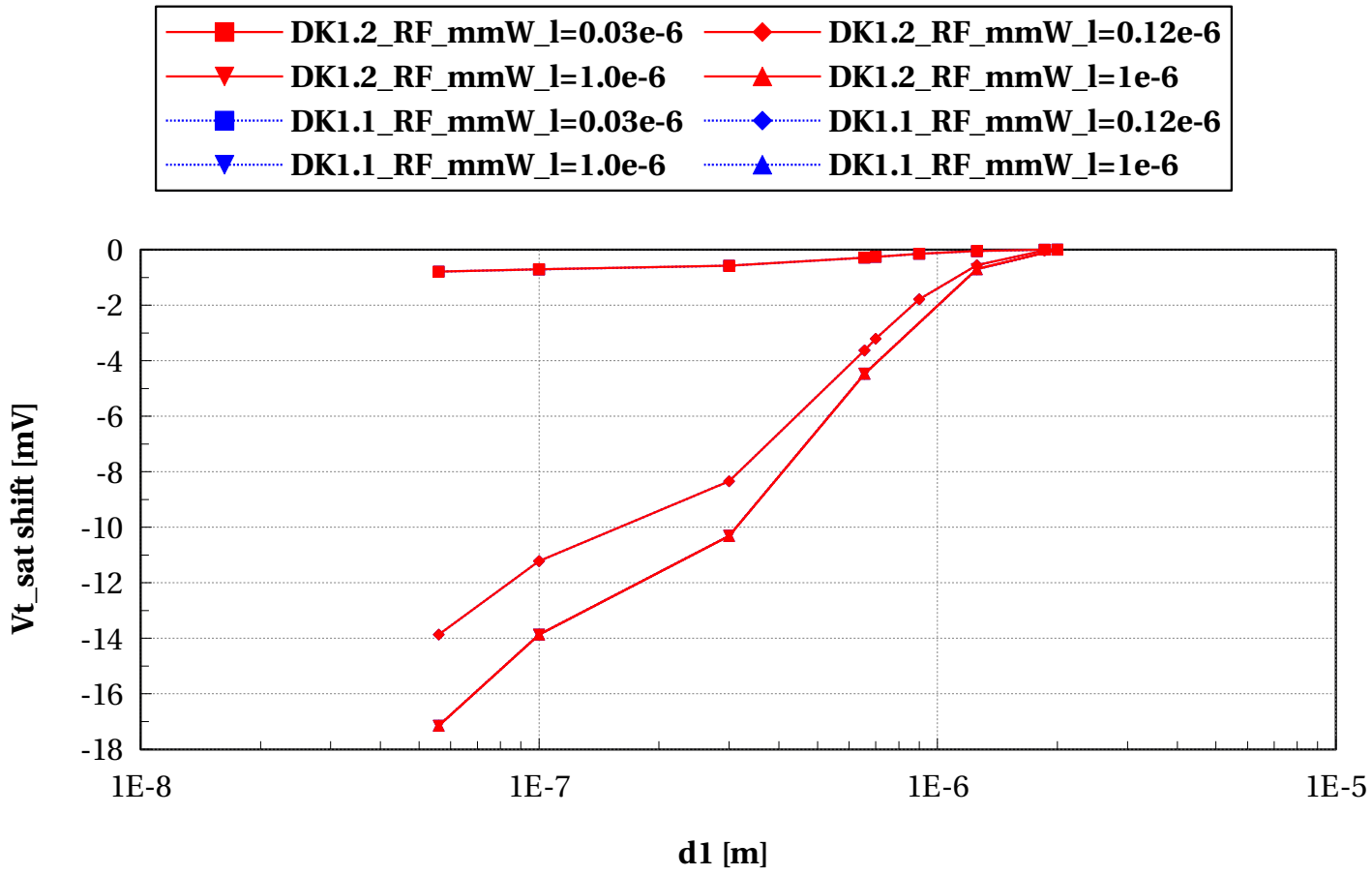
lvtpfet_acc, Vt_sat shift [mV] vs d1 [m]

temp==25 and d0==10e-6 and W==0.1e-6



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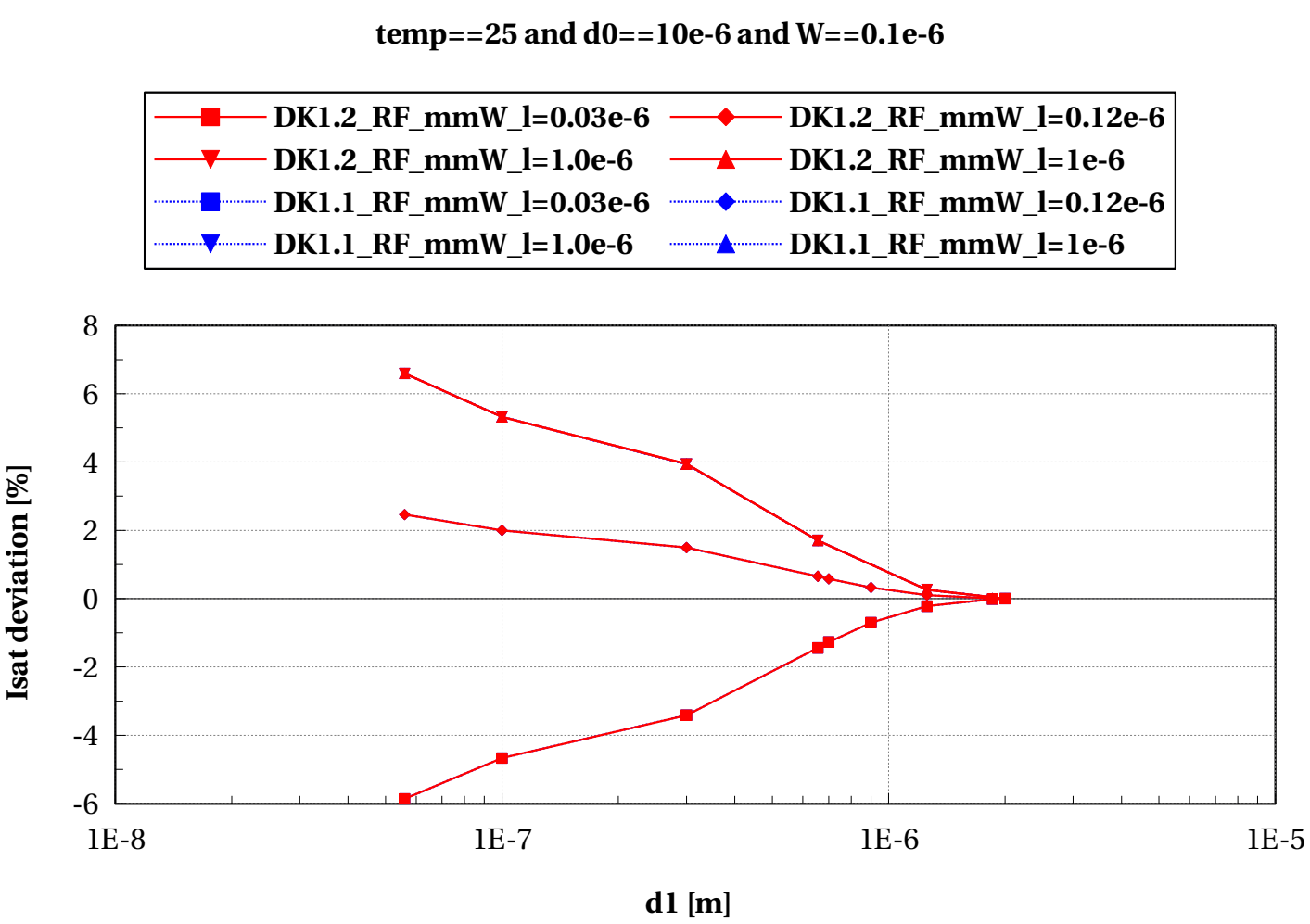
lvtpfet_acc, Isat deviation [%] vs d1 [m]



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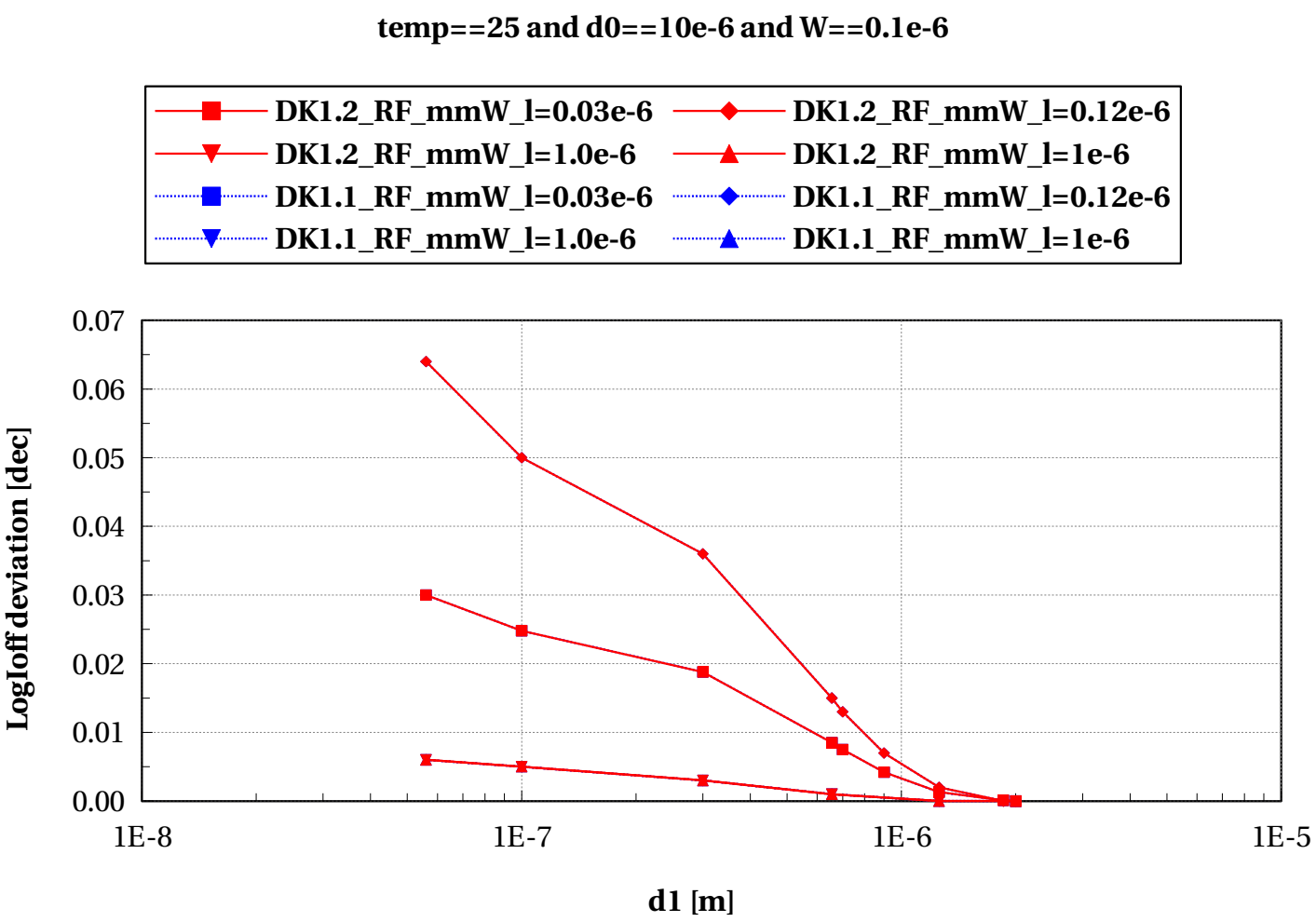
lvtpfet_acc, LogIoff deviation [dec] vs d1 [m]



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- WPE effect w.r.t d1 - Lscaling at W=1u

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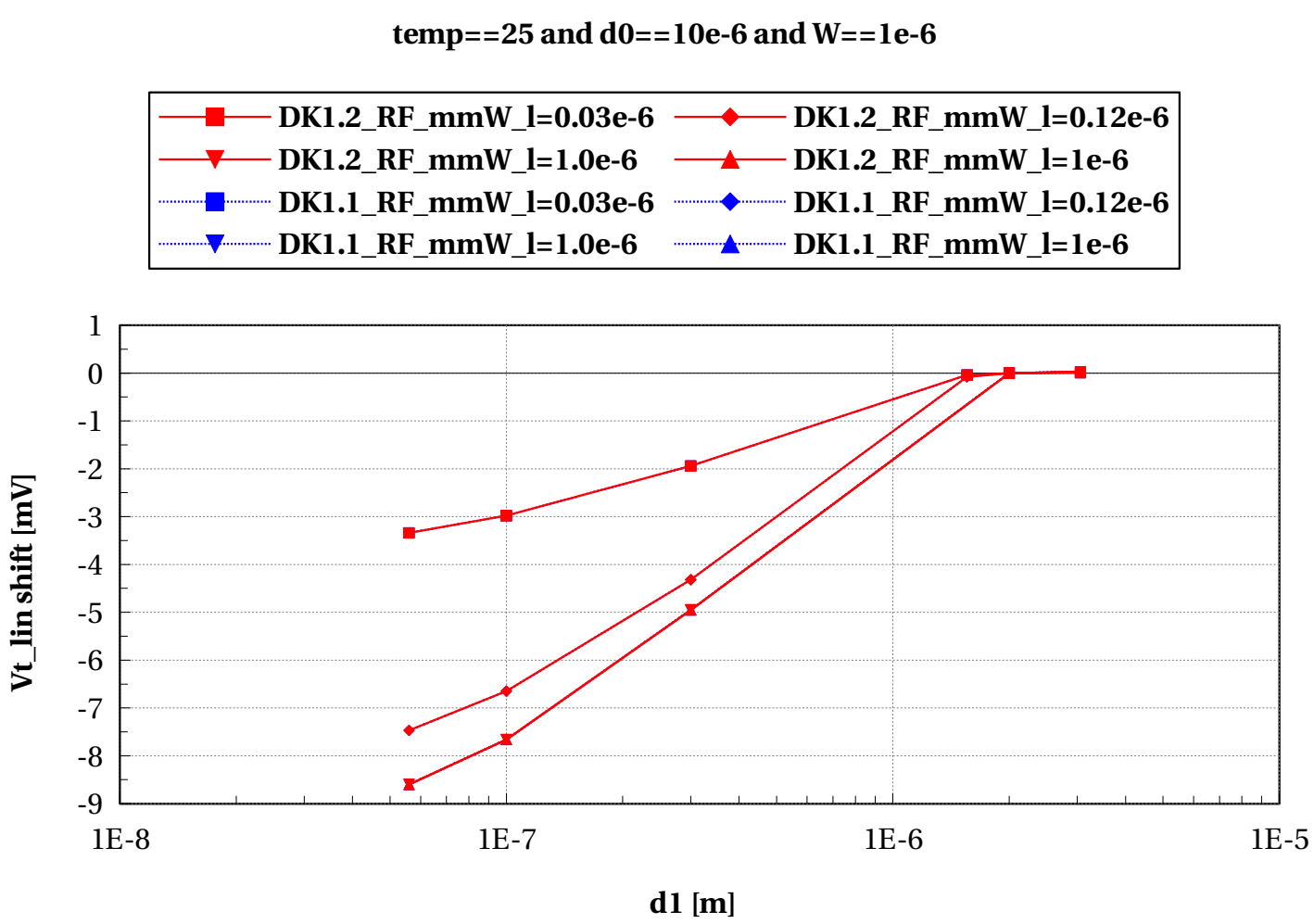
lvtpfet_acc, Vt_lin shift [mV] vs d1 [m]



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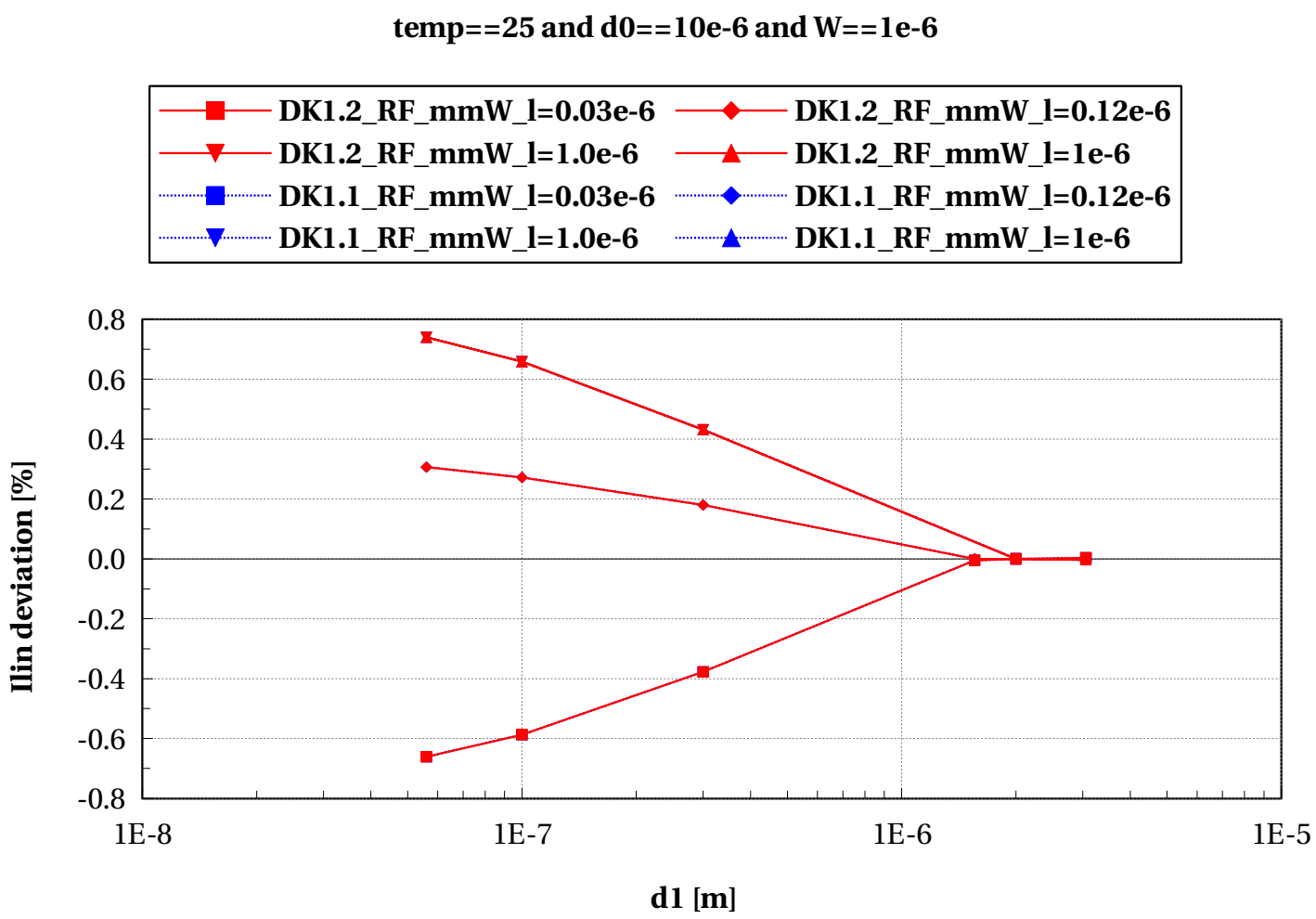
lvtpfet_acc, Ilin deviation [%] vs d1 [m]



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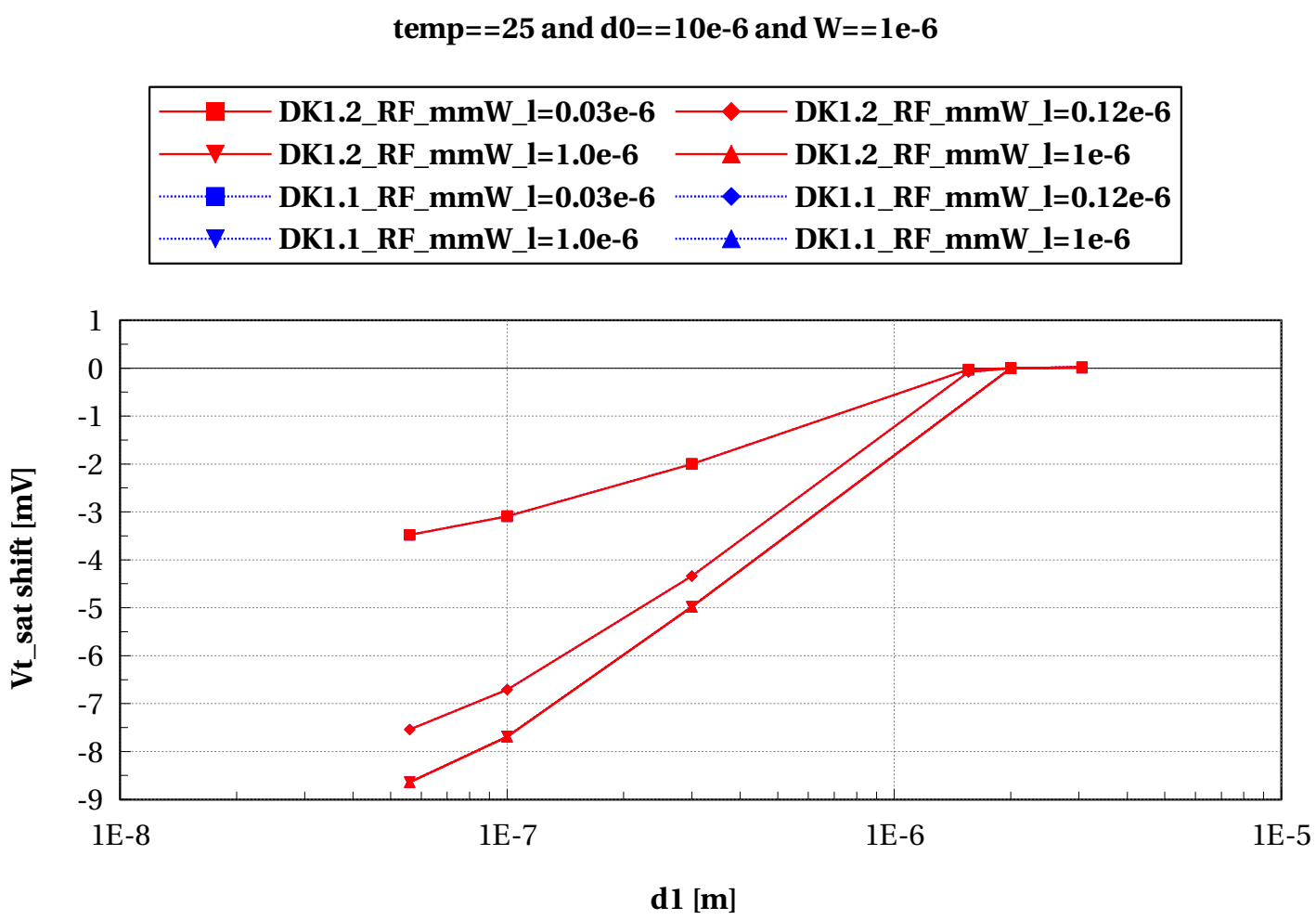
lvtpfet_acc, Vt_sat shift [mV] vs d1 [m]



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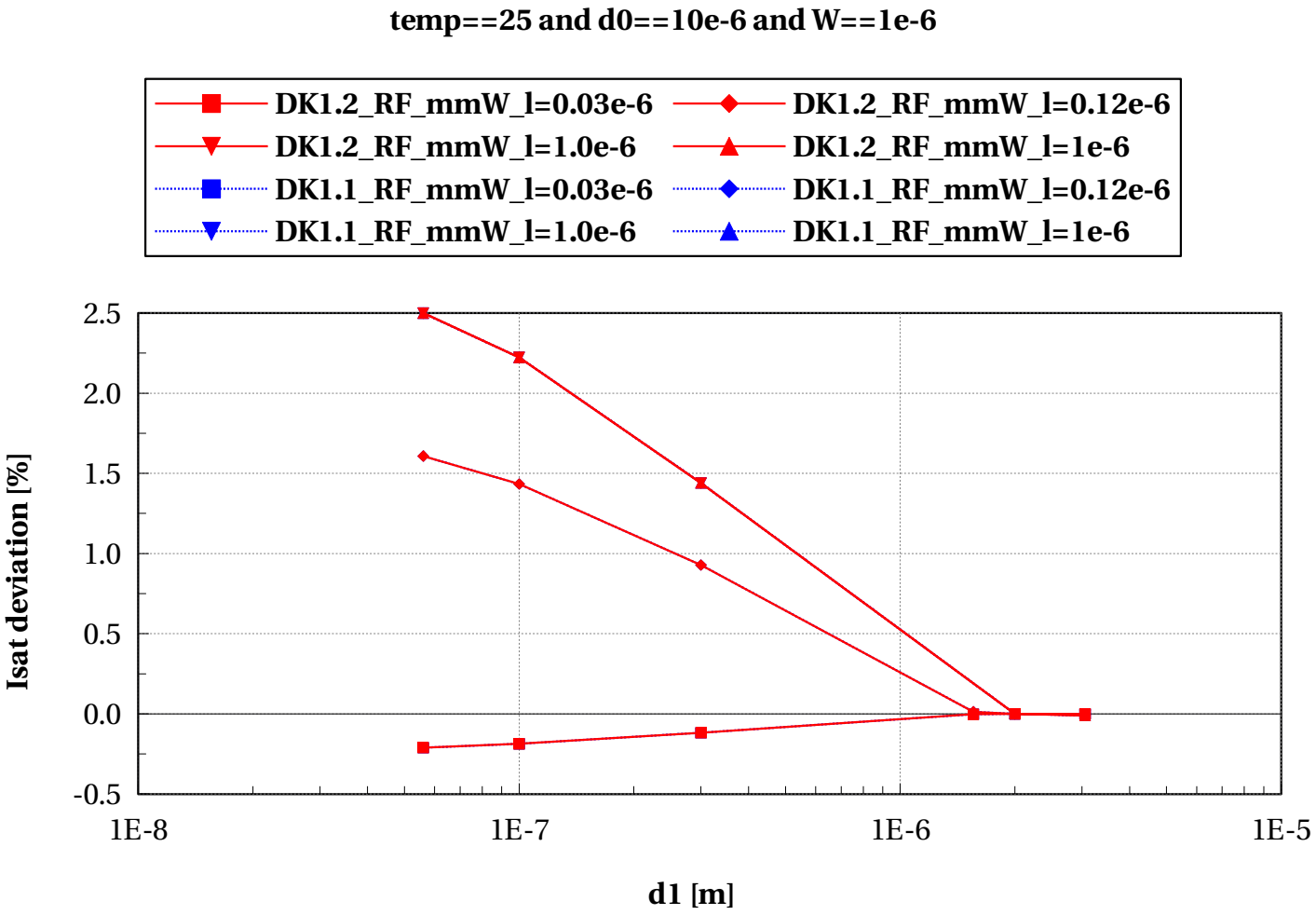
lvtpfet_acc, Isat deviation [%] vs d1 [m]



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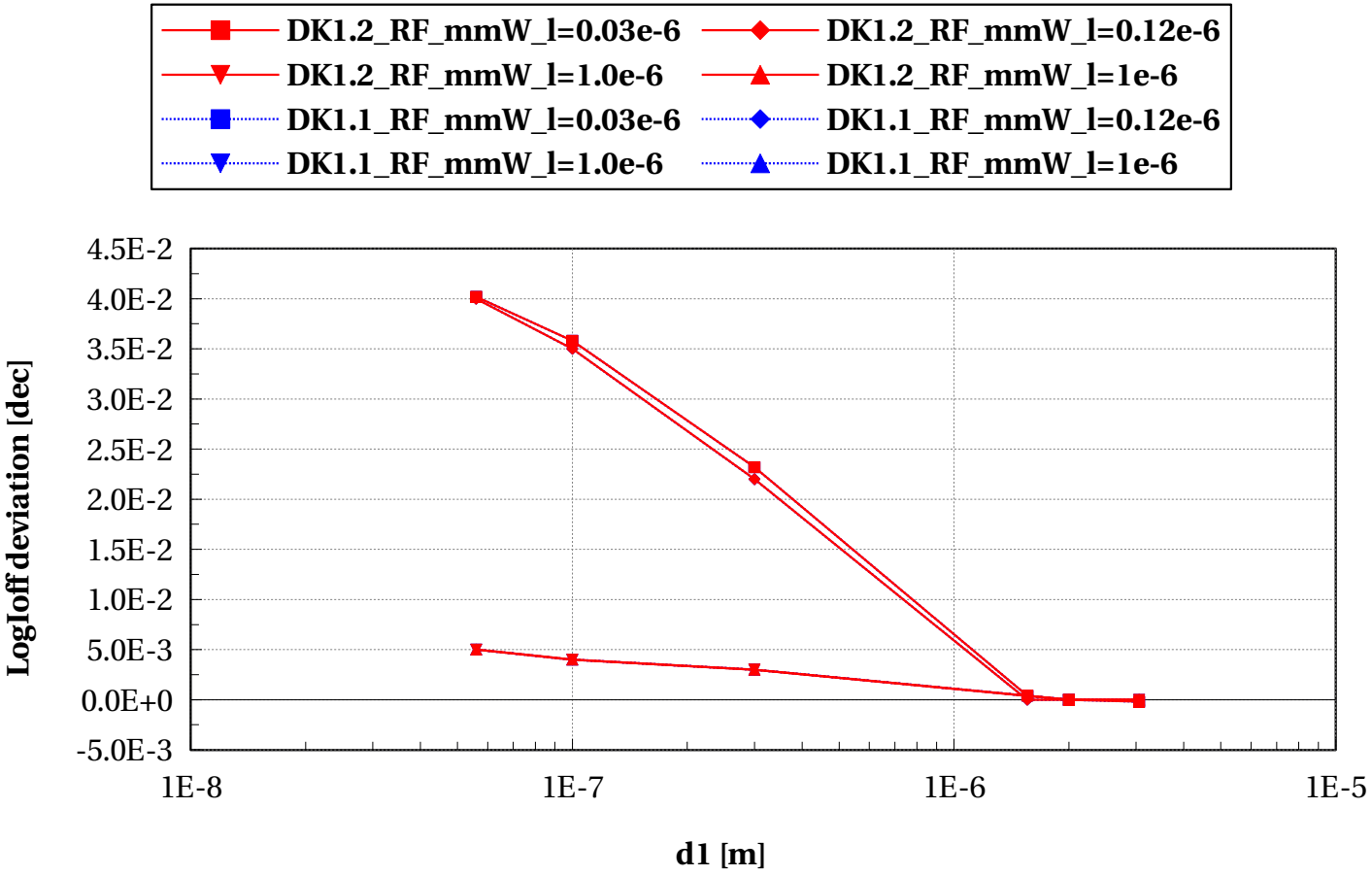
lvtpfet_acc, LogIoff deviation [dec] vs d1 [m]

temp==25 and d0==10e-6 and W==1e-6



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nfet_acc
Electrical characteristics scaling

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- WPE effect w.r.t d0 - Lscaling at W=1u

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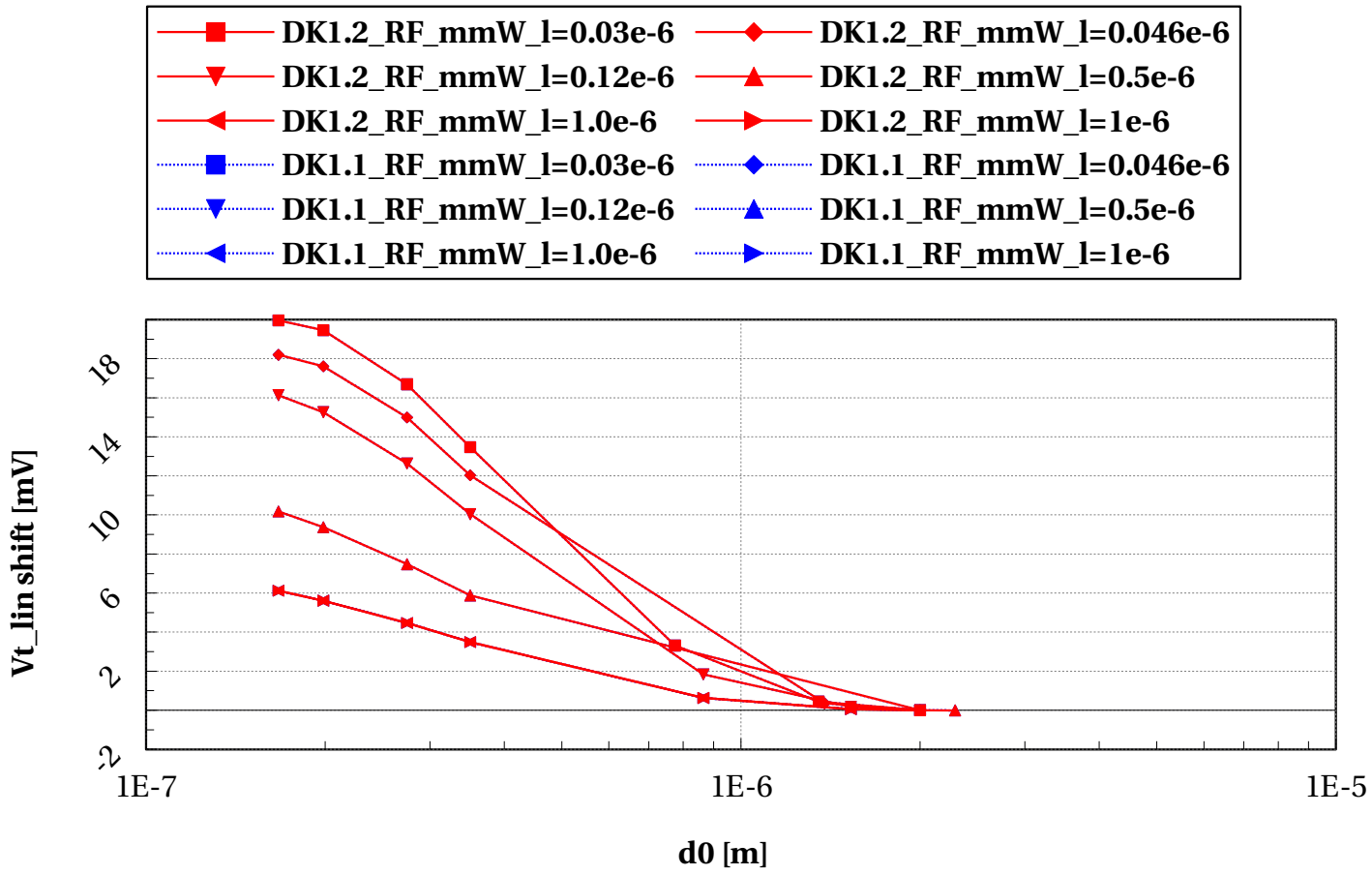
nfet_acc, Vt_lin shift [mV] vs d0 [m]

temp==25 and d1==10e-6 and W==1e-6



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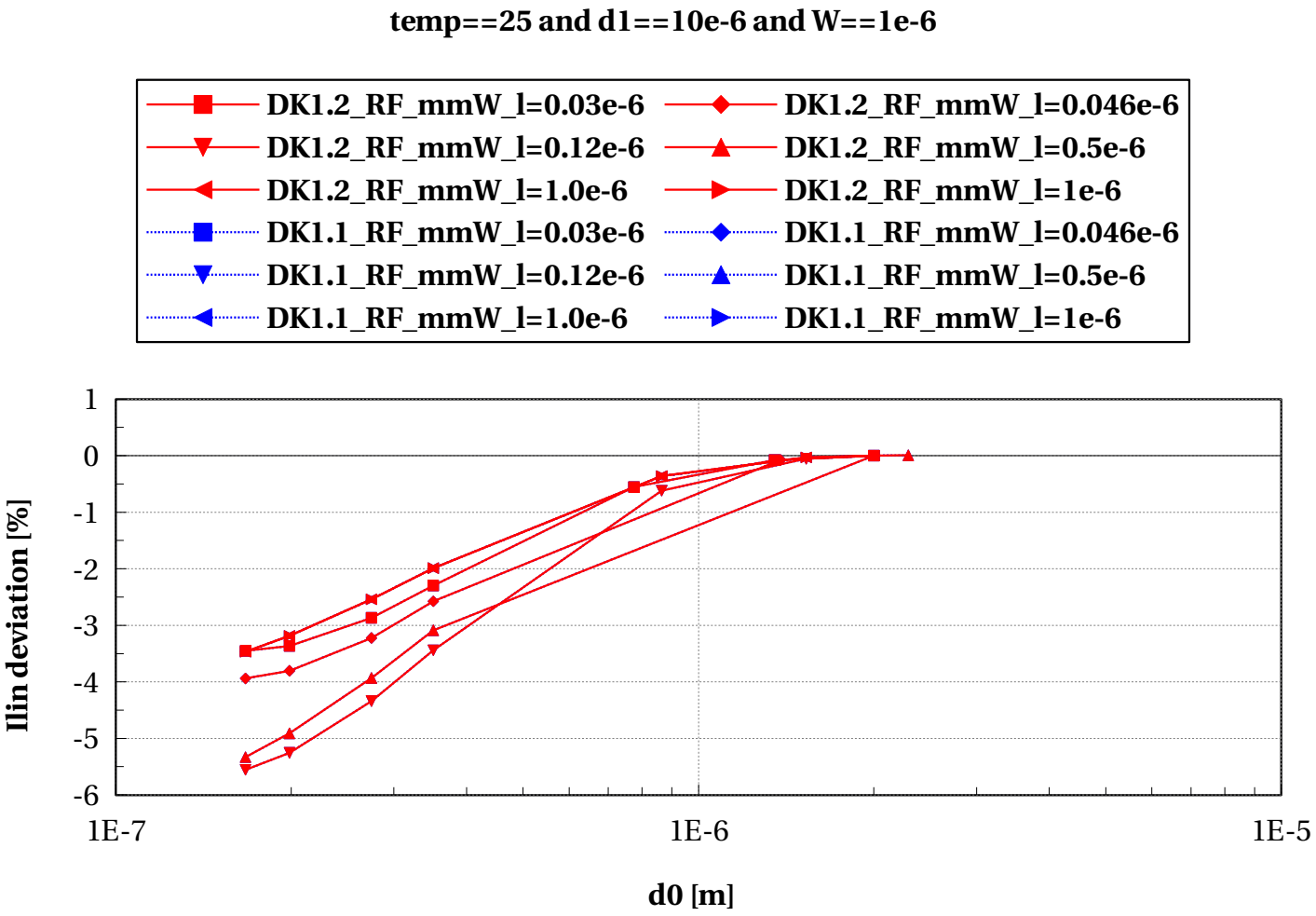
nfet_acc, Ilin deviation [%] vs d0 [m]



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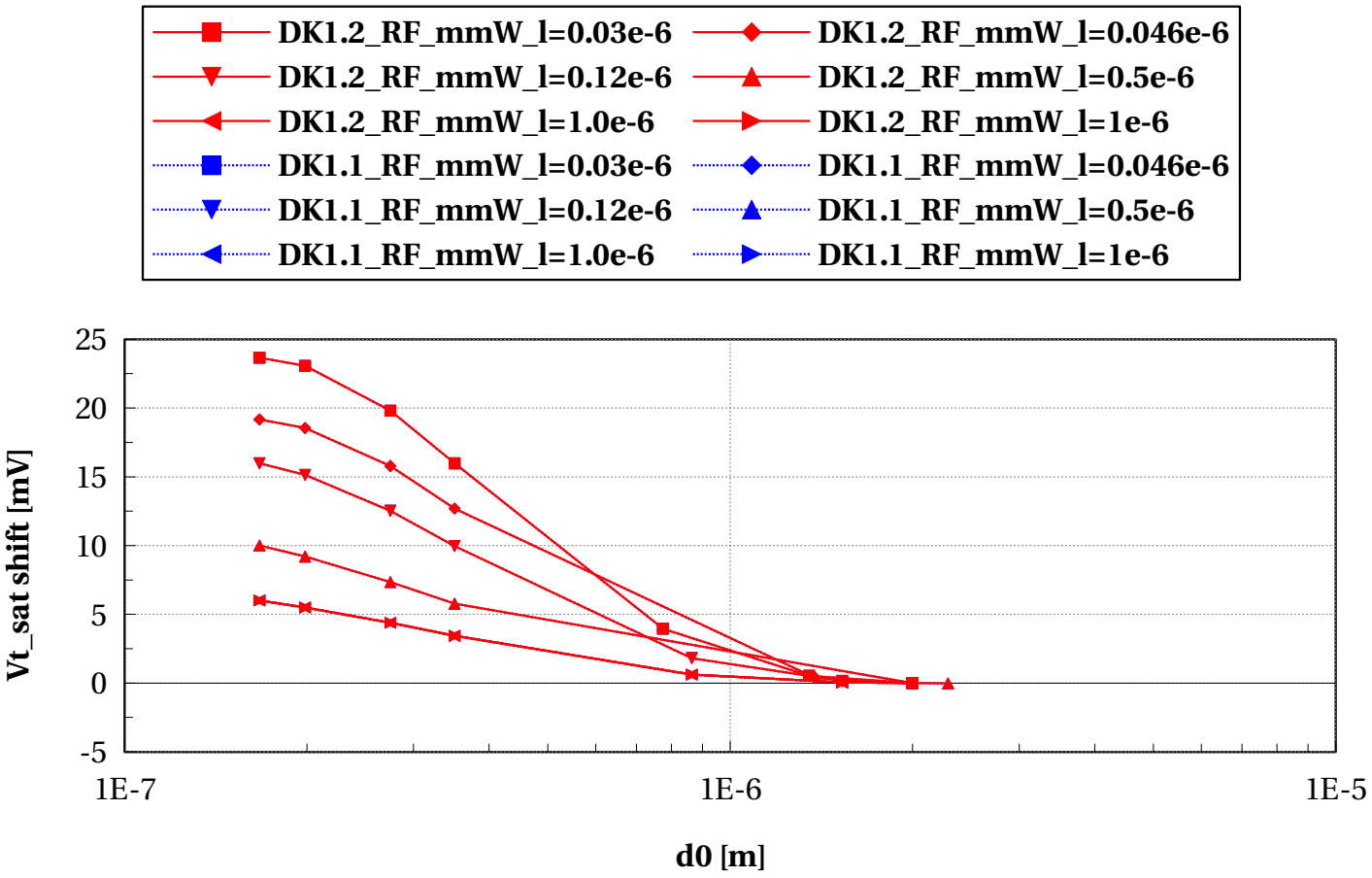
nfet_acc, Vt_sat shift [mV] vs d0 [m]

temp==25 and d1==10e-6 and W==1e-6



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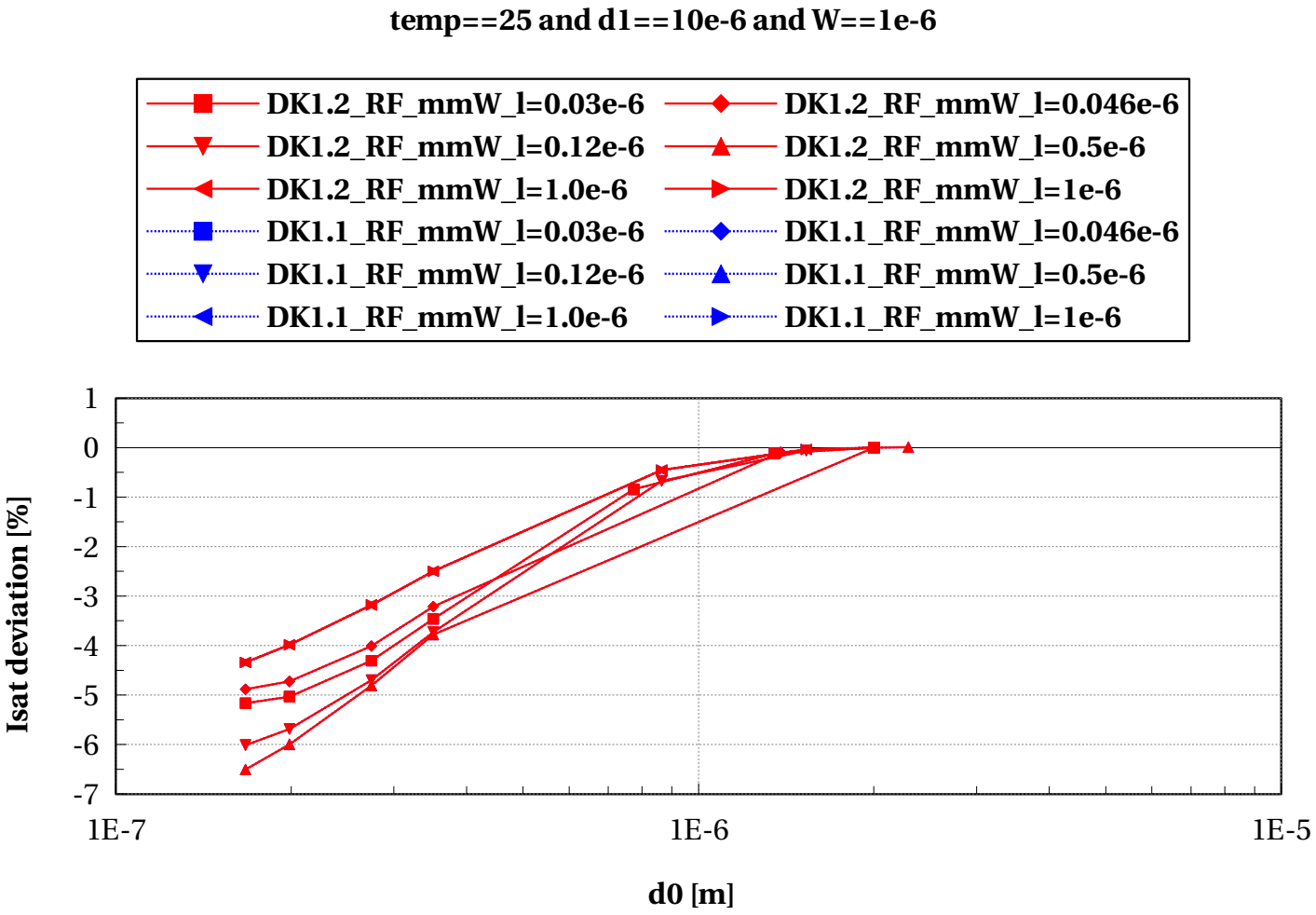
nfet_acc, Isat deviation [%] vs d0 [m]



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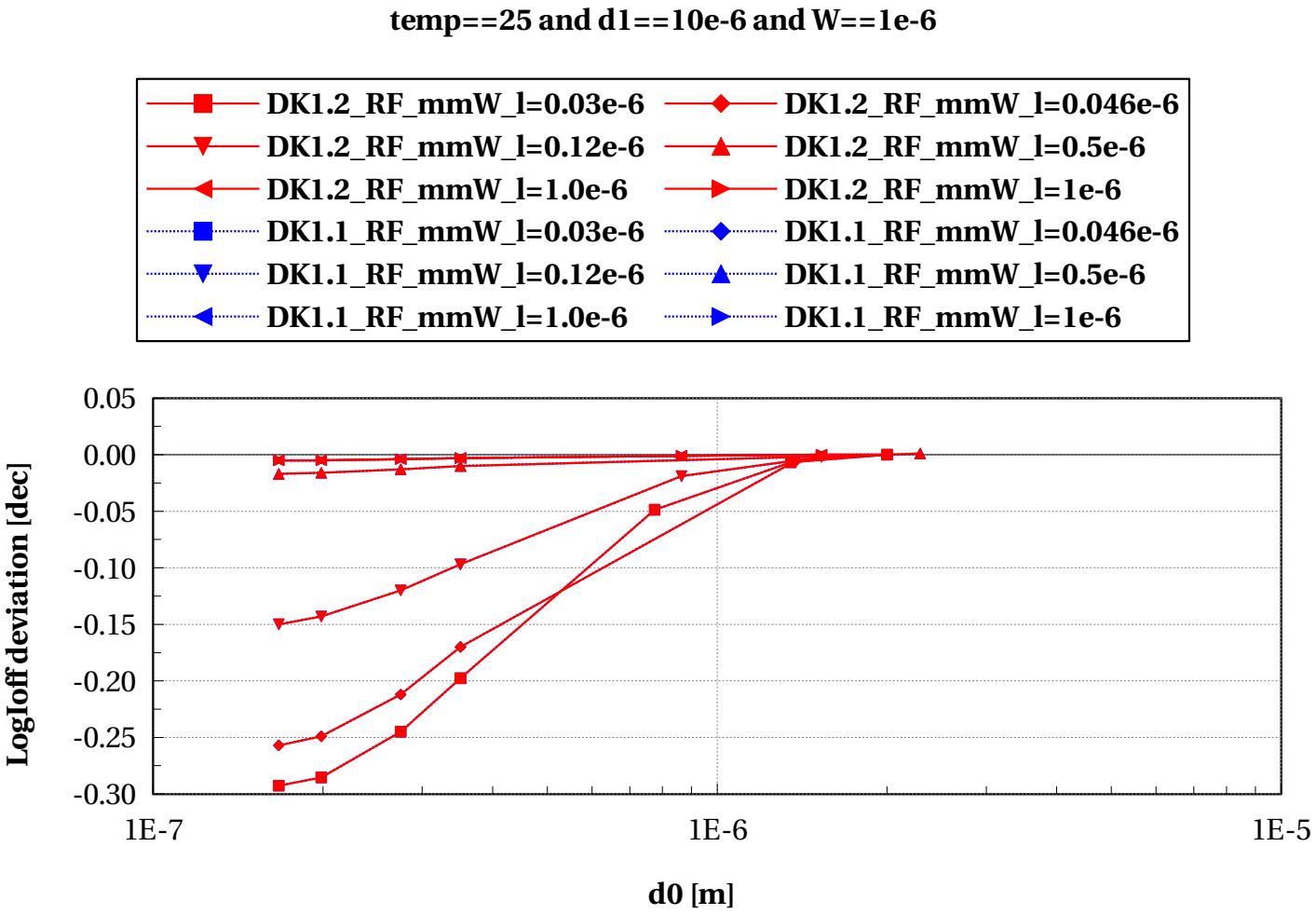
nfet_acc, LogIoff deviation [dec] vs d0 [m]



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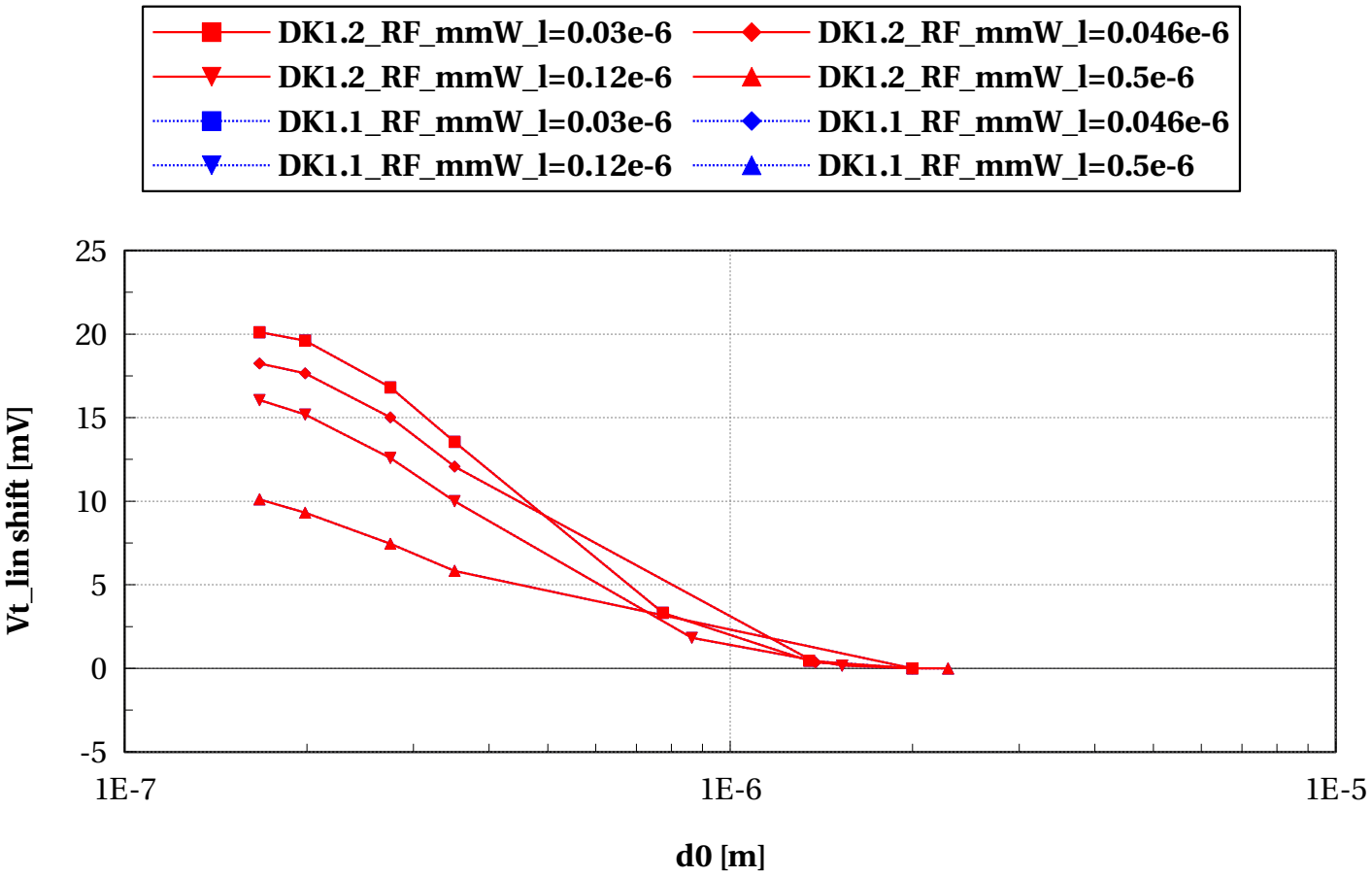
nfet_acc, Vt_lin shift [mV] vs d0 [m]

temp==25 and d1==10e-6 and W==0.34e-6



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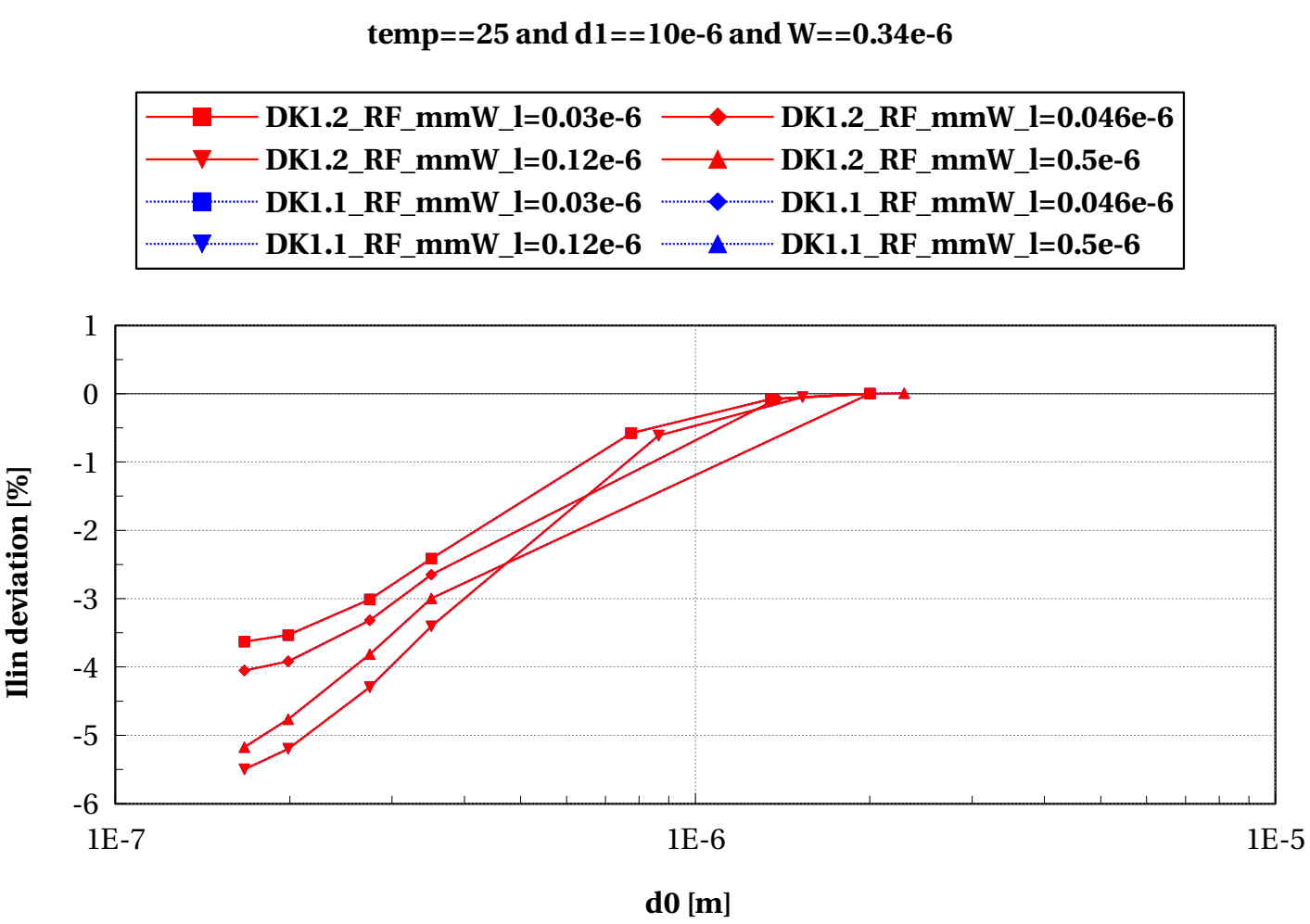
nfet_acc, Ilin deviation [%] vs d0 [m]



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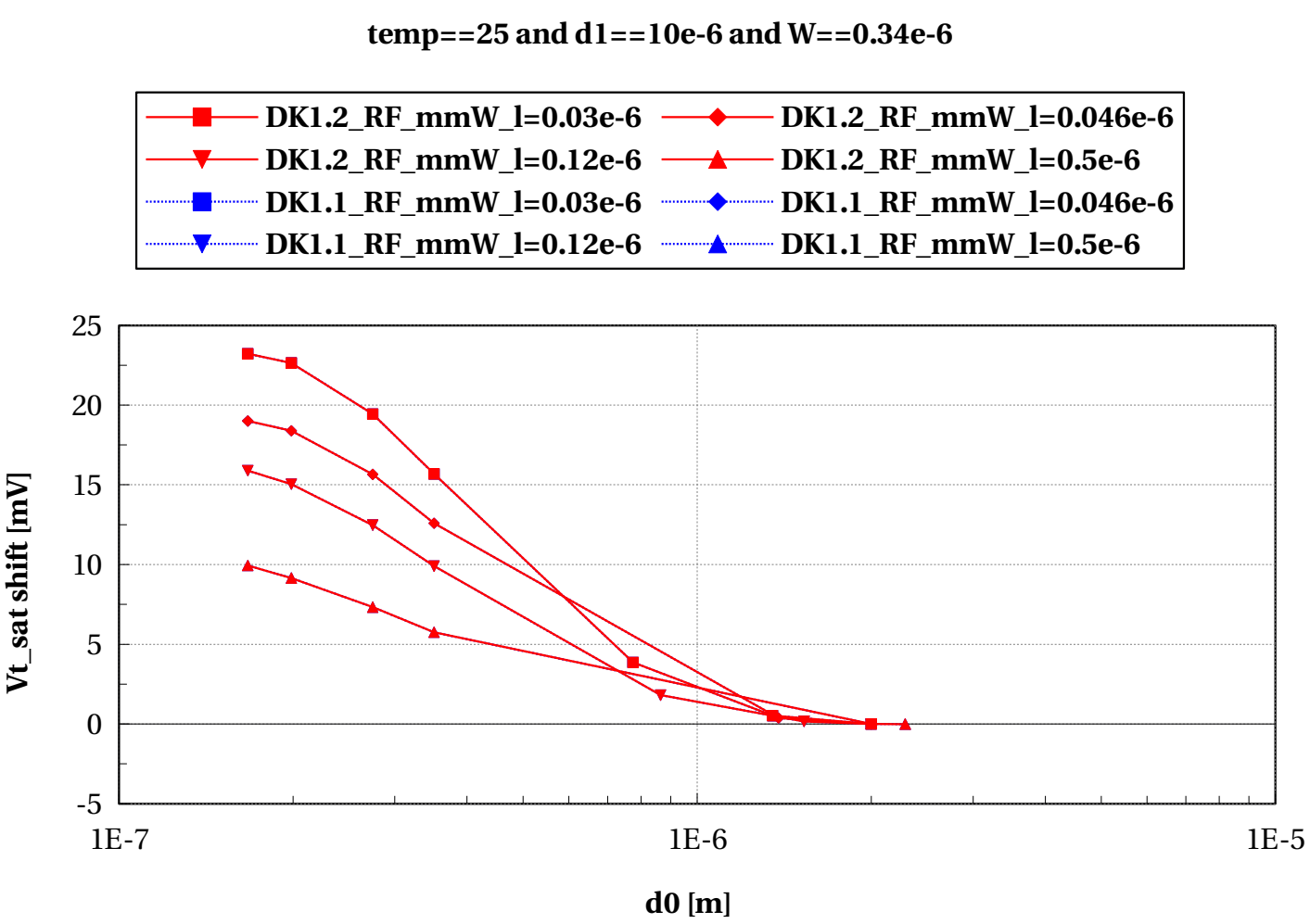
nfet_acc, Vt_sat shift [mV] vs d0 [m]



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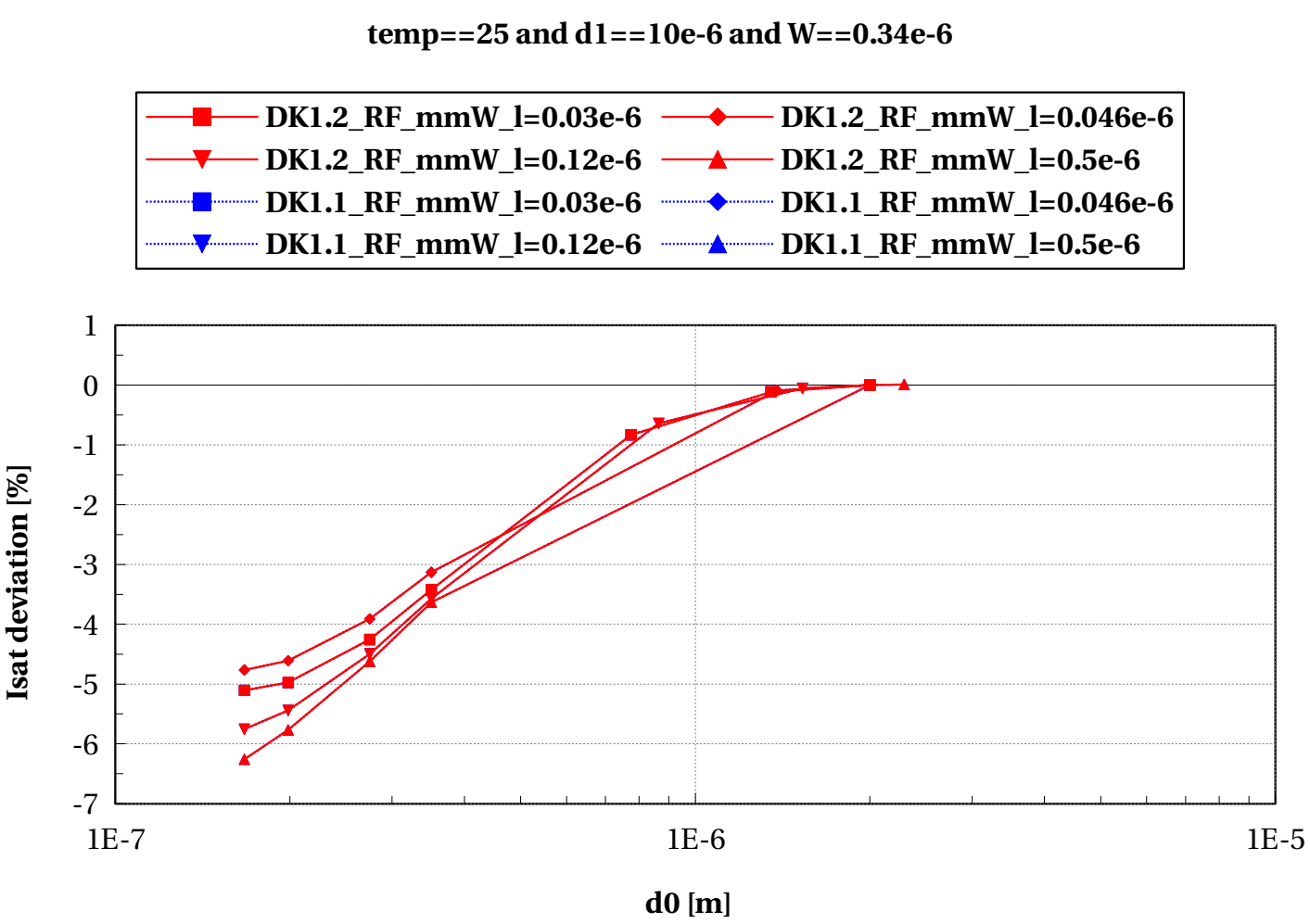
nfet_acc, Isat deviation [%] vs d0 [m]



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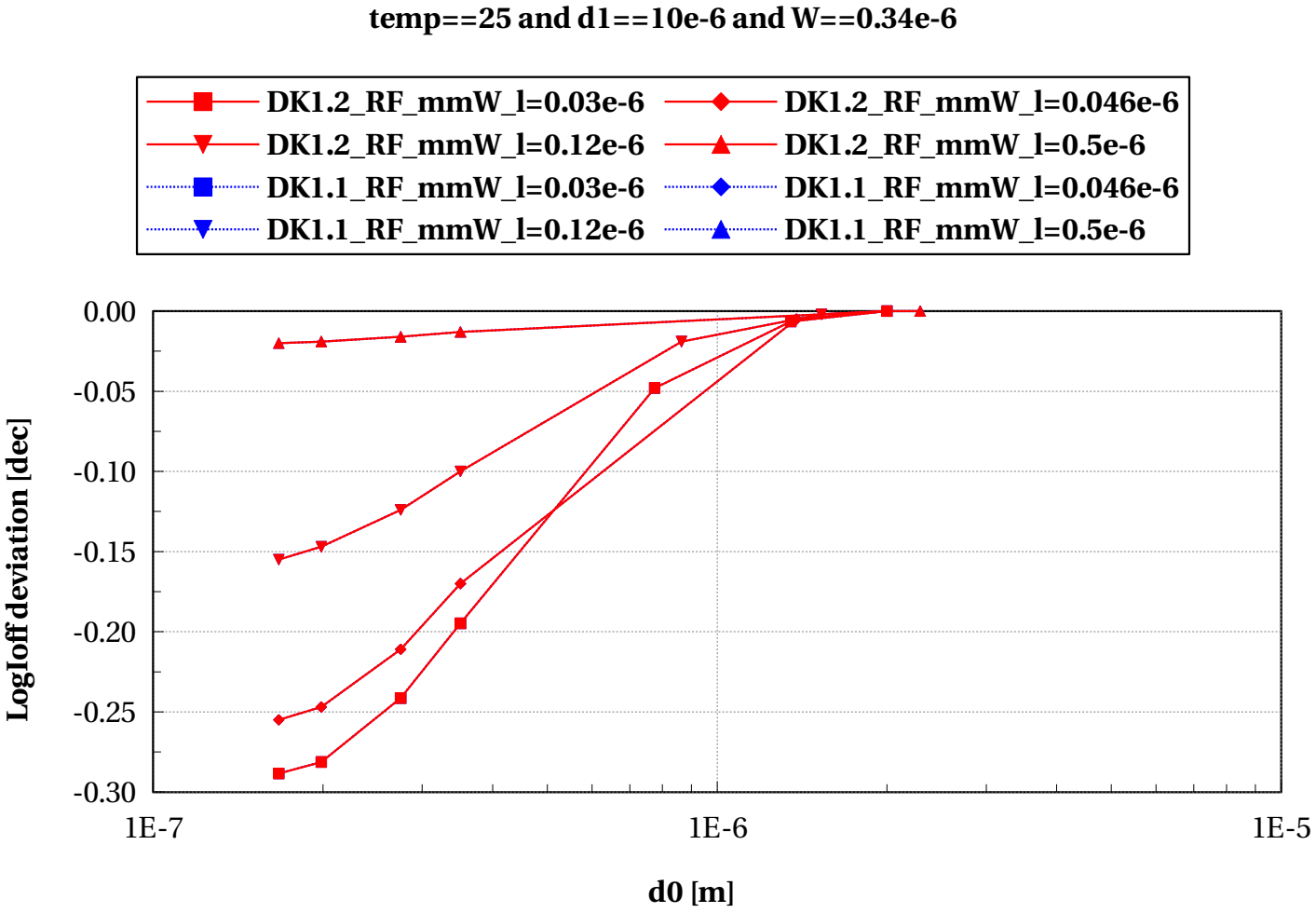
nfet_acc, LogIoff deviation [dec] vs d0 [m]



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- WPE effect w.r.t d0 - Lscaling at W=0.1u

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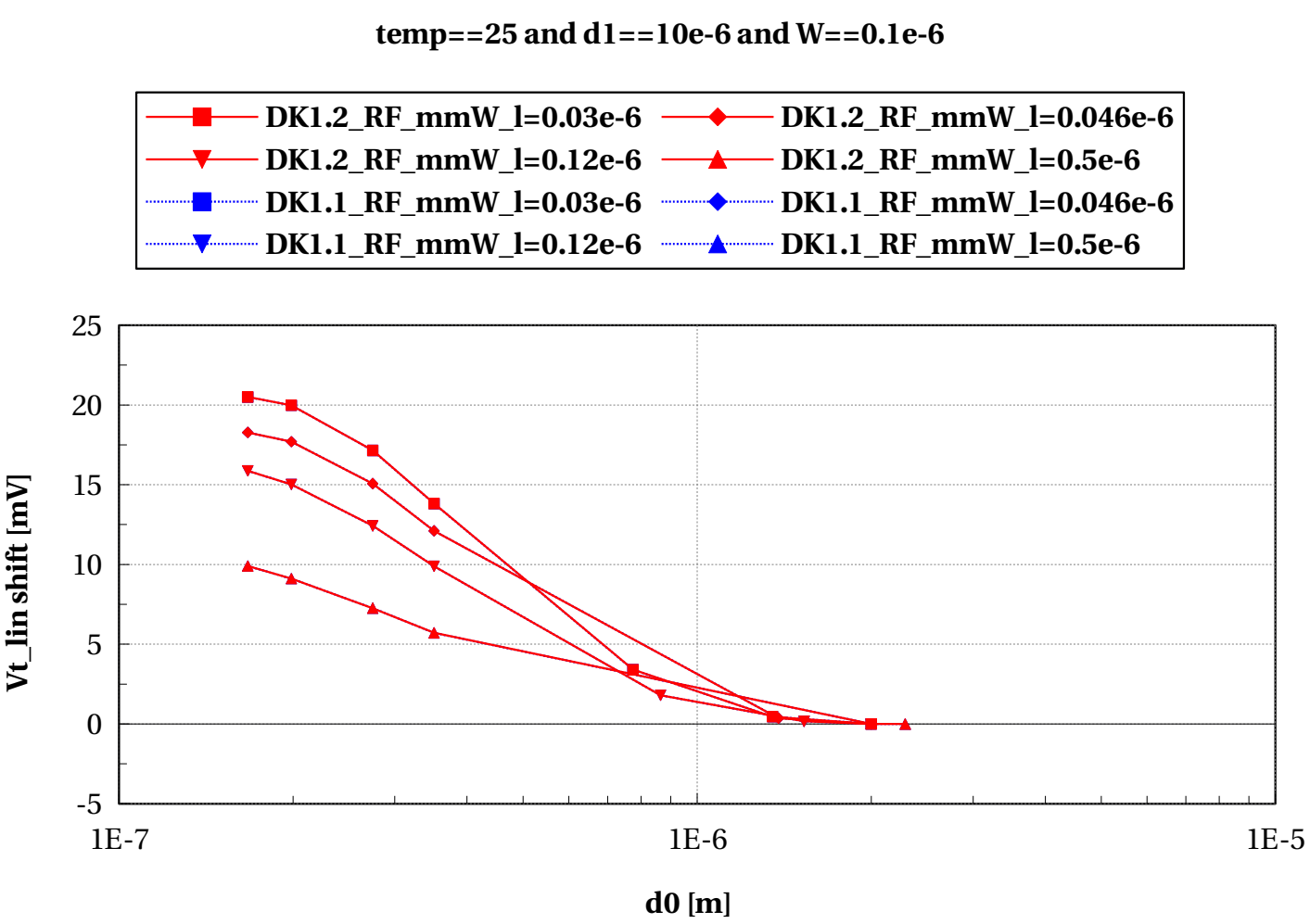
nfet_acc, Vt_lin shift [mV] vs d0 [m]



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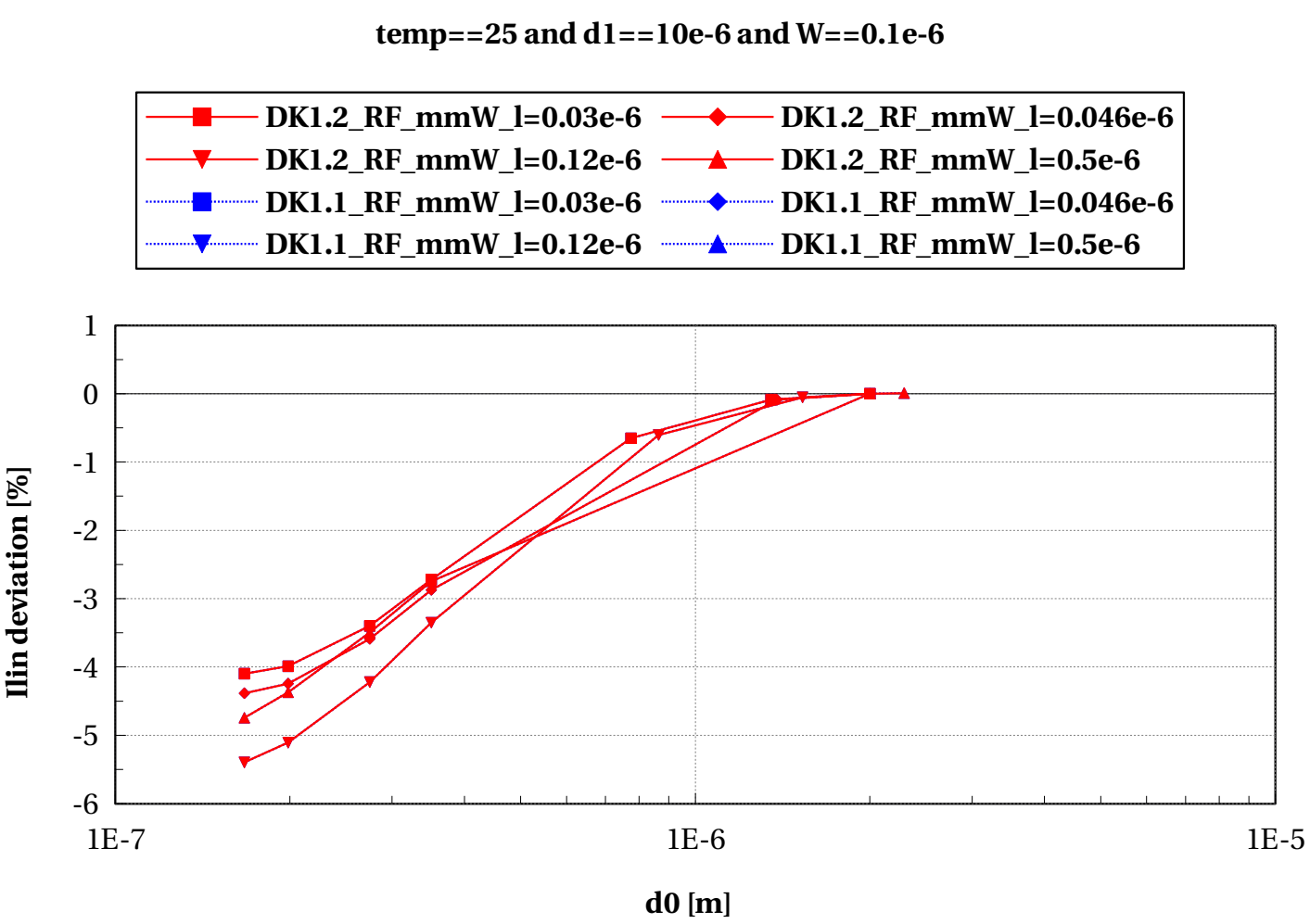
nfet_acc, Ilin deviation [%] vs d0 [m]



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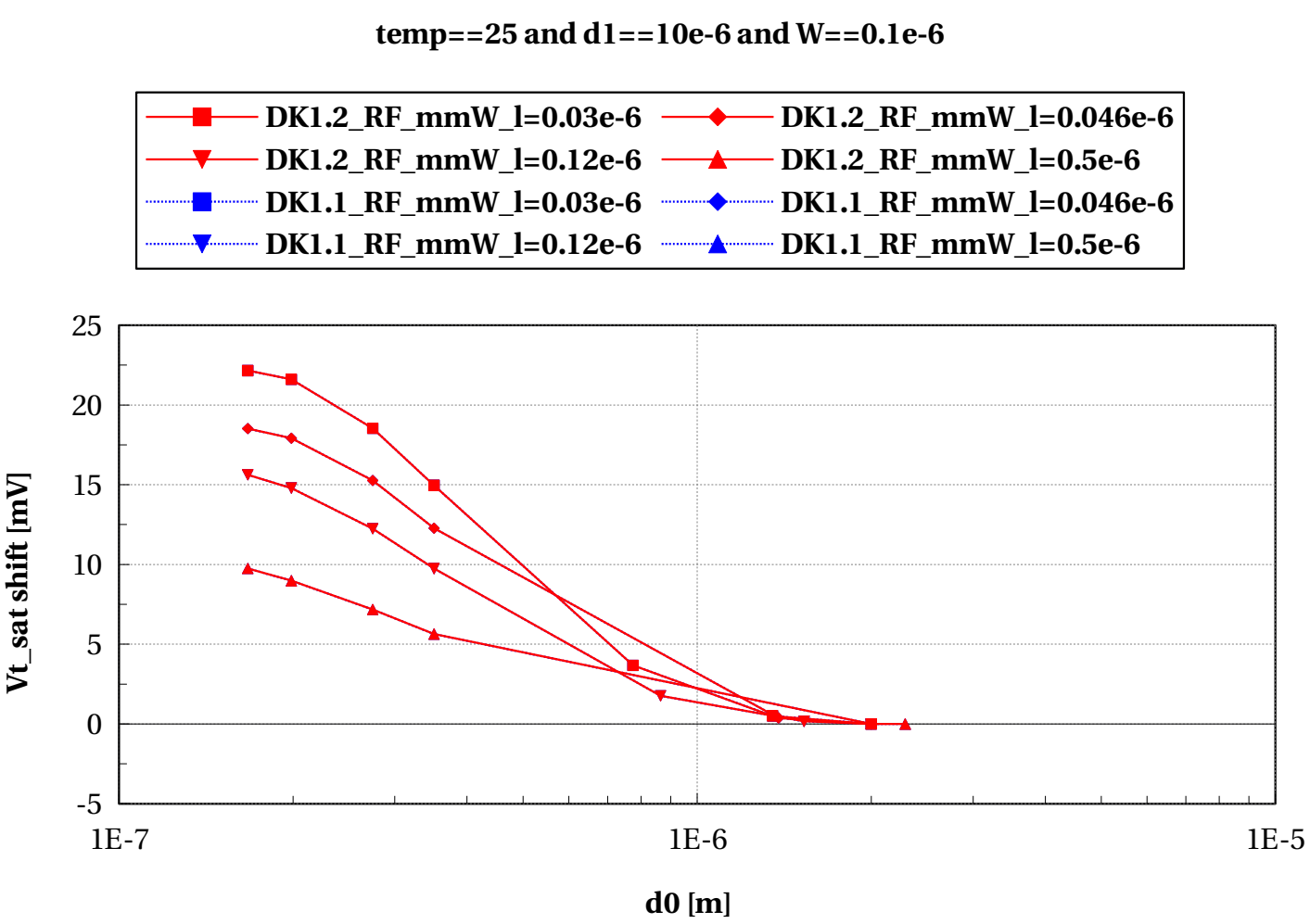
nfet_acc, Vt_sat shift [mV] vs d0 [m]



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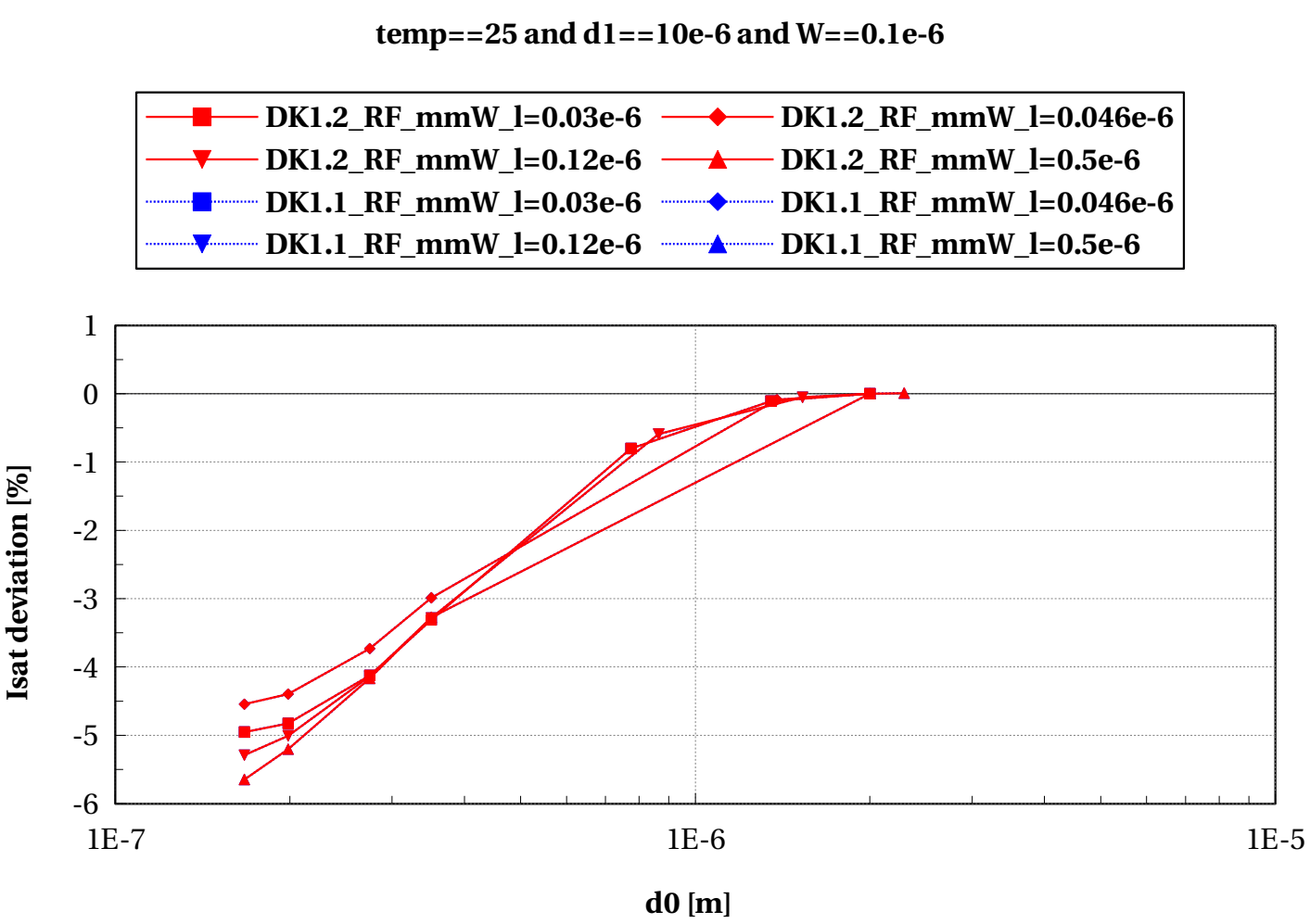
nfet_acc, Isat deviation [%] vs d0 [m]



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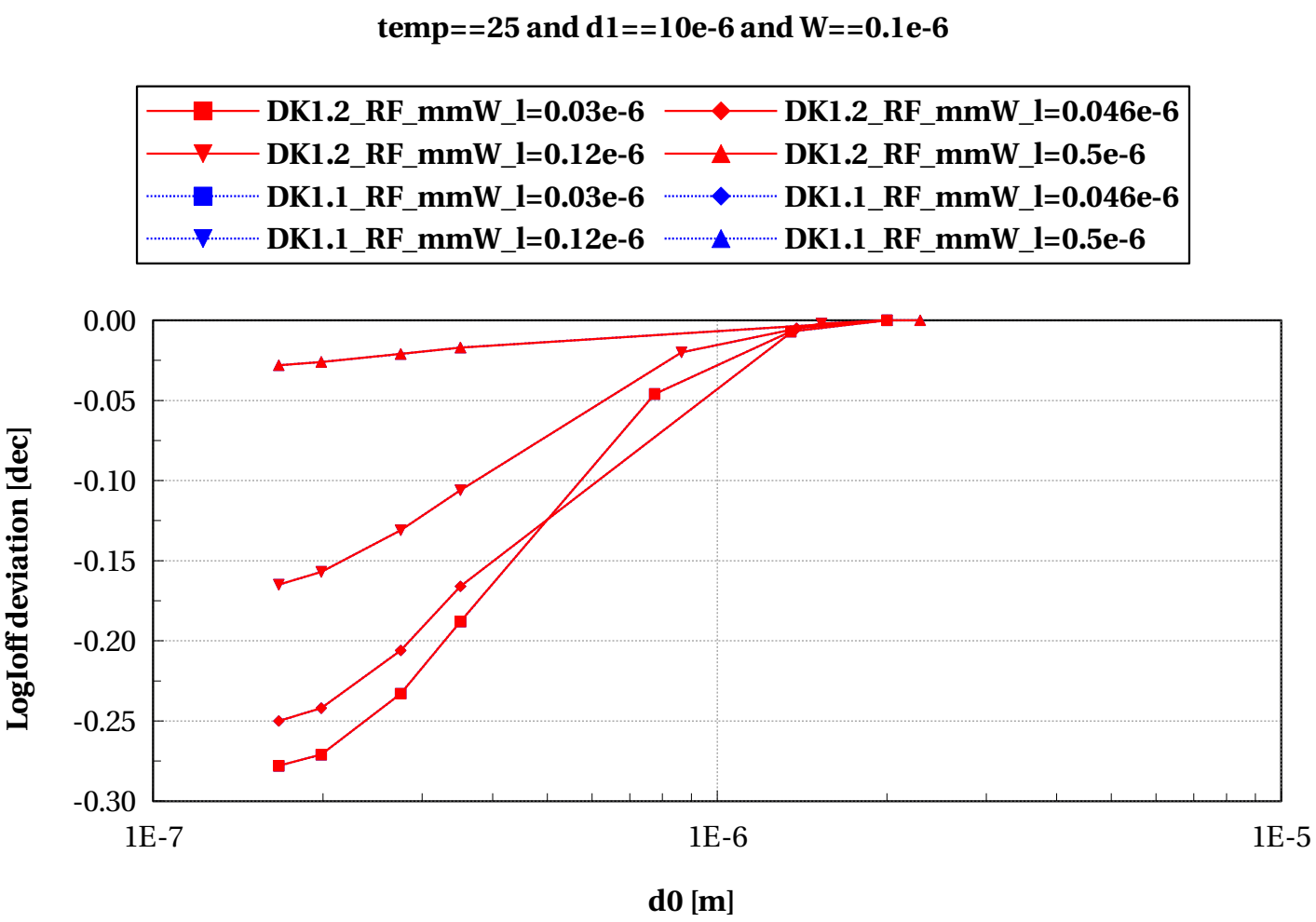
nfet_acc, LogIoff deviation [dec] vs d0 [m]



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- WPE effect w.r.t d0 - Wscaling at L=0.03u

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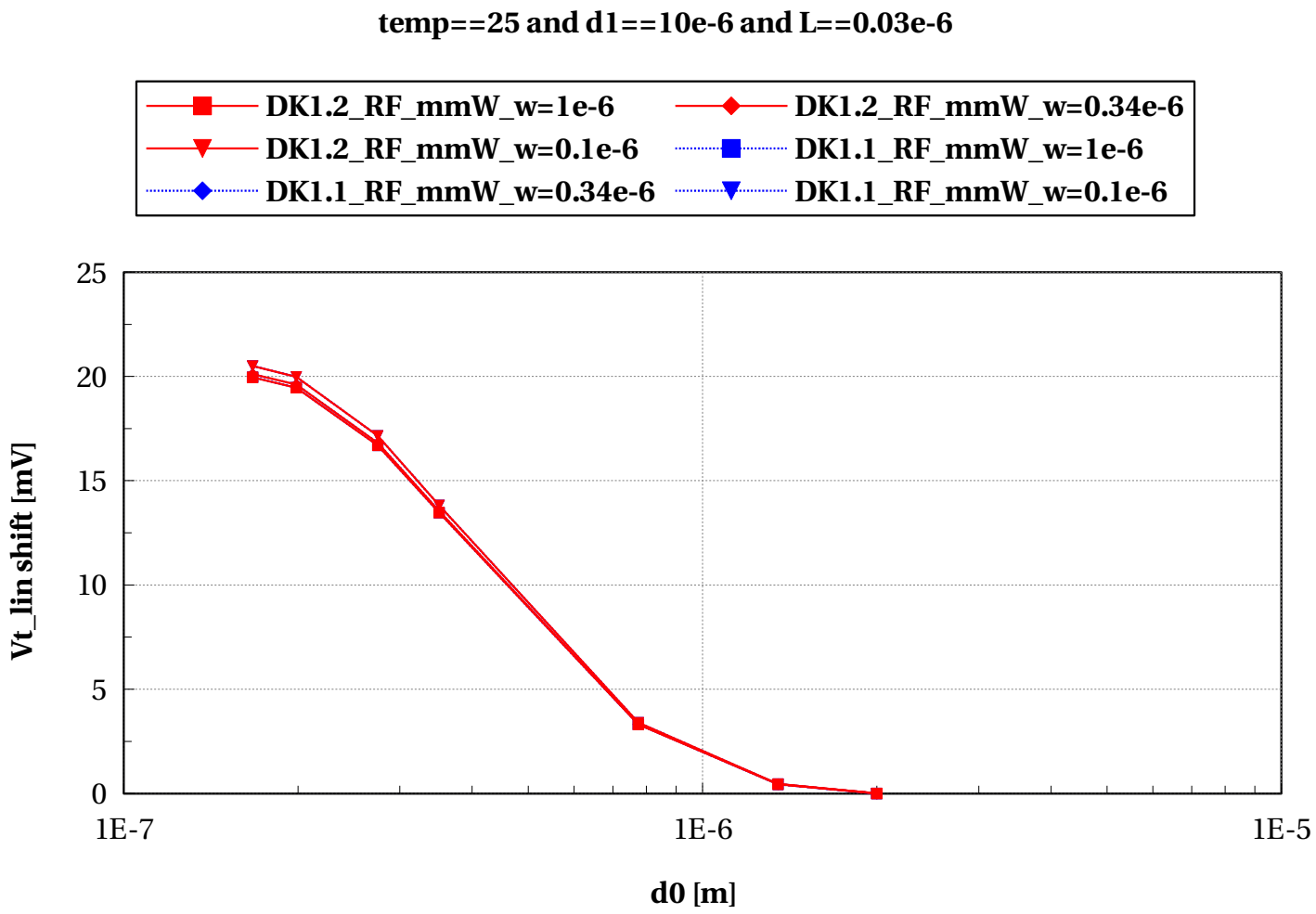
nfet_acc, Vt_lin shift [mV] vs d0 [m]



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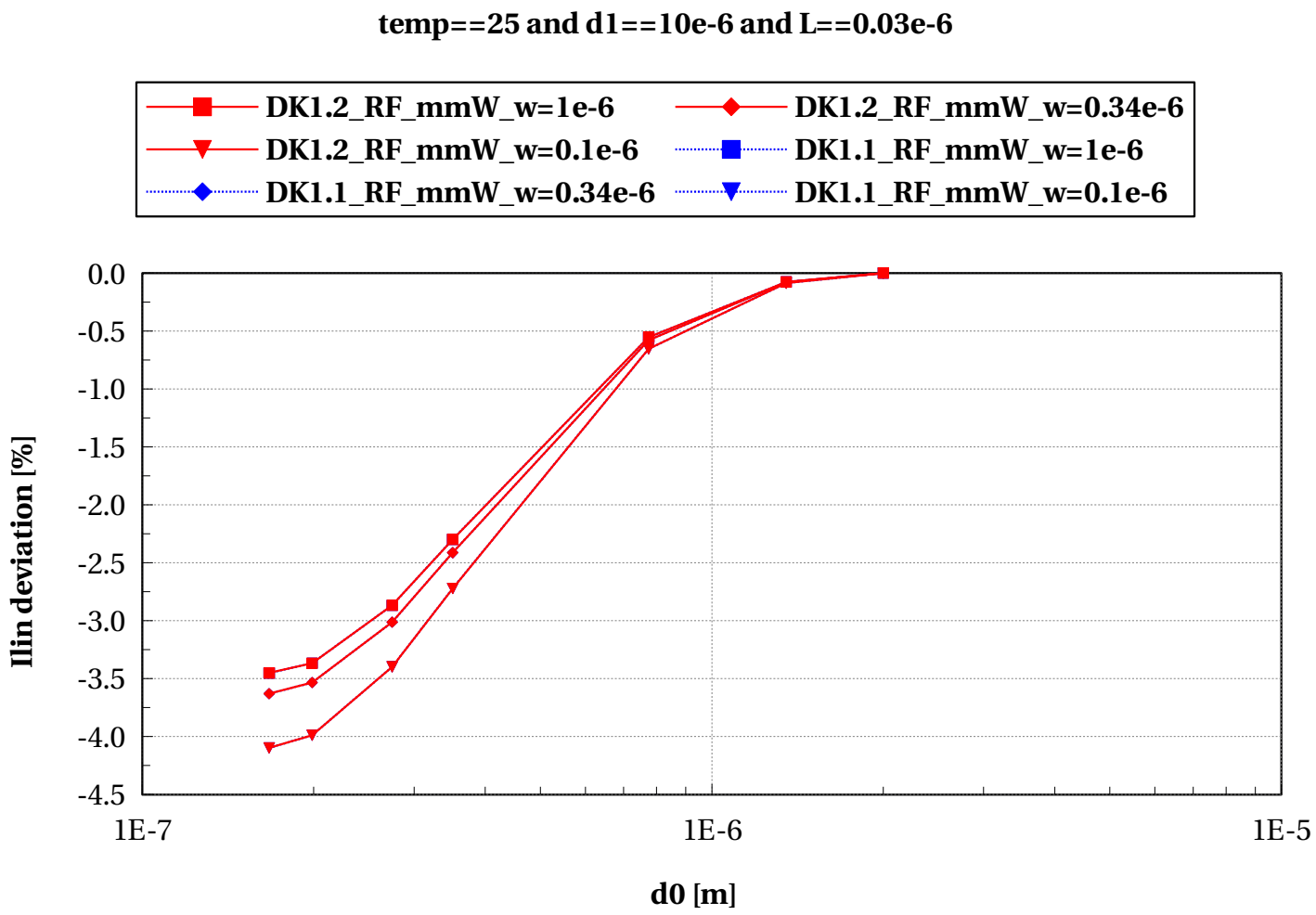
nfet_acc, Ilin deviation [%] vs d0 [m]



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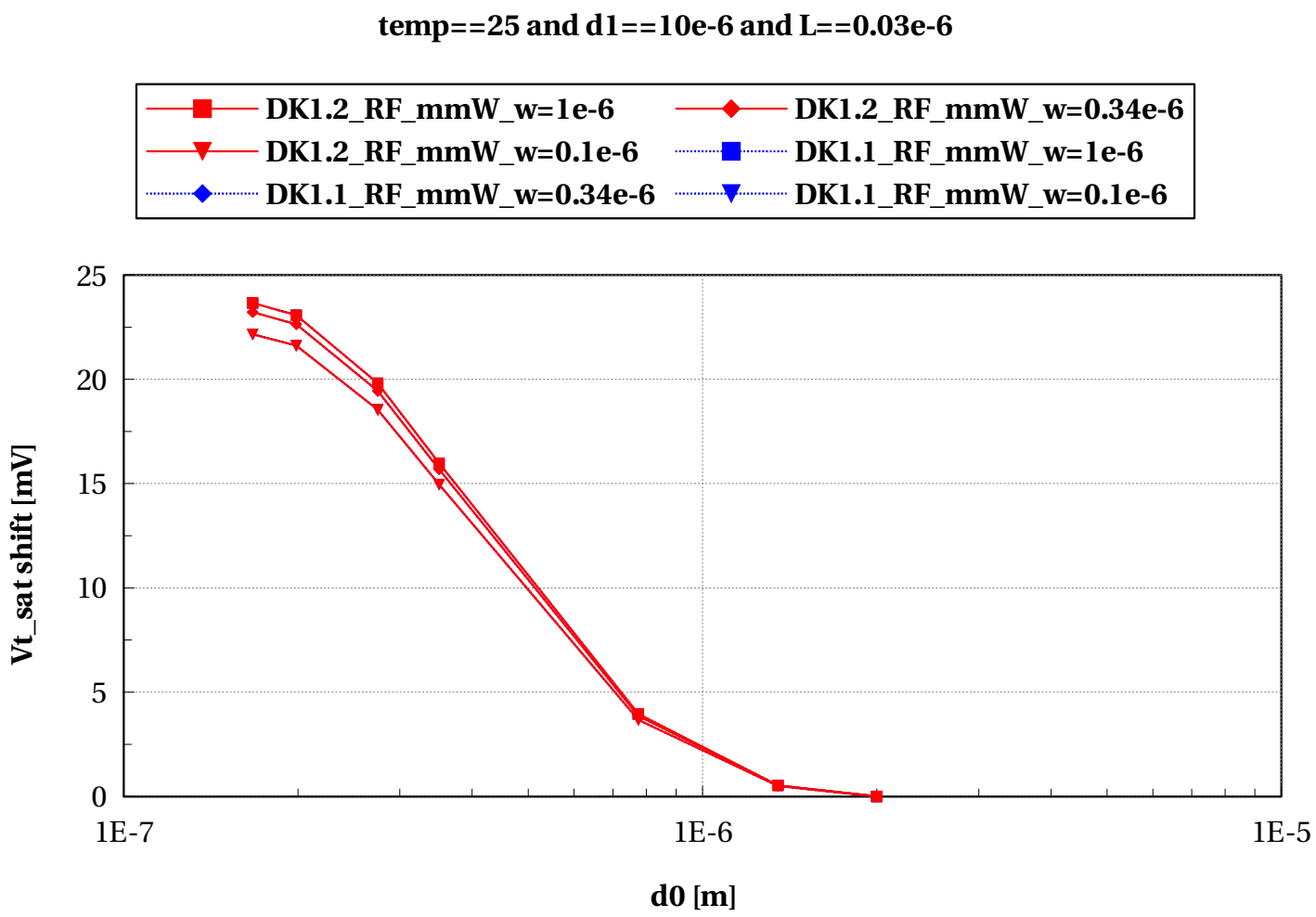
nfet_acc, Vt_sat shift [mV] vs d0 [m]



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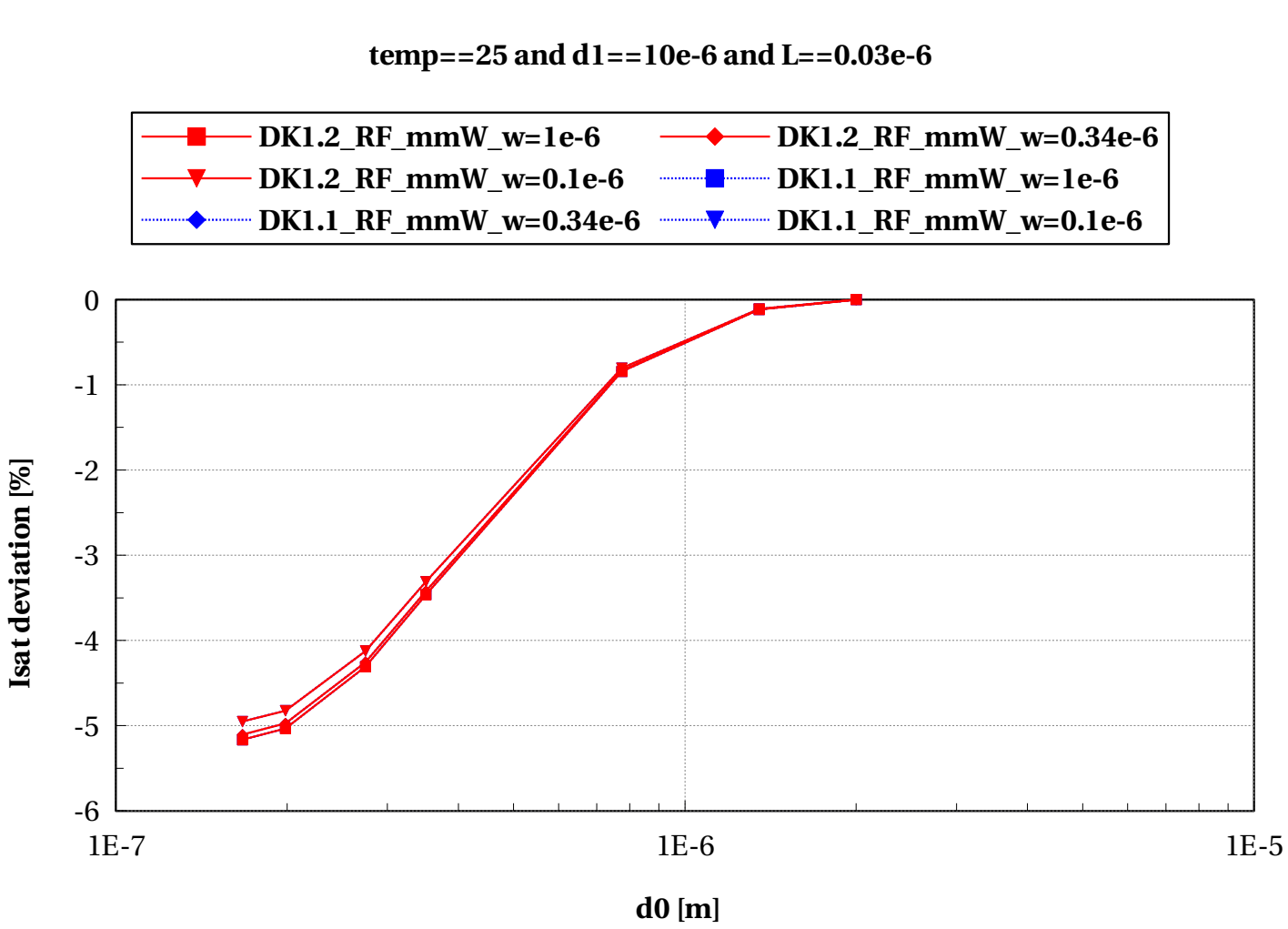
nfet_acc, Isat deviation [%] vs d0 [m]



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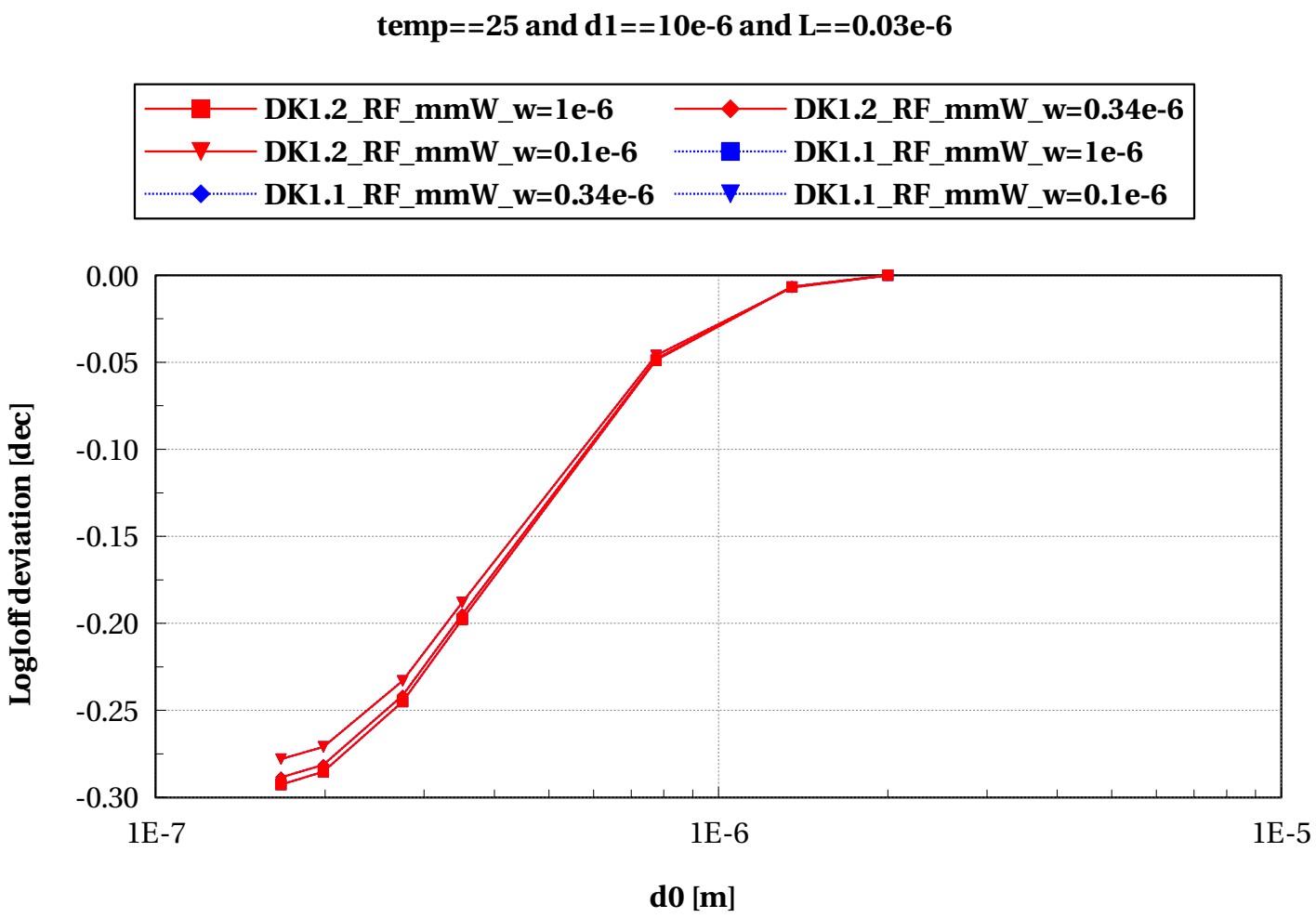
nfet_acc, LogIoff deviation [dec] vs d0 [m]



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- WPE effect w.r.t d0 - Wscaling at L=0.5u

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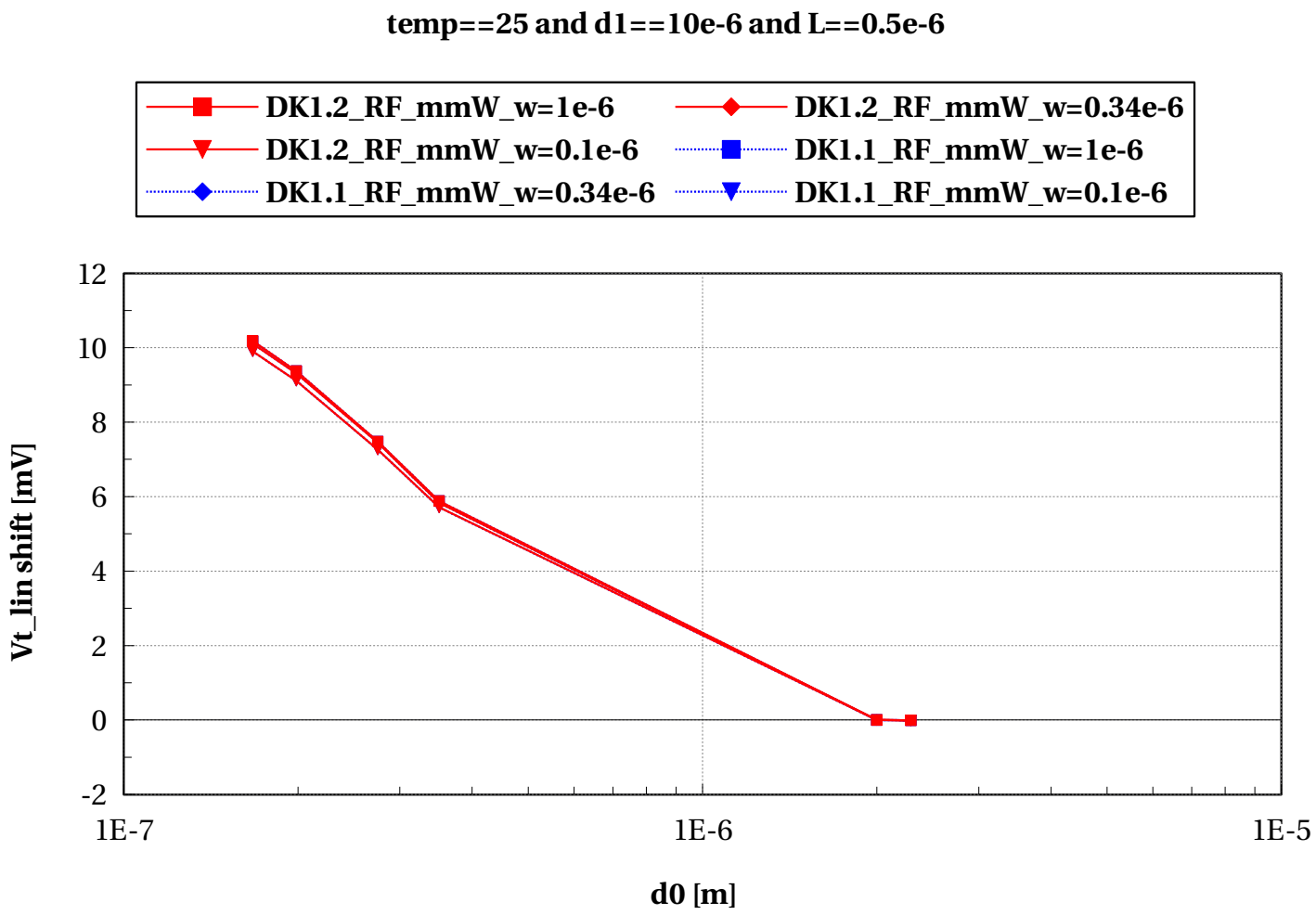
nfet_acc, Vt_lin shift [mV] vs d0 [m]



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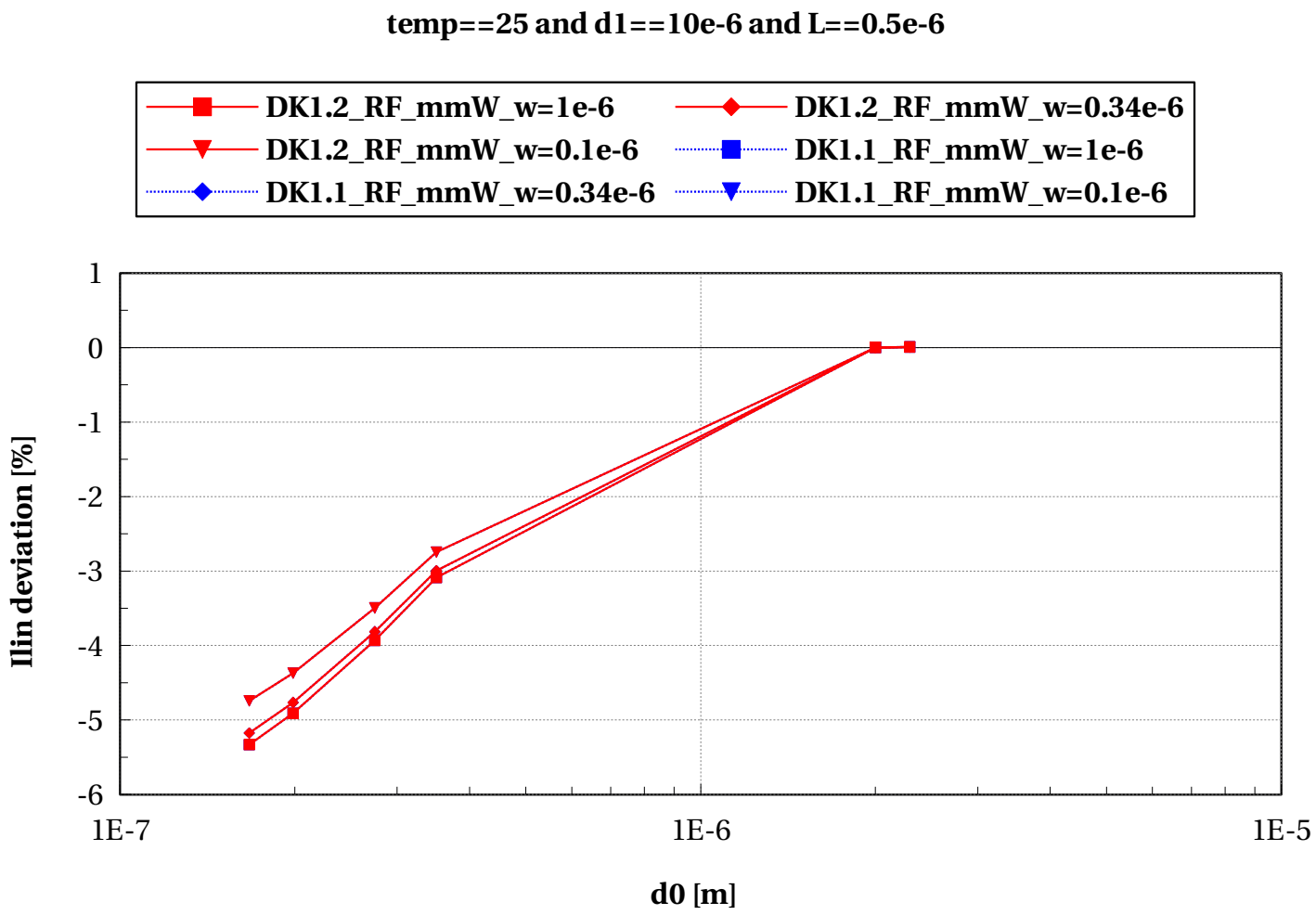
nfet_acc, Ilin deviation [%] vs d0 [m]



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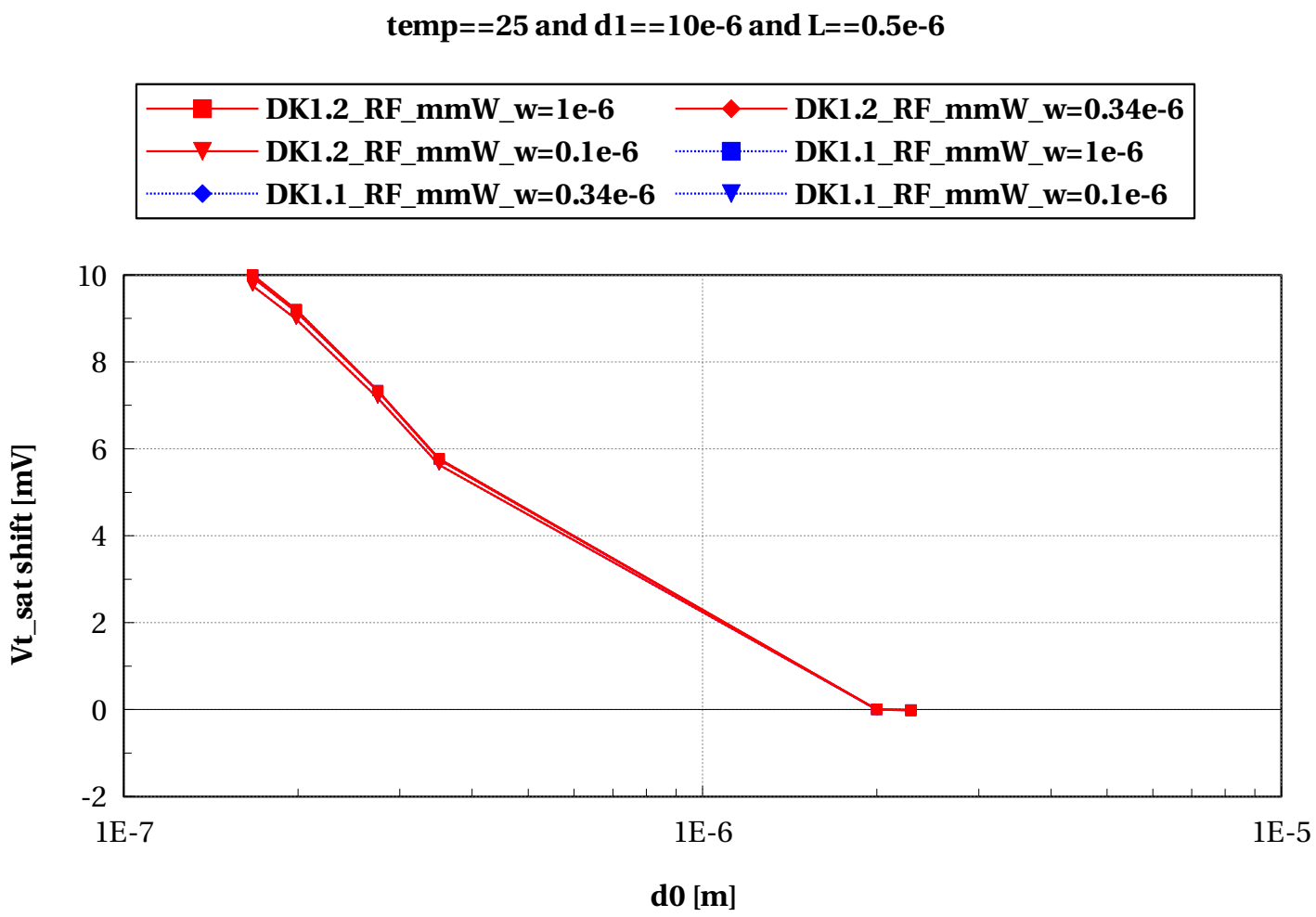
nfet_acc, Vt_sat shift [mV] vs d0 [m]



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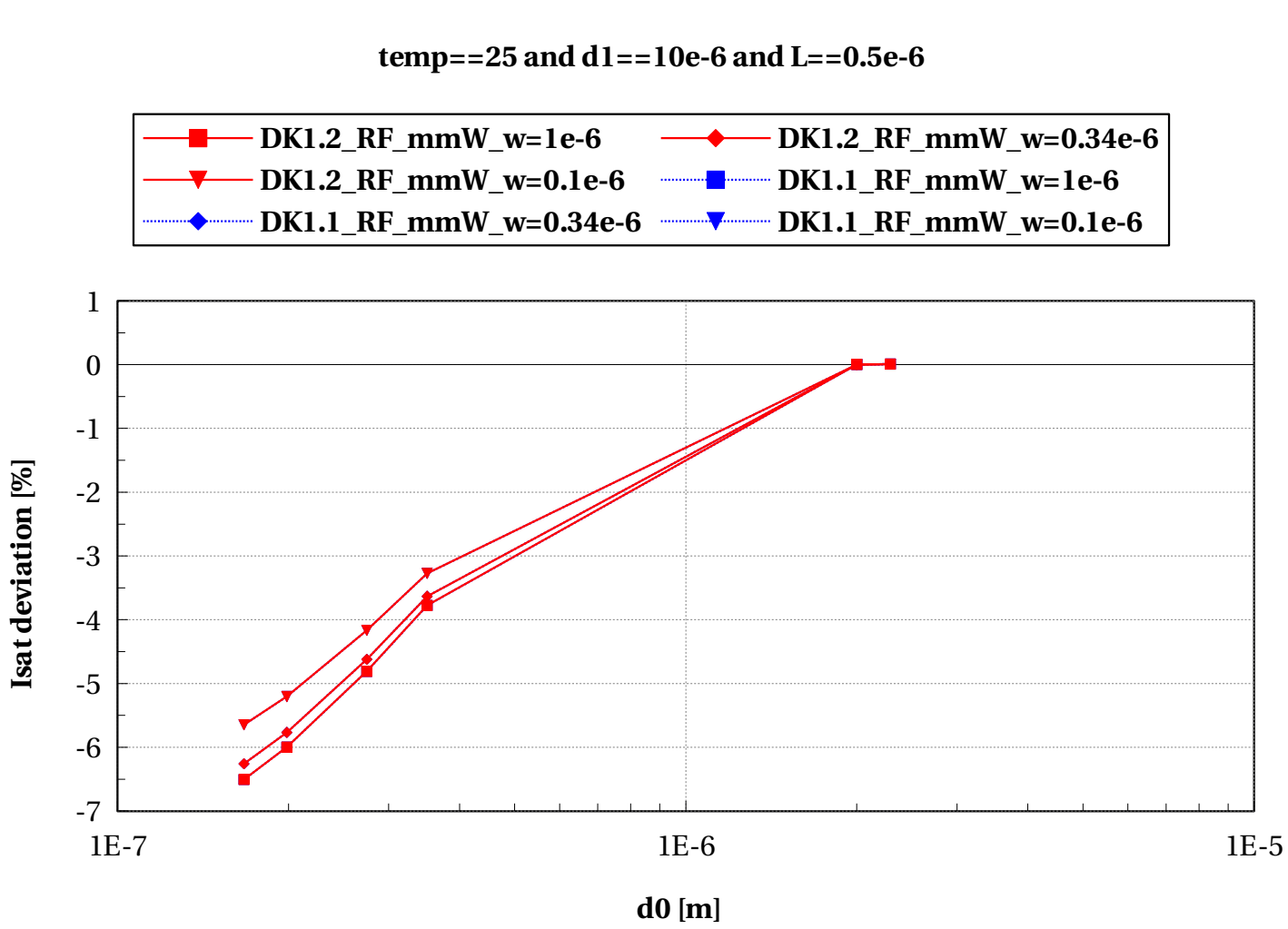
nfet_acc, Isat deviation [%] vs d0 [m]



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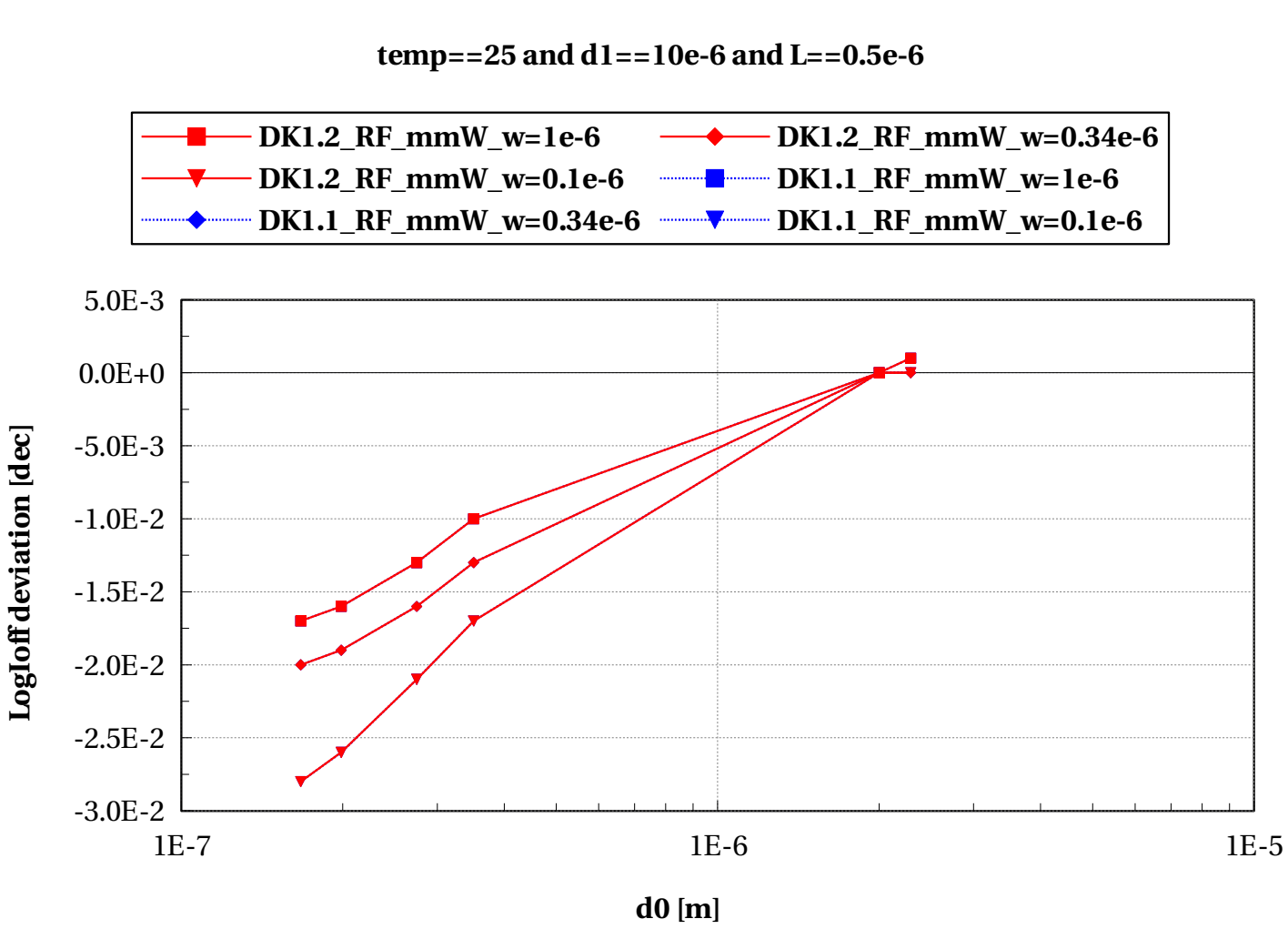
nfet_acc, LogIoff deviation [dec] vs d0 [m]



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- WPE effect w.r.t d1 - Wscaling at L=0.03u

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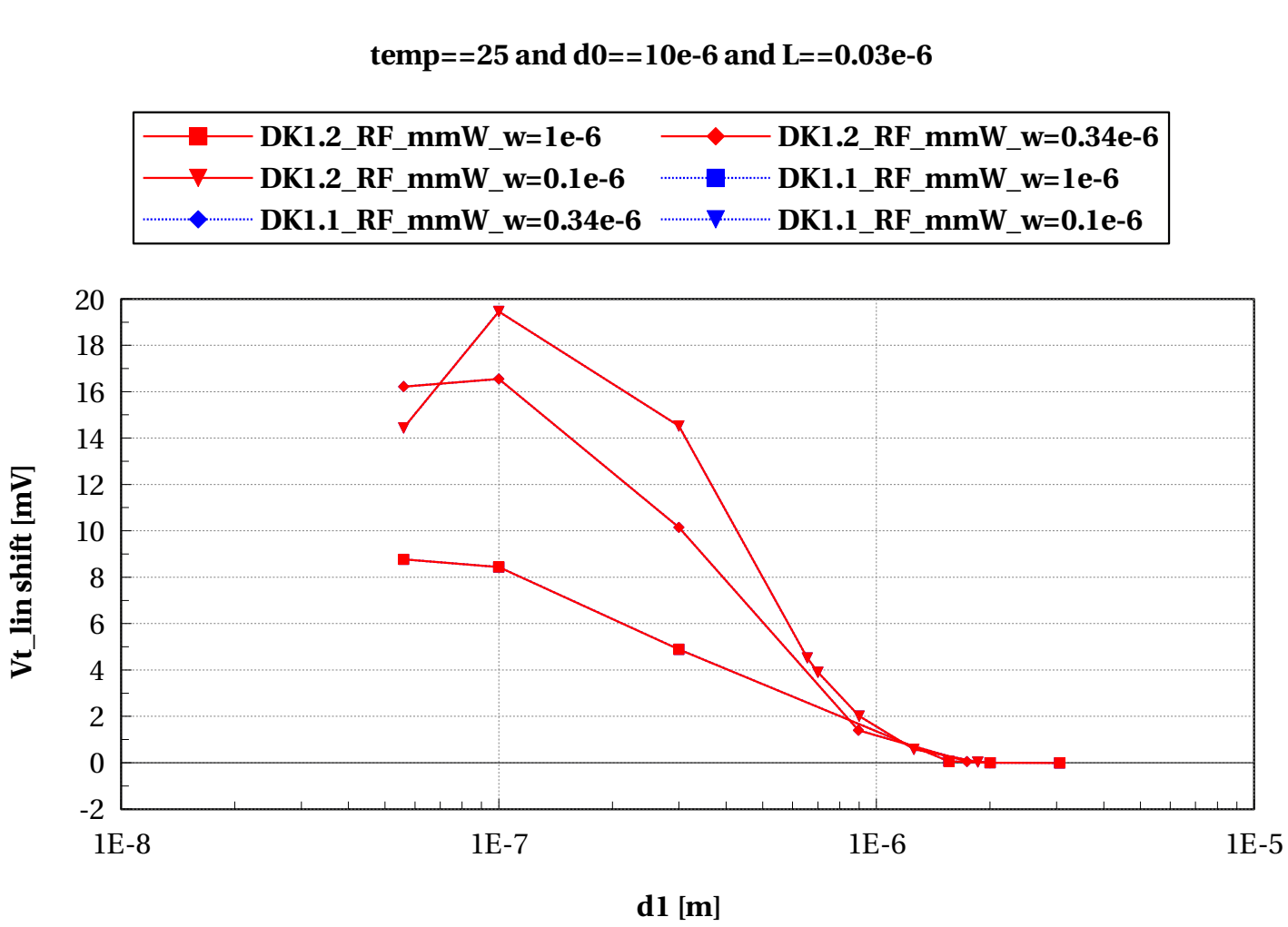
nfet_acc, Vt_lin shift [mV] vs d1 [m]



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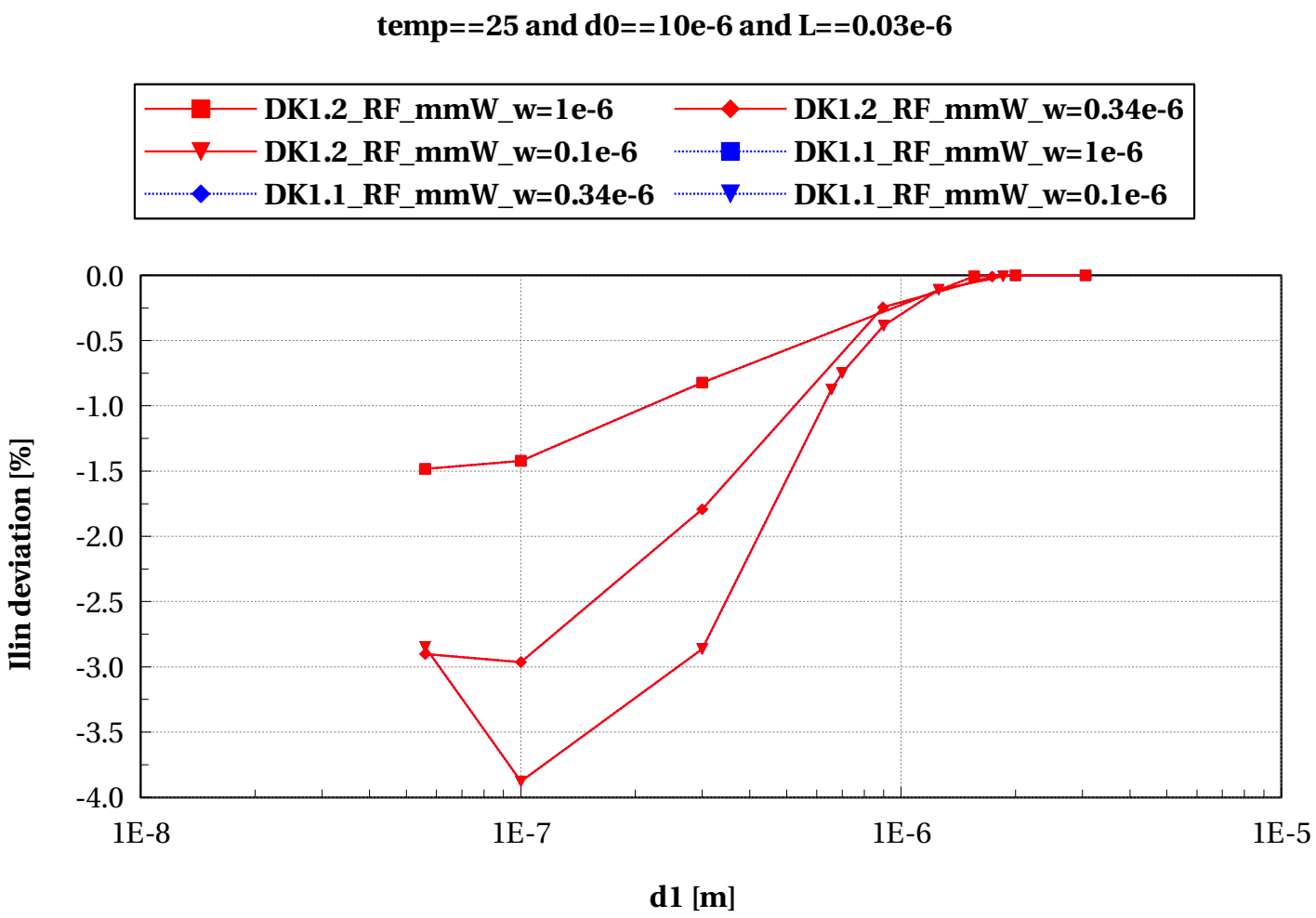
nfet_acc, Ilin deviation [%] vs d1 [m]



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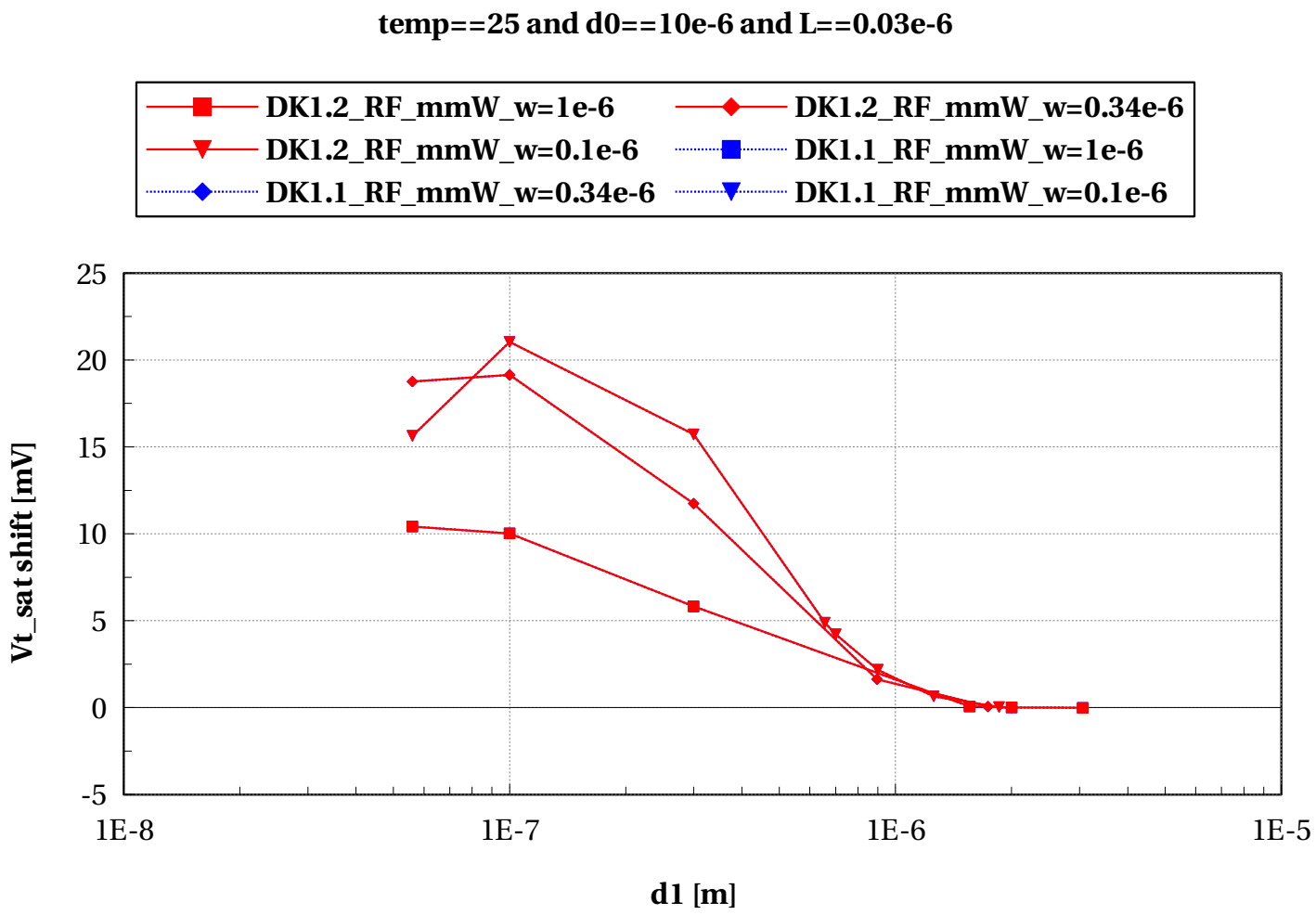
nfet_acc, Vt_sat shift [mV] vs d1 [m]



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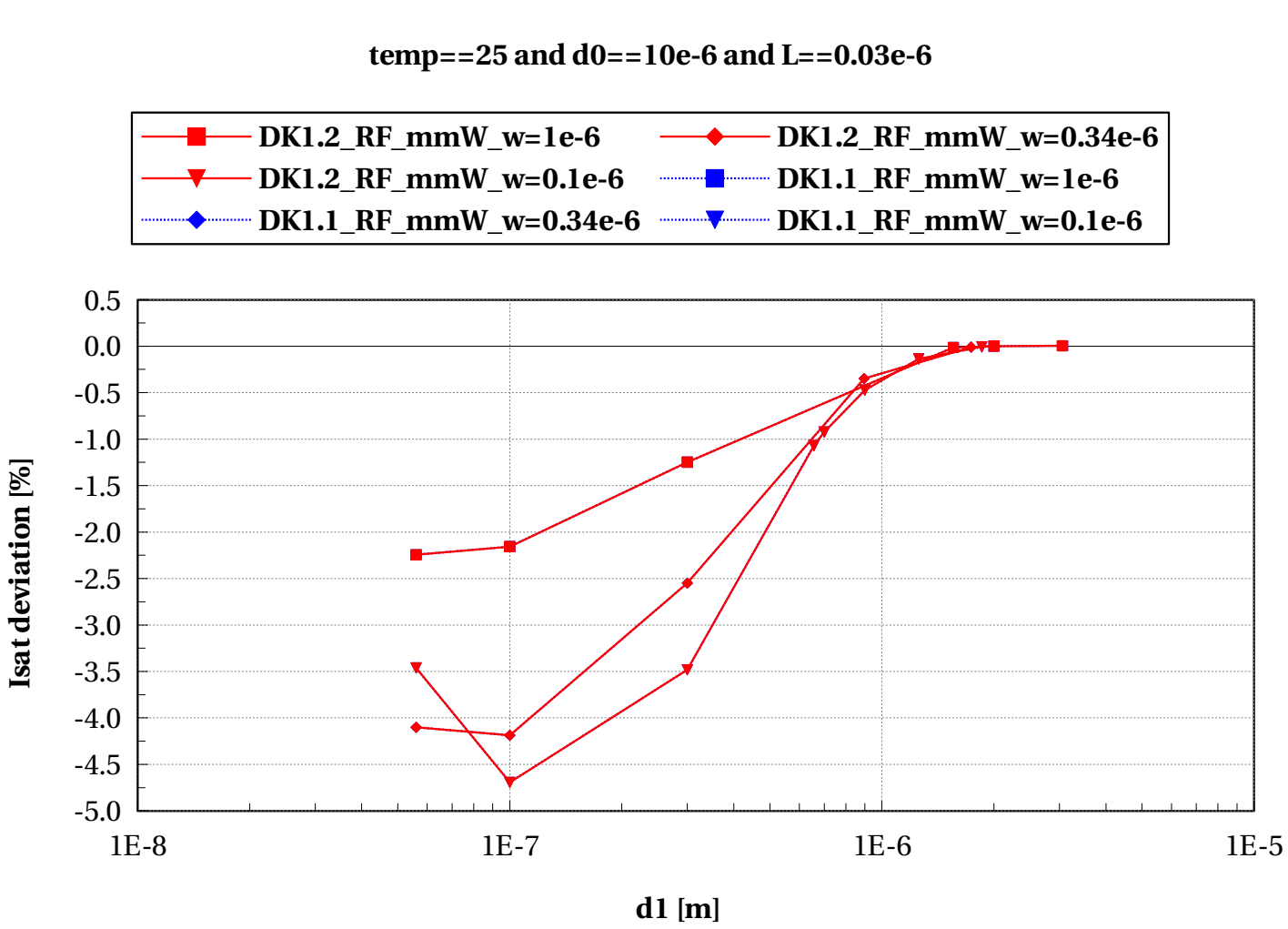
nfet_acc, Isat deviation [%] vs d1 [m]



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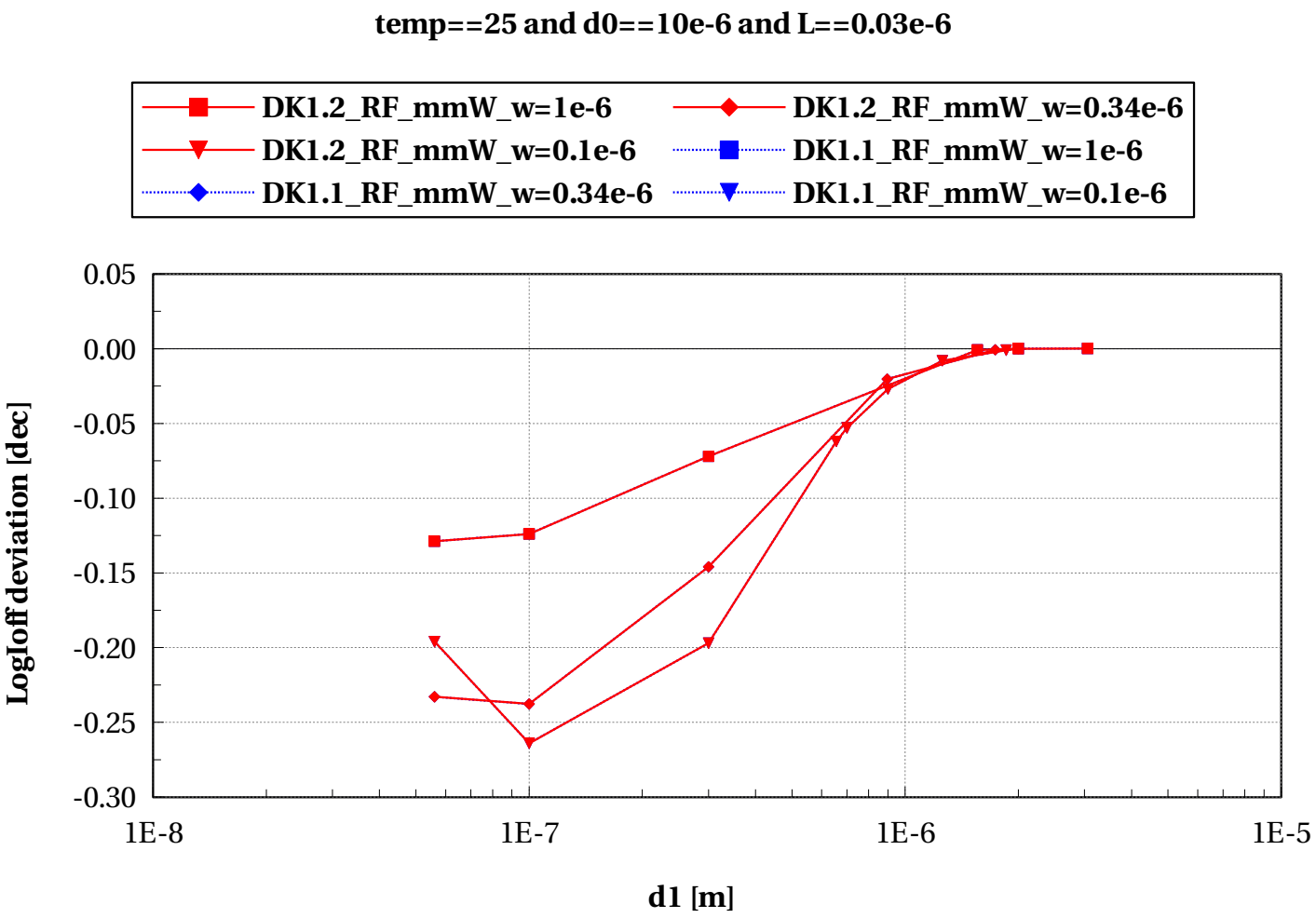
nfet_acc, LogIoff deviation [dec] vs d1 [m]



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- WPE effect w.r.t d1 - Wscaling at L=0.12u

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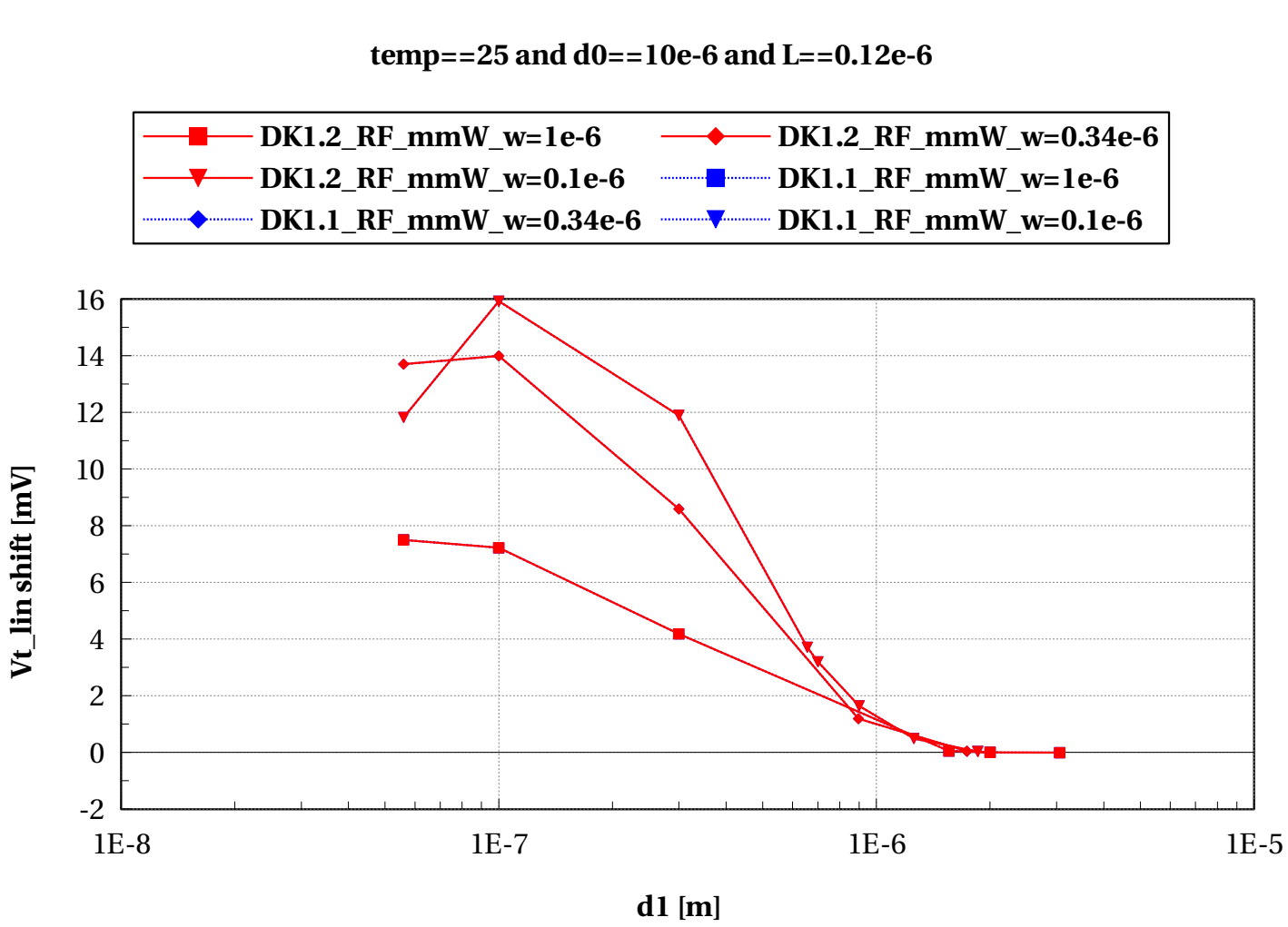

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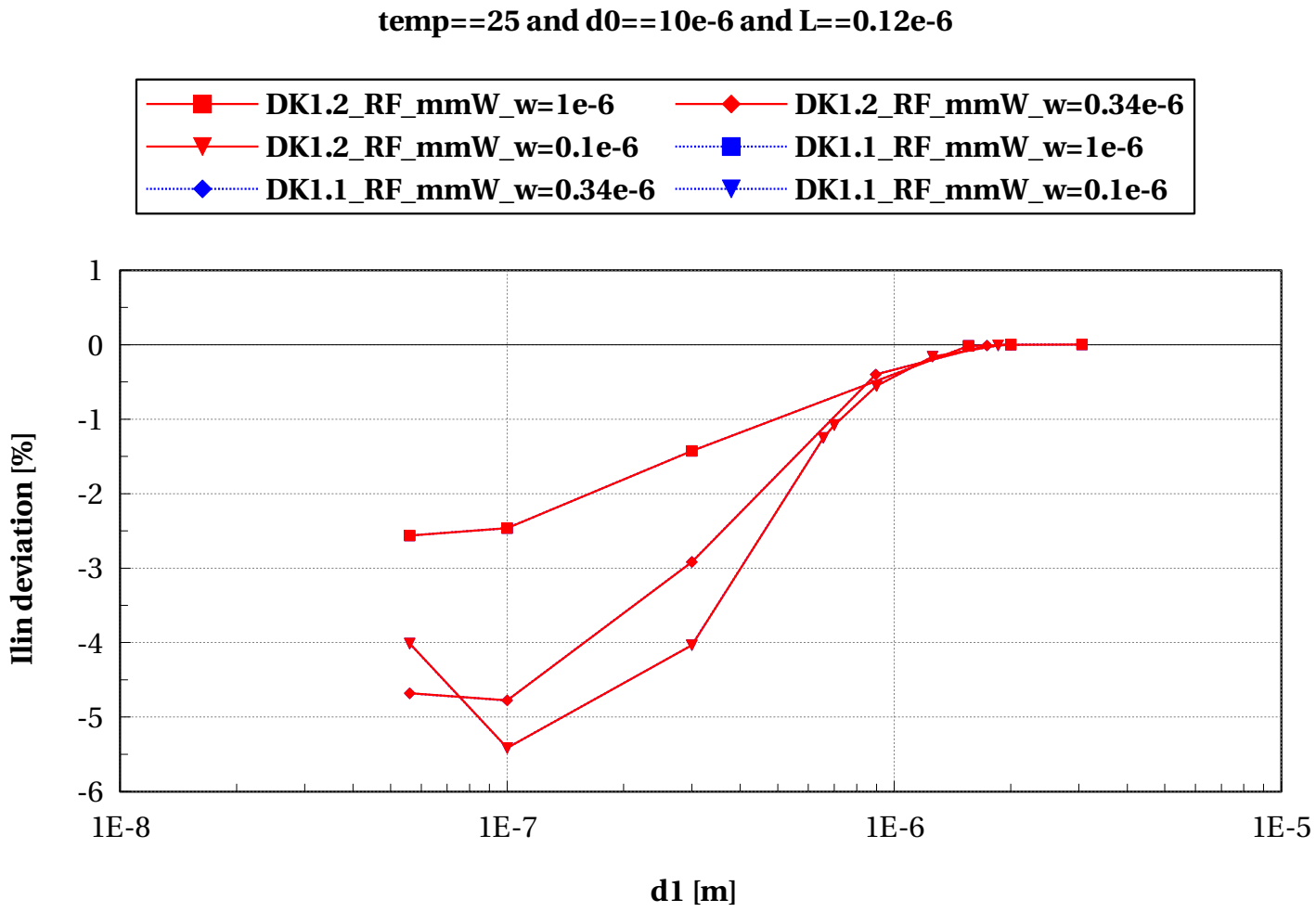
nfet_acc, Ilin deviation [%] vs d1 [m]



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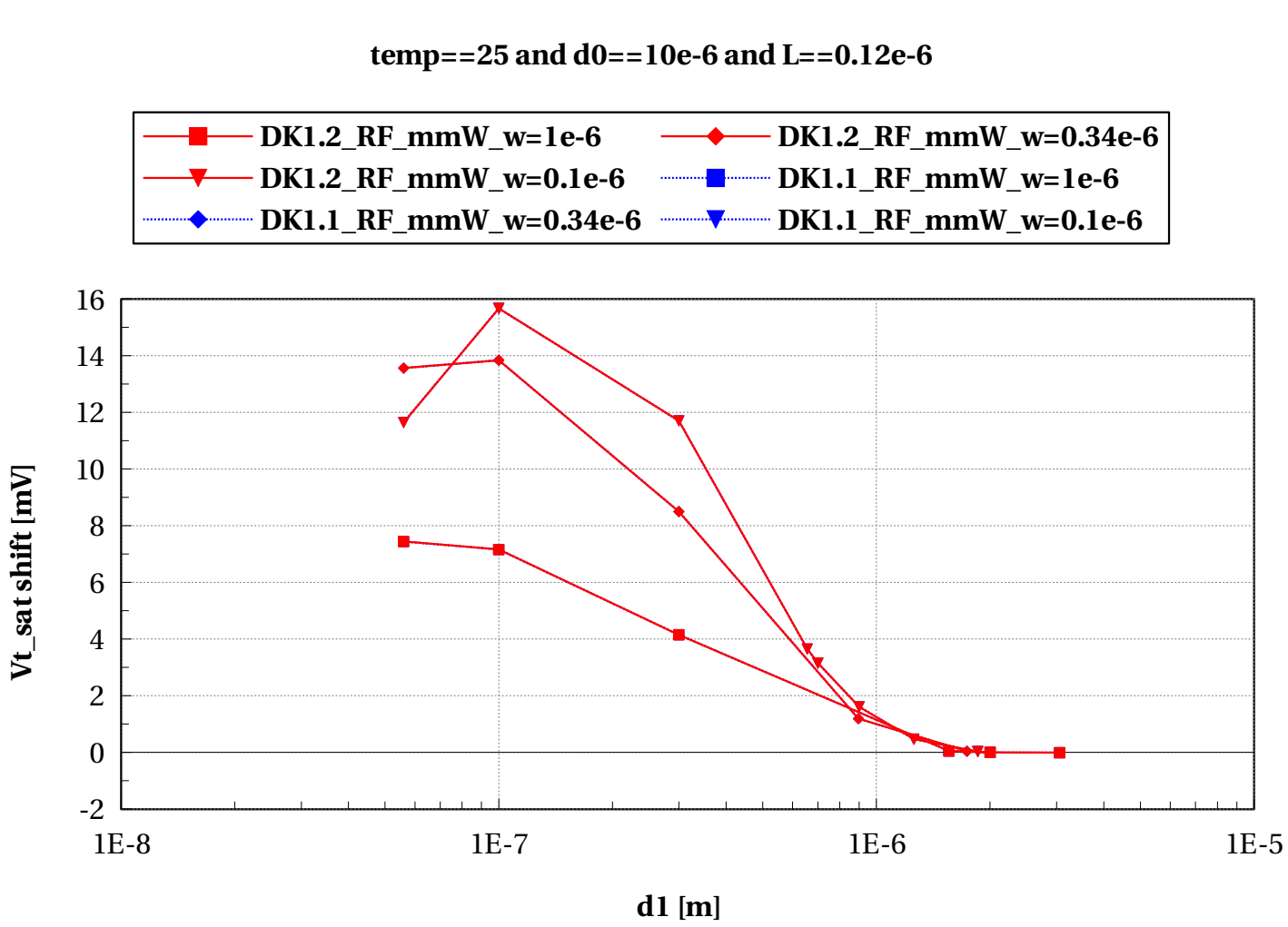
nfet_acc, Vt_sat shift [mV] vs d1 [m]



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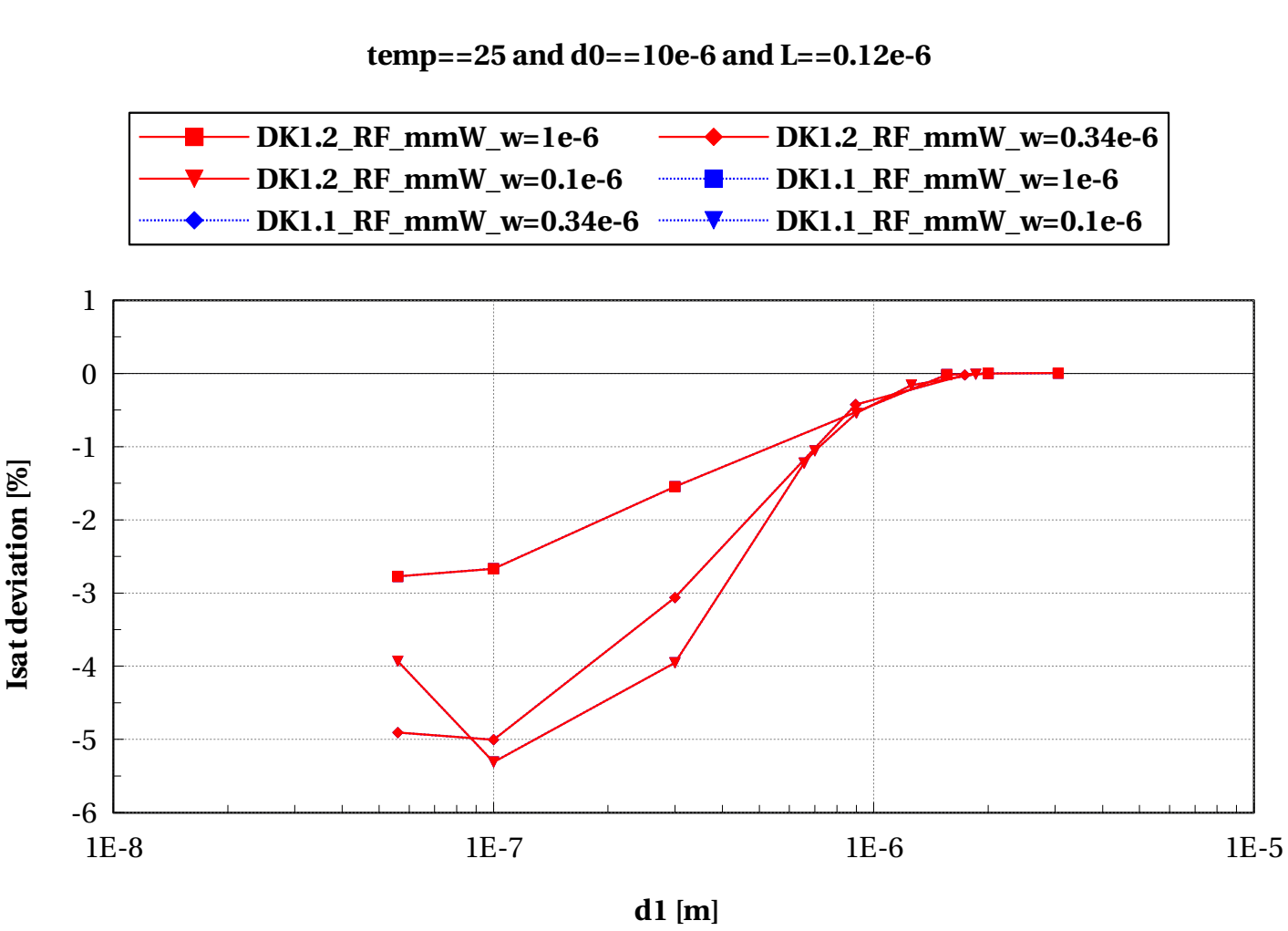
nfet_acc, Isat deviation [%] vs d1 [m]



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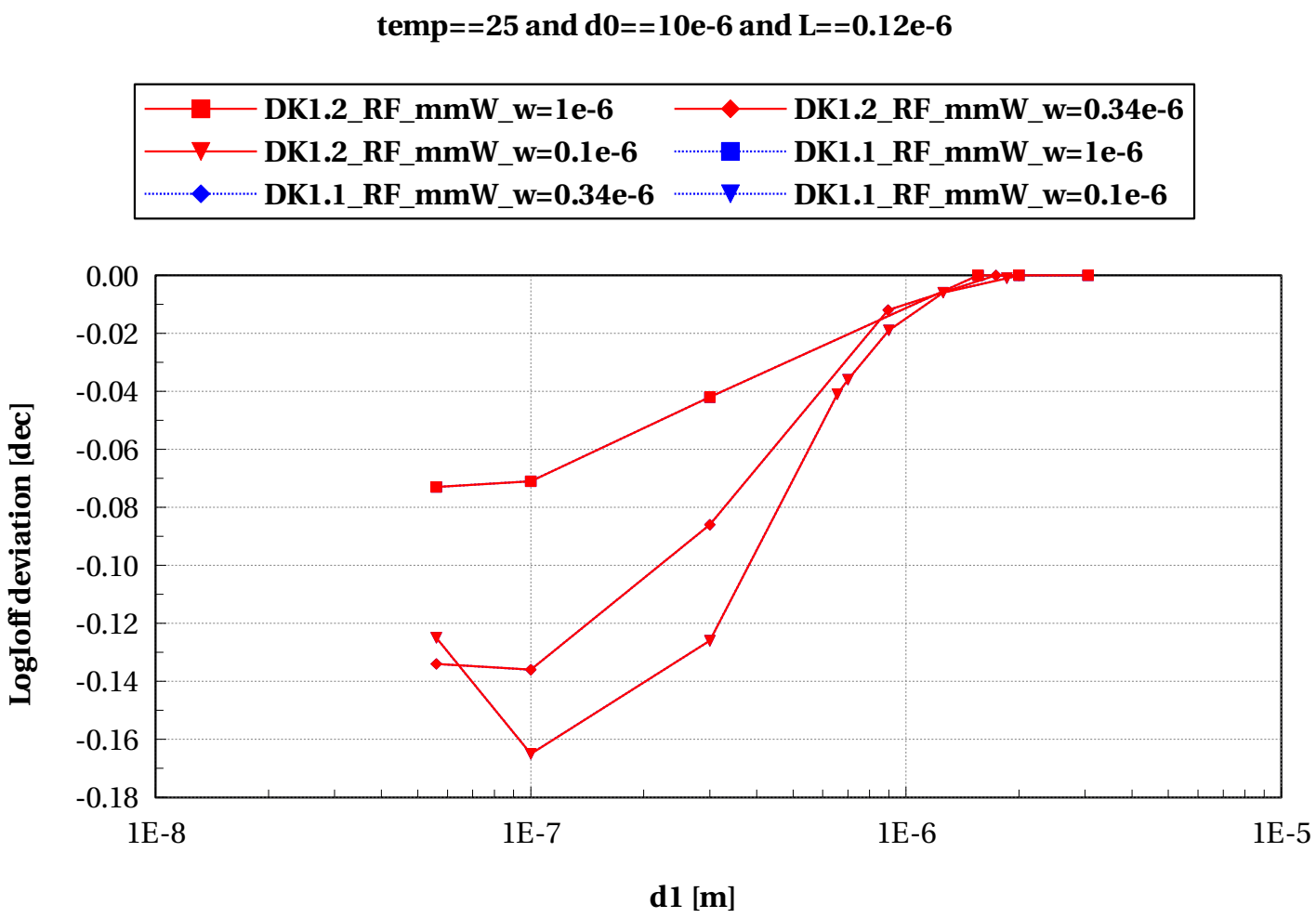

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- WPE effect w.r.t d1 - Wscaling at L=1u

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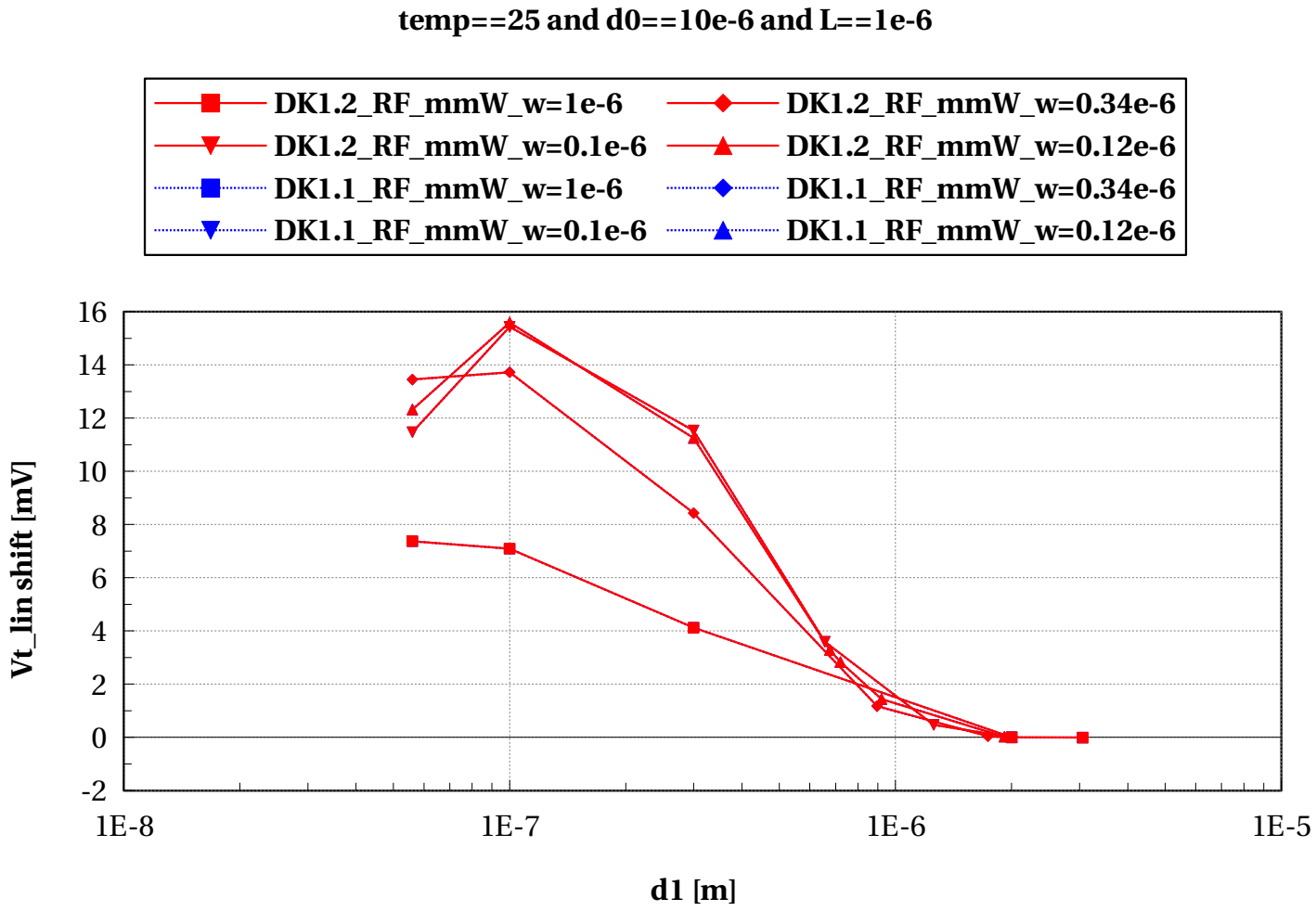
nfet_acc, Vt_lin shift [mV] vs d1 [m]



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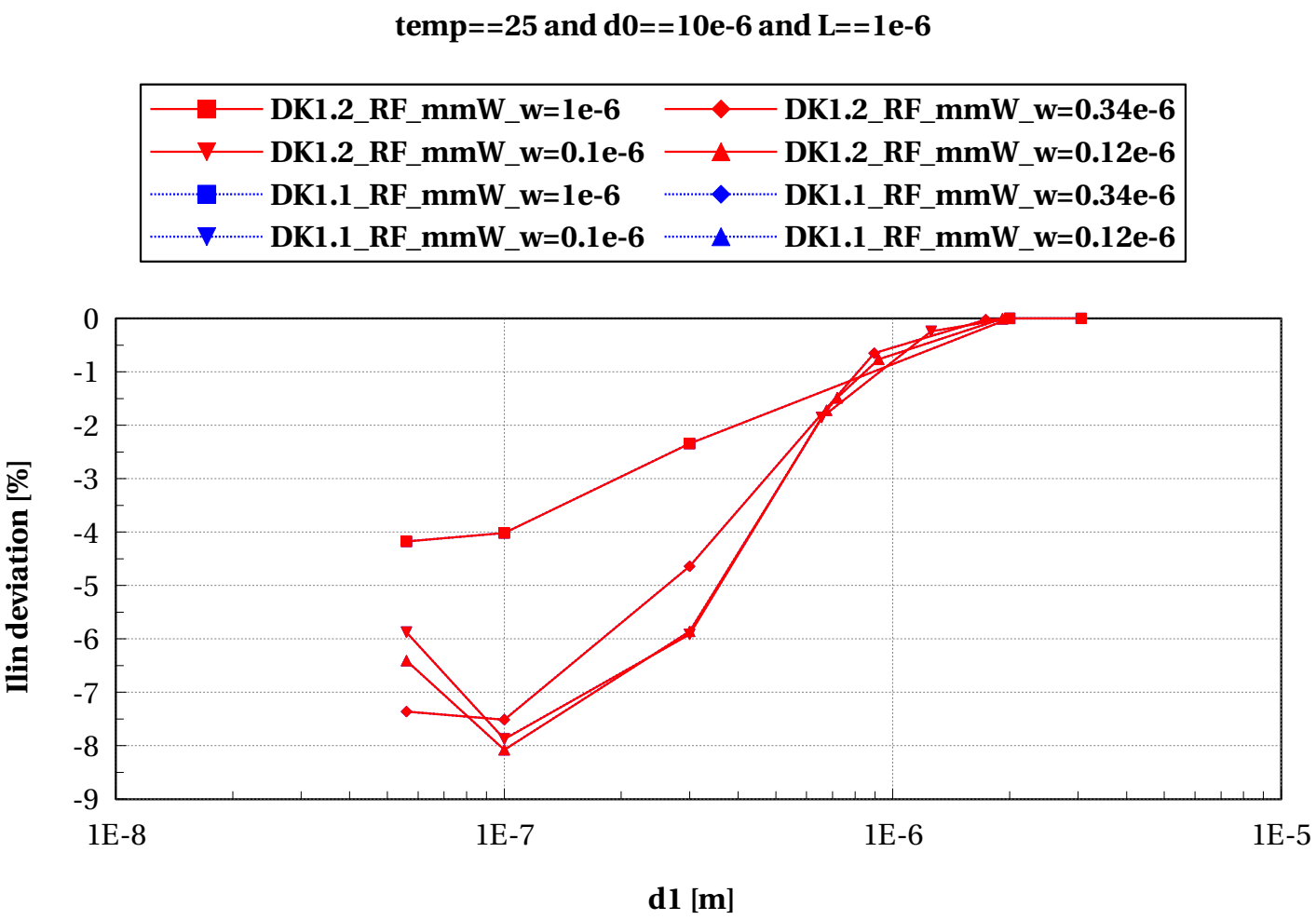
nfet_acc, Ilin deviation [%] vs d1 [m]



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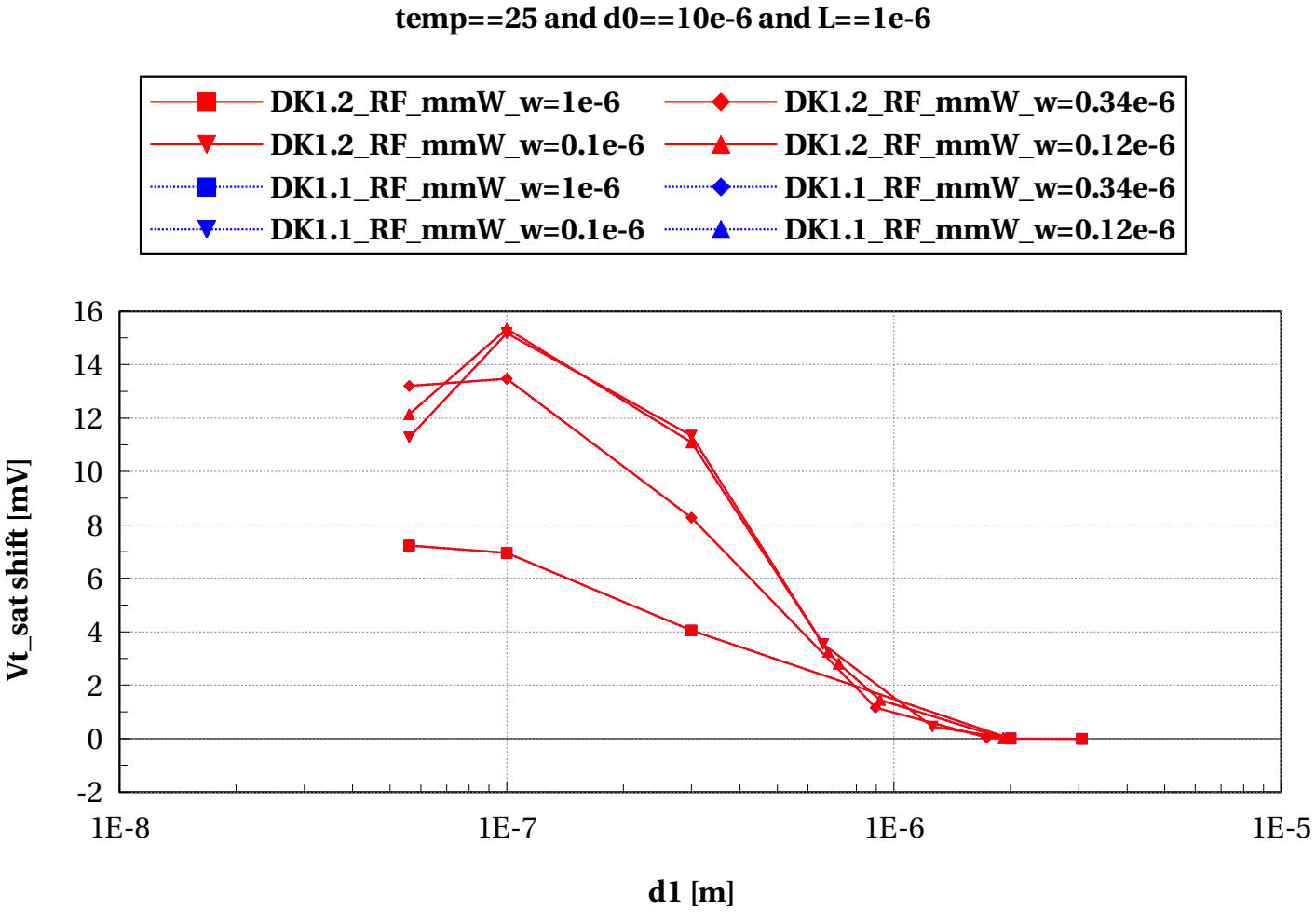
nfet_acc, Vt_sat shift [mV] vs d1 [m]



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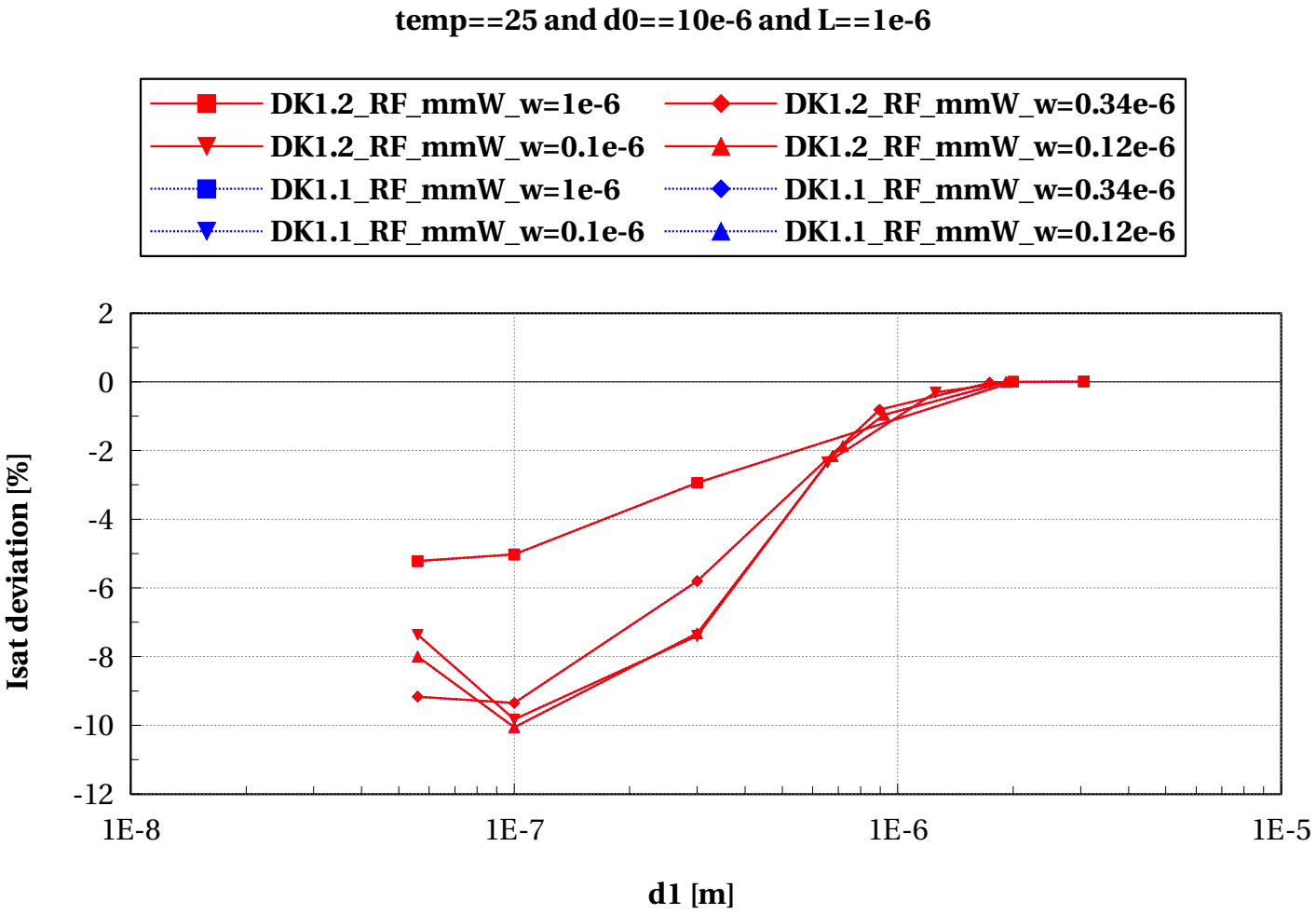
nfet_acc, Isat deviation [%] vs d1 [m]



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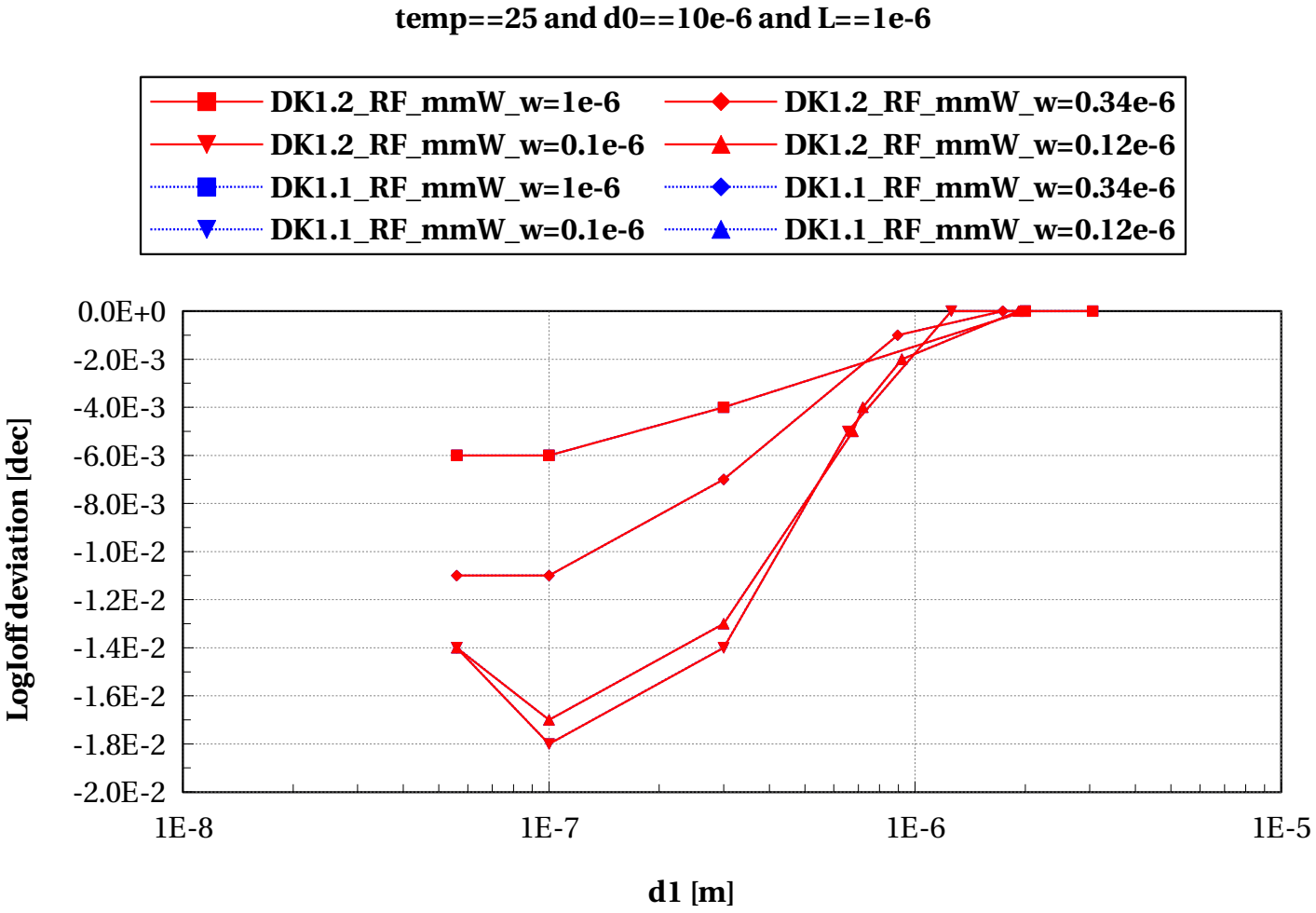
nfet_acc, LogIoff deviation [dec] vs d1 [m]



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- WPE effect w.r.t d1 - Lscaling at W=0.1u

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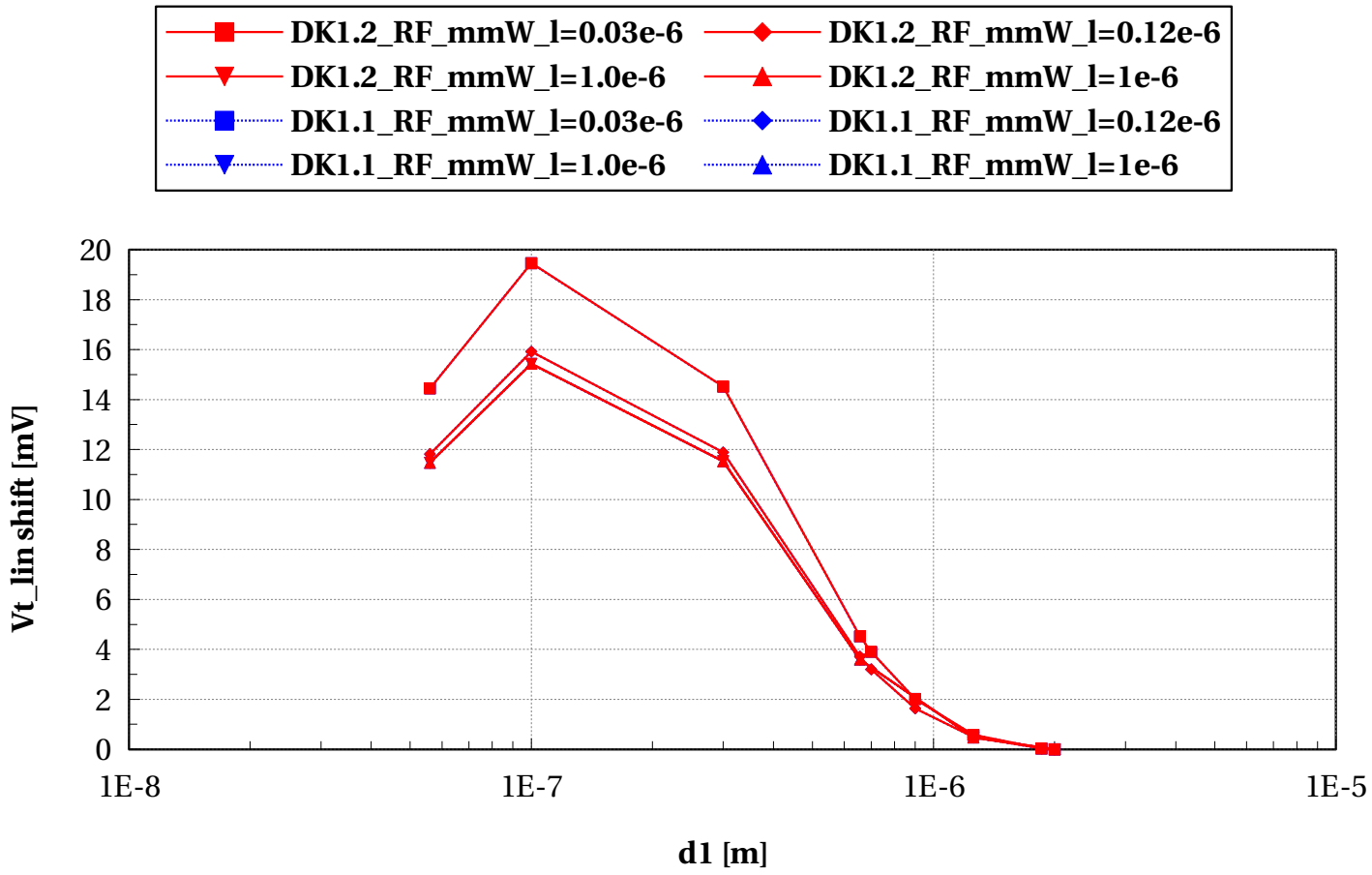
nfet_acc, Vt_lin shift [mV] vs d1 [m]

temp==25 and d0==10e-6 and W==0.1e-6



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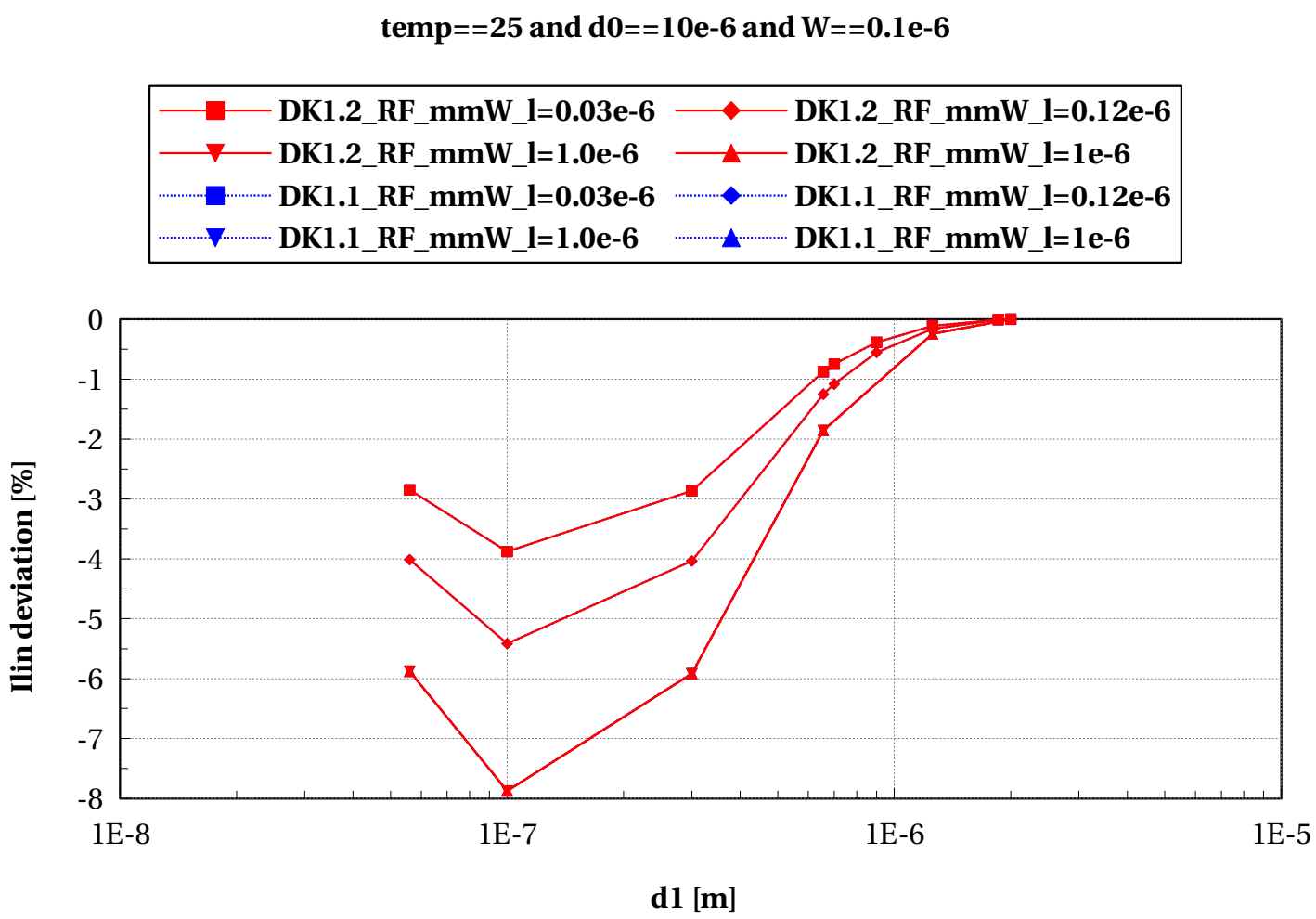
nfet_acc, Ilin deviation [%] vs d1 [m]



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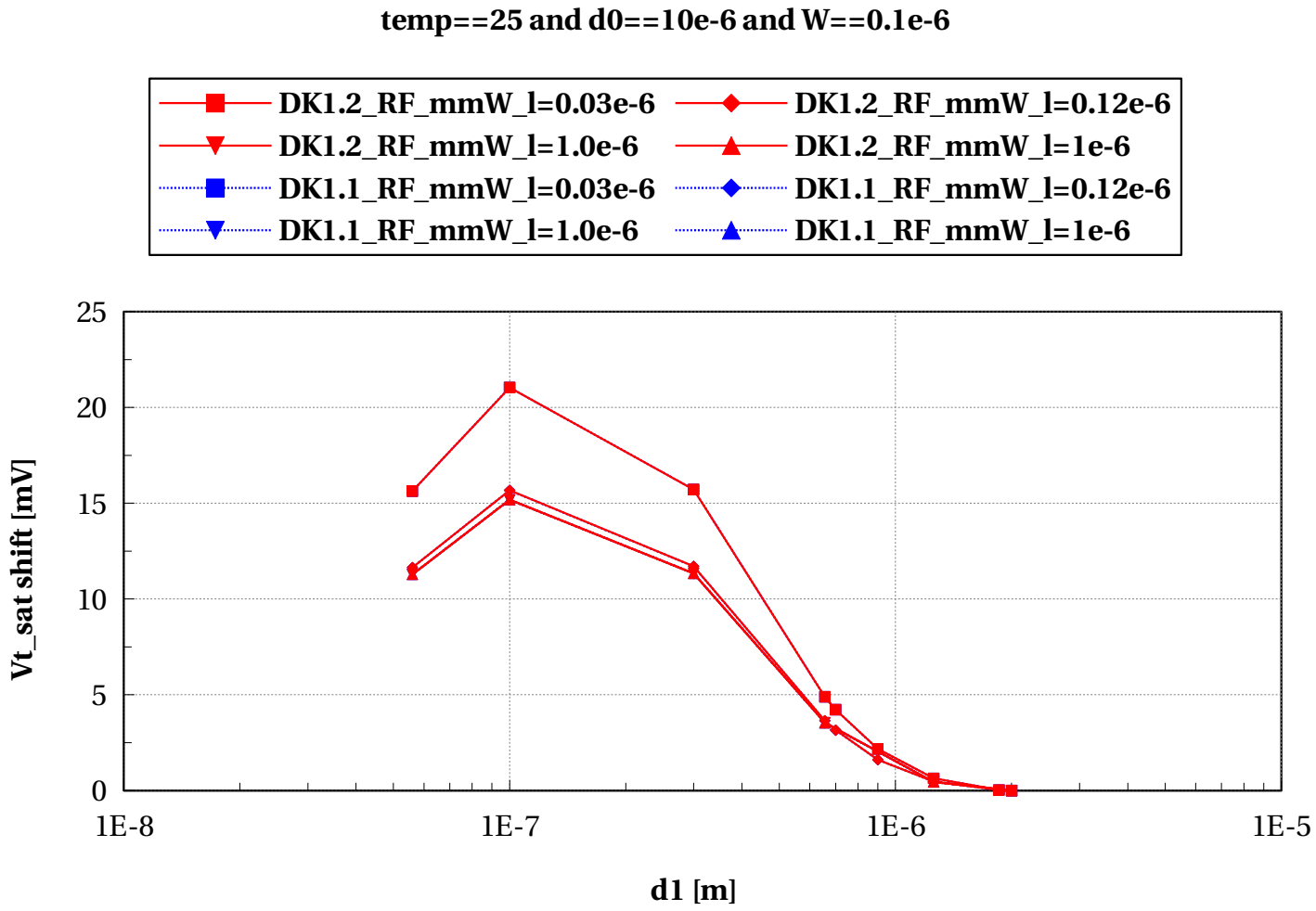
nfet_acc, Vt_sat shift [mV] vs d1 [m]



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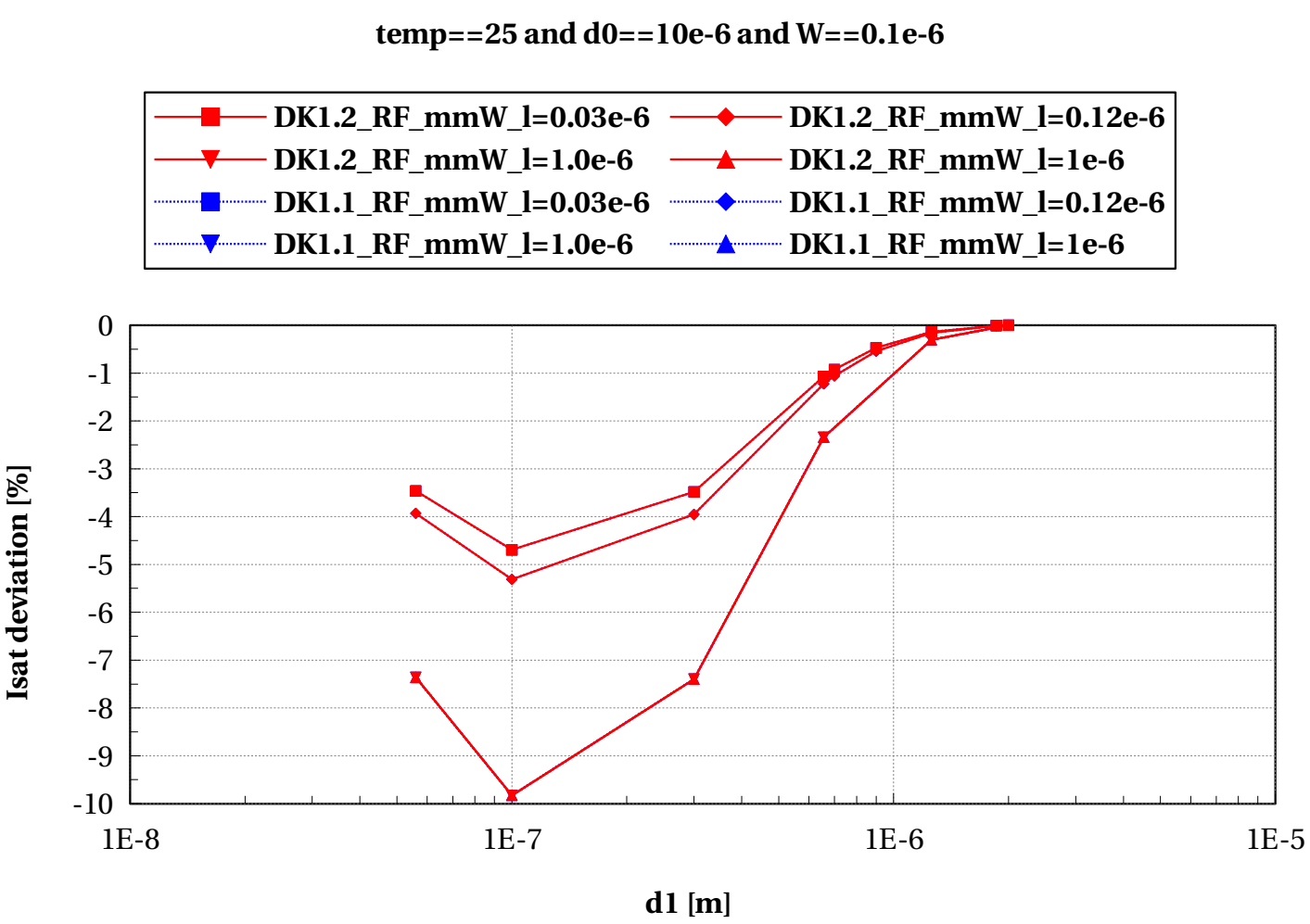
nfet_acc, Isat deviation [%] vs d1 [m]



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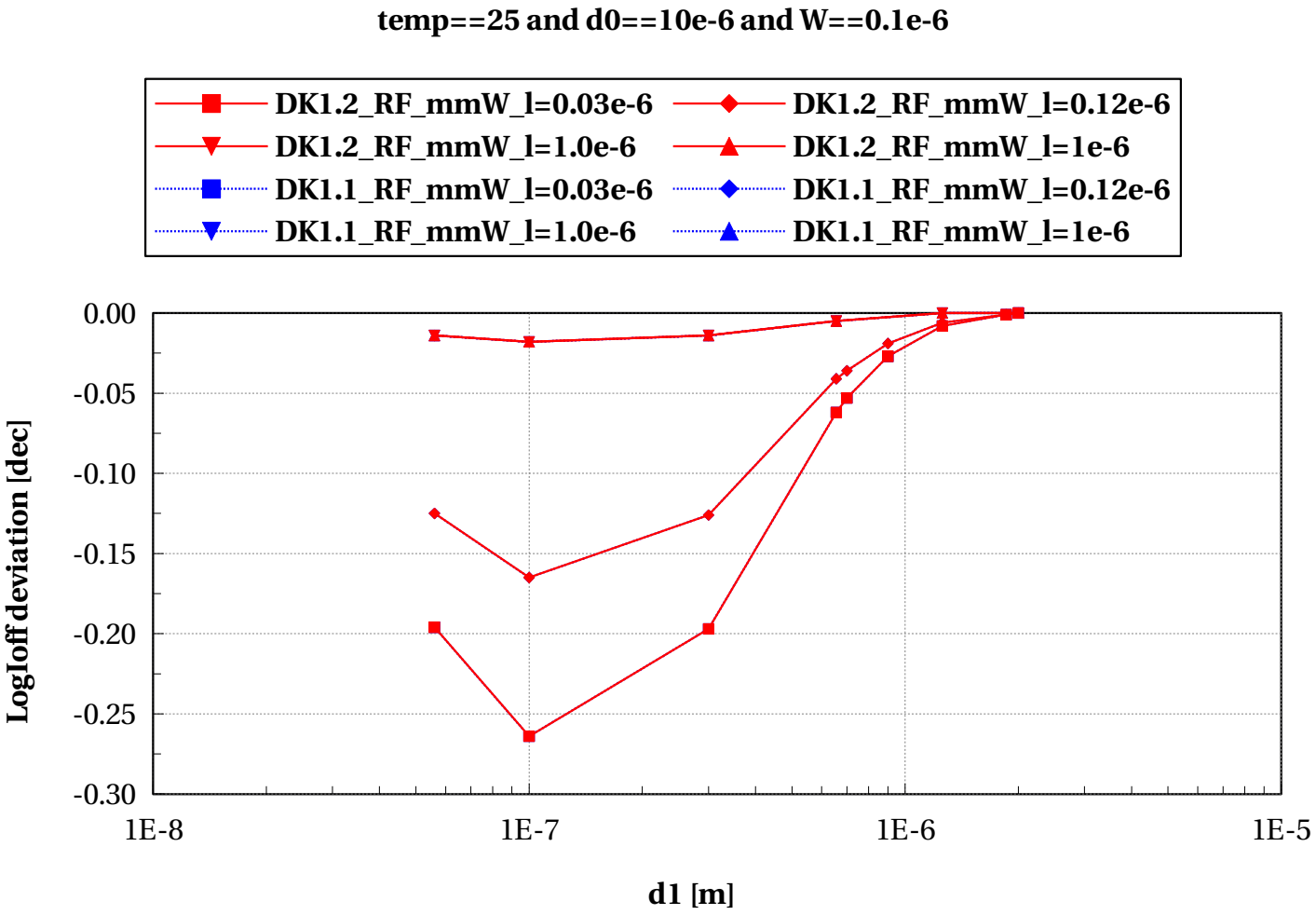
nfet_acc, LogIoff deviation [dec] vs d1 [m]



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- WPE effect w.r.t d1 - Lscaling at W=1u

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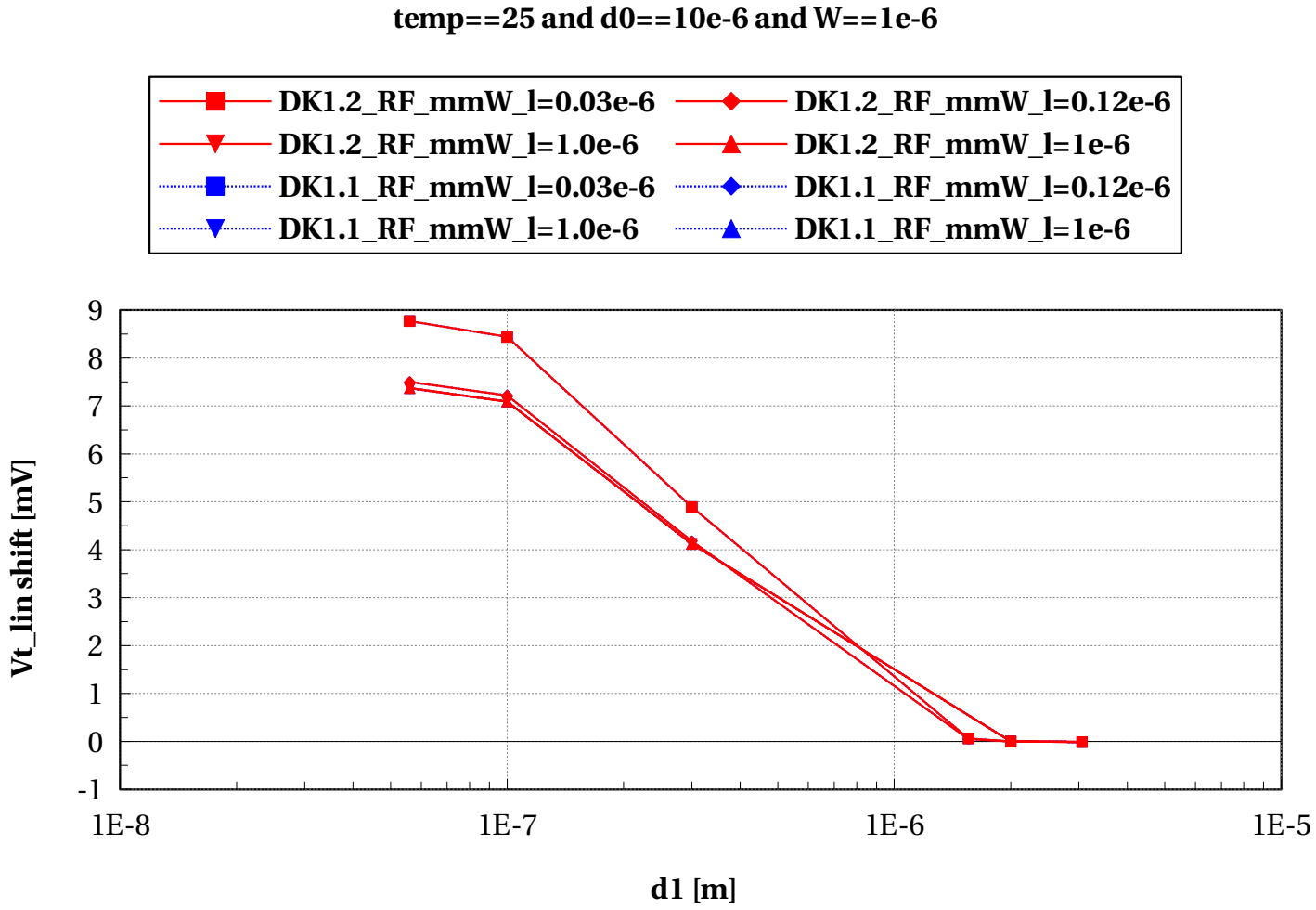
nfet_acc, Vt_lin shift [mV] vs d1 [m]



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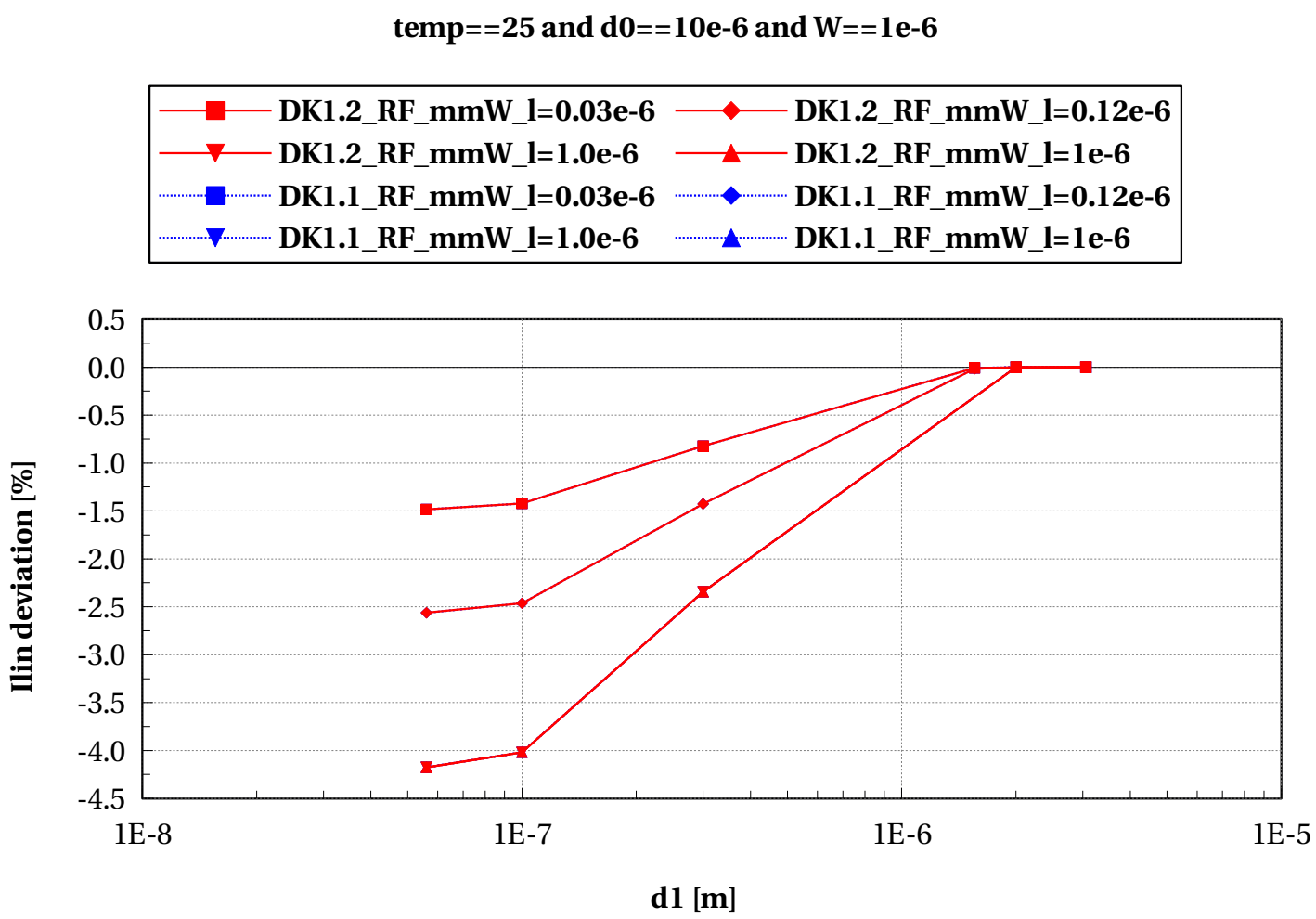
nfet_acc, Ilin deviation [%] vs d1 [m]



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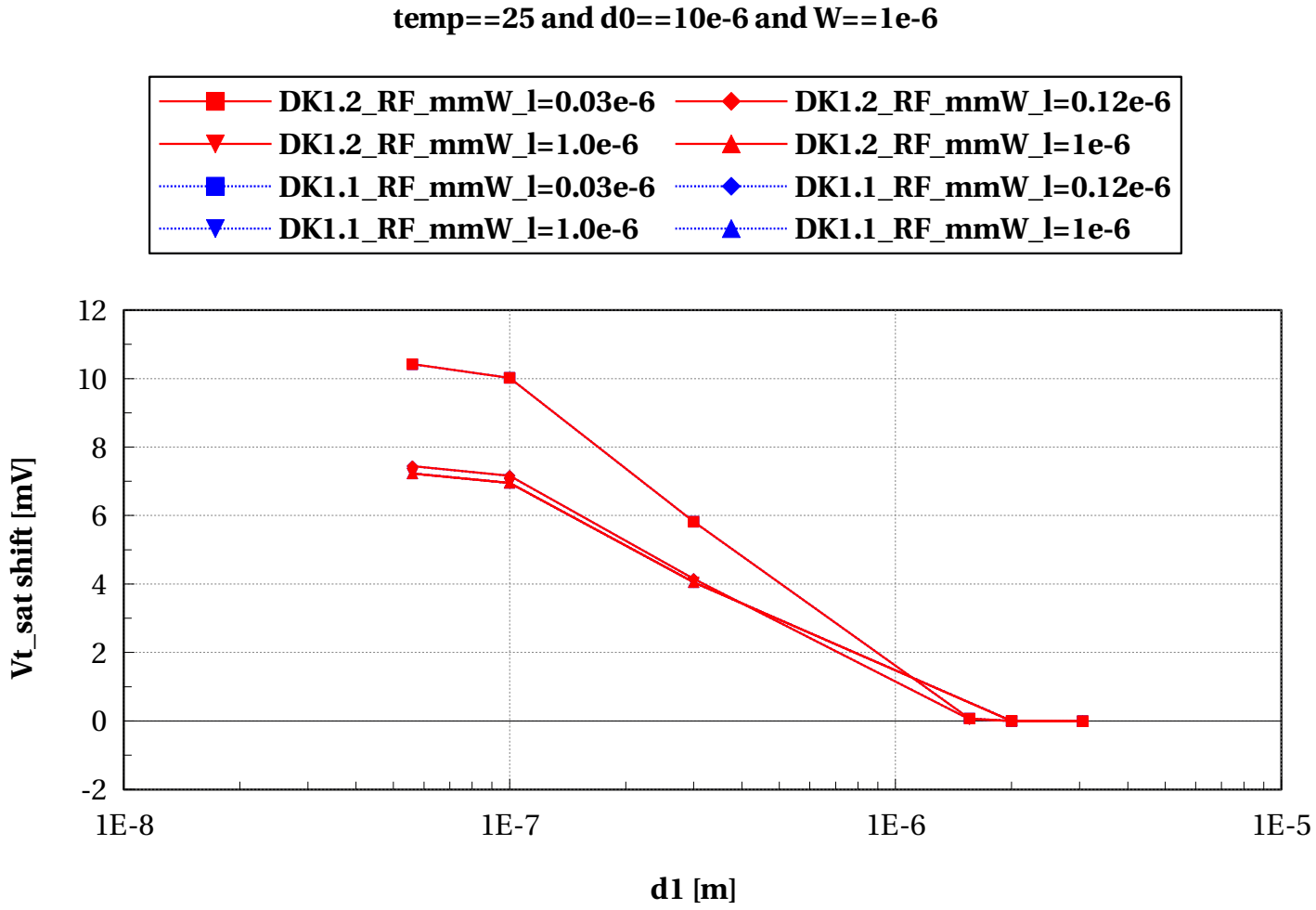
nfet_acc, Vt_sat shift [mV] vs d1 [m]



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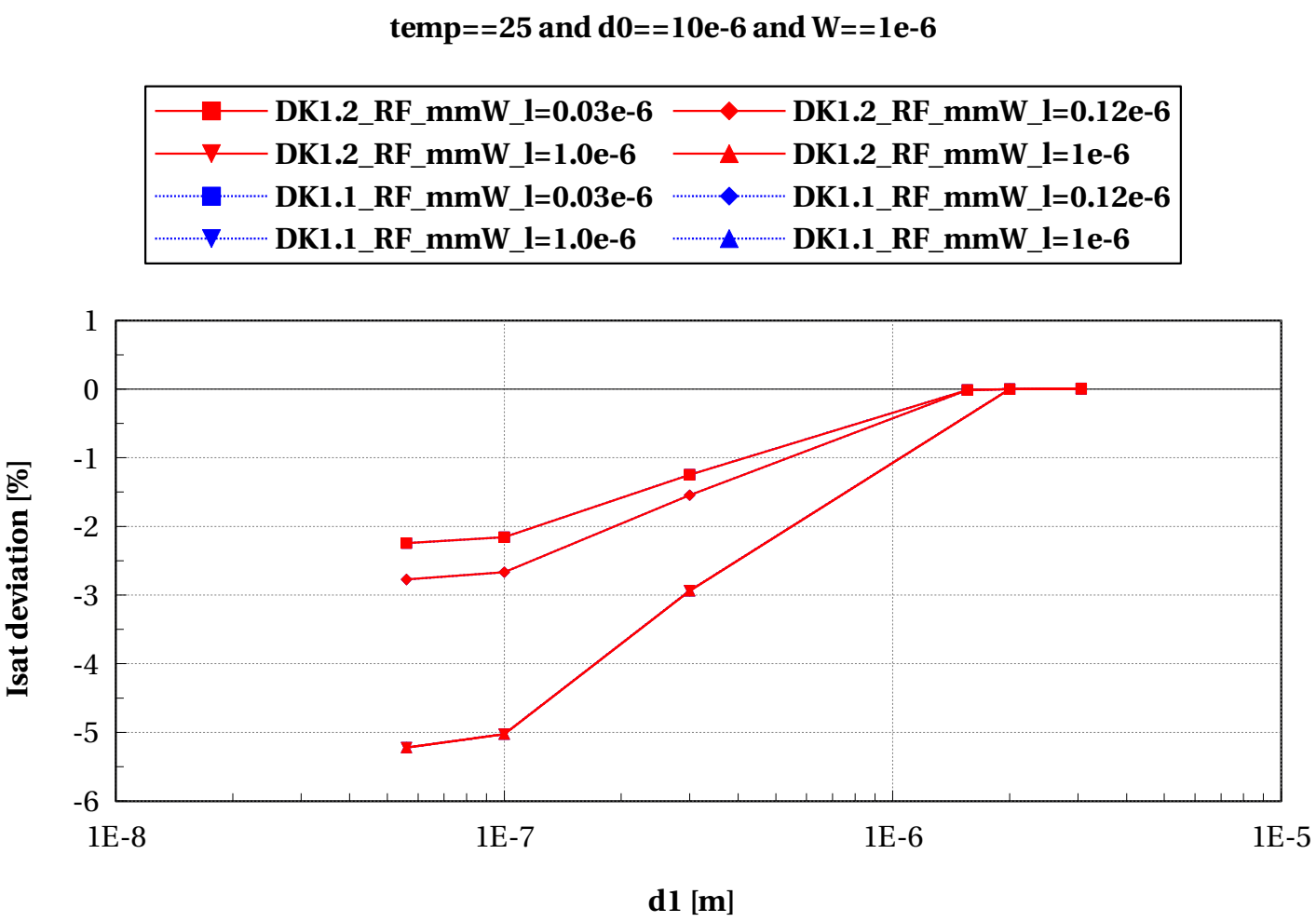
nfet_acc, Isat deviation [%] vs d1 [m]



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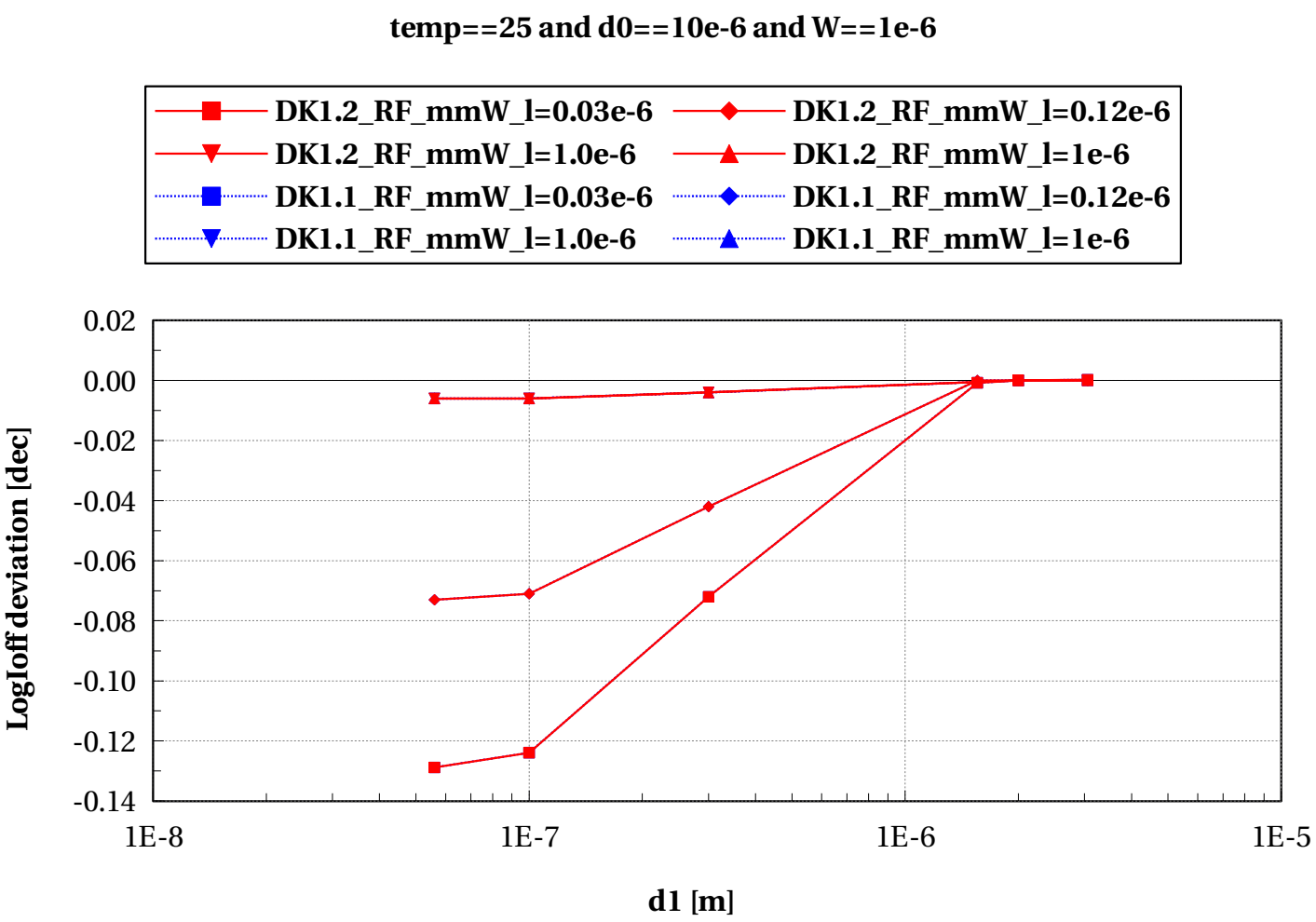
nfet_acc, LogIoff deviation [dec] vs d1 [m]



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pfet_acc
Electrical characteristics scaling

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- WPE effect w.r.t d0 - Lscaling at W=1u

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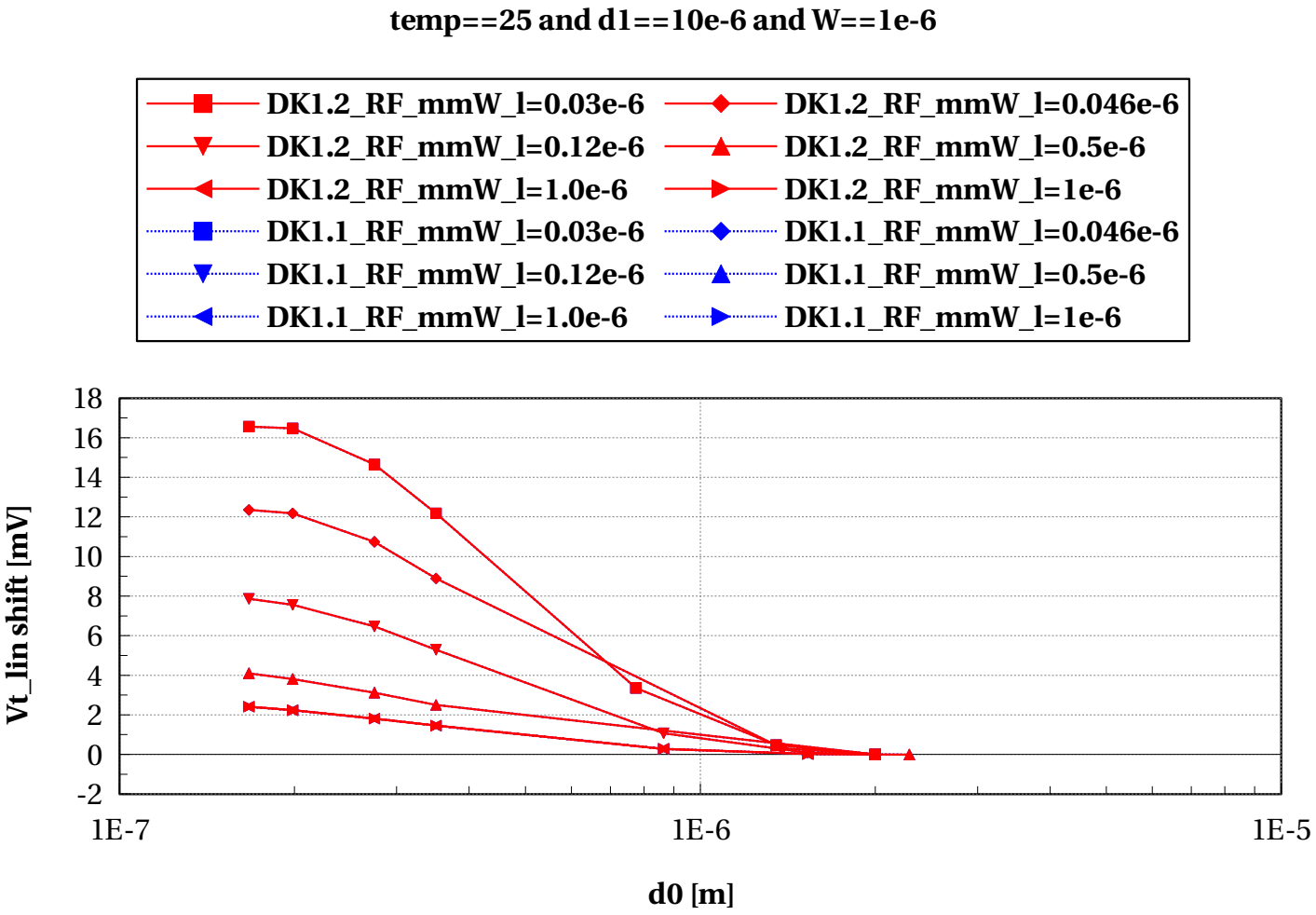
pfet_acc, Vt_lin shift [mV] vs d0 [m]



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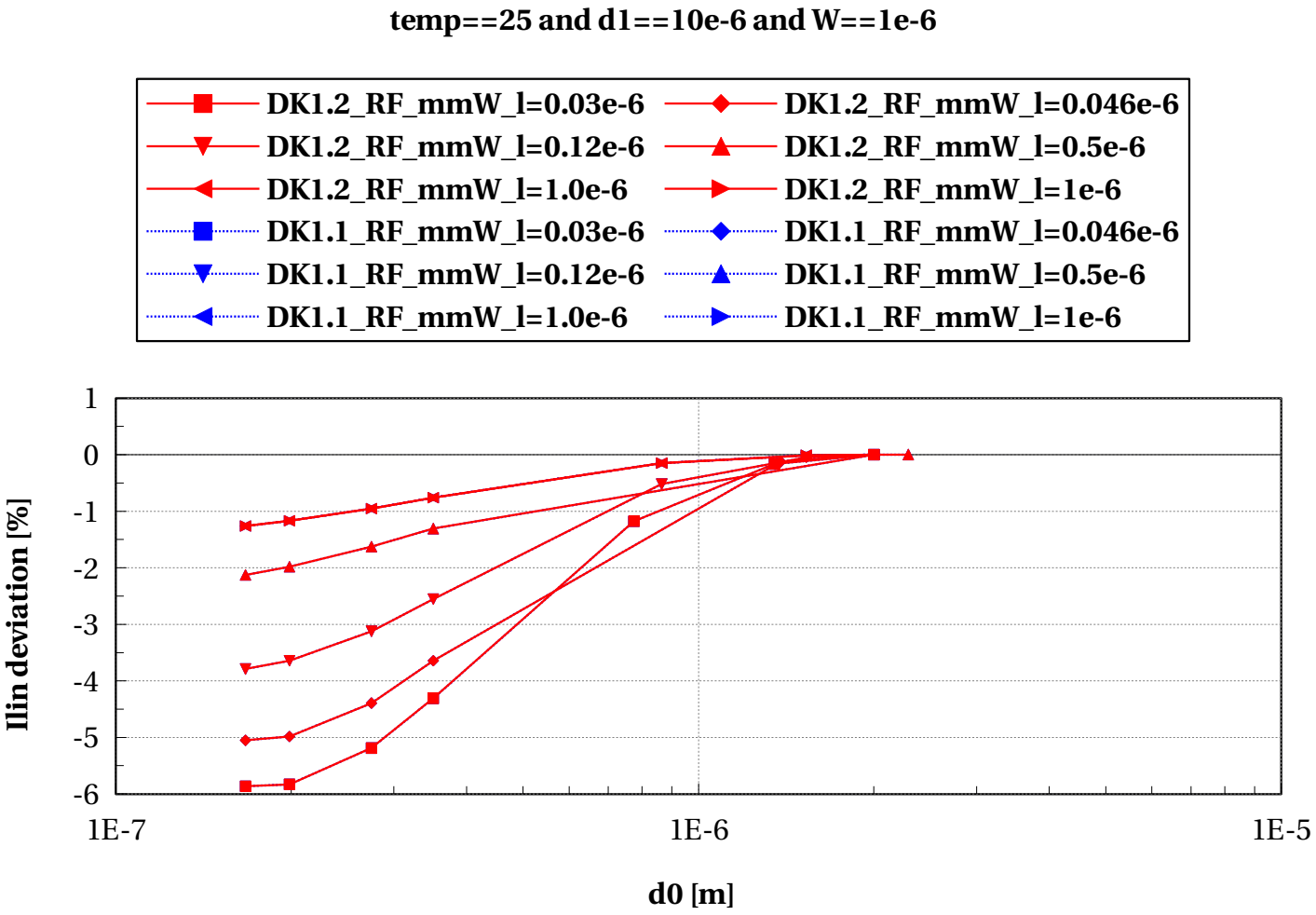
pfet_acc, Ilin deviation [%] vs d0 [m]



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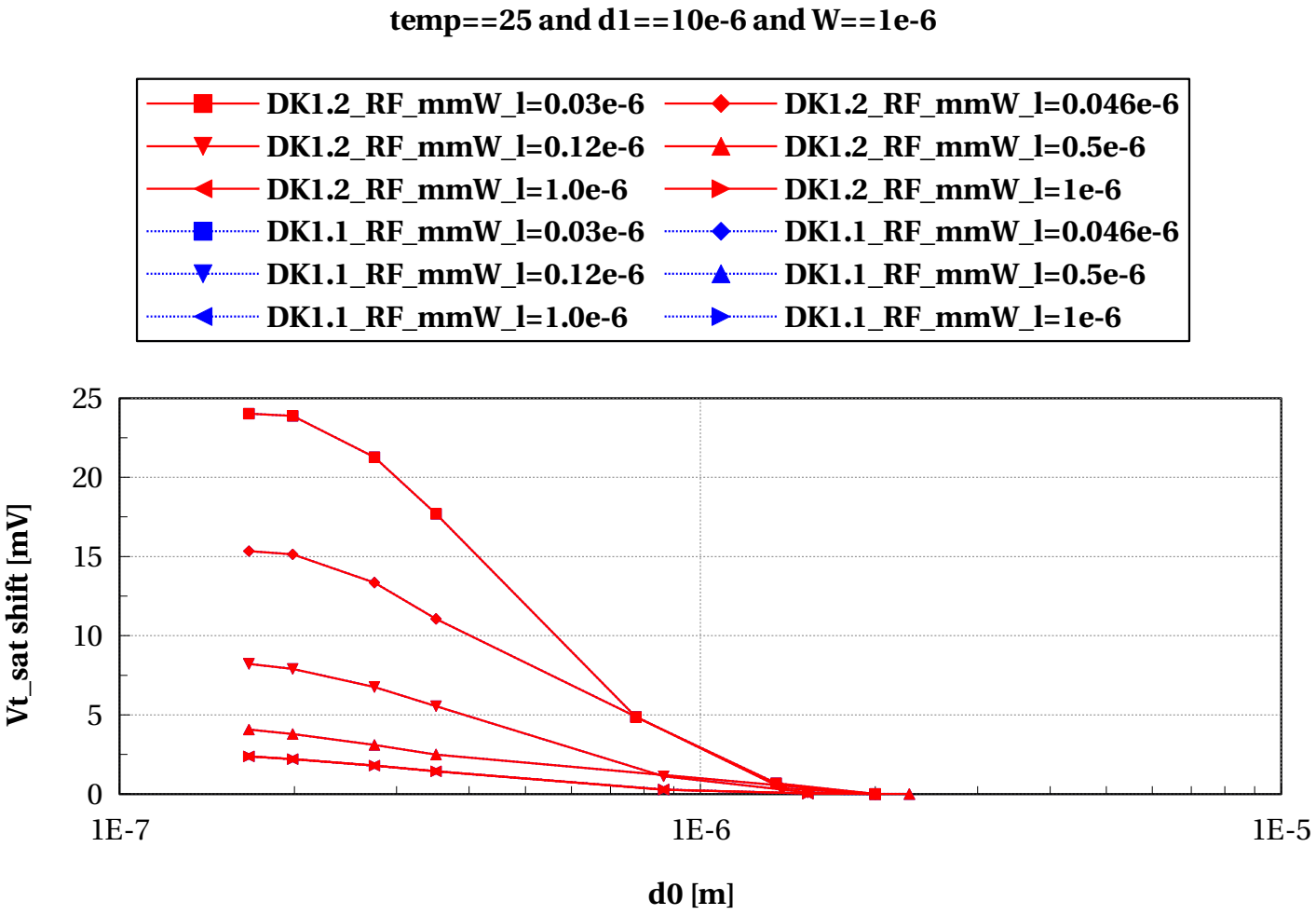
pfet_acc, Vt_sat shift [mV] vs d0 [m]



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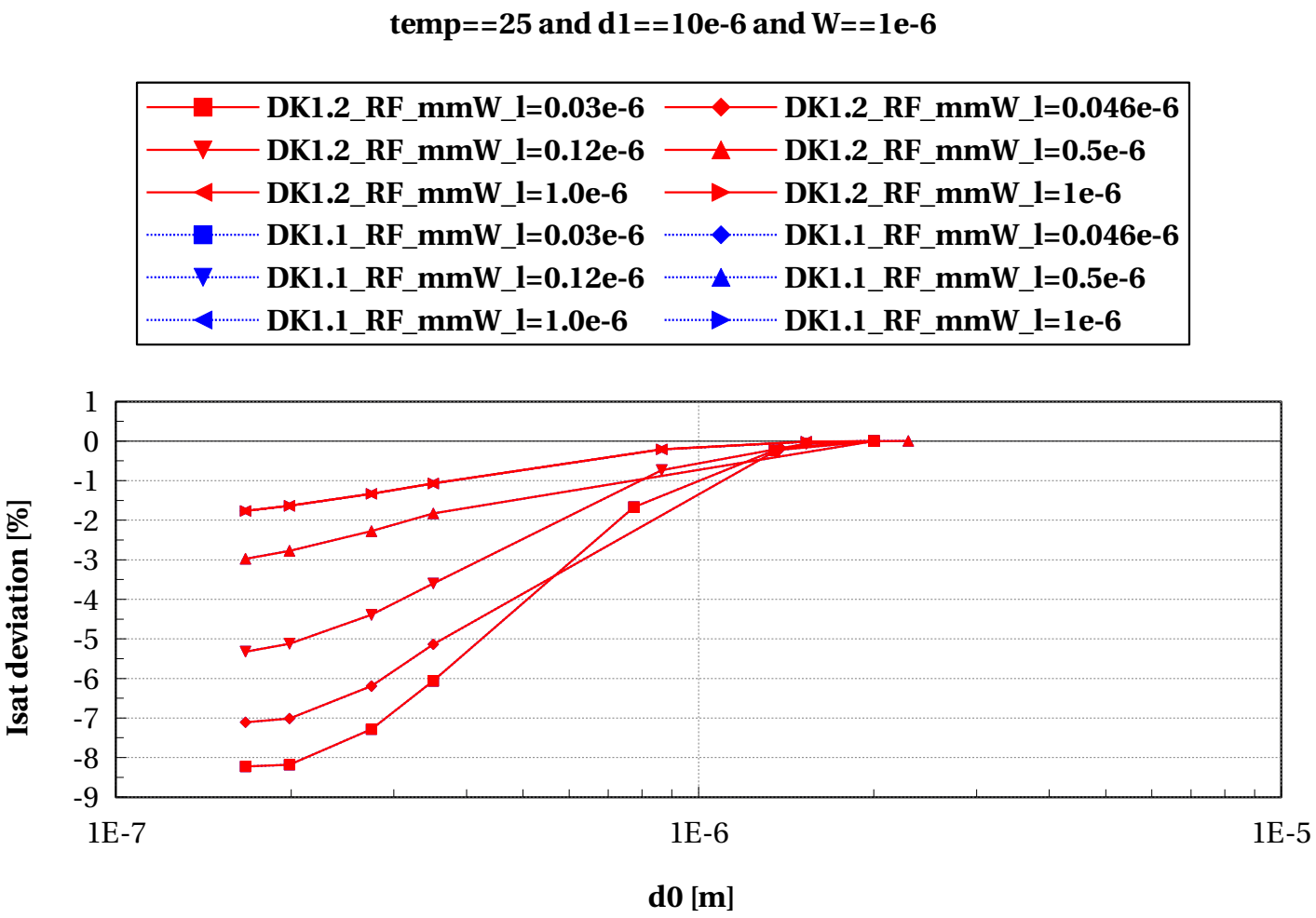
pfet_acc, Isat deviation [%] vs d0 [m]



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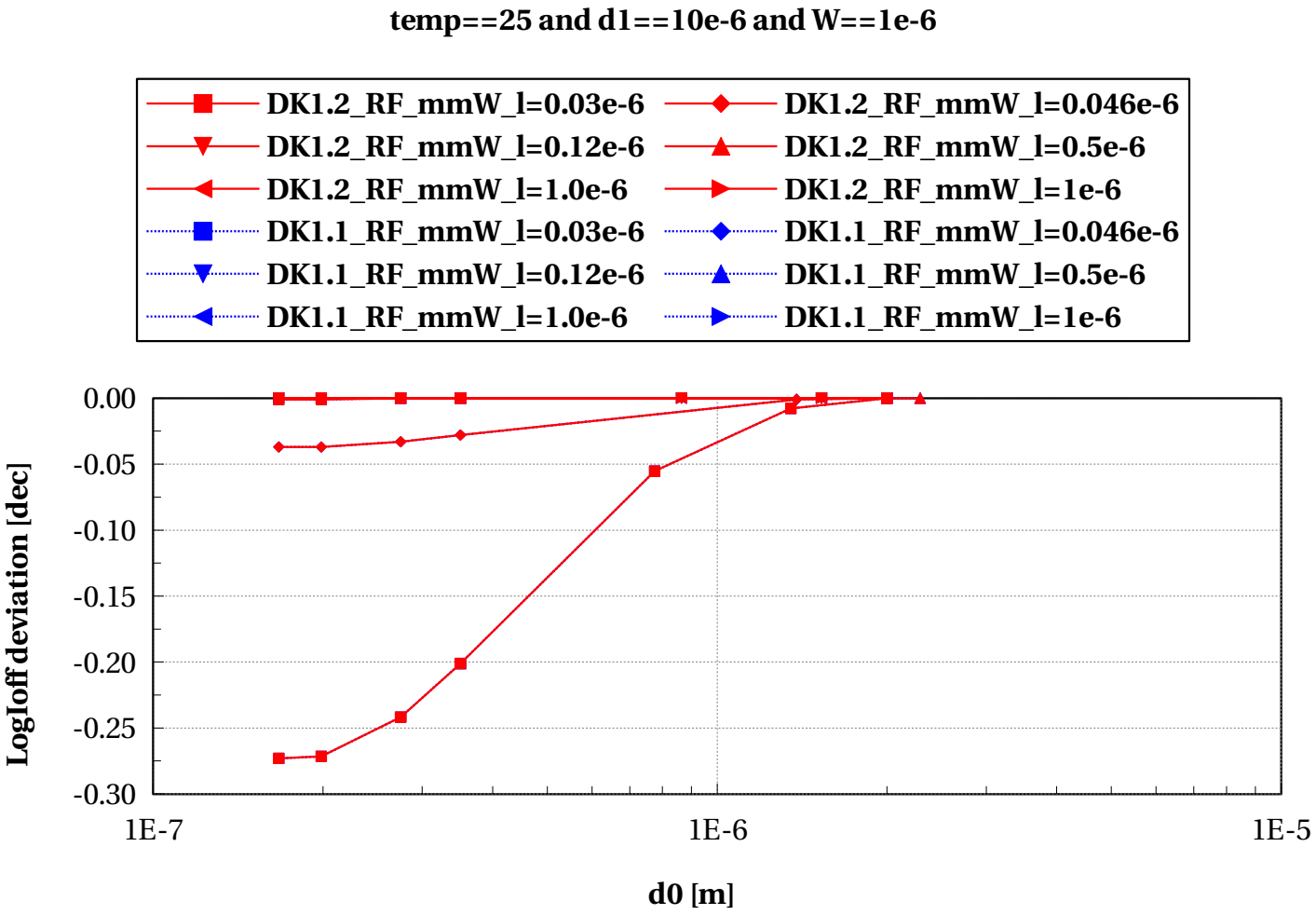
pfet_acc, LogIoff deviation [dec] vs d0 [m]



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- WPE effect w.r.t d0 - Lscaling at W=0.34u

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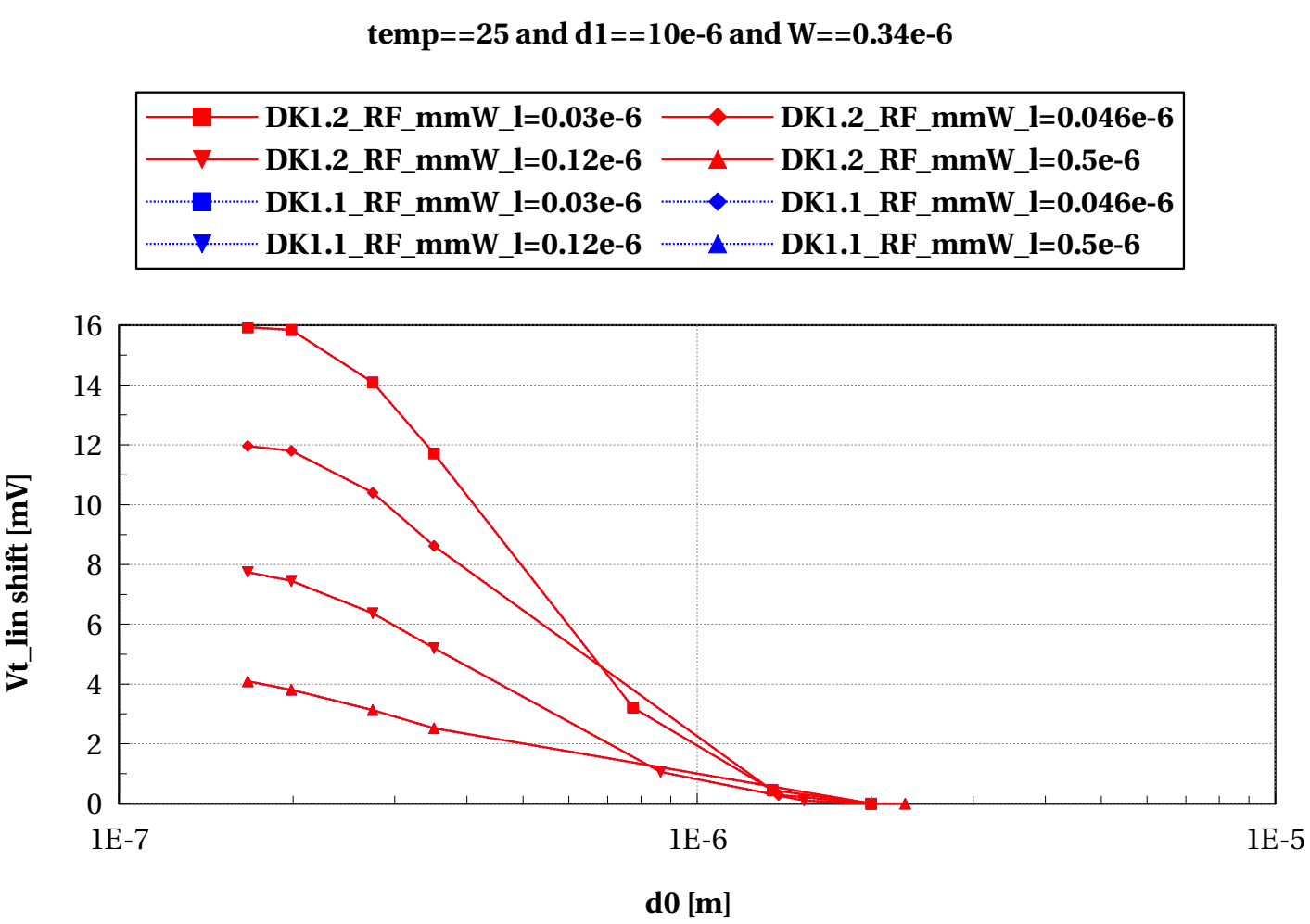
pfet_acc, Vt_lin shift [mV] vs d0 [m]



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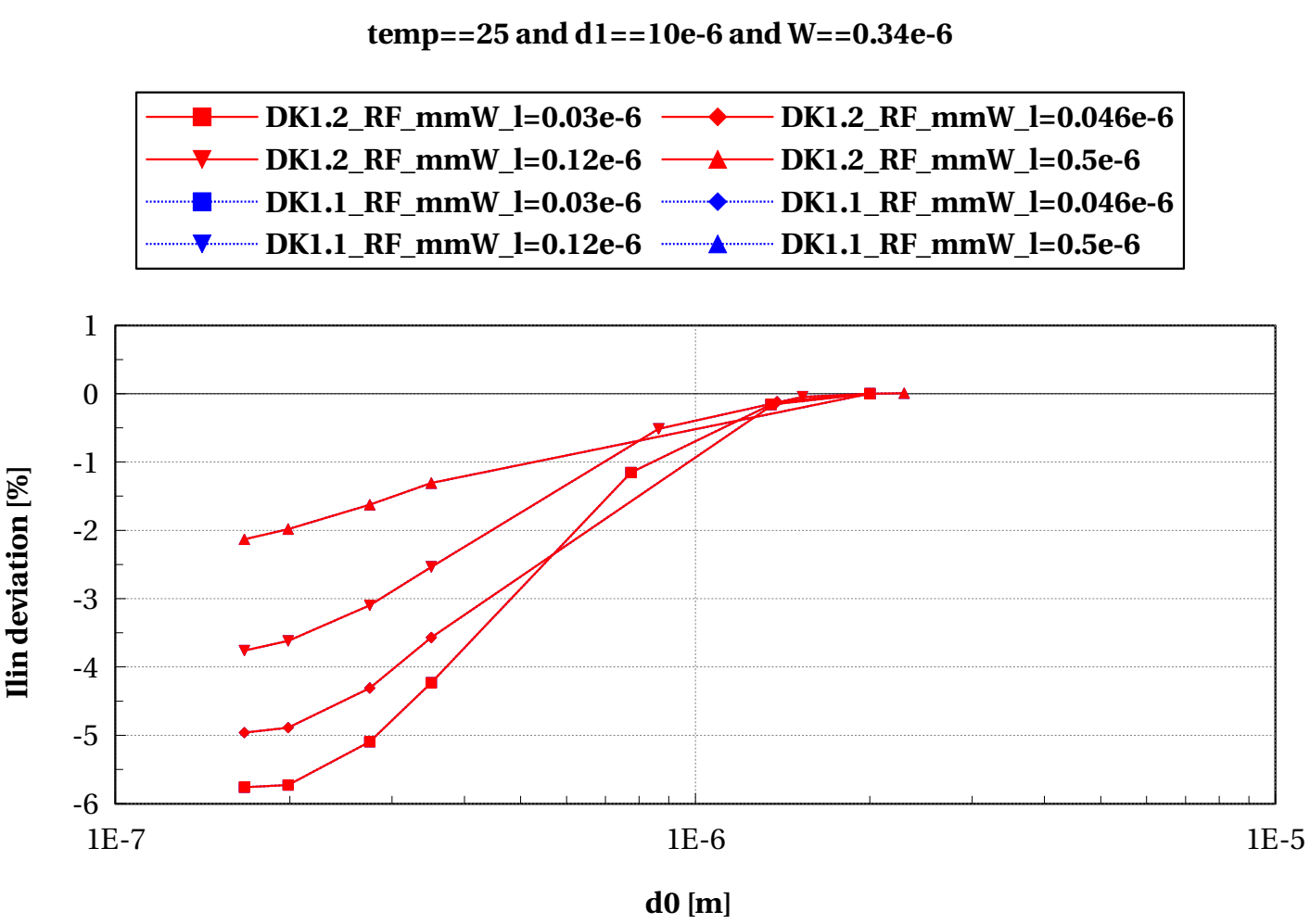
pfet_acc, Ilin deviation [%] vs d0 [m]



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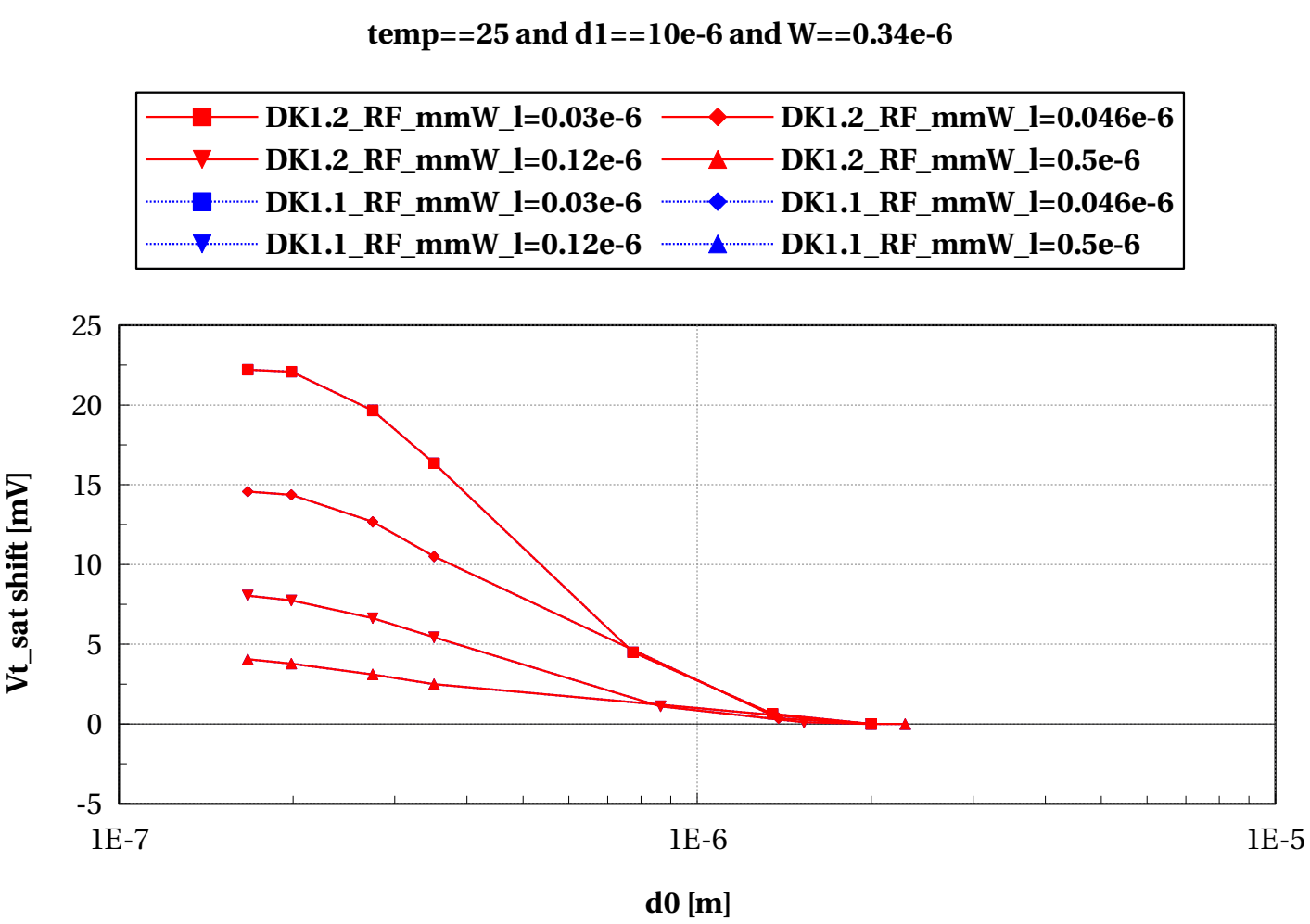
pfet_acc, Vt_sat shift [mV] vs d0 [m]



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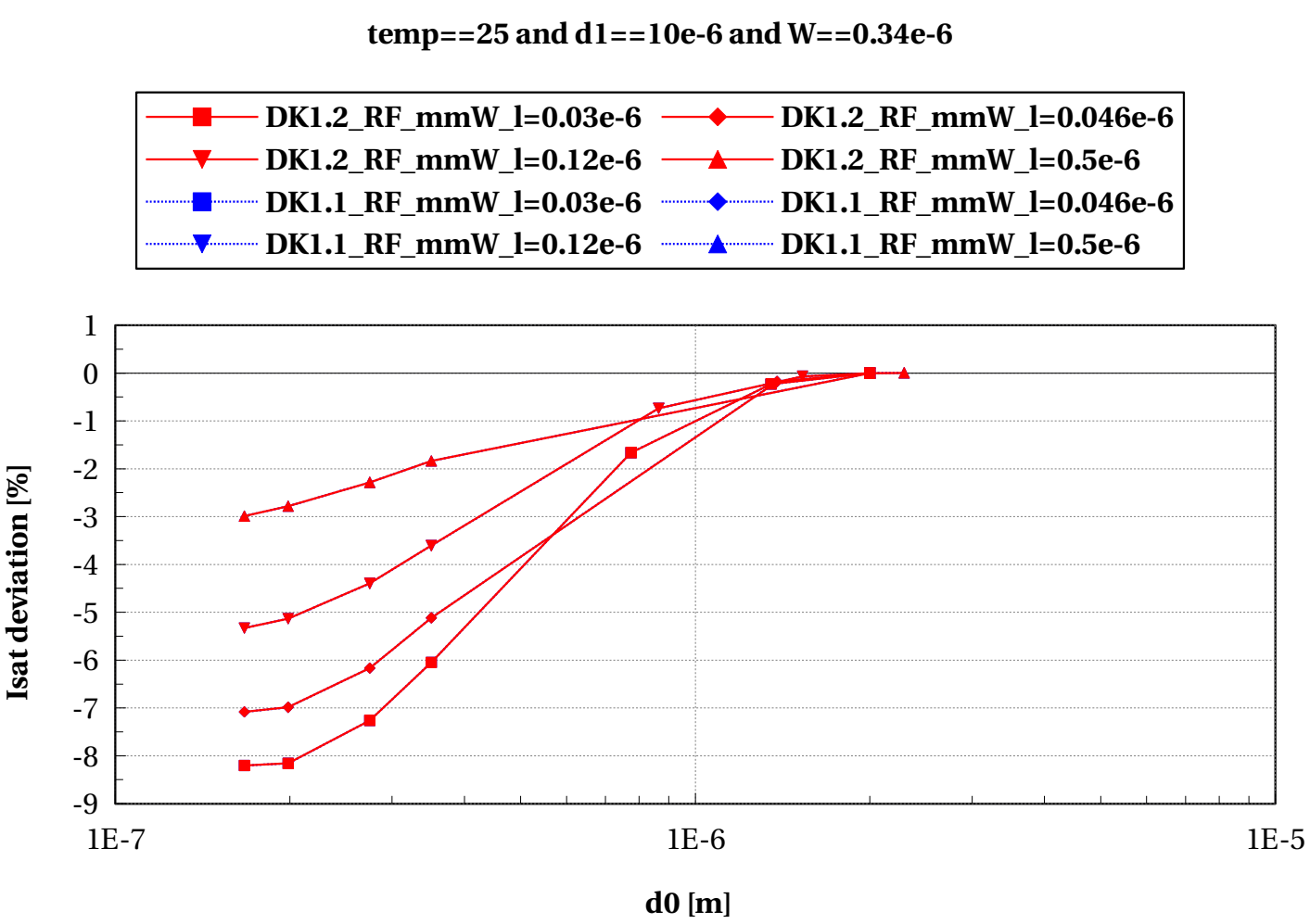
pfet_acc, Isat deviation [%] vs d0 [m]



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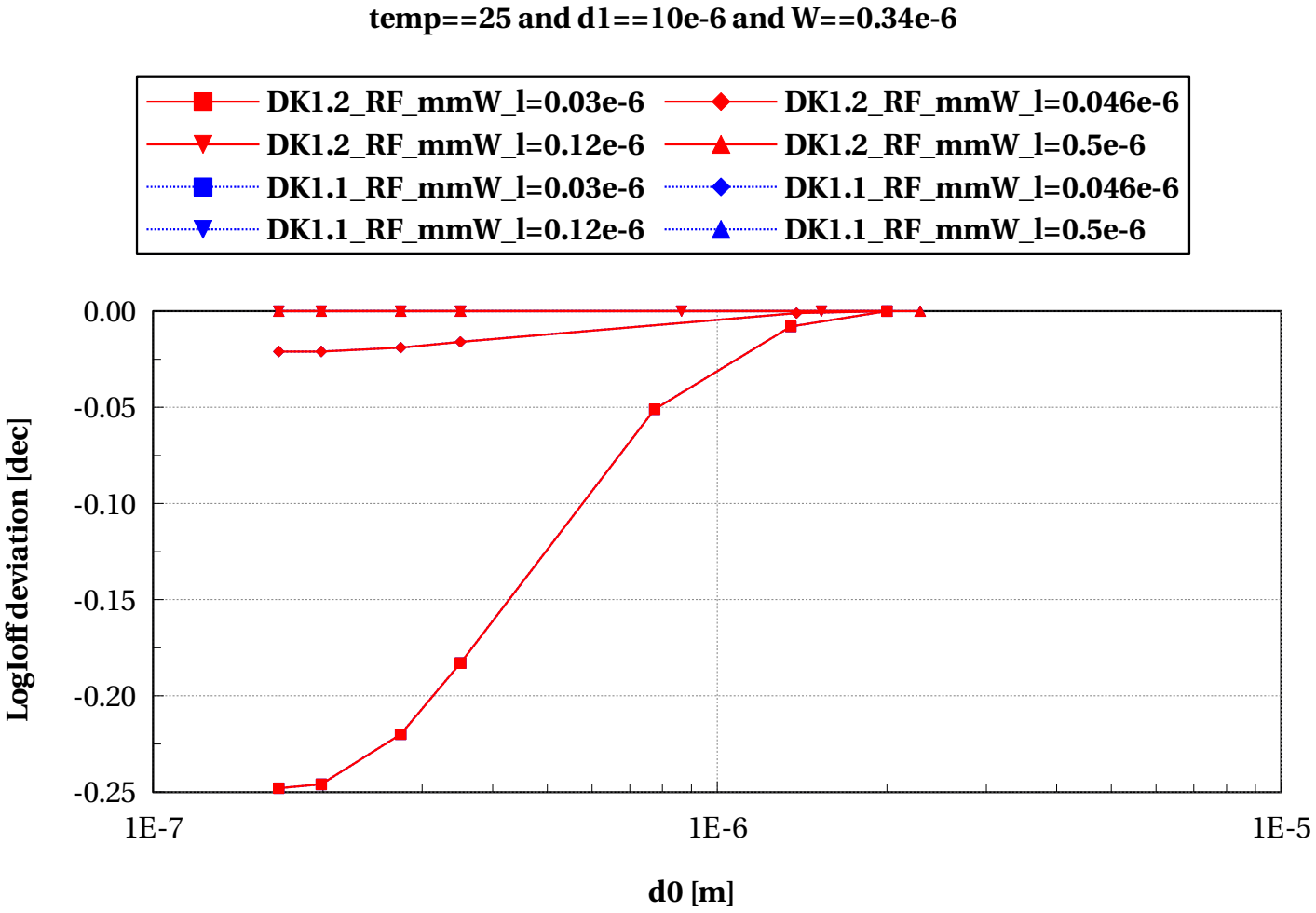
pfet_acc, LogIoff deviation [dec] vs d0 [m]



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- WPE effect w.r.t d0 - Lscaling at W=0.1u

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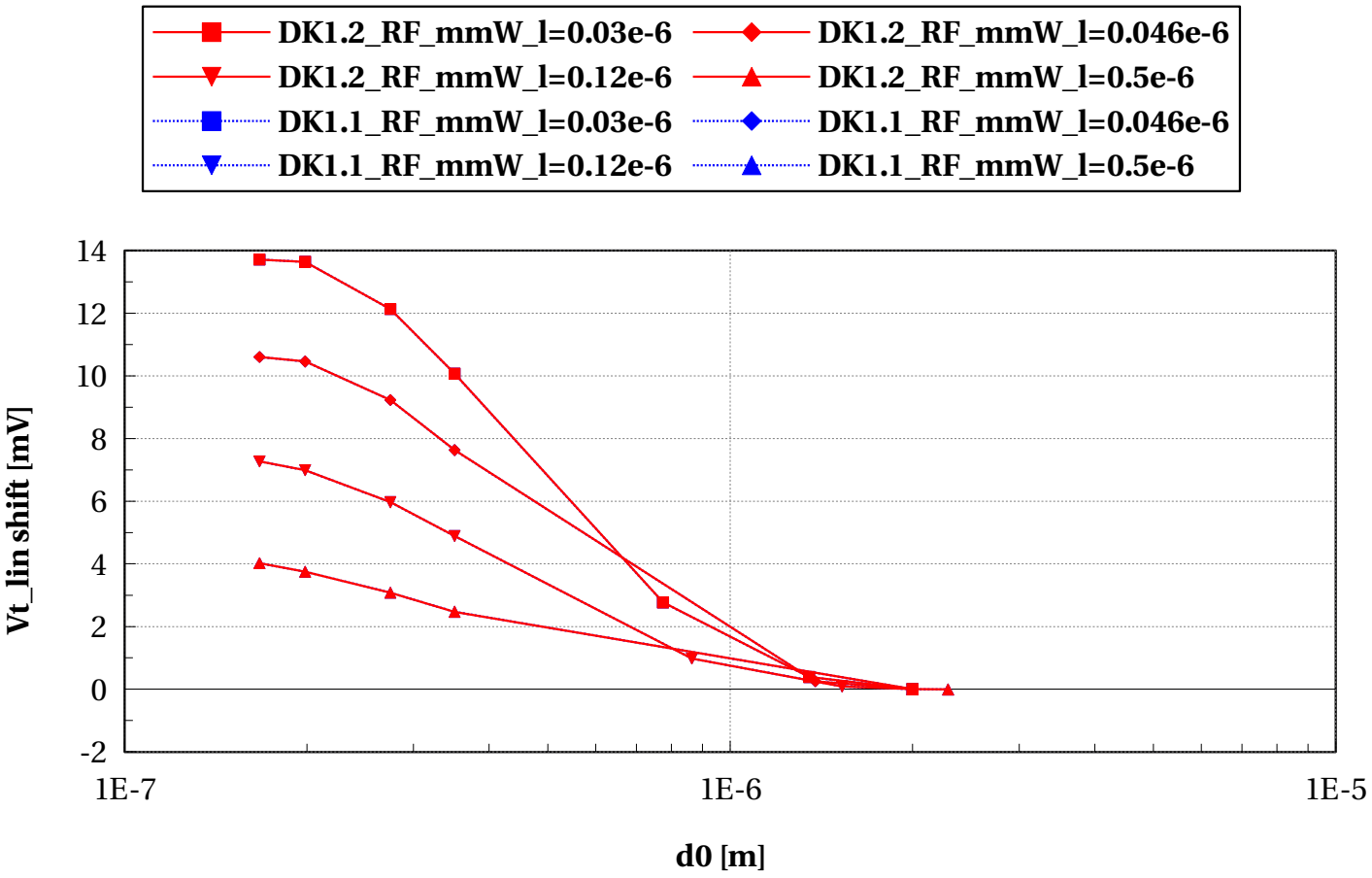
pfet_acc, Vt_lin shift [mV] vs d0 [m]

temp==25 and d1==10e-6 and W==0.1e-6



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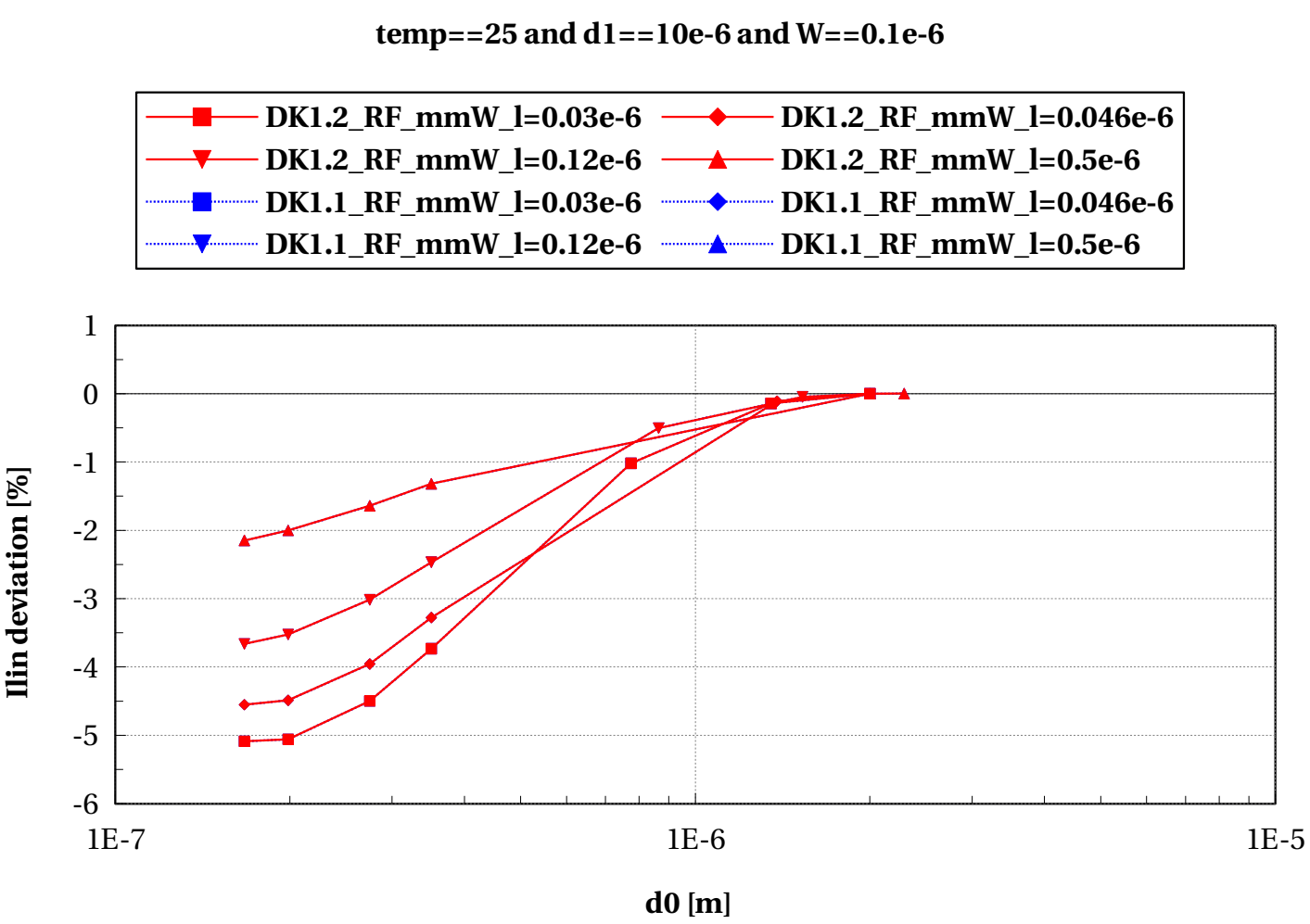
pfet_acc, Ilin deviation [%] vs d0 [m]



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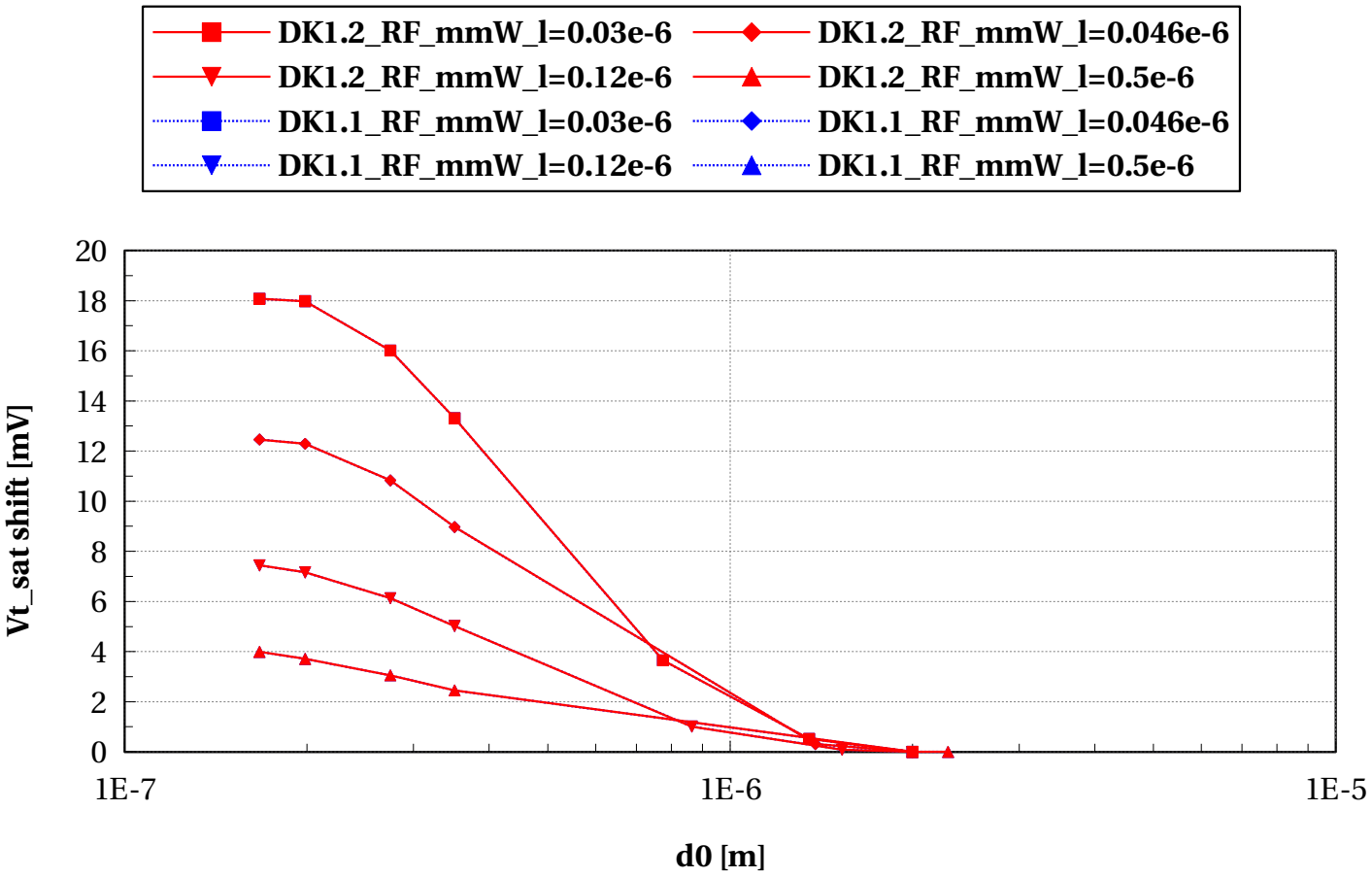
pfet_acc, Vt_sat shift [mV] vs d0 [m]

temp==25 and d1==10e-6 and W==0.1e-6



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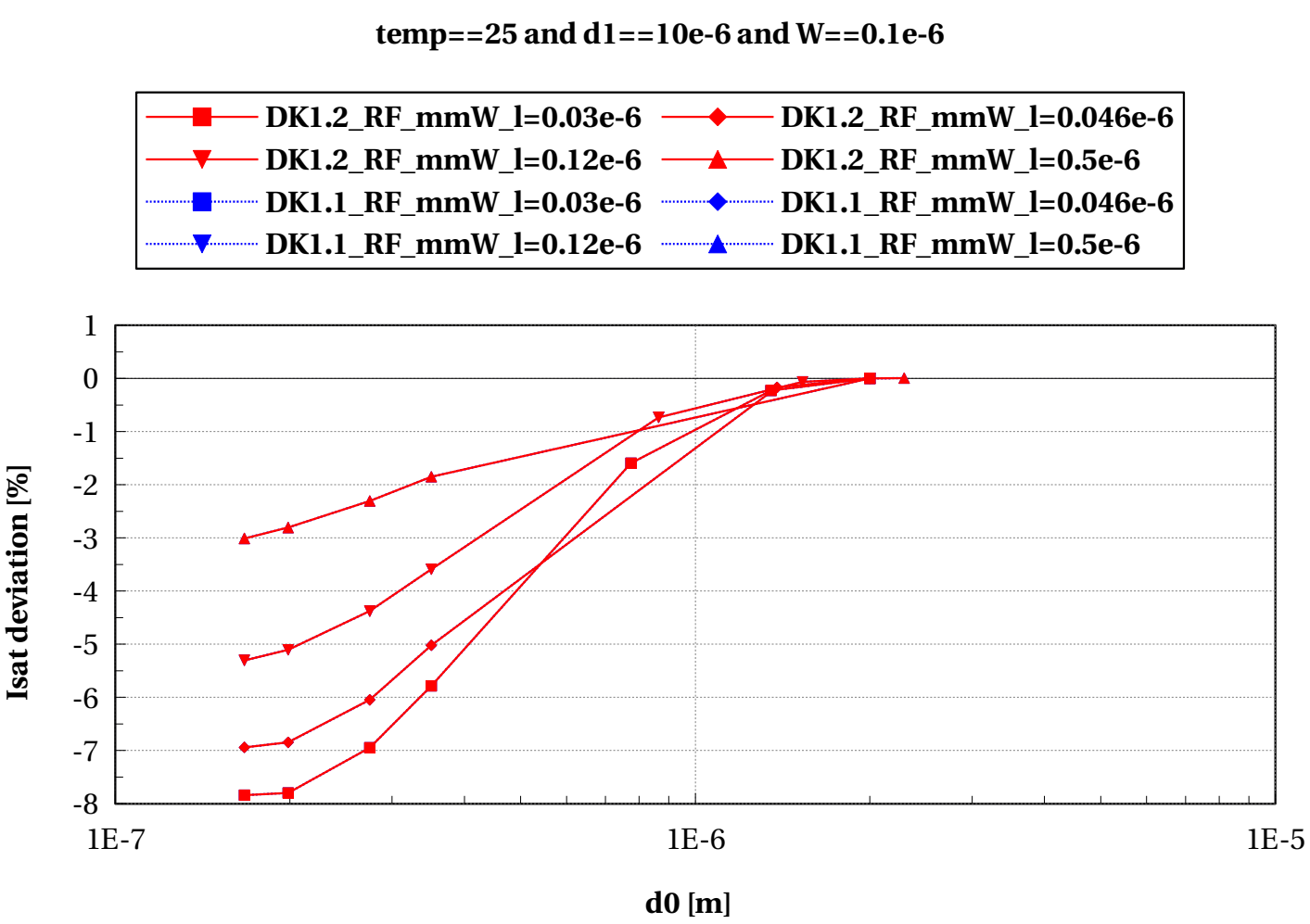
pfet_acc, Isat deviation [%] vs d0 [m]



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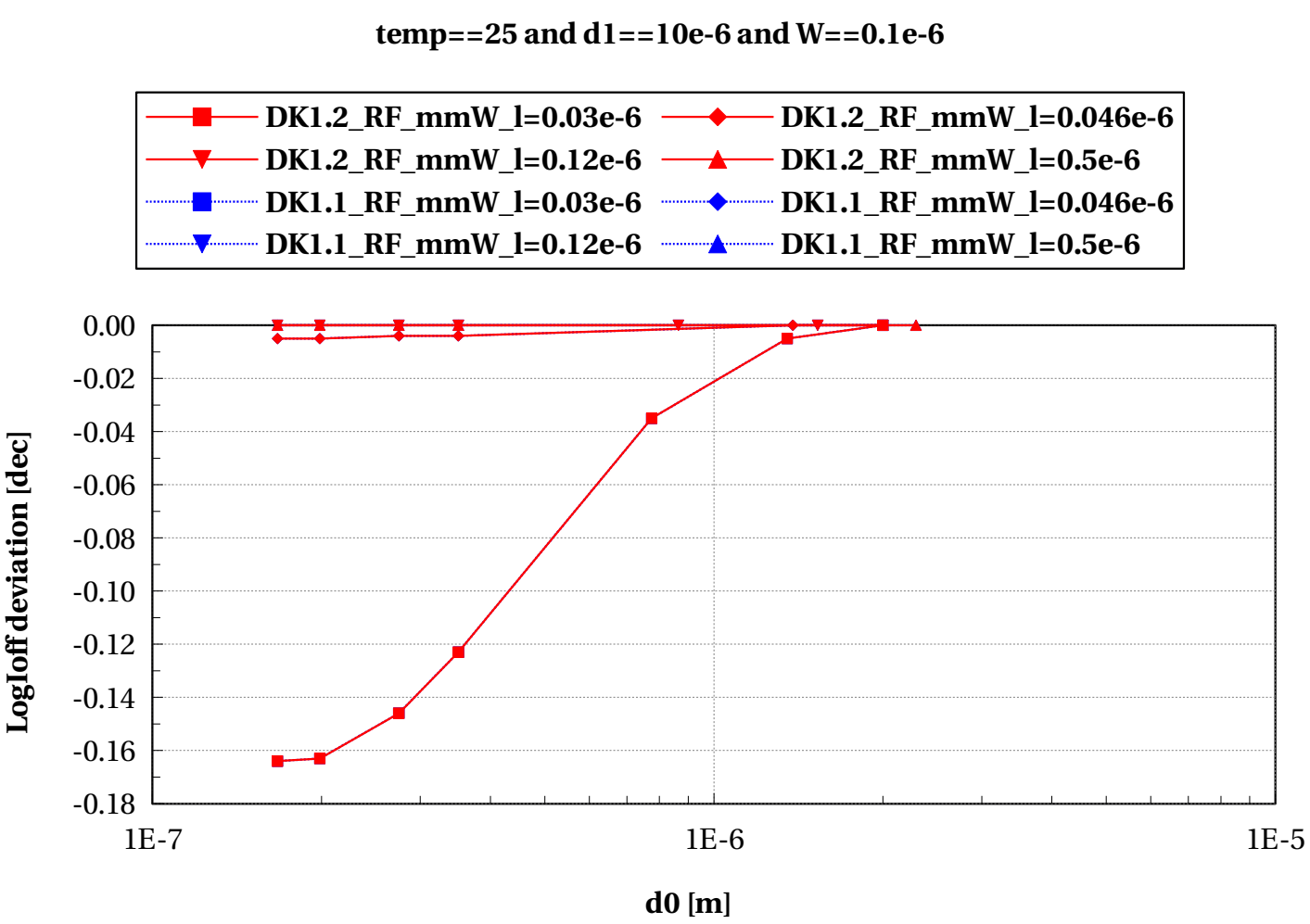
pfet_acc, LogIoff deviation [dec] vs d0 [m]



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- WPE effect w.r.t d0 - Wscaling at L=0.03u

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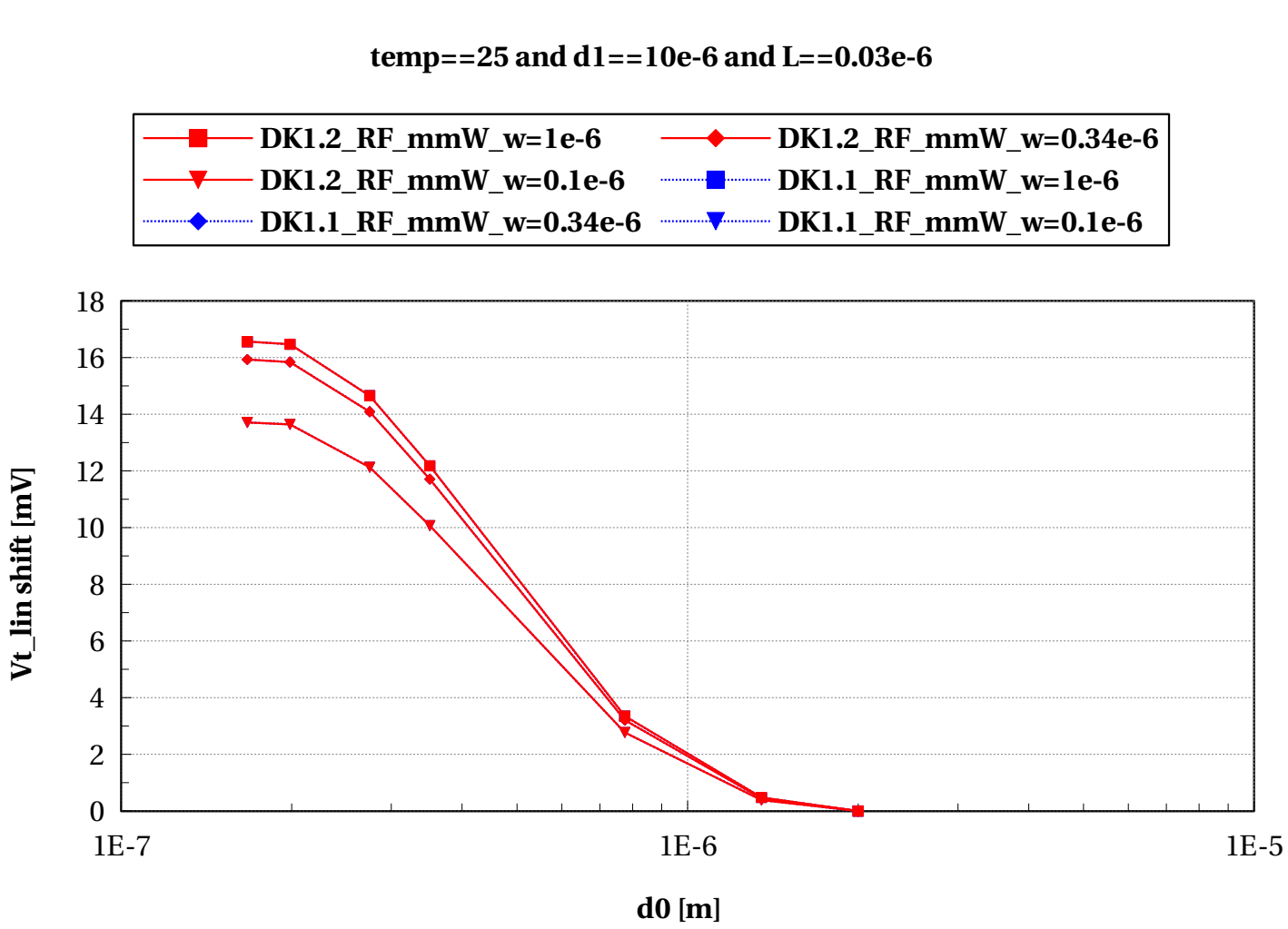
pfet_acc, Vt_lin shift [mV] vs d0 [m]



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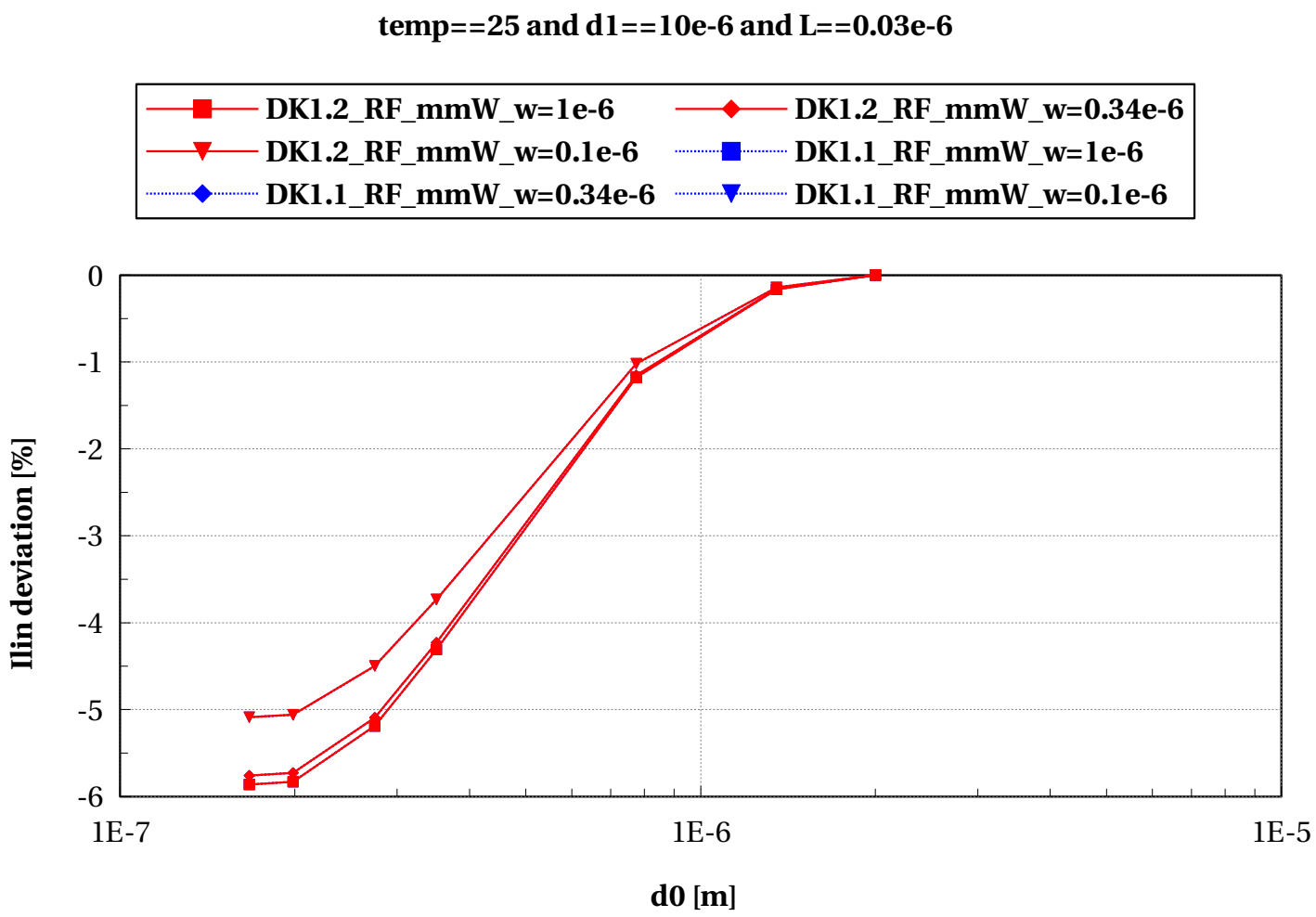
pfet_acc, Ilin deviation [%] vs d0 [m]



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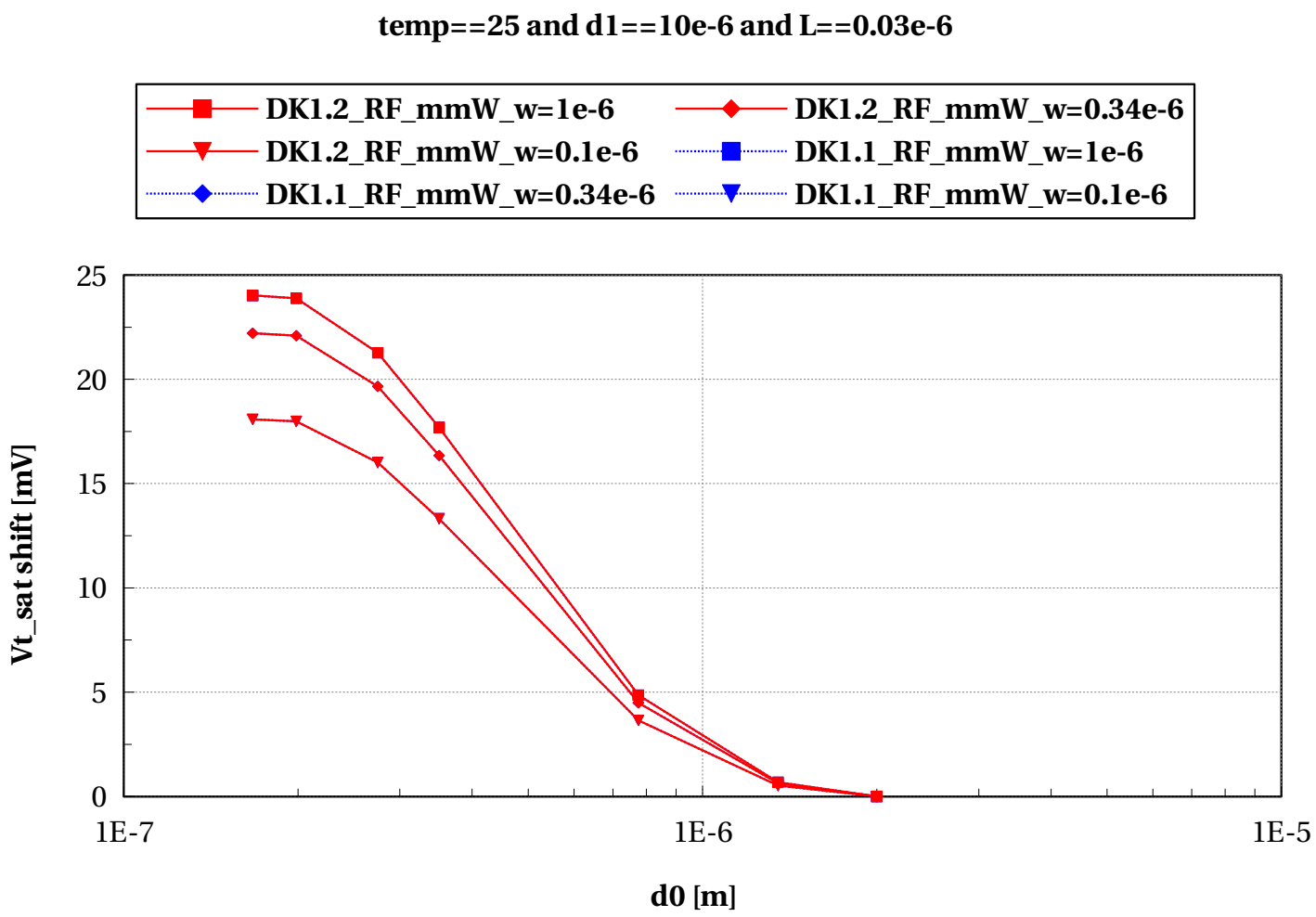
pfet_acc, Vt_sat shift [mV] vs d0 [m]



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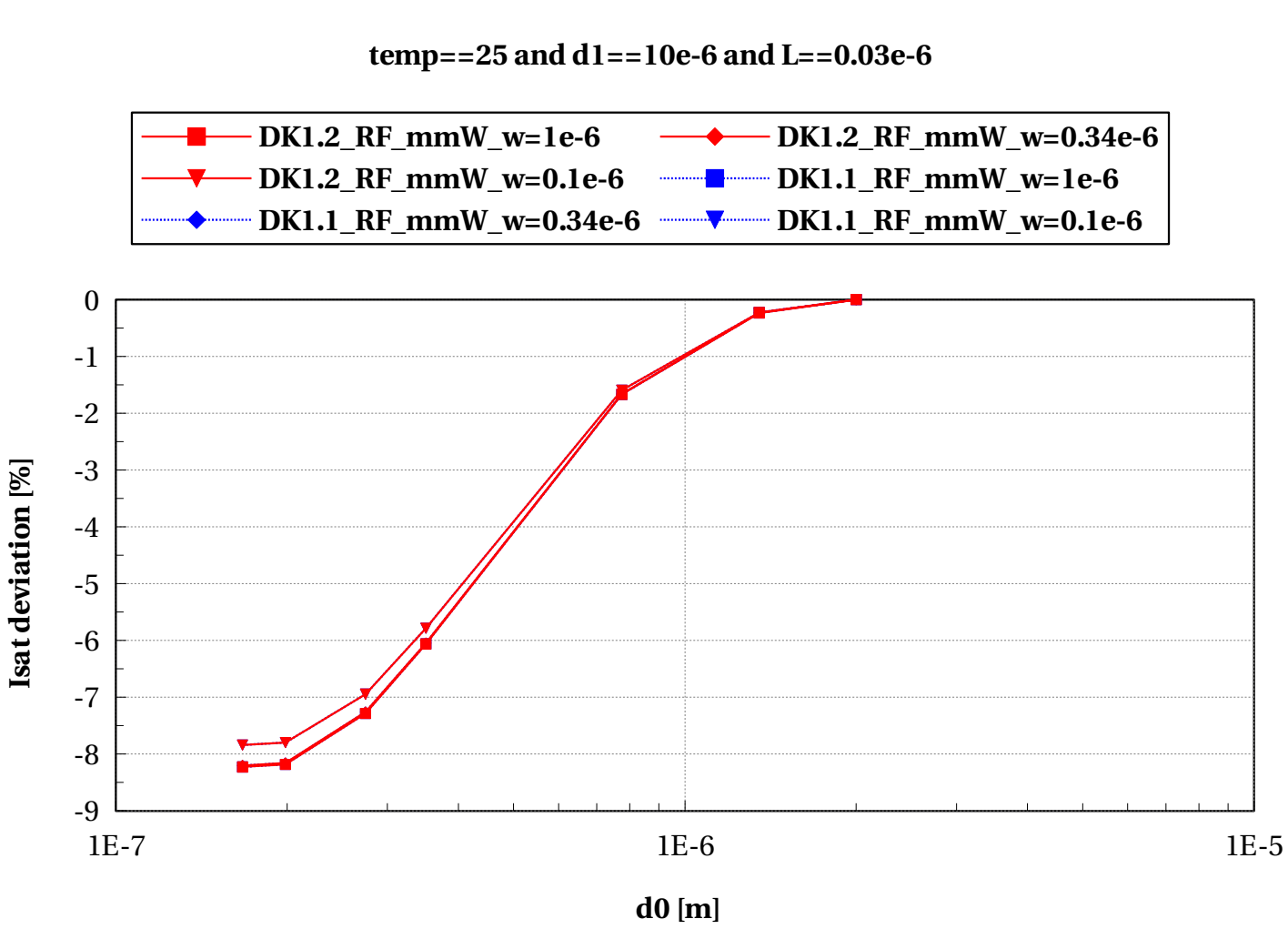
pfet_acc, Isat deviation [%] vs d0 [m]



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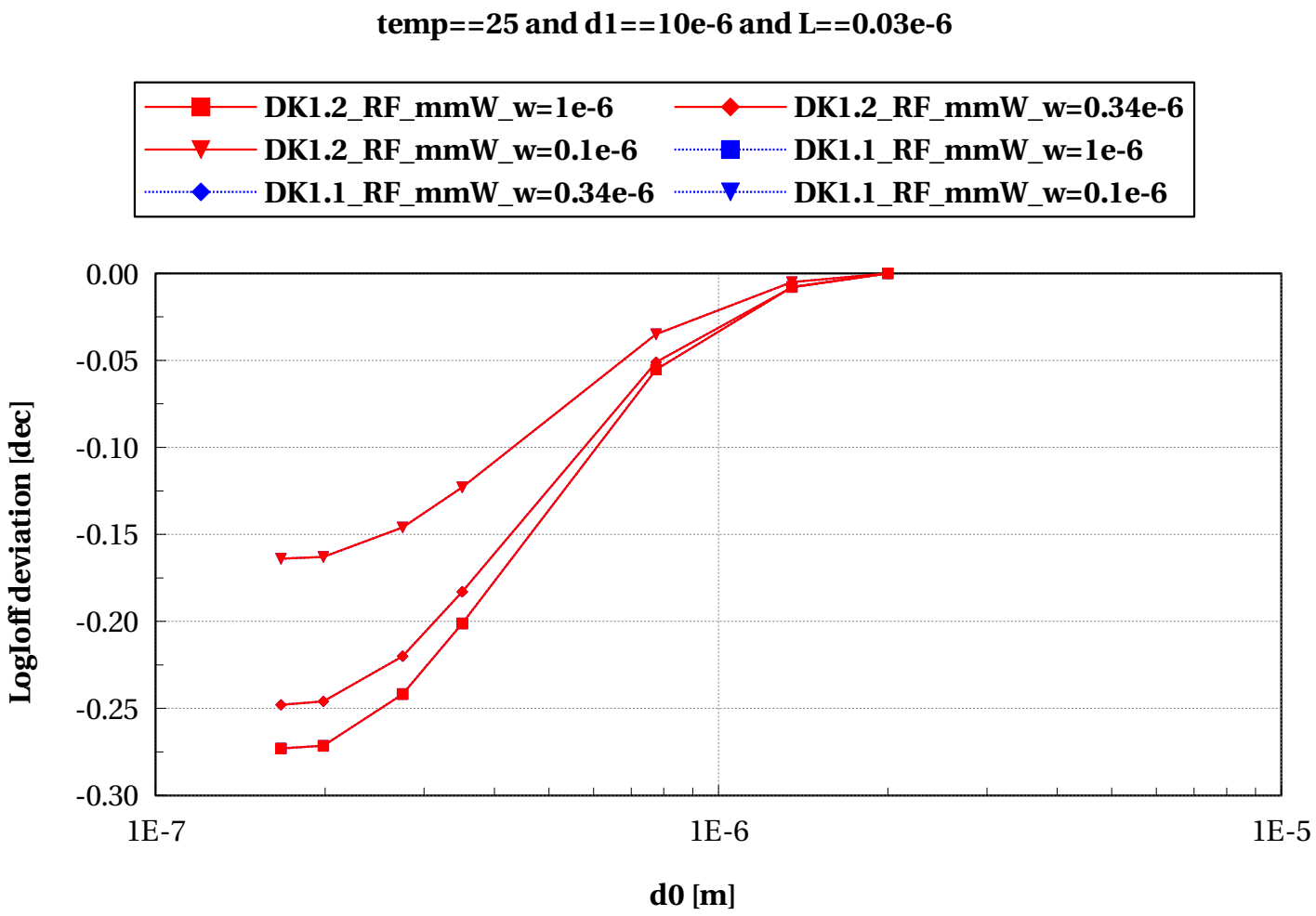
pfet_acc, LogIoff deviation [dec] vs d0 [m]



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- WPE effect w.r.t d0 - Wscaling at L=0.5u

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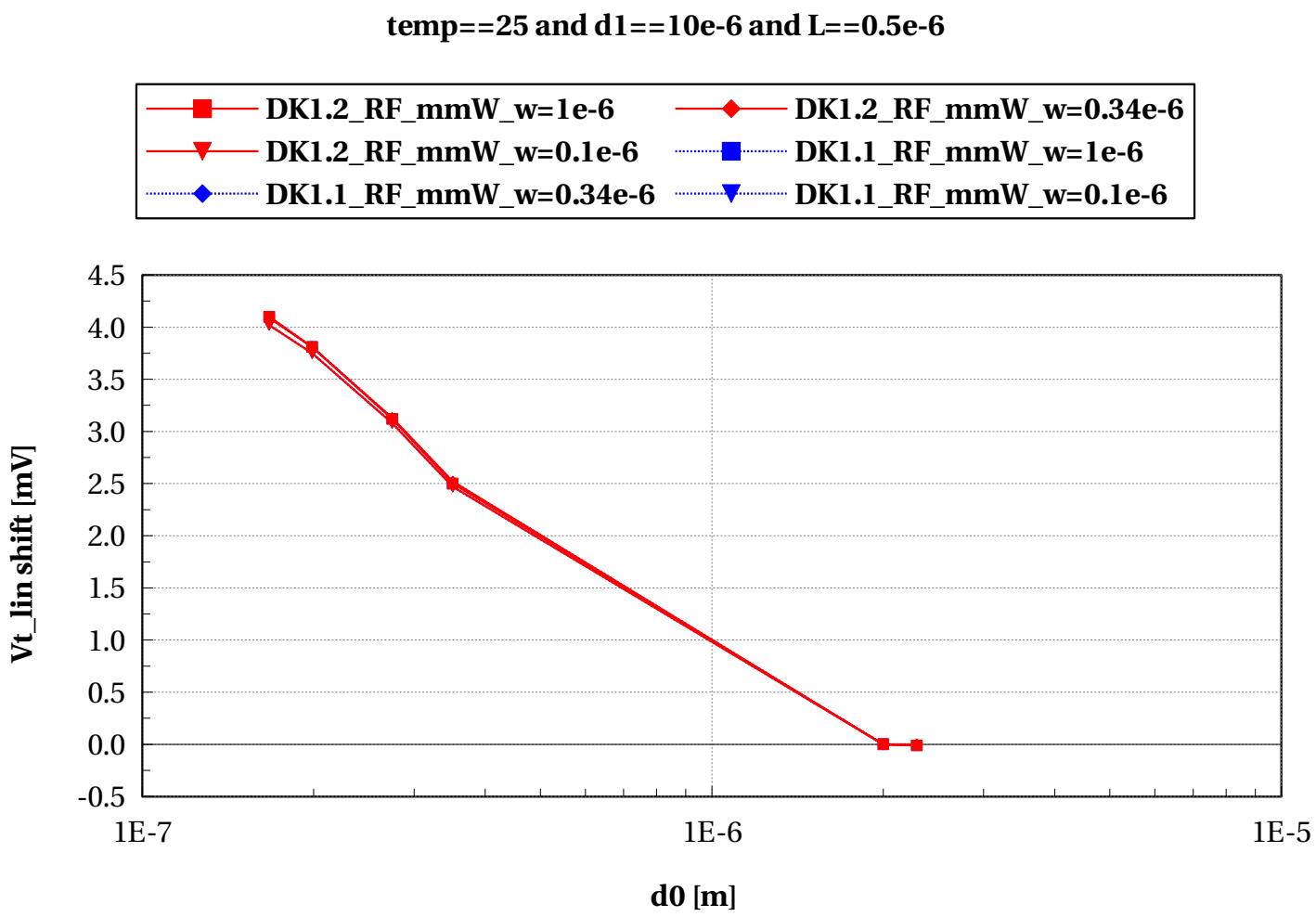
pfet_acc, Vt_lin shift [mV] vs d0 [m]



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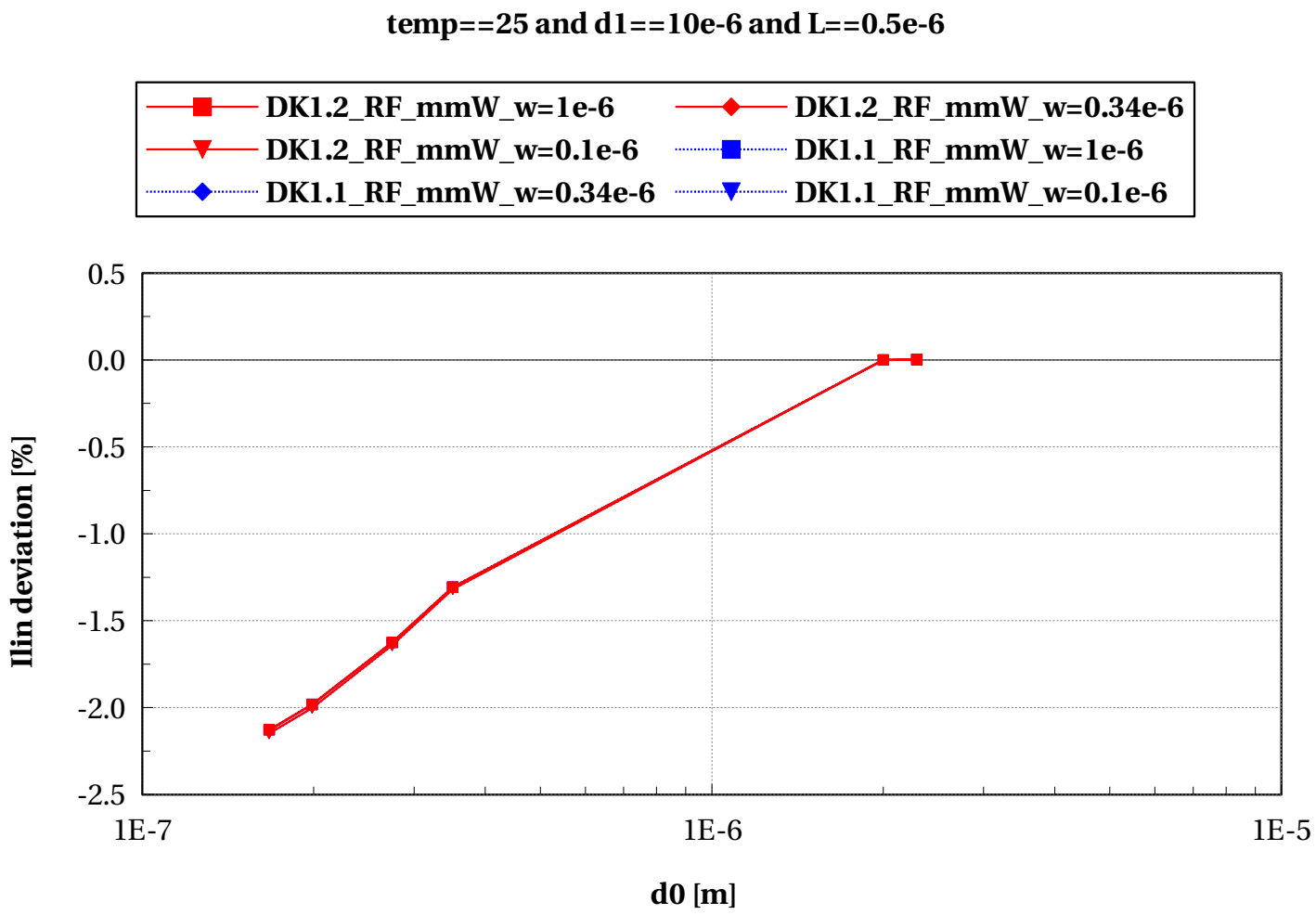
pfet_acc, Ilin deviation [%] vs d0 [m]



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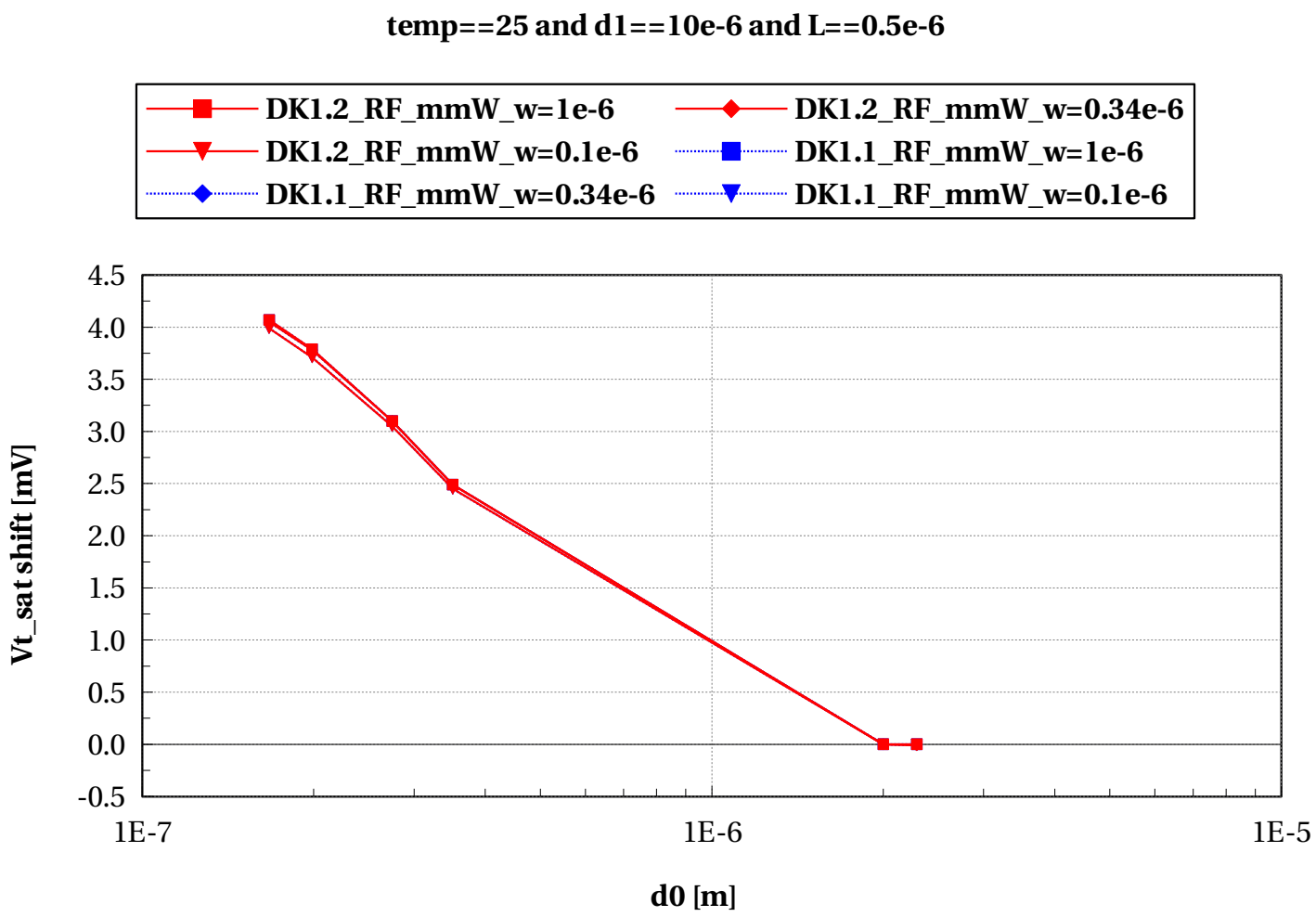
pfet_acc, Vt_sat shift [mV] vs d0 [m]



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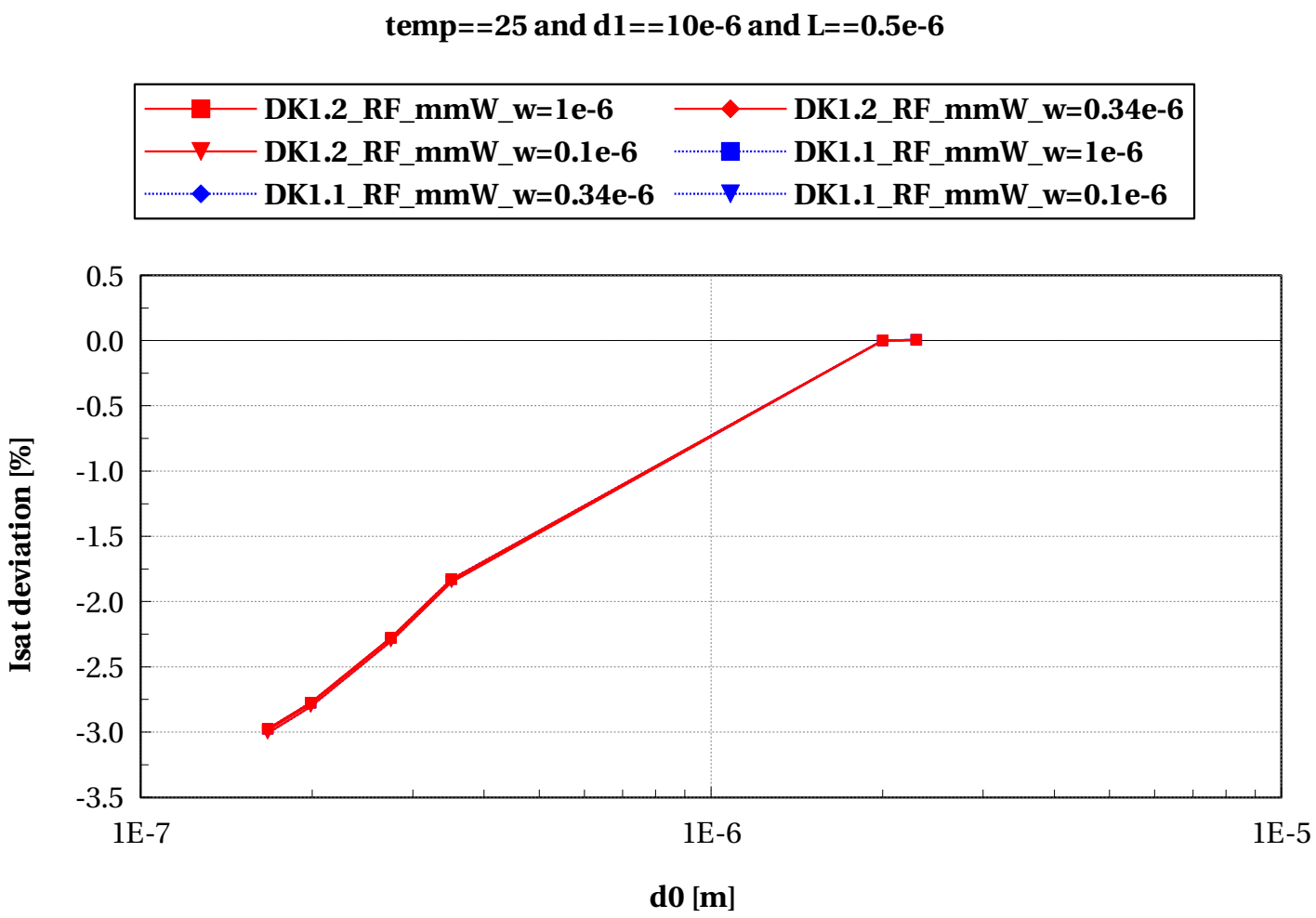
pfet_acc, Isat deviation [%] vs d0 [m]



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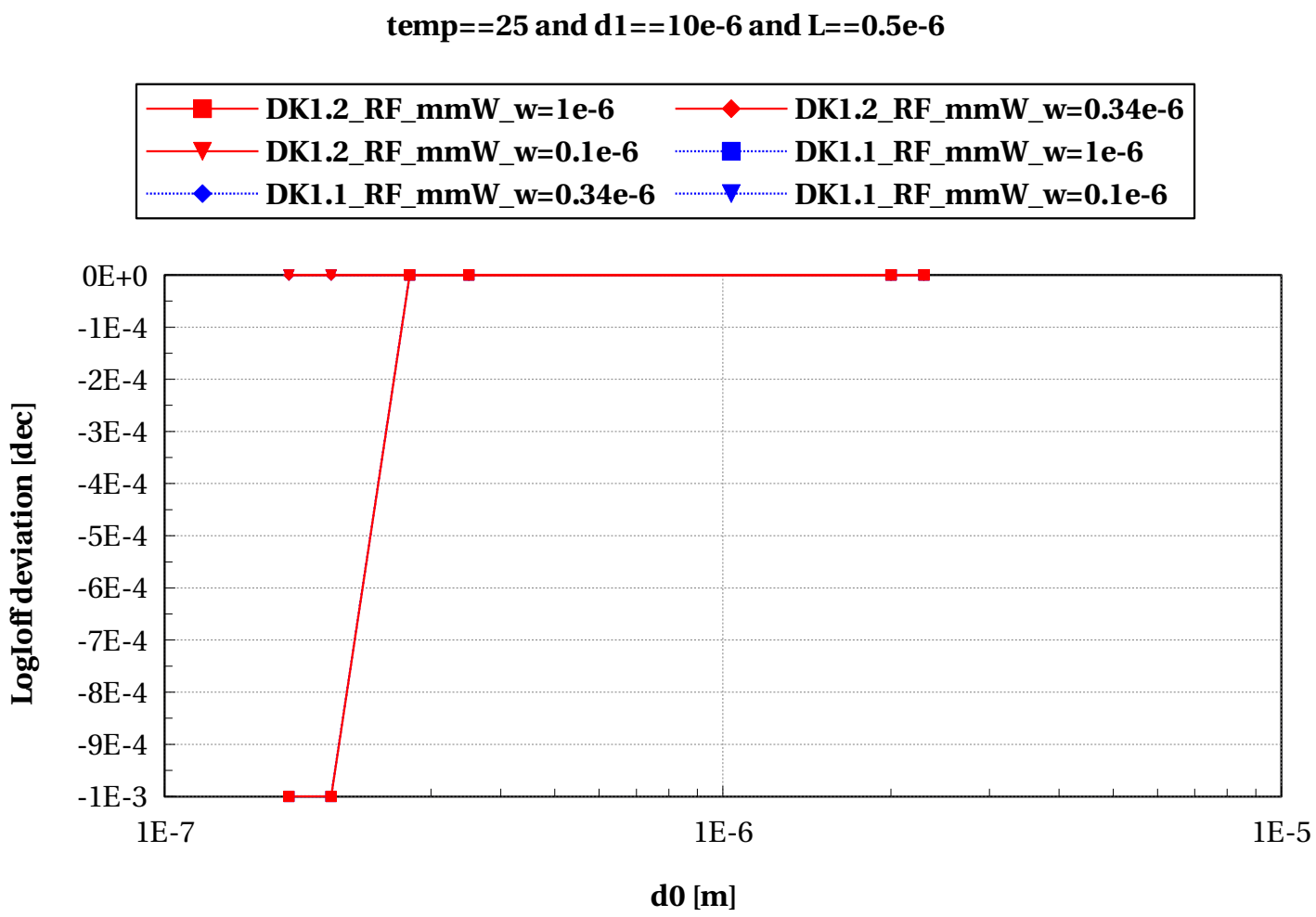
pfet_acc, LogIoff deviation [dec] vs d0 [m]



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- WPE effect w.r.t d1 - Wscaling at L=0.03u

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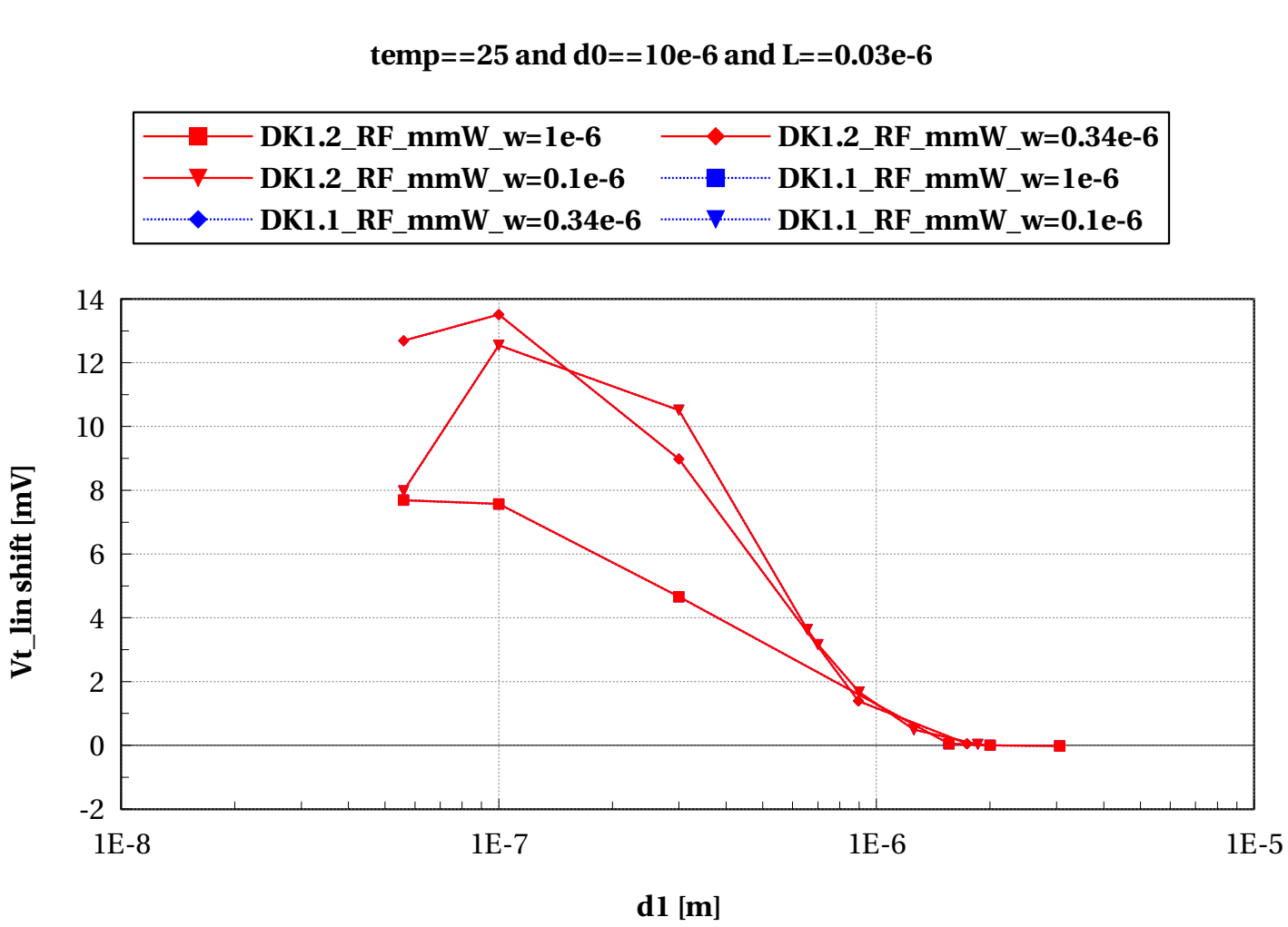
pfet_acc, Vt_lin shift [mV] vs d1 [m]



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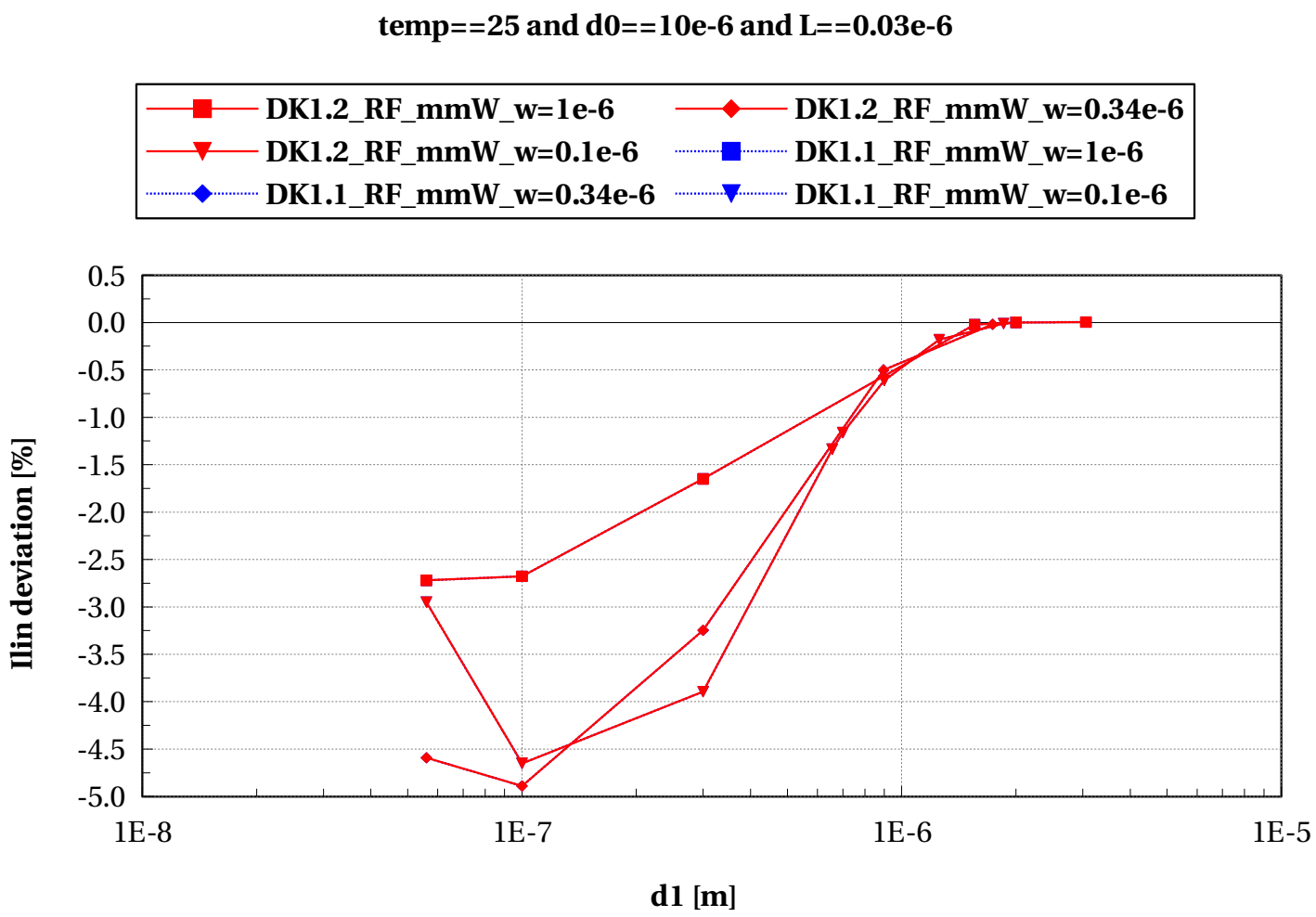
pfet_acc, Ilin deviation [%] vs d1 [m]



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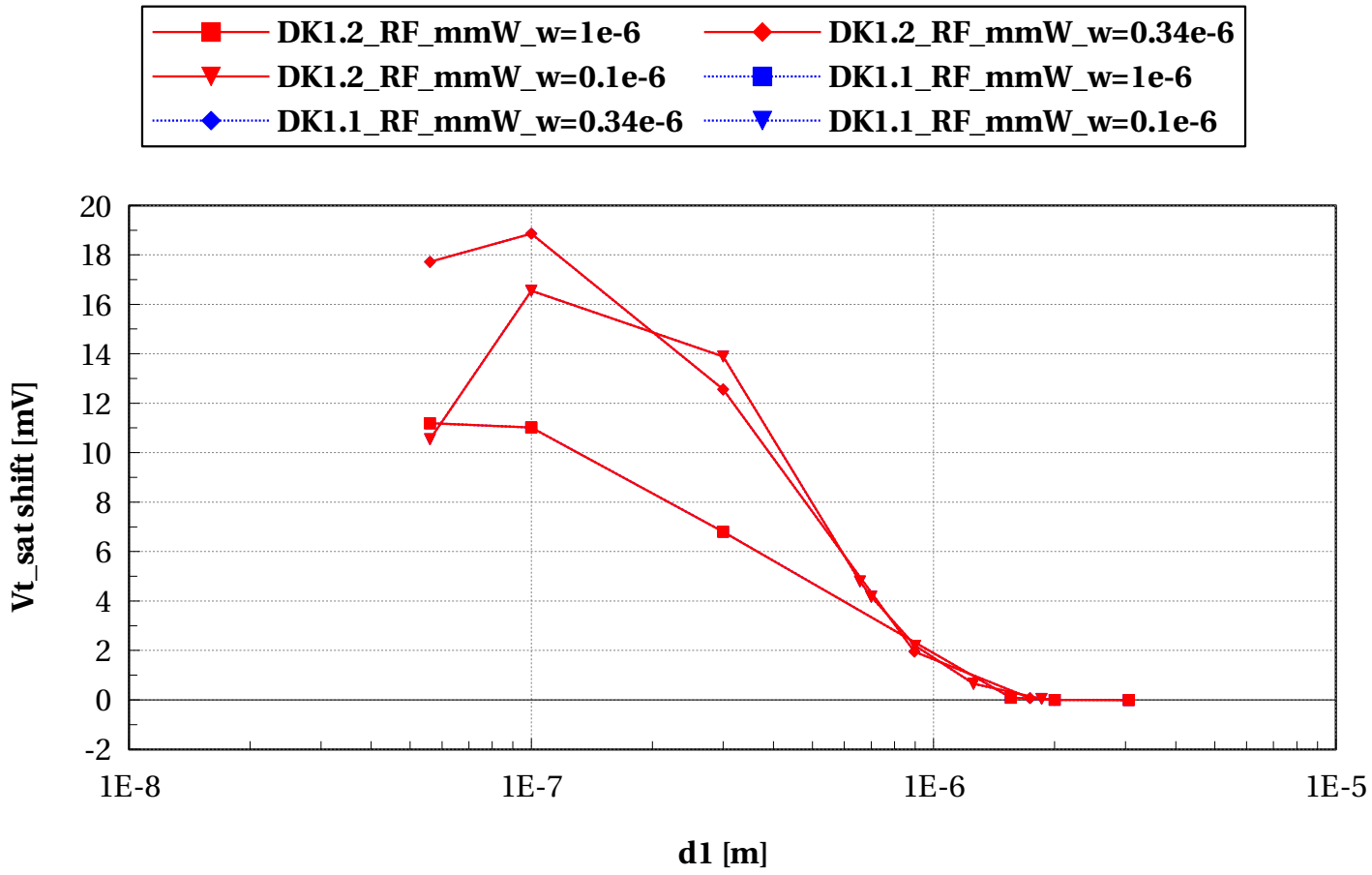
pfet_acc, Vt_sat shift [mV] vs d1 [m]

temp==25 and d0==10e-6 and L==0.03e-6



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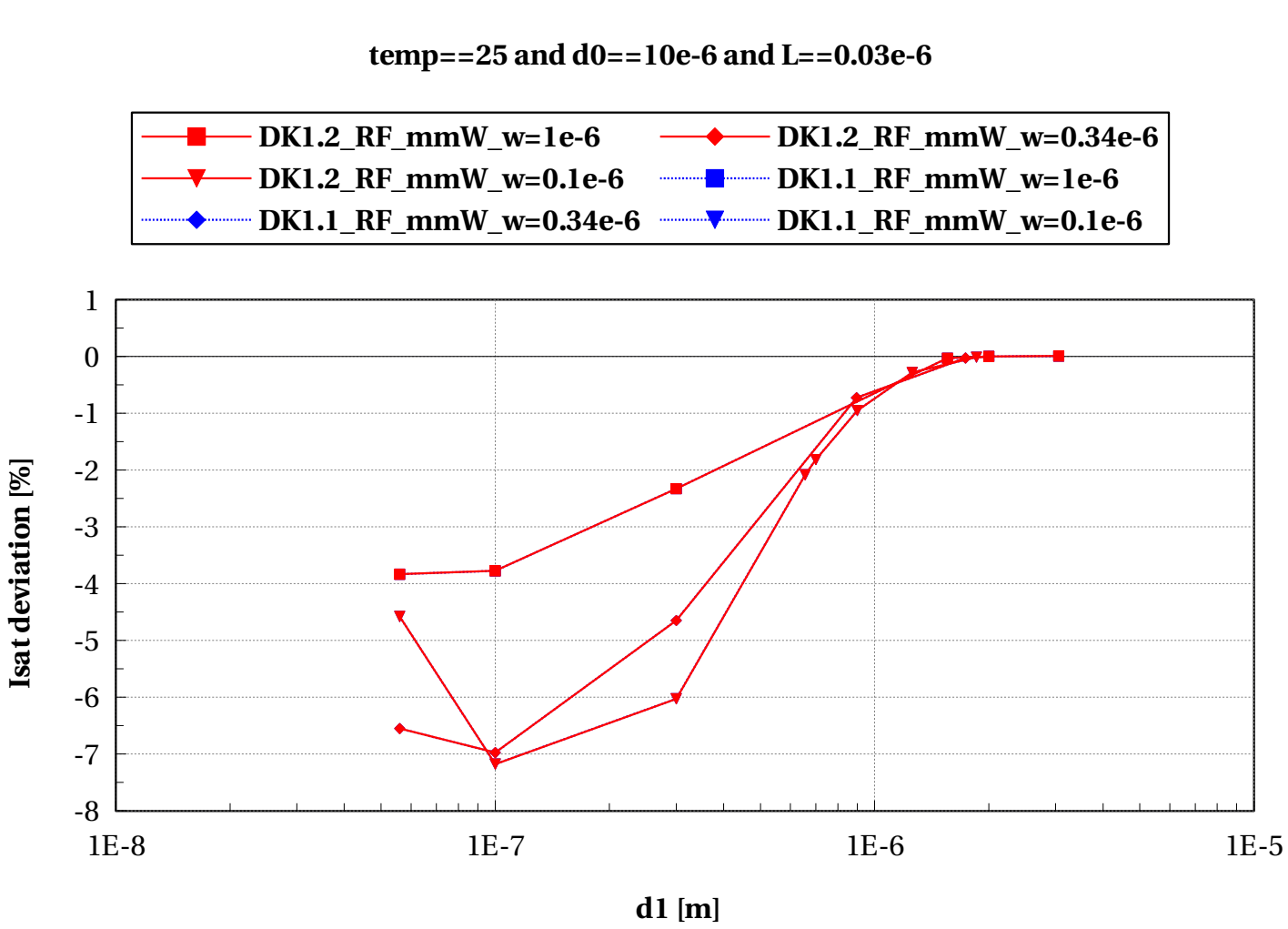
pfet_acc, Isat deviation [%] vs d1 [m]



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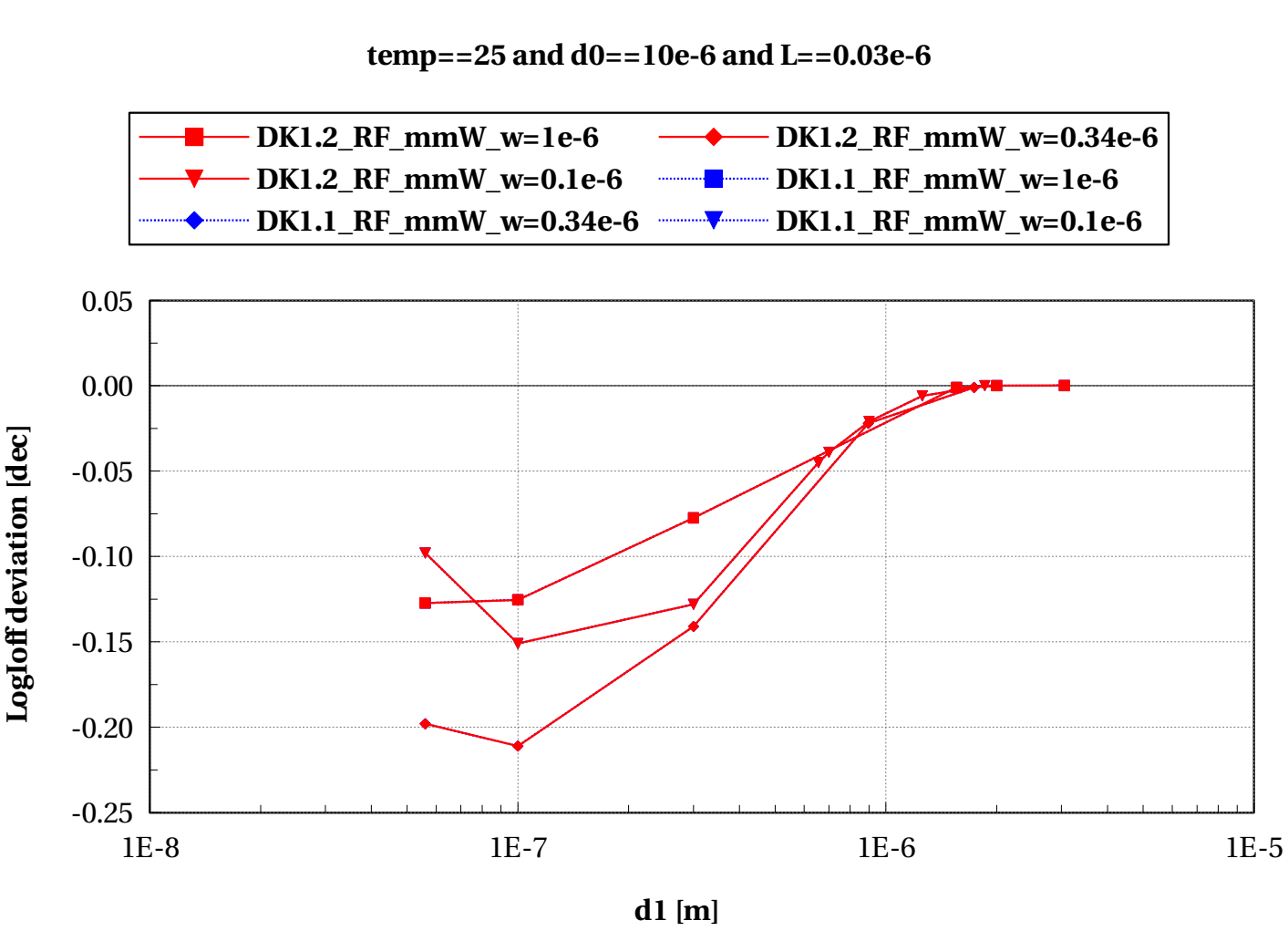
pfet_acc, LogIoff deviation [dec] vs d1 [m]



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- WPE effect w.r.t d1 - Wscaling at L=0.12u

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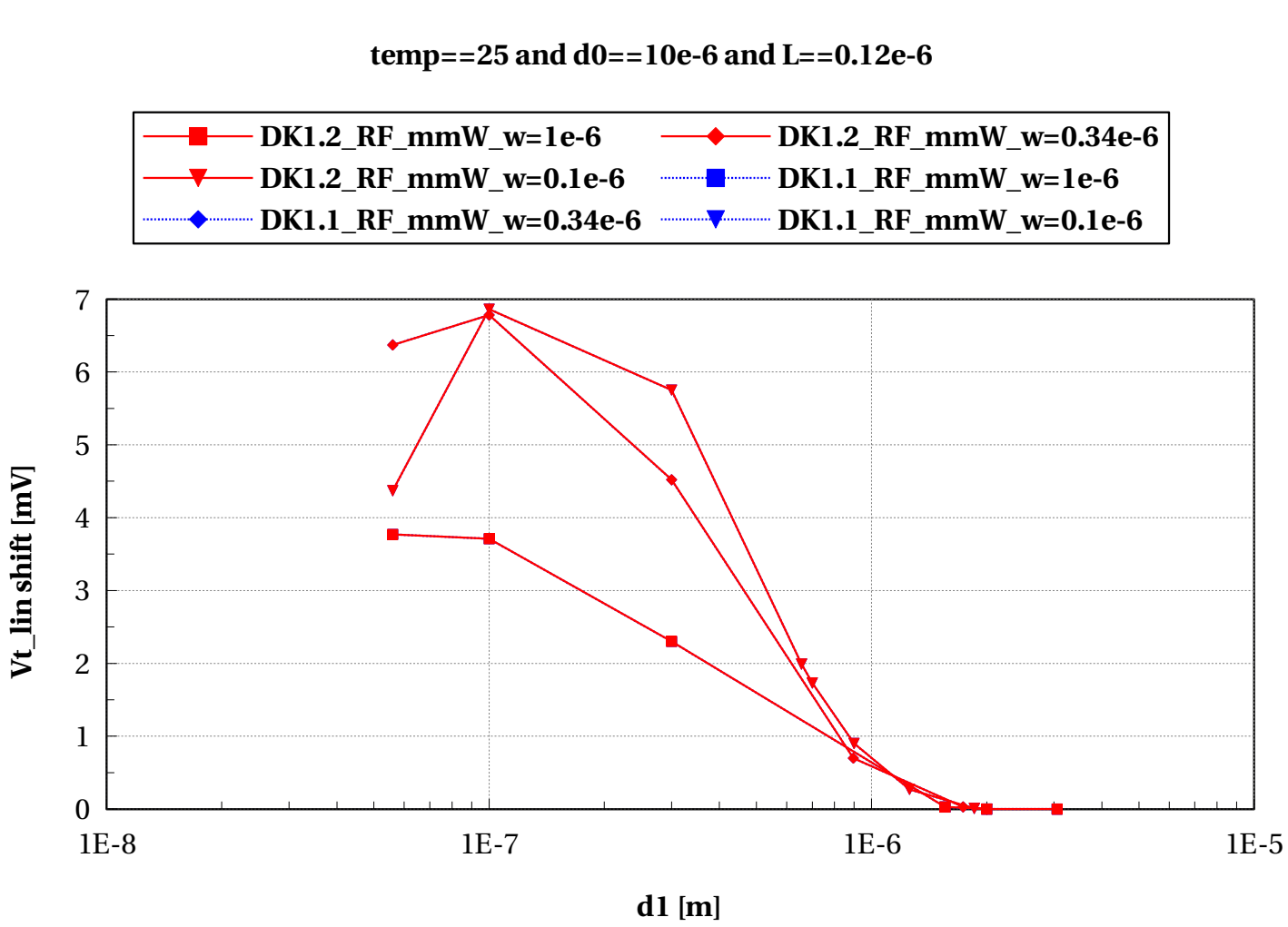
pfet_acc, Vt_lin shift [mV] vs d1 [m]



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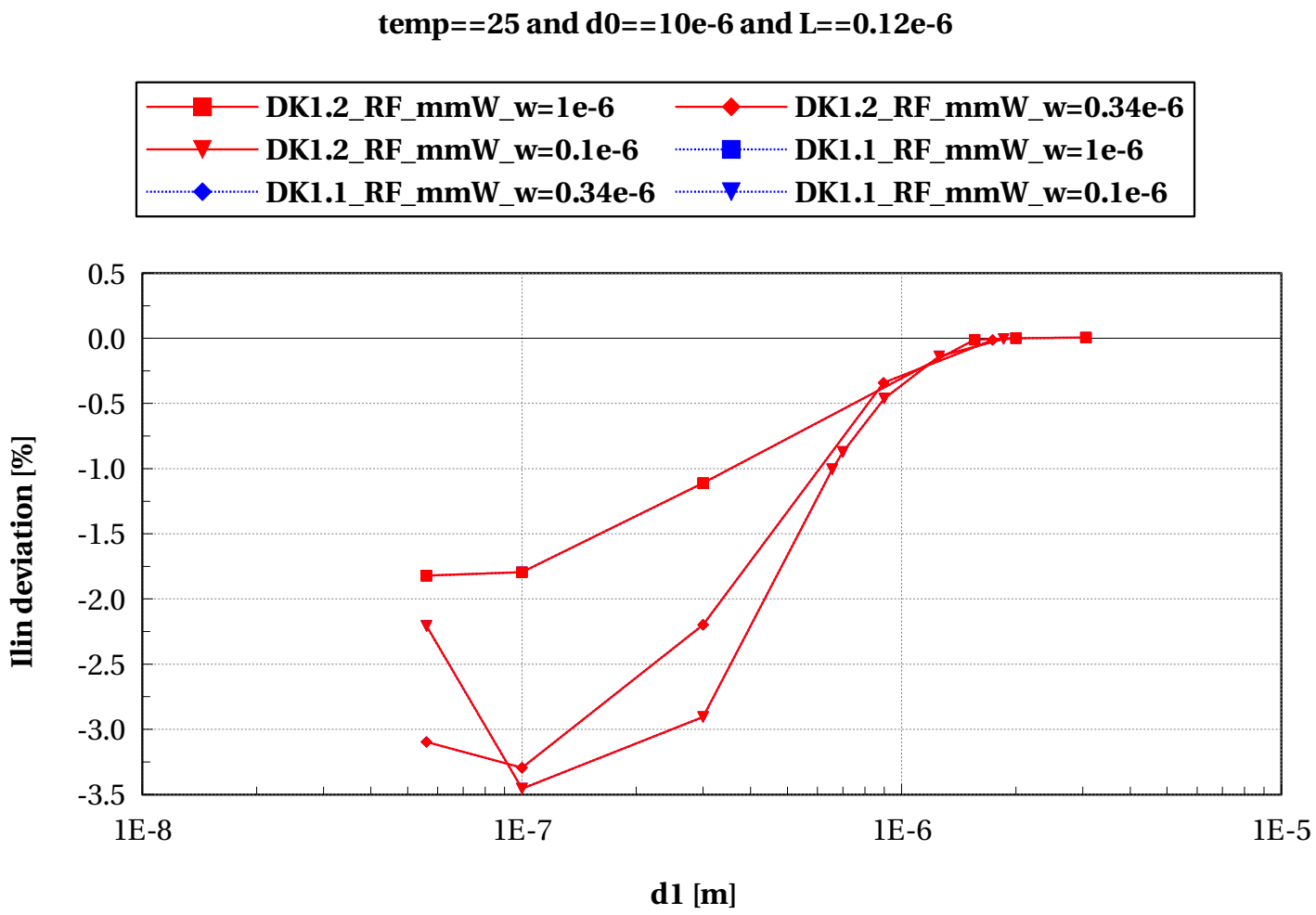
pfet_acc, Ilin deviation [%] vs d1 [m]



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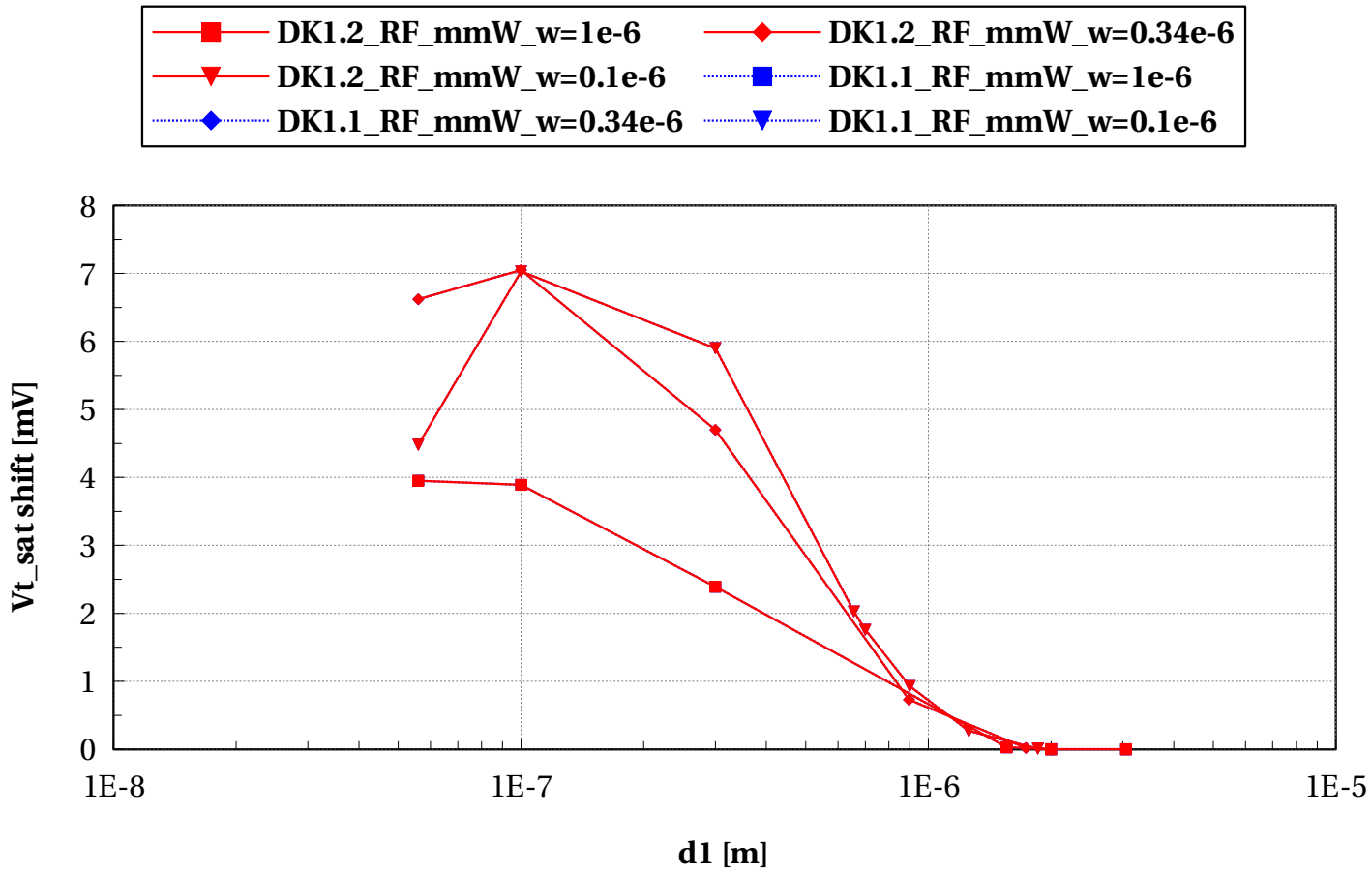
pfet_acc, Vt_sat shift [mV] vs d1 [m]

temp==25 and d0==10e-6 and L==0.12e-6



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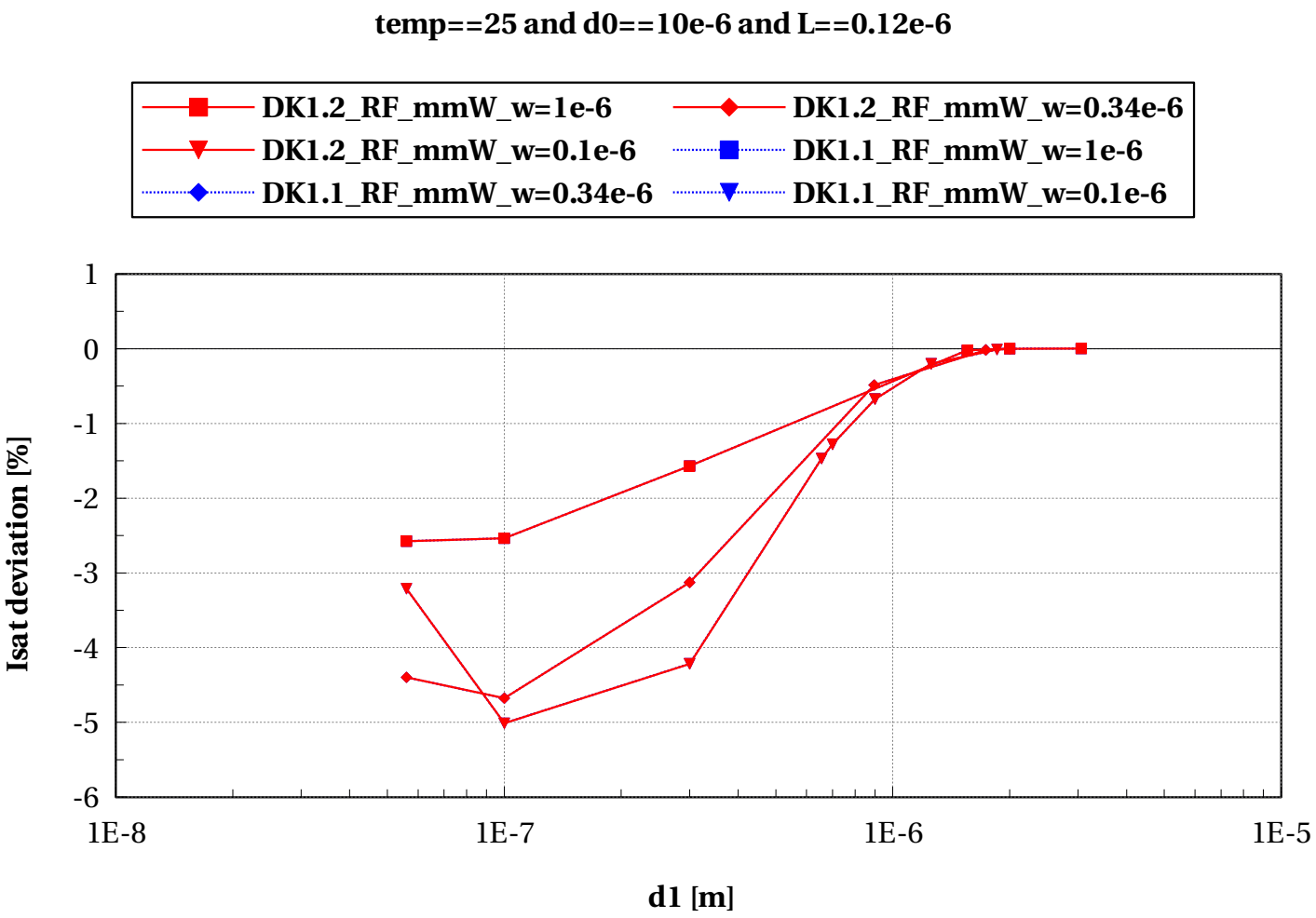
pfet_acc, Isat deviation [%] vs d1 [m]



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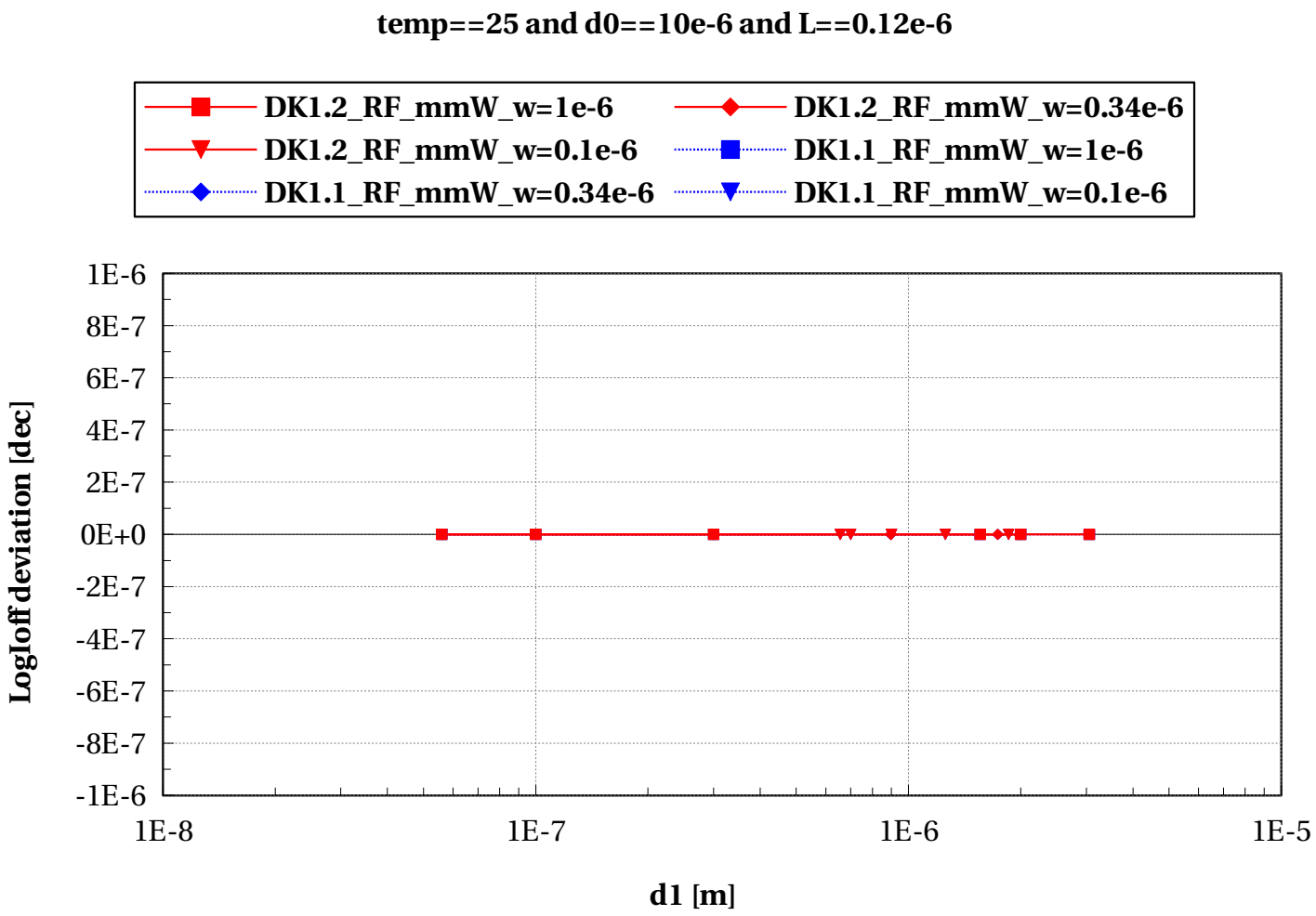
pfet_acc, LogIoff deviation [dec] vs d1 [m]



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- WPE effect w.r.t d1 - Wscaling at L=1u

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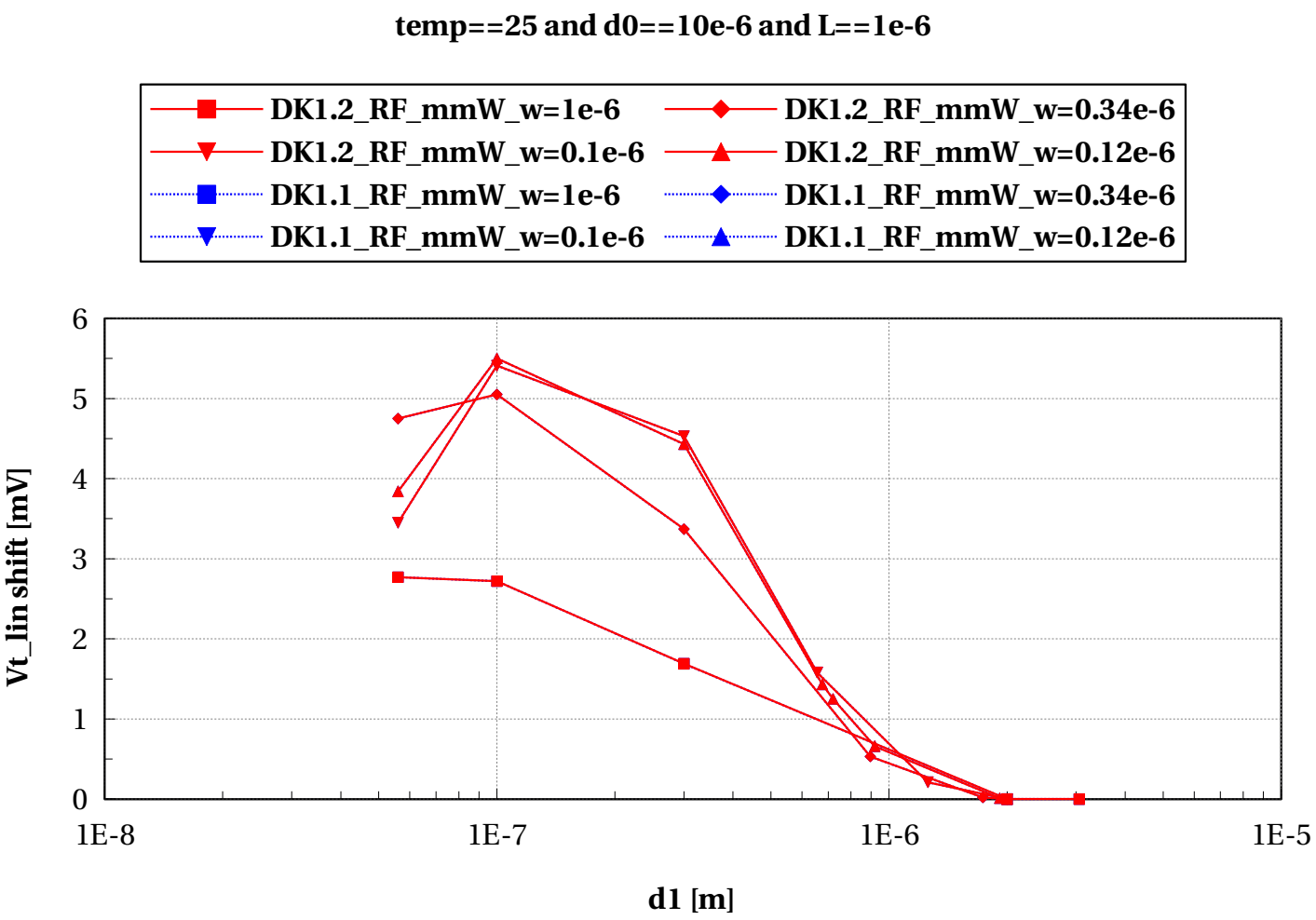
pfet_acc, Vt_lin shift [mV] vs d1 [m]



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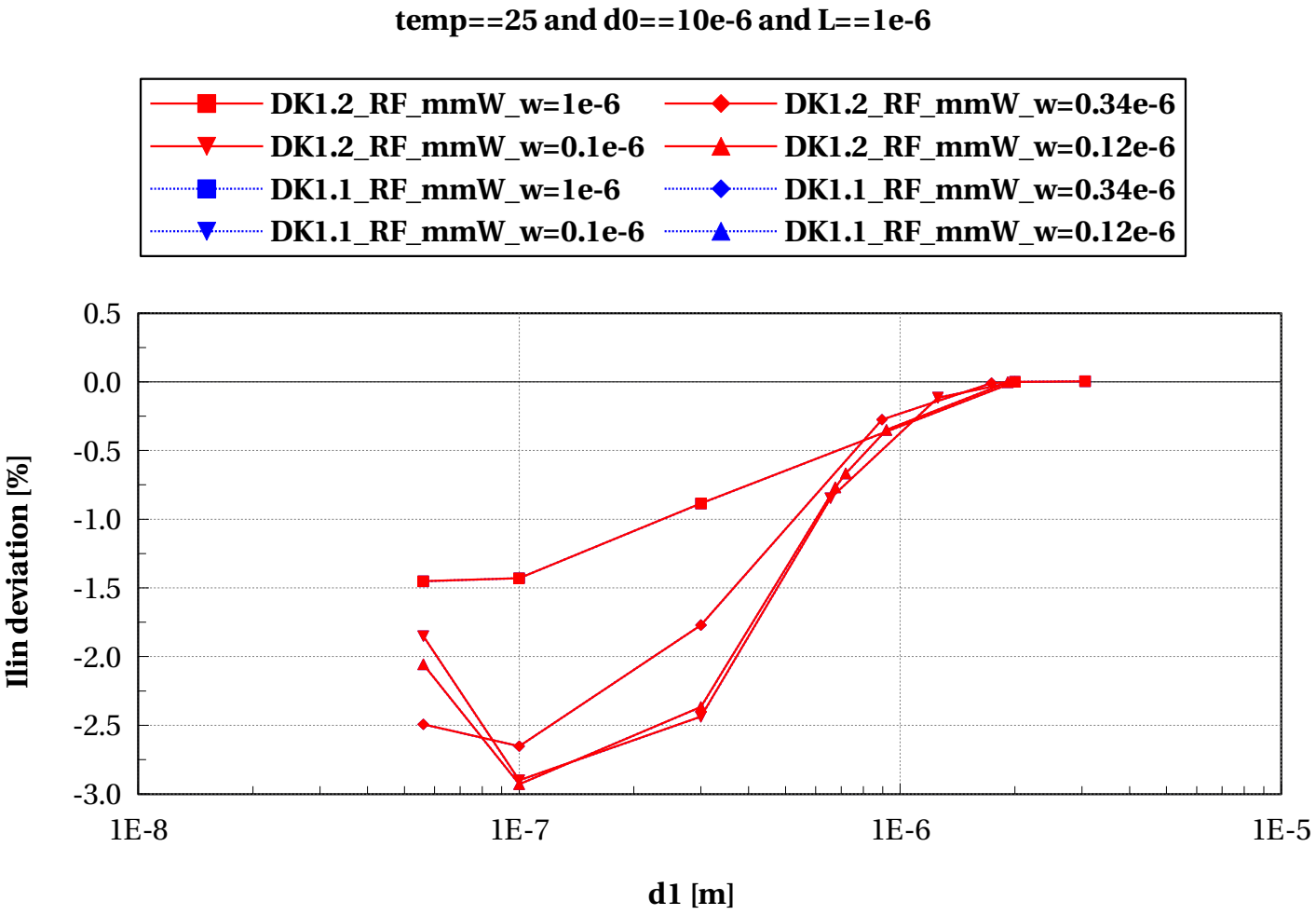
pfet_acc, Ilin deviation [%] vs d1 [m]



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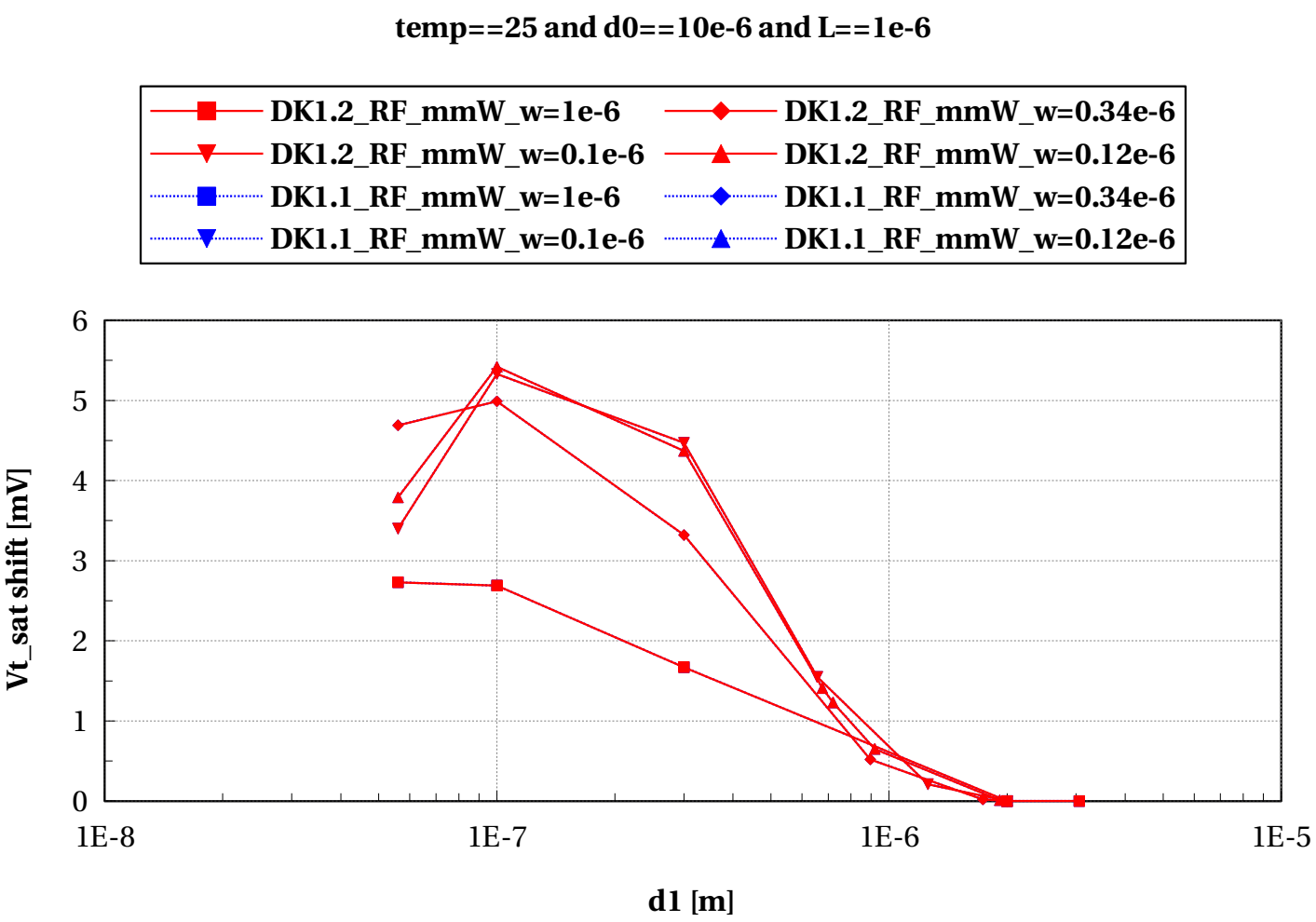
pfet_acc, Vt_sat shift [mV] vs d1 [m]



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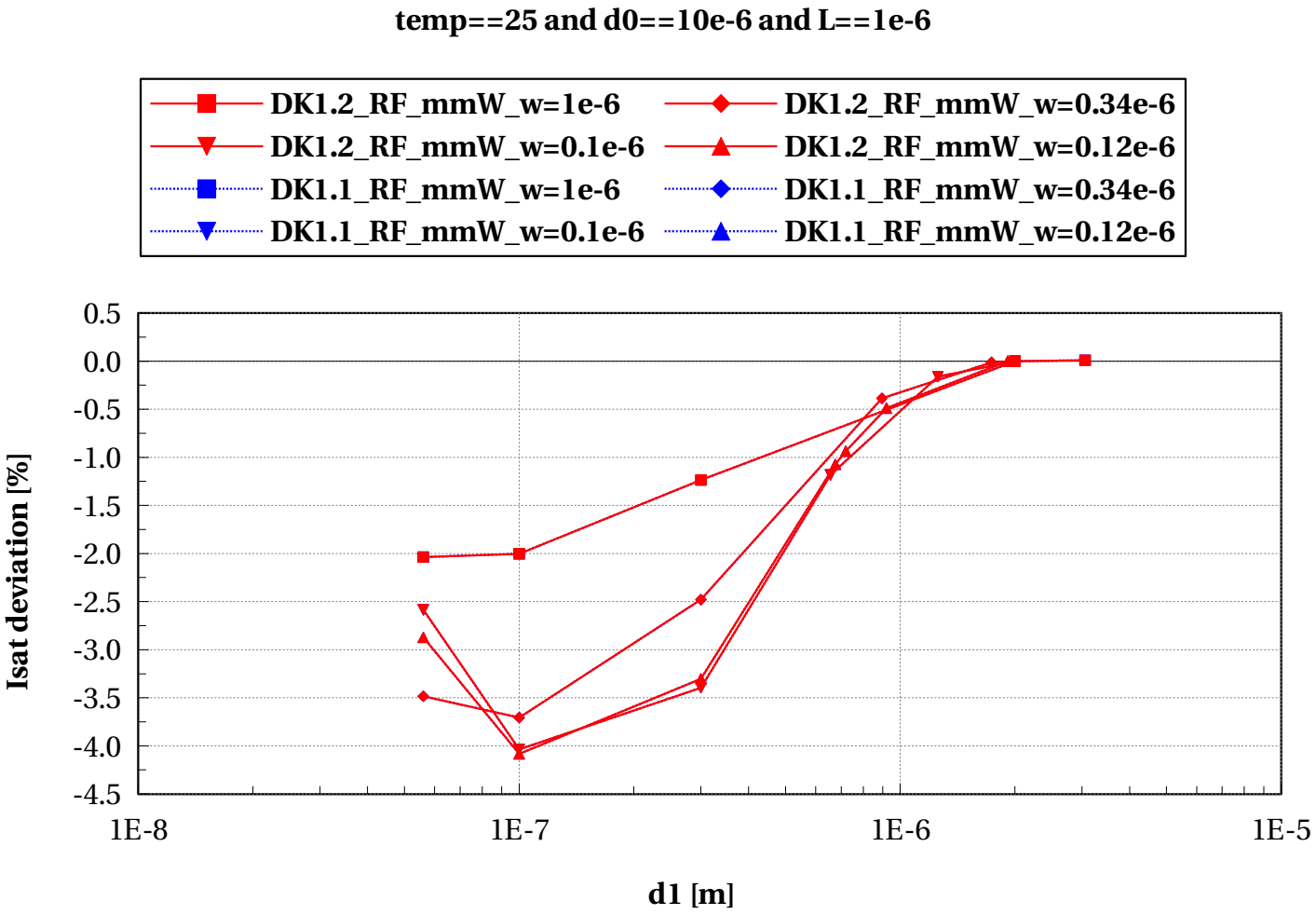
pfet_acc, Isat deviation [%] vs d1 [m]



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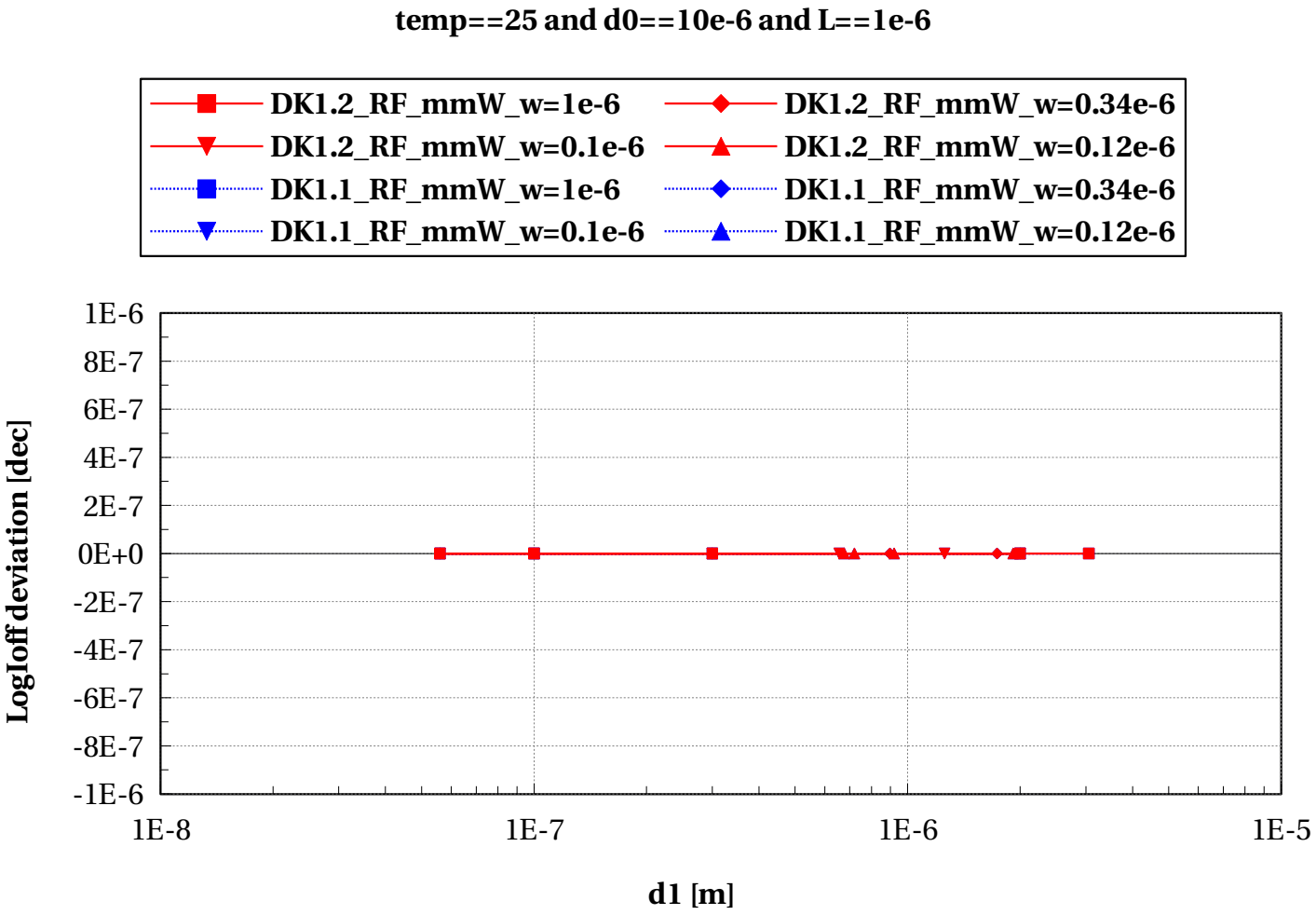
pfet_acc, LogIoff deviation [dec] vs d1 [m]



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- WPE effect w.r.t d1 - Lscaling at W=0.1u

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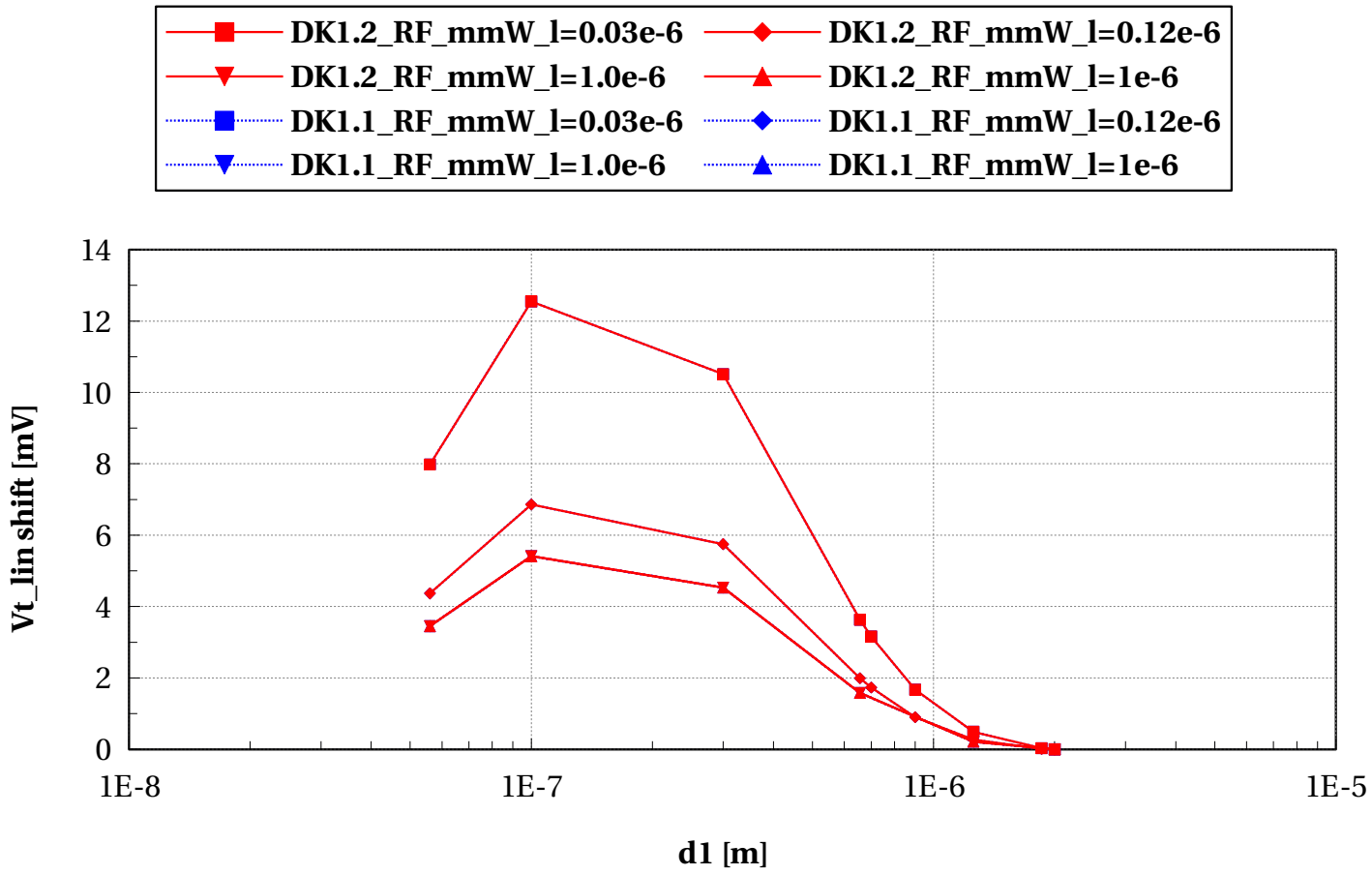
pfet_acc, Vt_lin shift [mV] vs d1 [m]

temp==25 and d0==10e-6 and W==0.1e-6



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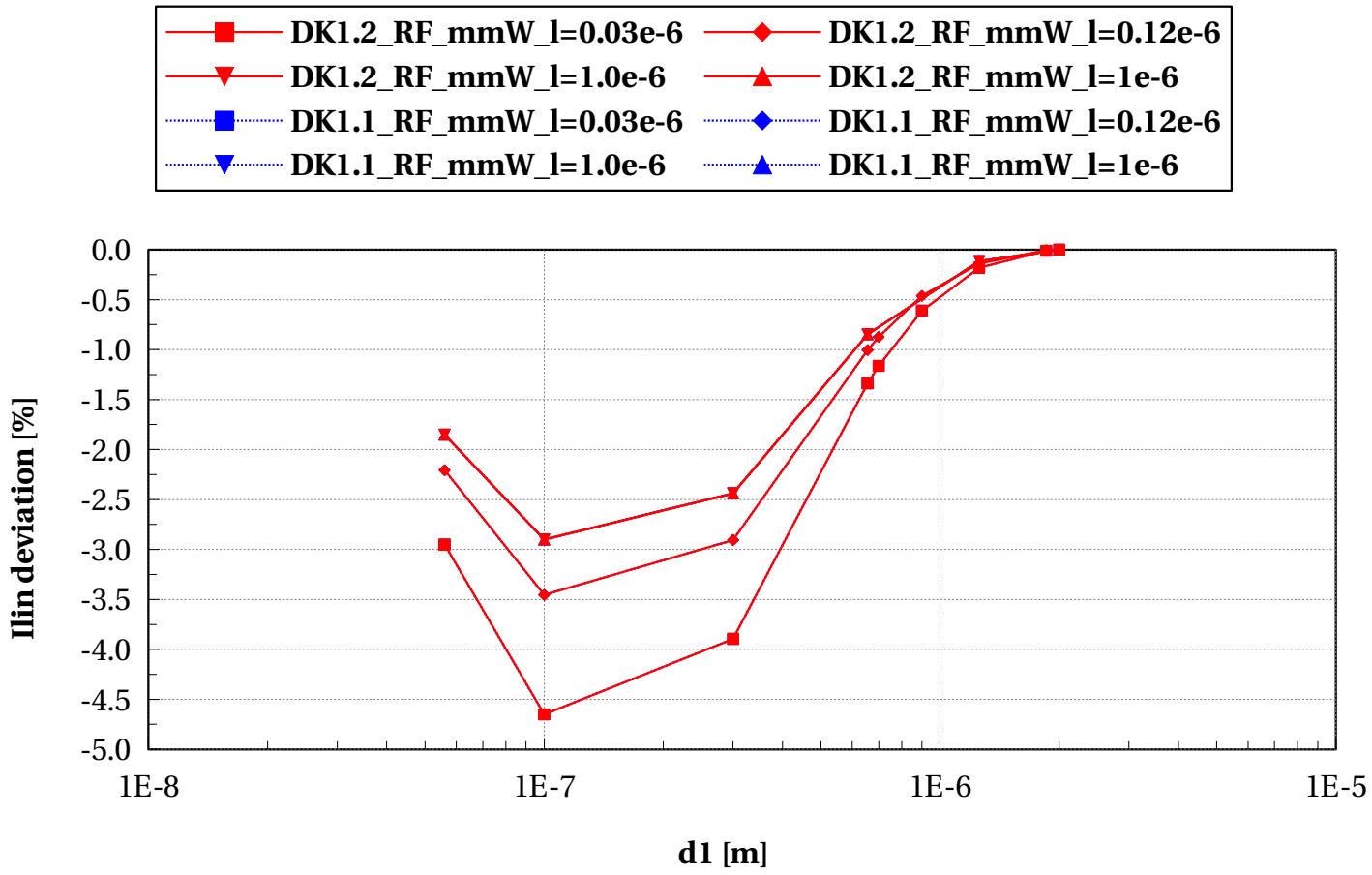
pfet_acc, Ilin deviation [%] vs d1 [m]

temp==25 and d0==10e-6 and W==0.1e-6



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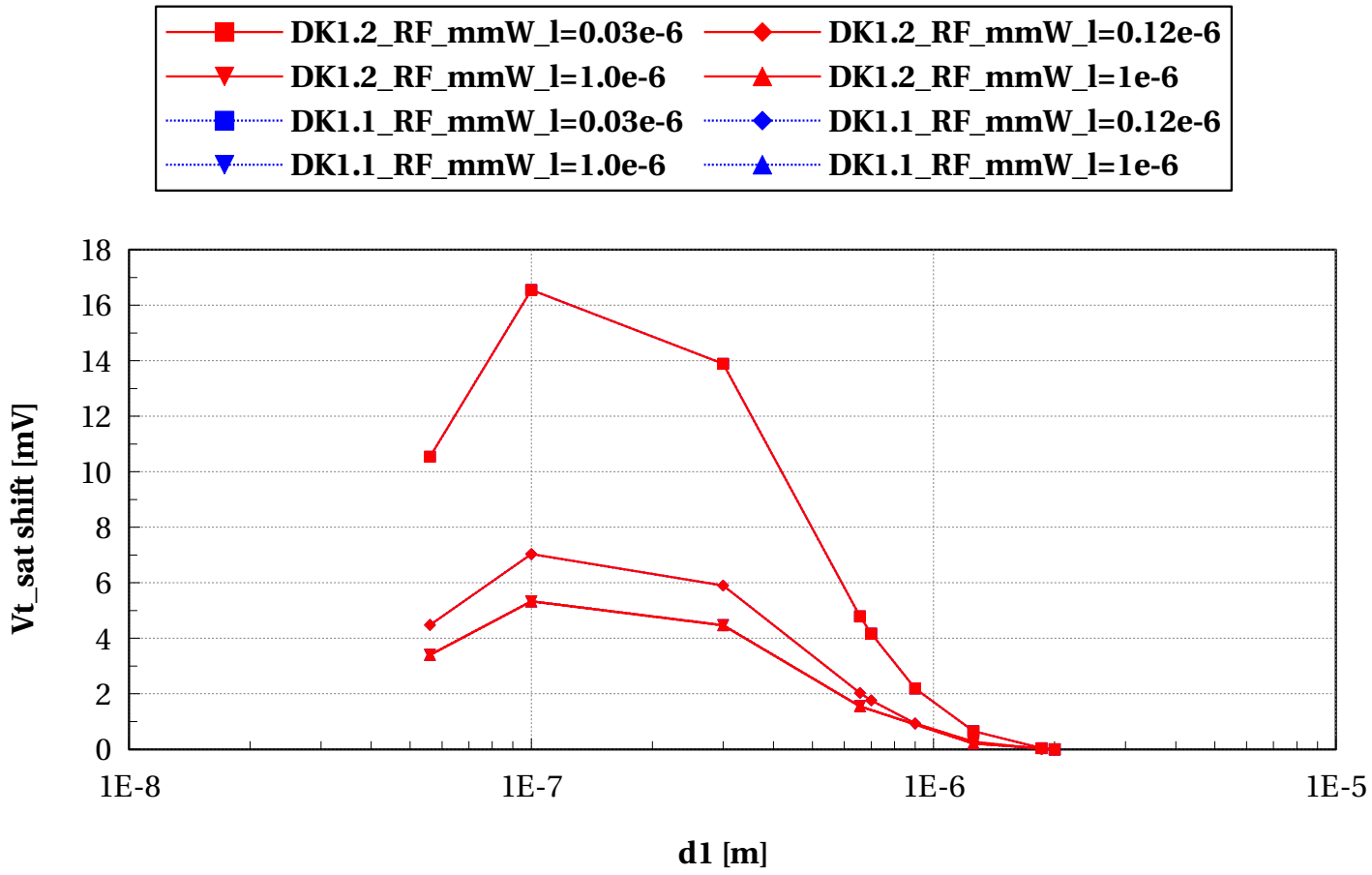
pfet_acc, Vt_sat shift [mV] vs d1 [m]

temp==25 and d0==10e-6 and W==0.1e-6



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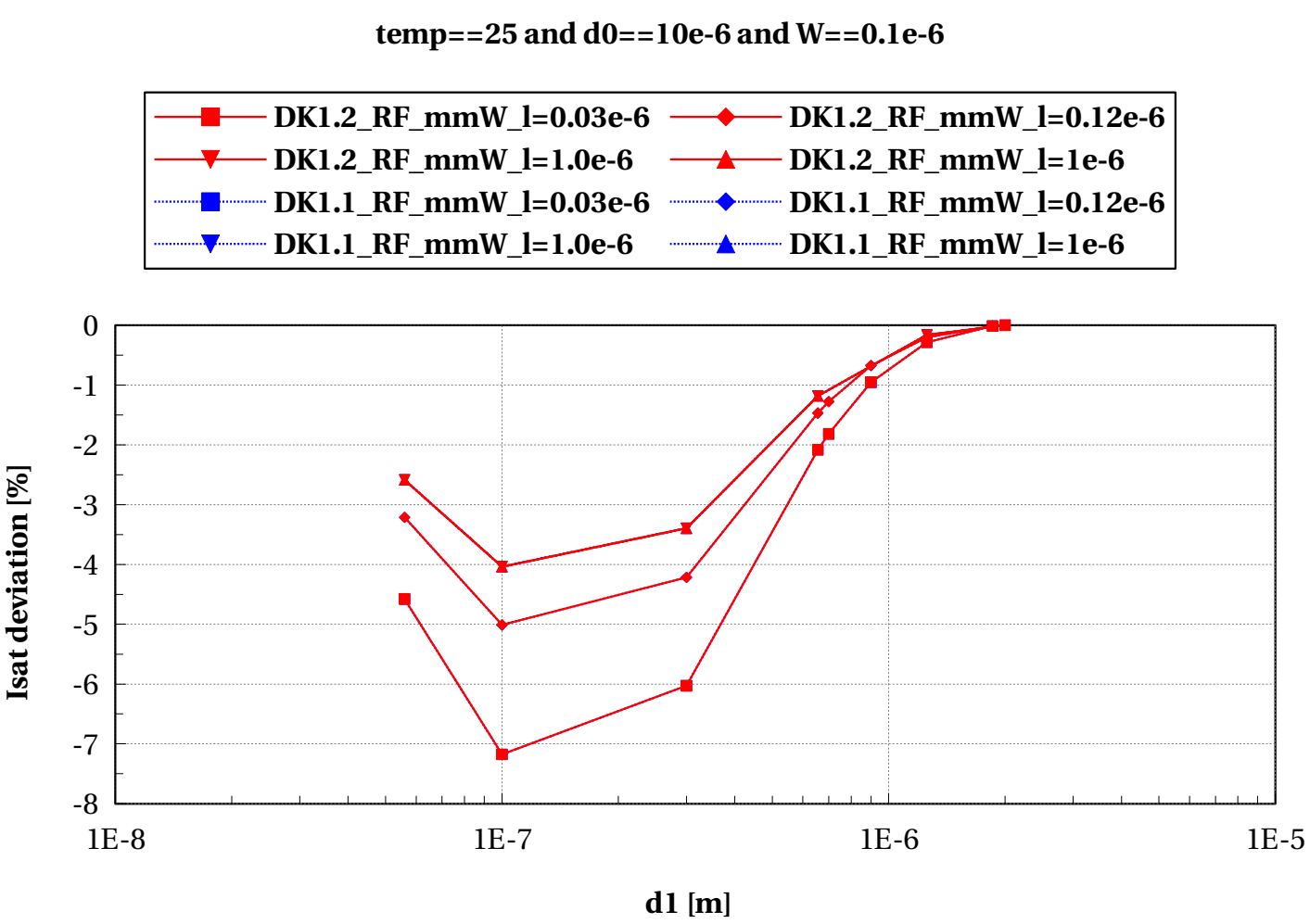
pfet_acc, Isat deviation [%] vs d1 [m]



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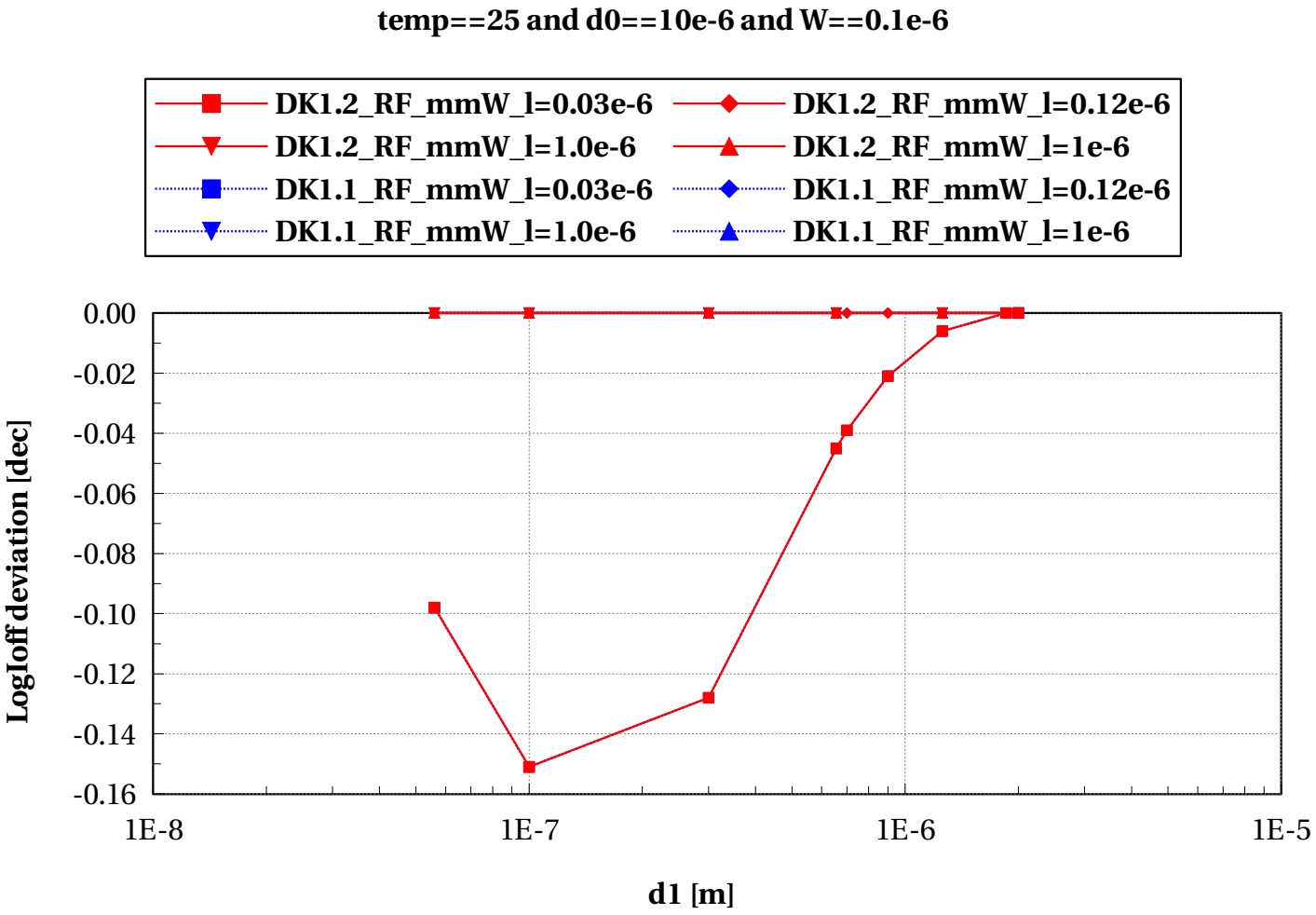
pfet_acc, LogIoff deviation [dec] vs d1 [m]



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- WPE effect w.r.t d1 - Lscaling at W=1u

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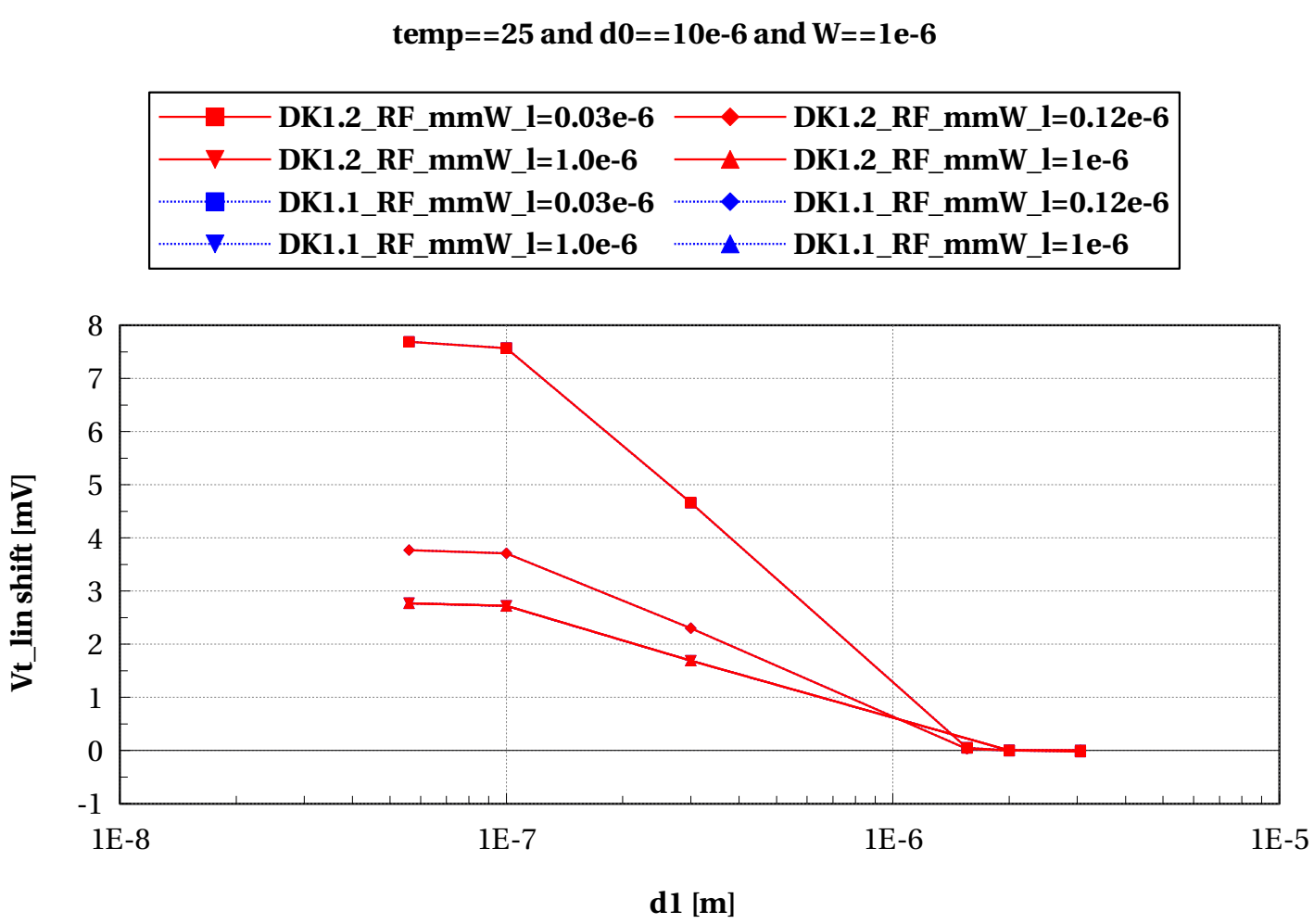
pfet_acc, Vt_lin shift [mV] vs d1 [m]



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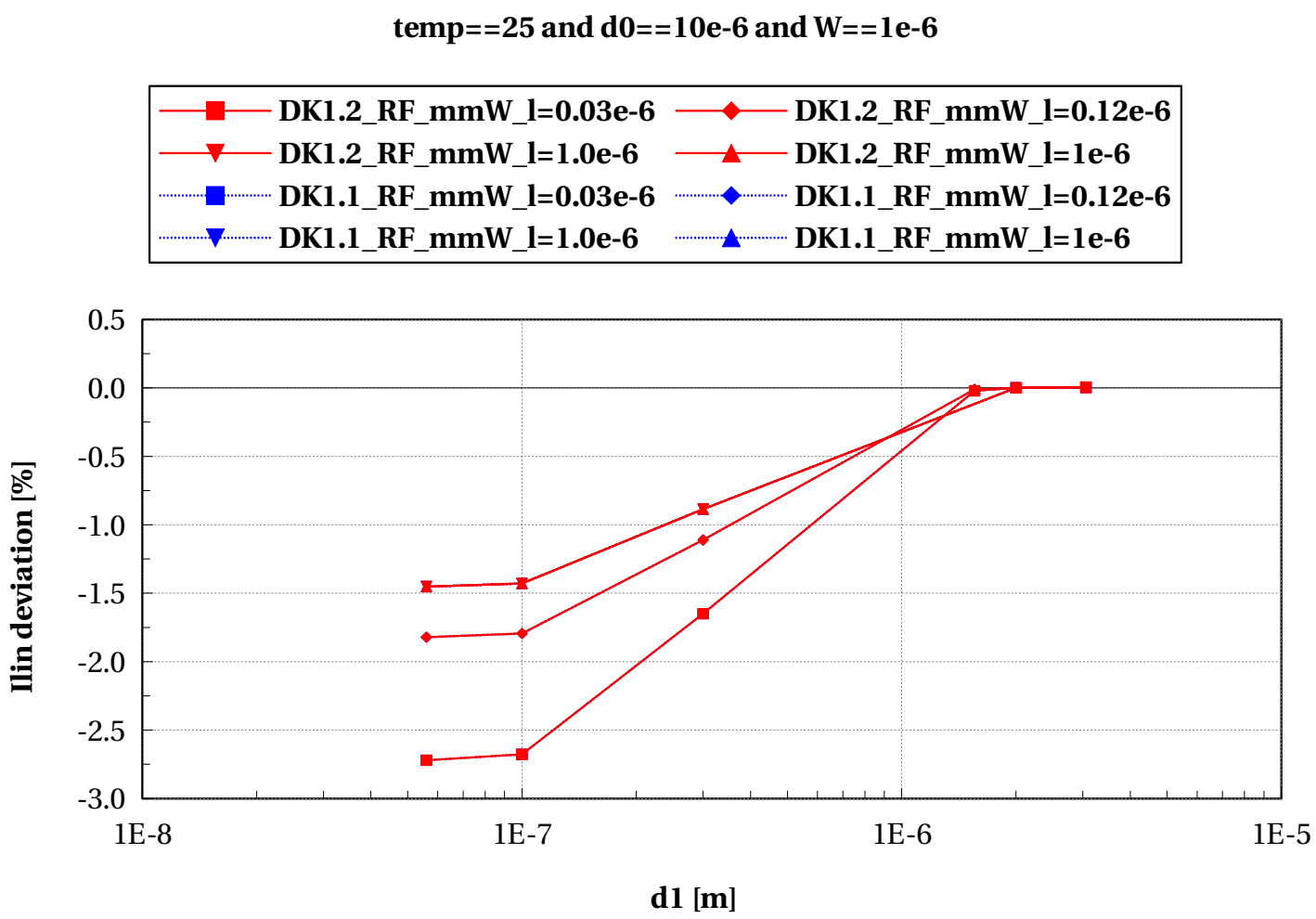
pfet_acc, Ilin deviation [%] vs d1 [m]



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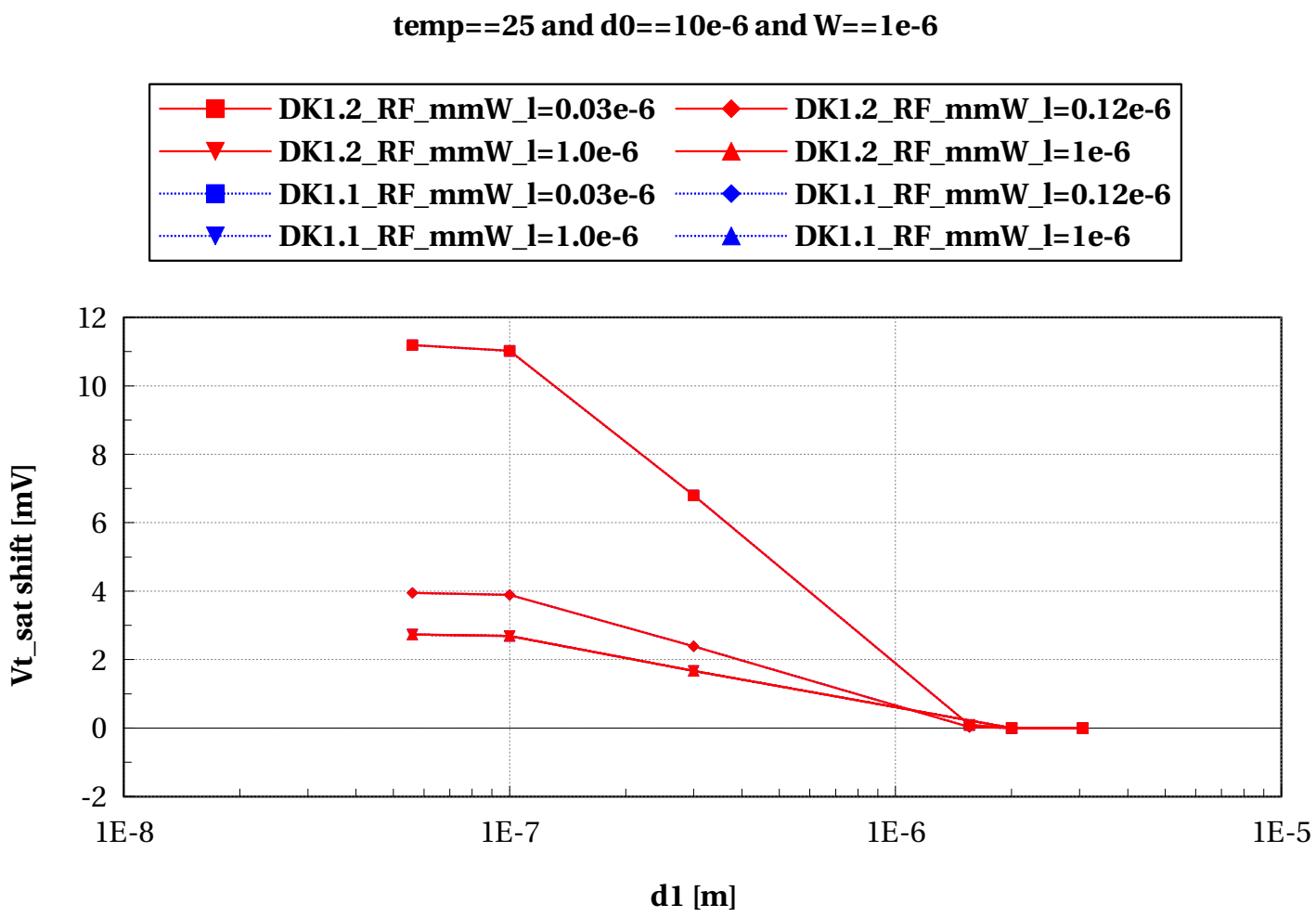
pfet_acc, Vt_sat shift [mV] vs d1 [m]



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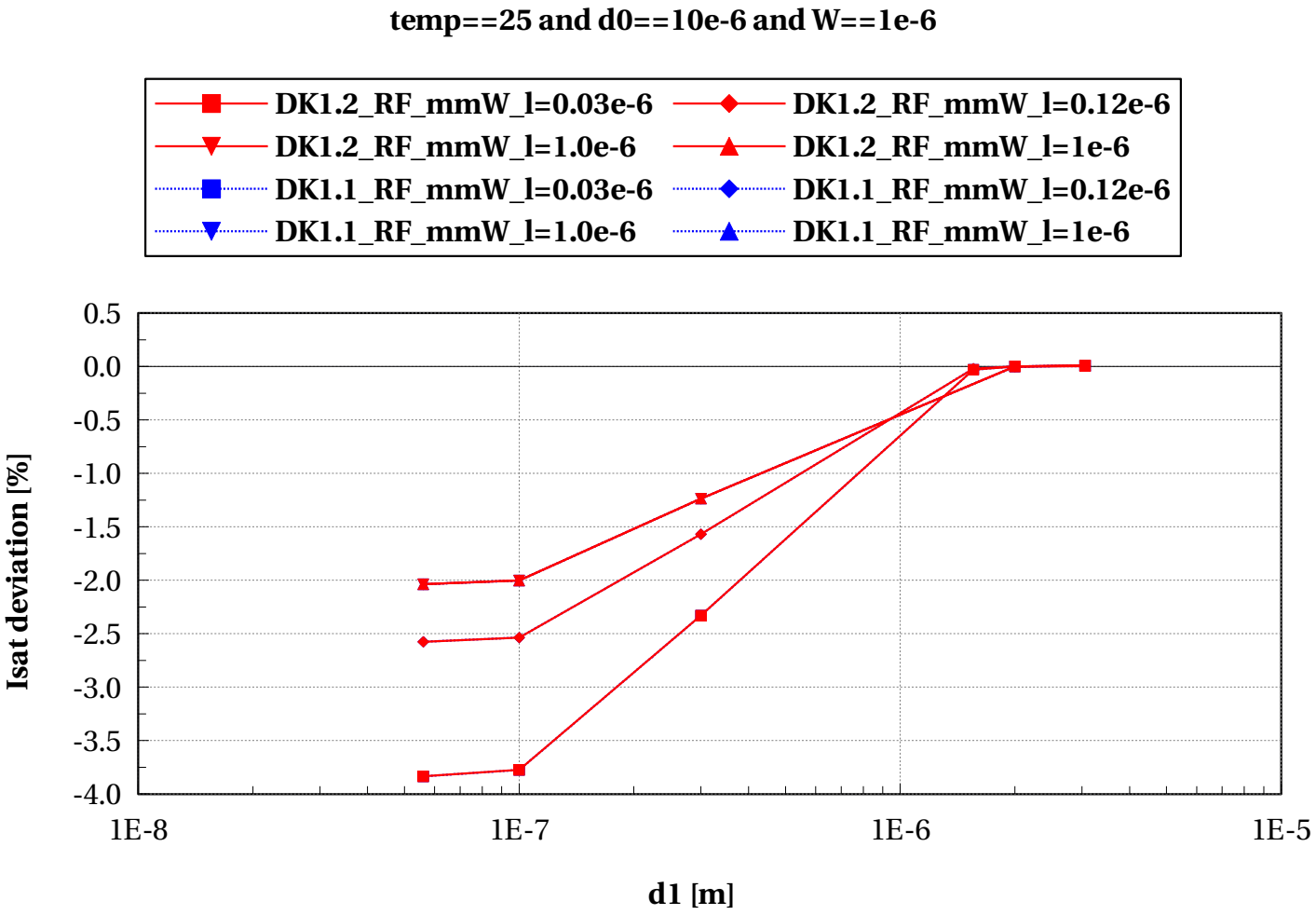
pfet_acc, Isat deviation [%] vs d1 [m]



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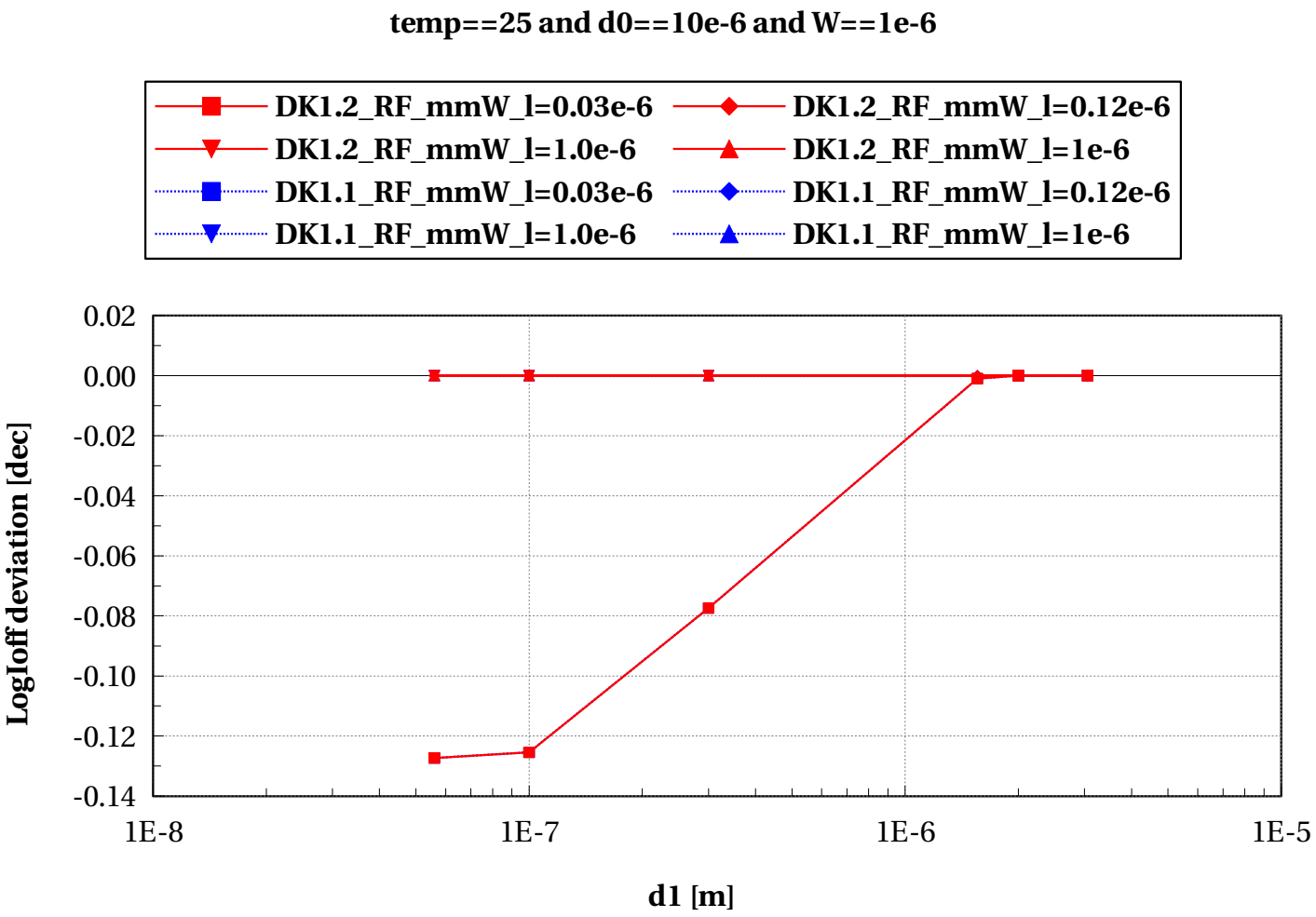
pfet_acc, LogIoff deviation [dec] vs d1 [m]



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Annex

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Conditions of simulations

The simulations were done with SBenchLSF Alpha using Eldo simulator 2018.3.

- Model lvtnfet_acc (DK1.2_RF_mmW)
  - ✔ Input Parameters
    - ✘ ivt = 300e-9 A
    - ✘ mc_runs = 1000
    - ✘ vgs_stop = vdd V
    - ✘ vds_off = vds_sat V
    - ✘ vgs_off = 0 V
    - ✘ temp = 25 °C
    - ✘ vgs_start = -0.5 V
    - ✘ mc_sens = 0
    - ✘ vds_lin = 0.05 V
    - ✘ sbenchlsf_release = Alpha
    - ✘ plashrink_ivt = 1
    - ✘ vbs = 0 V
    - ✘ ams_release = 2018.3
    - ✘ model_version = 1.3.e

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- ✘ mc_nsigma = 3
- ✘ ithslwi = 10e-9 A
- ✘ vstep_ivt = 0.005 V
- ✘ vds_sat = Vdd V
- ✘ shrink_ivt = 1
- ✘ vdd = 1 V
- ✘ dlshrink_ivt = 0

✔ Sweep Parameters

✔ Extra parameters

- ✘ lvt_dev = 0
- ✘ gflag__noisedev__rvt__cmos028fdsoi = 0
- ✘ gflag__noisedev__lvt__cmos028fdsoi = 0
- ✘ rvt_dev = 0

● Model lvtpfet_acc (DK1.2_RF_mmW)

✔ Input Parameters

- ✘ ivt = 70e-9 A
- ✘ mc_runs = 1000
- ✘ vgs_stop = vdd V
- ✘ vds_off = vds_sat V
- ✘ vgs_off = 0 V
- ✘ temp = 25 °C
- ✘ vgs_start = -0.5 V
- ✘ mc_sens = 0
- ✘ vds_lin = 0.05 V
- ✘ sbenchlsf_release = Alpha

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- ✘ plashrink_ivt = 1
- ✘ vbs = 1 V
- ✘ ams_release = 2018.3
- ✘ model_version = 1.3.e
- ✘ mc_nsigma = 3
- ✘ ithslwi = 10e-9 A
- ✘ vstep_ivt = 0.005 V
- ✘ vds_sat = Vdd V
- ✘ shrink_ivt = 1
- ✘ vdd = 1 V
- ✘ dlshrink_ivt = 0

✔ Sweep Parameters

✔ Extra parameters

- ✘ lvt_dev = 0
- ✘ gflag__noisedev__rvt__cmos028fdsoi = 0
- ✘ gflag__noisedev__lvt__cmos028fdsoi = 0
- ✘ rvt_dev = 0

● Model nfet_acc (DK1.2_RF_mmW)

✔ Input Parameters

- ✘ ivt = 300e-9 A
- ✘ mc_runs = 1000
- ✘ vgs_stop = vdd V
- ✘ vds_off = vds_sat V
- ✘ vgs_off = 0 V
- ✘ temp = 25 °C

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- ✘ vgs_start = -0.5 V
- ✘ mc_sens = 0
- ✘ vds_lin = 0.05 V
- ✘ sbenchlsf_release = Alpha
- ✘ plashrink_ivt = 1
- ✘ vbs = 0 V
- ✘ ams_release = 2018.3
- ✘ model_version = 1.2.d
- ✘ mc_nsigma = 3
- ✘ ithslwi = 10e-9 A
- ✘ vstep_ivt = 0.005 V
- ✘ vds_sat = Vdd V
- ✘ shrink_ivt = 1
- ✘ vdd = 1 V
- ✘ dlshrink_ivt = 0

✔ Sweep Parameters

✔ Extra parameters

- ✘ lvt_dev = 0
- ✘ gflag__noisedev__rvt__cmos028fdsoi = 0
- ✘ gflag__noisedev__lvt__cmos028fdsoi = 0
- ✘ rvt_dev = 0

● Model pfet_acc (DK1.2_RF_mmW)

✔ Input Parameters

- ✘ ivt = 70e-9 A
- ✘ mc_runs = 1000

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- ✘ vgs_stop = vdd V
- ✘ vds_off = vds_sat V
- ✘ vgs_off = 0 V
- ✘ temp = 25 °C
- ✘ vgs_start = -0.5 V
- ✘ mc_sens = 0
- ✘ vds_lin = 0.05 V
- ✘ sbenchlsf_release = Alpha
- ✘ plashrink_ivt = 1
- ✘ vbs = 0 V
- ✘ ams_release = 2018.3
- ✘ model_version = 1.2.d
- ✘ mc_nsigma = 3
- ✘ ithslwi = 10e-9 A
- ✘ vstep_ivt = 0.005 V
- ✘ vds_sat = Vdd V
- ✘ shrink_ivt = 1
- ✘ vdd = 1 V
- ✘ dlshrink_ivt = 0

✔ Sweep Parameters

✔ Extra parameters

- ✘ lvt_dev = 0
- ✘ gflag__noisedev__rvt__cmos028fdsoi = 0
- ✘ gflag__noisedev__lvt__cmos028fdsoi = 0
- ✘ rvt_dev = 0

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- Model lvtnfet_acc (DK1.1_RF_mmW)
  - ✔ Input Parameters
    - ✘ ivt = 300e-9 A
    - ✘ mc_runs = 1000
    - ✘ vgs_stop = vdd V
    - ✘ vds_off = vds_sat V
    - ✘ vgs_off = 0 V
    - ✘ temp = 25 °C
    - ✘ vgs_start = -0.5 V
    - ✘ mc_sens = 0
    - ✘ vds_lin = 0.05 V
    - ✘ sbenchlsf_release = Alpha
    - ✘ plashrink_ivt = 1
    - ✘ vbs = 0 V
    - ✘ ams_release = 2018.3
    - ✘ model_version = 1.3.d
    - ✘ mc_nsigma = 3
    - ✘ ithslwi = 10e-9 A
    - ✘ vstep_ivt = 0.005 V
    - ✘ vds_sat = Vdd V
    - ✘ shrink_ivt = 1
    - ✘ vdd = 1 V
    - ✘ dlshrink_ivt = 0
  - ✔ Sweep Parameters
  - ✔ Extra parameters

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- ✘ lvt_dev = 0
- ✘ gflag__noisedev__rvt__cmos028fdsoi = 0
- ✘ gflag__noisedev__lvt__cmos028fdsoi = 0
- ✘ rvt_dev = 0

● Model lvtpfet_acc (DK1.1_RF_mmW)

- ✔ Input Parameters
  - ✘ ivt = 70e-9 A
  - ✘ mc_runs = 1000
  - ✘ vgs_stop = vdd V
  - ✘ vds_off = vds_sat V
  - ✘ vgs_off = 0 V
  - ✘ temp = 25 °C
  - ✘ vgs_start = -0.5 V
  - ✘ mc_sens = 0
  - ✘ vds_lin = 0.05 V
  - ✘ sbenchlsf_release = Alpha
  - ✘ plashrink_ivt = 1
  - ✘ vbs = 1 V
  - ✘ ams_release = 2018.3
  - ✘ model_version = 1.3.d
  - ✘ mc_nsigma = 3
  - ✘ ithslwi = 10e-9 A
  - ✘ vstep_ivt = 0.005 V
  - ✘ vds_sat = Vdd V
  - ✘ shrink_ivt = 1

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- ✘ vdd = 1 V
- ✘ dlshrink_ivt = 0

✔ Sweep Parameters

✔ Extra parameters

- ✘ lvt_dev = 0
- ✘ gflag__noisedev__rvt__cmos028fdsoi = 0
- ✘ gflag__noisedev__lvt__cmos028fdsoi = 0
- ✘ rvt_dev = 0

● Model nfet_acc (DK1.1_RF_mmW)

✔ Input Parameters

- ✘ ivt = 300e-9 A
- ✘ mc_runs = 1000
- ✘ vgs_stop = vdd V
- ✘ vds_off = vds_sat V
- ✘ vgs_off = 0 V
- ✘ temp = 25 °C
- ✘ vgs_start = -0.5 V
- ✘ mc_sens = 0
- ✘ vds_lin = 0.05 V
- ✘ sbenchlsf_release = Alpha
- ✘ plashrink_ivt = 1
- ✘ vbs = 0 V
- ✘ ams_release = 2018.3
- ✘ model_version = 1.2.c
- ✘ mc_nsigma = 3

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- ✘ ithslwi = 10e-9 A
- ✘ vstep_ivt = 0.005 V
- ✘ vds_sat = Vdd V
- ✘ shrink_ivt = 1
- ✘ vdd = 1 V
- ✘ dlshrink_ivt = 0

✔ Sweep Parameters

✔ Extra parameters

- ✘ lvt_dev = 0
- ✘ gflag__noisedev__rvt__cmos028fdsoi = 0
- ✘ gflag__noisedev__lvt__cmos028fdsoi = 0
- ✘ rvt_dev = 0

● Model pfet_acc (DK1.1_RF_mmW)

✔ Input Parameters

- ✘ ivt = 70e-9 A
- ✘ mc_runs = 1000
- ✘ vgs_stop = vdd V
- ✘ vds_off = vds_sat V
- ✘ vgs_off = 0 V
- ✘ temp = 25 °C
- ✘ vgs_start = -0.5 V
- ✘ mc_sens = 0
- ✘ vds_lin = 0.05 V
- ✘ sbenchlsf_release = Alpha
- ✘ plashrink_ivt = 1

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- ✘ vbs = 0 V
- ✘ ams_release = 2018.3
- ✘ model_version = 1.2.c
- ✘ mc_nsigma = 3
- ✘ ithslwi = 10e-9 A
- ✘ vstep_ivt = 0.005 V
- ✘ vds_sat = Vdd V
- ✘ shrink_ivt = 1
- ✘ vdd = 1 V
- ✘ dlshrink_ivt = 0

✔ Sweep Parameters

✔ Extra parameters

- ✘ lvt_dev = 0
- ✘ gflag__noisedev__rvt__cmos028fdsoi = 0
- ✘ gflag__noisedev__lvt__cmos028fdsoi = 0
- ✘ rvt_dev = 0

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