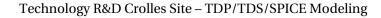


# Please use the bookmark to navigate







### **General information on LVT models**

- Maximum supply voltage is V.
- Validity domain is defined as follows:
  - ✓ Drawn gate length varies from 30nm to 10um.
  - ✓ Drawn transistor width varies from 80nm to 10um.
  - ✓ Device temperature varies from -40 °C to 125 °C.



# **Output parameters definitions**

- Model(s): lvtnfet\_acc, lvtpfet\_acc
  - ✓ Vt\_lin: Threshold voltage defined as Vgs value for which drain current is ivt\*M\*1\*W/(1\*L+0+1\*p\_la) at Vds = 0.05V.
  - ✓ Ig\_on: Gate current at Vds = 0V and Vgs = 1V.
  - ✓ Taueff: Time constant computed as Vdd/Ieff\*Cggmean.
  - ✓ Ilin : Drain current at Vgs = 1V, Vds = 0.05V.
  - ✓ Ioff\_g: Gate current at Vgs = 0V, Vds = vds\_satV.
  - ✓ Vt\_sat: Threshold voltage defined as Vgs value for which drain current is ivt\*M\*1\*W/(1\*L+0+1\*p\_la) at Vds = vds\_satV.
  - ✓ Ieff: Average drain current (Ilow + Ihigh) / 2.
  - ✓ Isat : Drain current at Vgs = 1V, Vds = VddV.
  - ✓ Logioff: log10(Ioffsat).







# lvtnfet\_acc Electrical characteristics scaling







# Scaling versus Vbs (L=1um,W=1um)

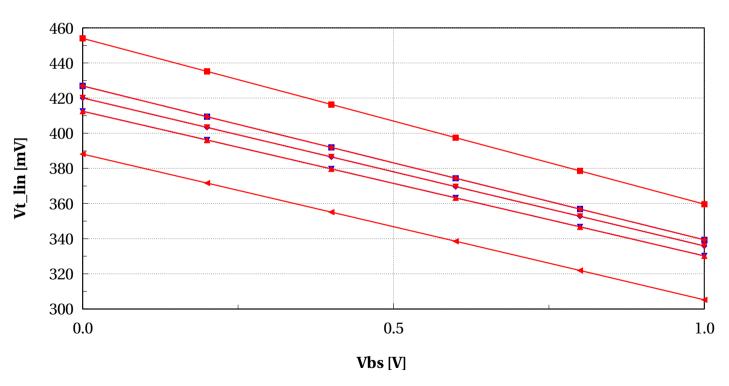






### lvtnfet\_acc, Vt\_lin [mV] vs Vbs [V]





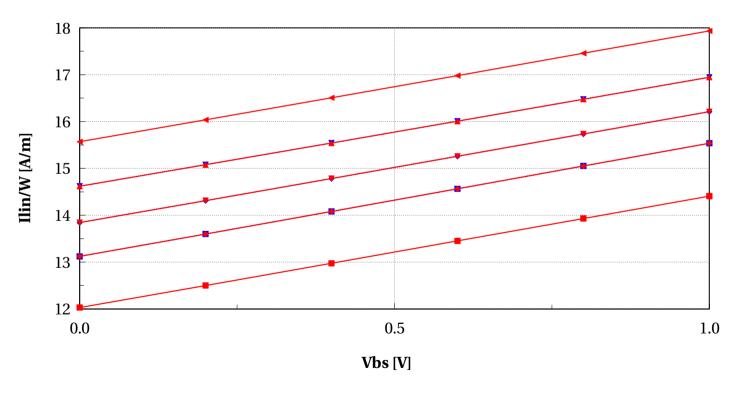






### lvtnfet\_acc, Ilin/W [A/m] vs Vbs [V]





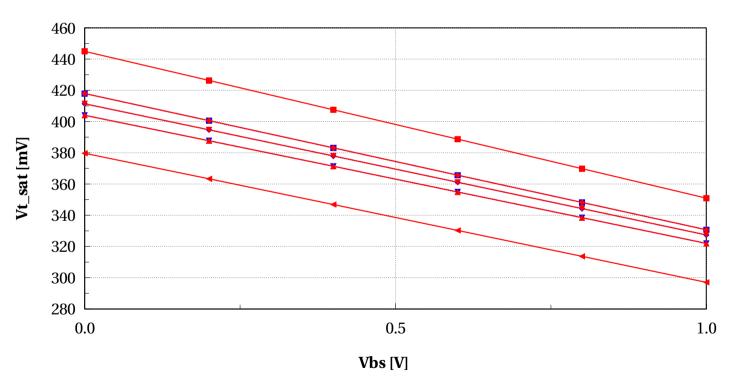


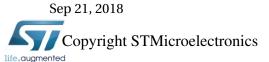




### lvtnfet\_acc, Vt\_sat [mV] vs Vbs [V]





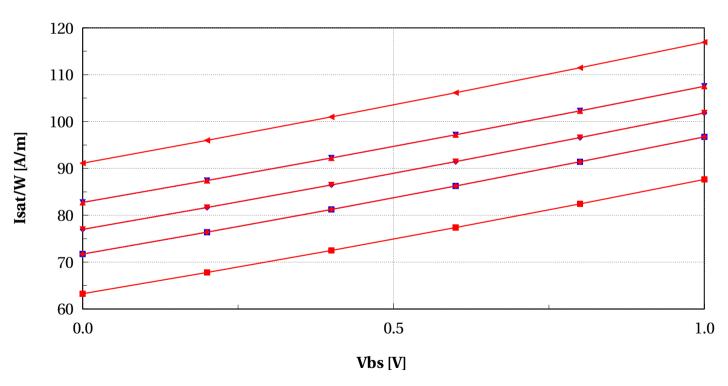






### lvtnfet\_acc, Isat/W [A/m] vs Vbs [V]





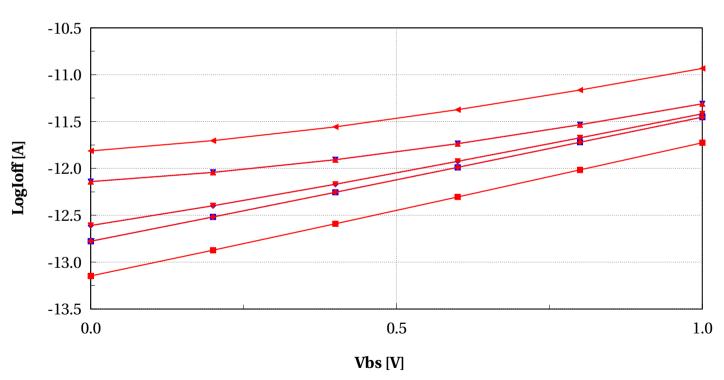






# lvtnfet\_acc, LogIoff [A] vs Vbs [V]





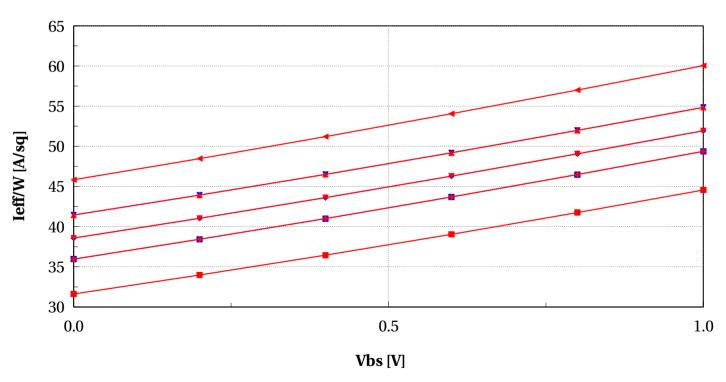






### lvtnfet\_acc, Ieff/W [A/sq] vs Vbs [V]











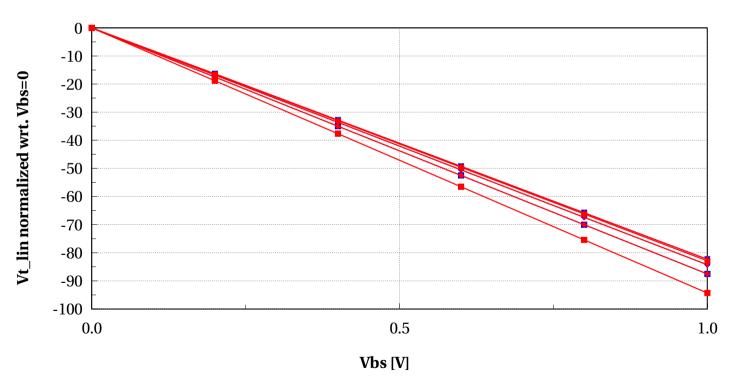
# Scaling versus Vbs normalized (L=1um,W=1um)





## lvtnfet\_acc, Vt\_lin normalized wrt. Vbs=0 vs Vbs [V]





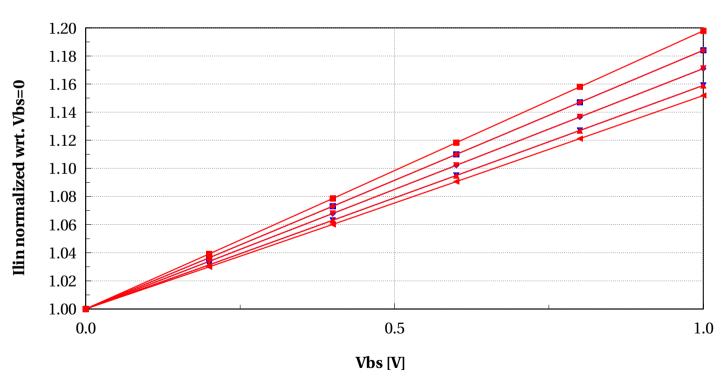






### lvtnfet\_acc, Ilin normalized wrt. Vbs=0 vs Vbs [V]



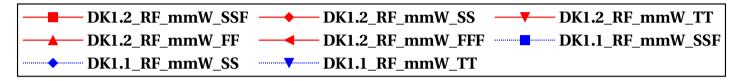


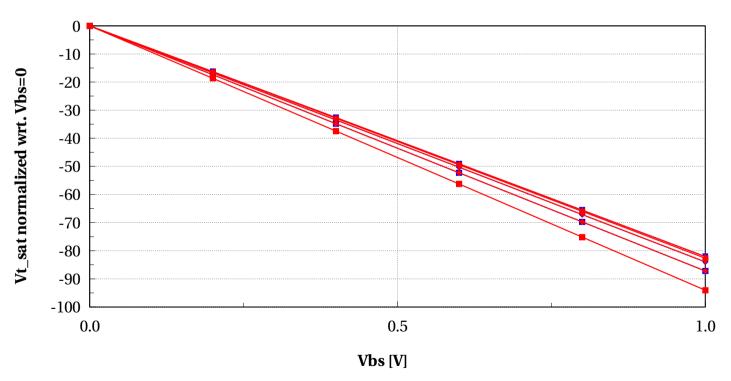






### lvtnfet\_acc, Vt\_sat normalized wrt. Vbs=0 vs Vbs [V]





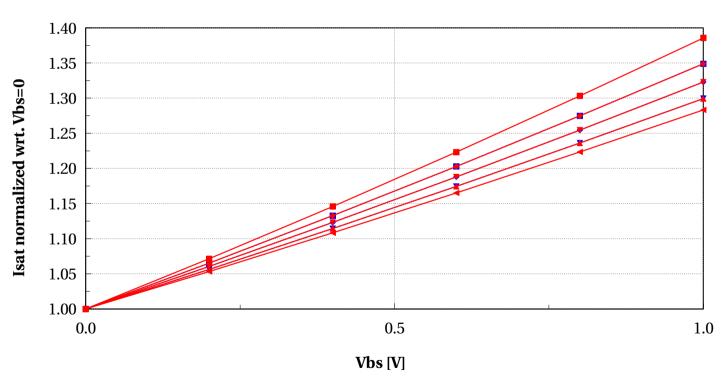






### lvtnfet\_acc, Isat normalized wrt. Vbs=0 vs Vbs [V]





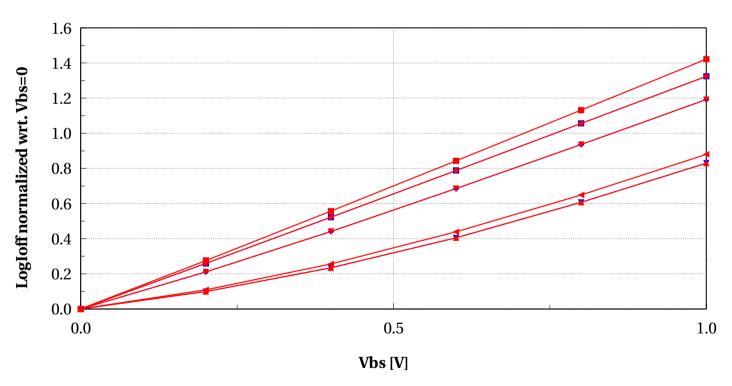






# lvtnfet\_acc, LogIoff normalized wrt. Vbs=0 vs Vbs [V]





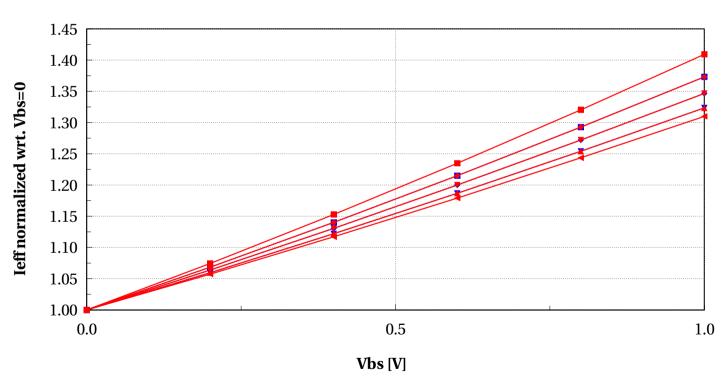






## lvtnfet\_acc, Ieff normalized wrt. Vbs=0 vs Vbs [V]











# Scaling versus Vbs (L=30nm,W=210nm)

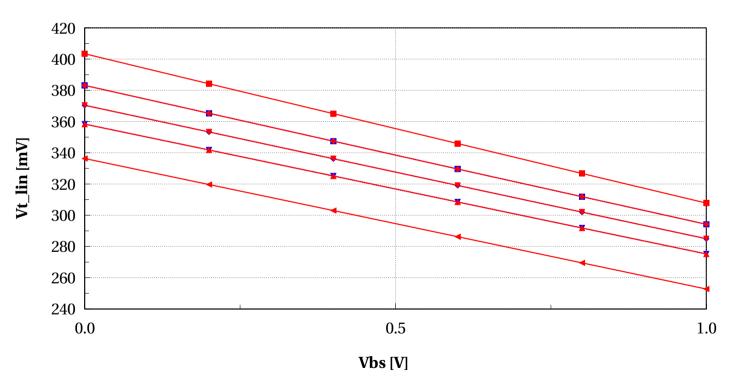






### lvtnfet\_acc, Vt\_lin [mV] vs Vbs [V]





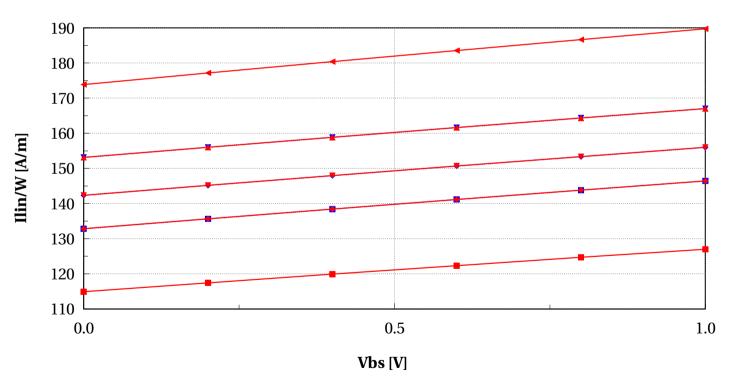






### lvtnfet\_acc, Ilin/W [A/m] vs Vbs [V]





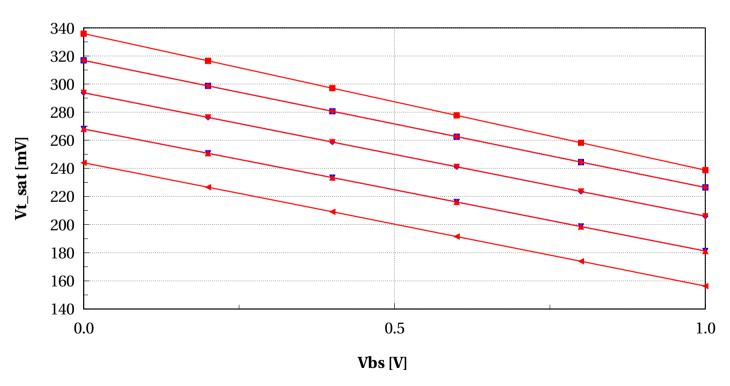






### lvtnfet\_acc, Vt\_sat [mV] vs Vbs [V]





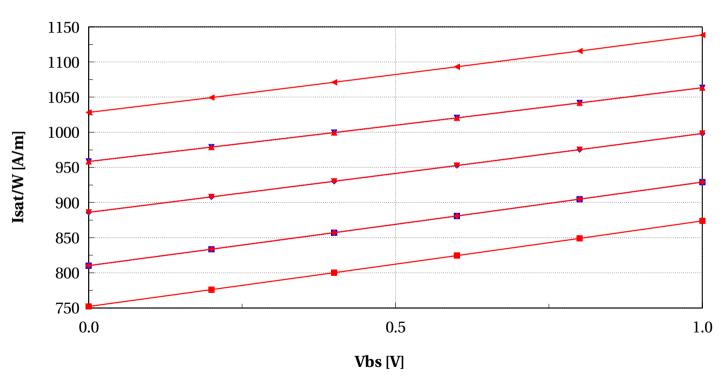






## lvtnfet\_acc, Isat/W [A/m] vs Vbs [V]





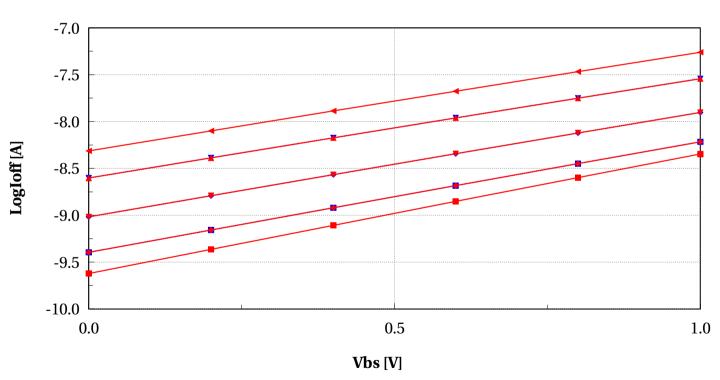






# lvtnfet\_acc, LogIoff [A] vs Vbs [V]







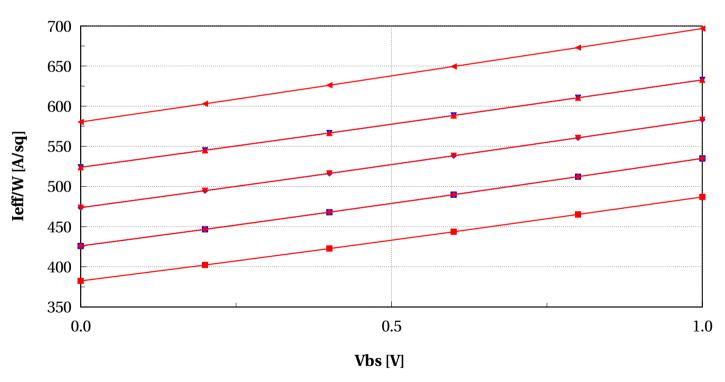




## lvtnfet\_acc, Ieff/W [A/sq] vs Vbs [V]

w==210e-9 and L==0.03e-6 and Temp==25 and vbs<1.1 and p\_la==0 and Vdd==1









dormieub



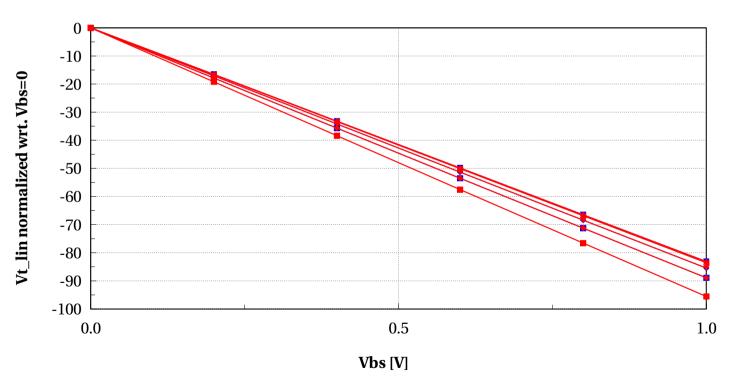
# Scaling versus Vbs normalized (L=30nm,W=210nm)





## lvtnfet\_acc, Vt\_lin normalized wrt. Vbs=0 vs Vbs [V]





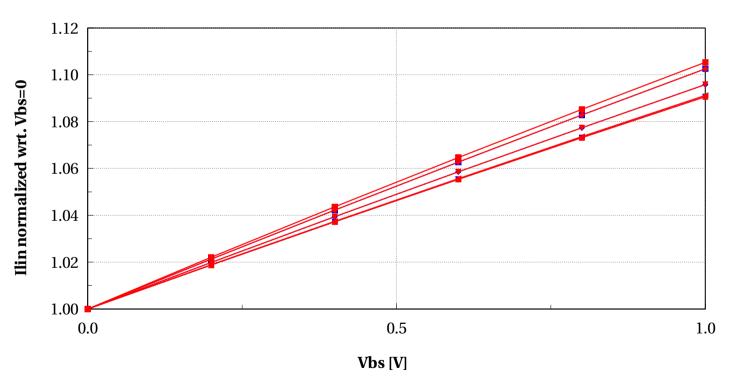






### lvtnfet\_acc, Ilin normalized wrt. Vbs=0 vs Vbs [V]





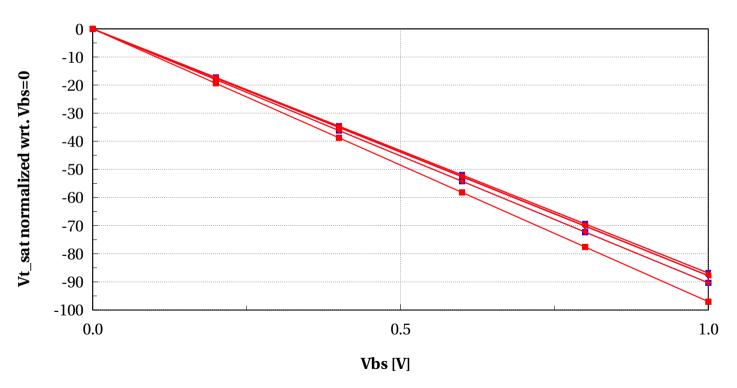






### lvtnfet\_acc, Vt\_sat normalized wrt. Vbs=0 vs Vbs [V]





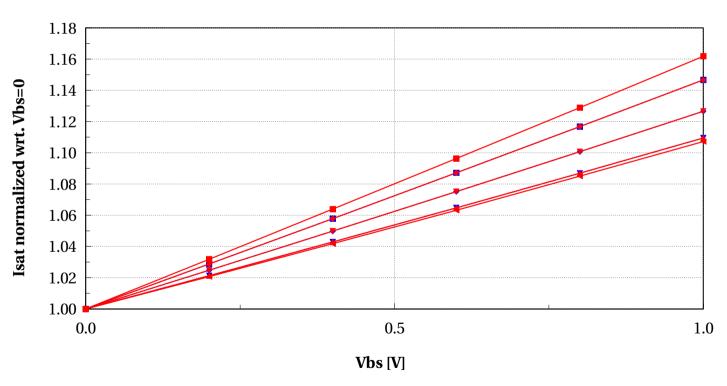






### lvtnfet\_acc, Isat normalized wrt. Vbs=0 vs Vbs [V]





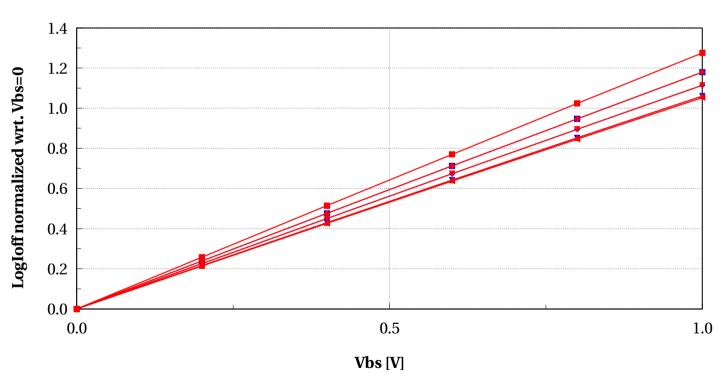






# lvtnfet\_acc, LogIoff normalized wrt. Vbs=0 vs Vbs [V]





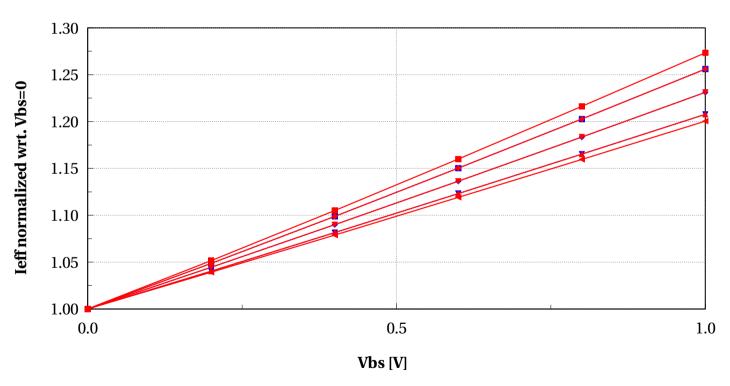






### lvtnfet\_acc, Ieff normalized wrt. Vbs=0 vs Vbs [V]











Vdd effect (l=1um,w=1um)





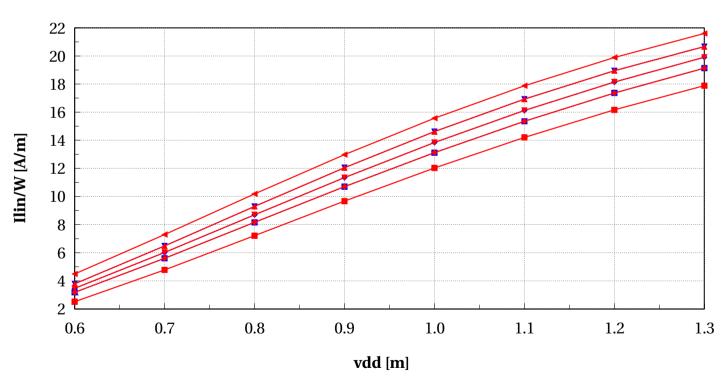
dormieub



### lvtnfet\_acc, Ilin/W [A/m] vs vdd [m]

l==1e-6 and w==1e-6 and Temp==25 and Vbs==0 and  $p_la==0$ 







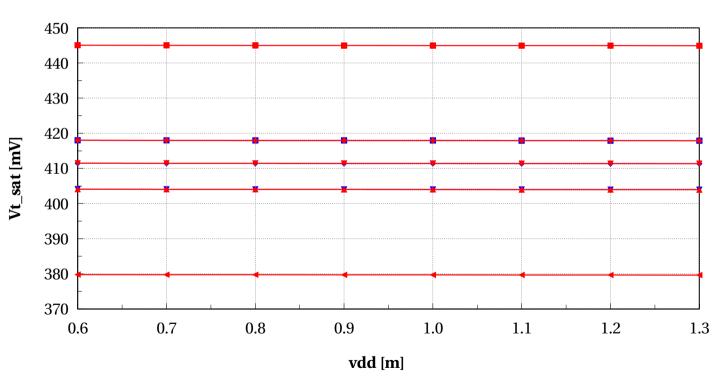




### lvtnfet\_acc, Vt\_sat [mV] vs vdd [m]

l==1e-6 and w==1e-6 and Temp==25 and Vbs==0 and  $p_la==0$ 





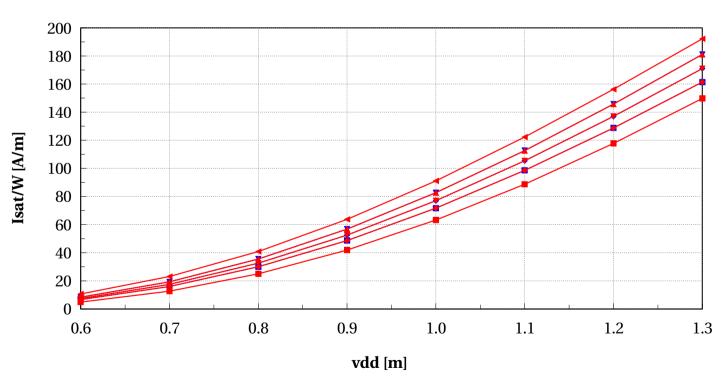






#### lvtnfet\_acc, Isat/W [A/m] vs vdd [m]





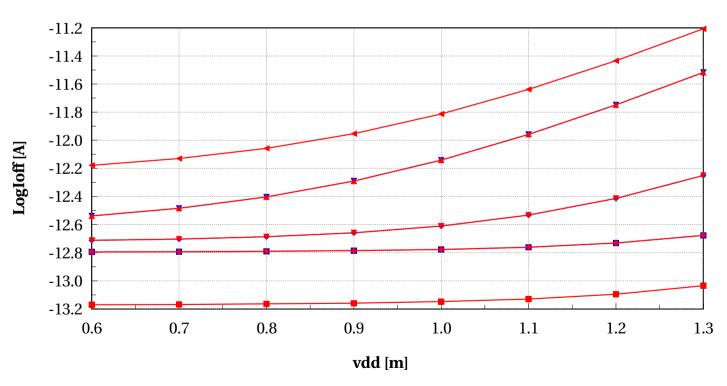






# lvtnfet\_acc, LogIoff [A] vs vdd [m]





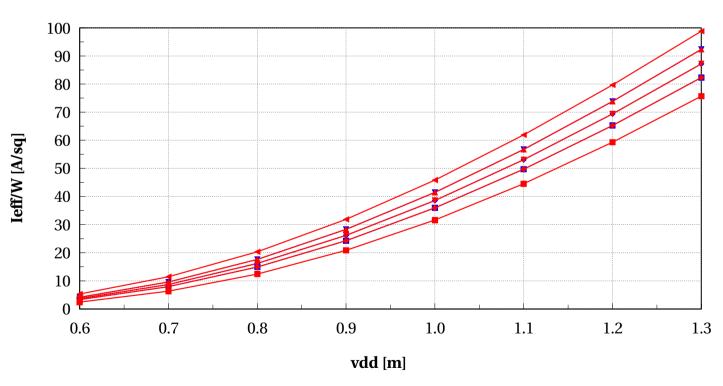






#### lvtnfet\_acc, Ieff/W [A/sq] vs vdd [m]





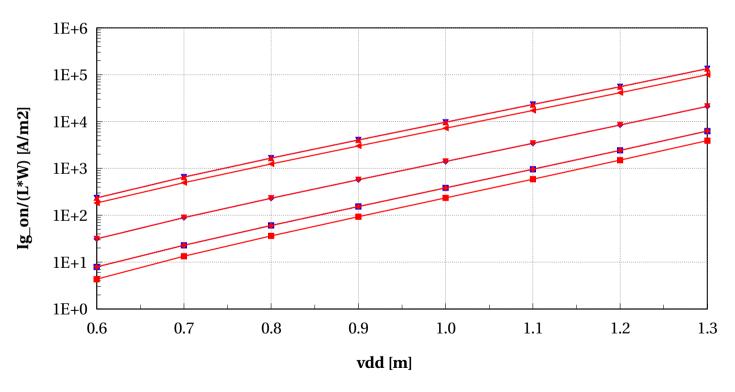






# lvtnfet\_acc, Ig\_on/(L\*W) [A/m2] vs vdd [m]





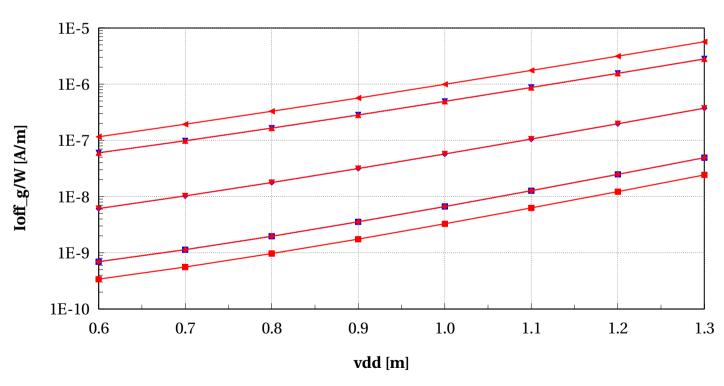






# lvtnfet\_acc, Ioff\_g/W [A/m] vs vdd [m]











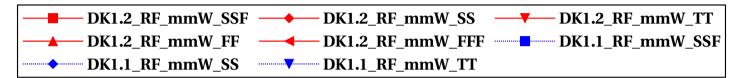
# Vdd effect normalized (l=1um,w=1um)

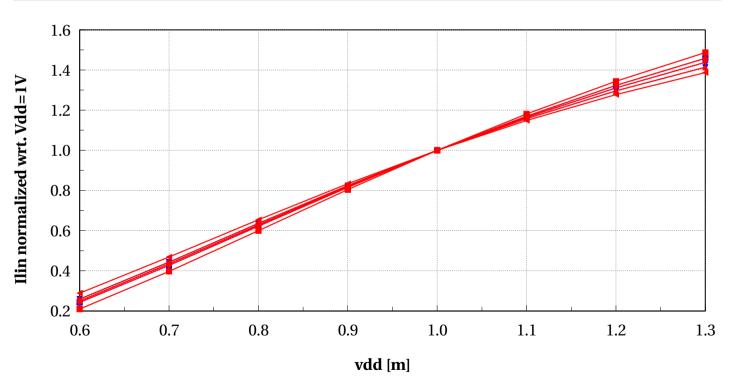






# lvtnfet\_acc, Ilin normalized wrt. Vdd=1V vs vdd [m]



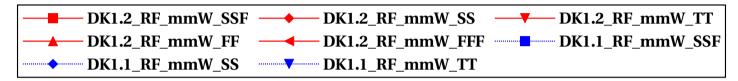


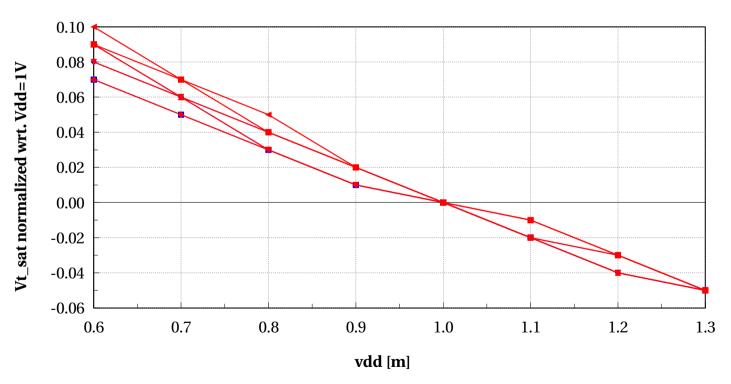






#### lvtnfet\_acc, Vt\_sat normalized wrt. Vdd=1V vs vdd [m]





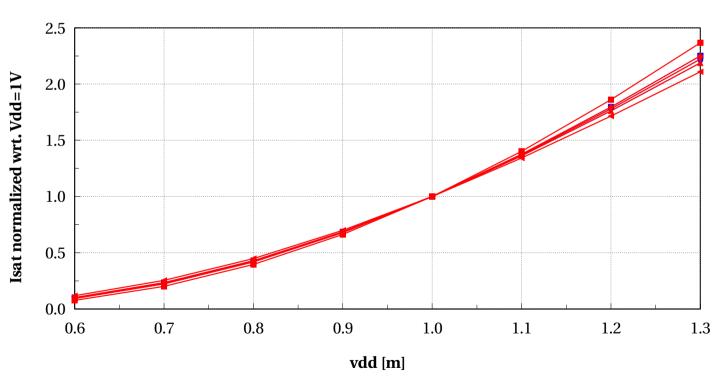






# lvtnfet\_acc, Isat normalized wrt. Vdd=1V vs vdd [m]





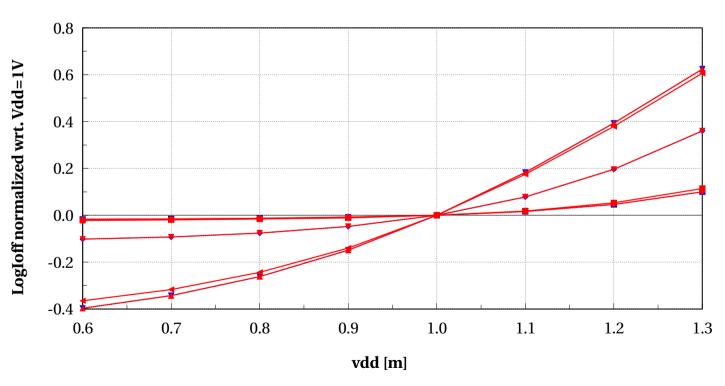






# lvtnfet\_acc, LogIoff normalized wrt. Vdd=1V vs vdd [m]





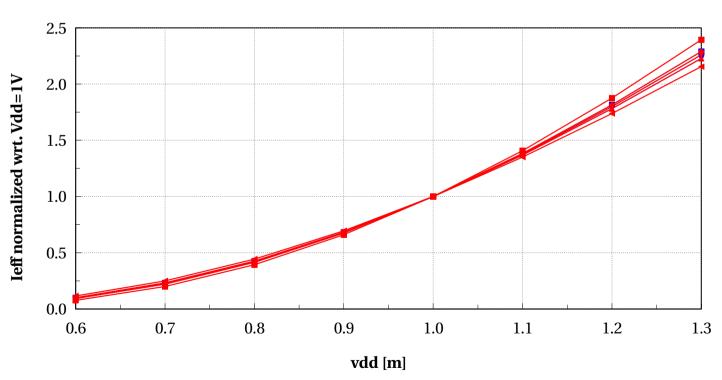






# lvtnfet\_acc, Ieff normalized wrt. Vdd=1V vs vdd [m]





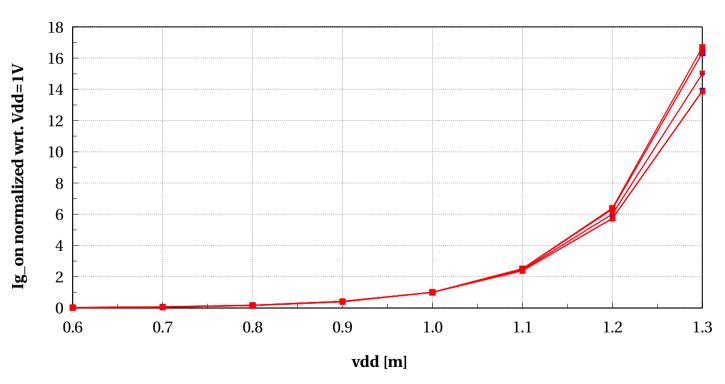






# lvtnfet\_acc, Ig\_on normalized wrt. Vdd=1V vs vdd [m]





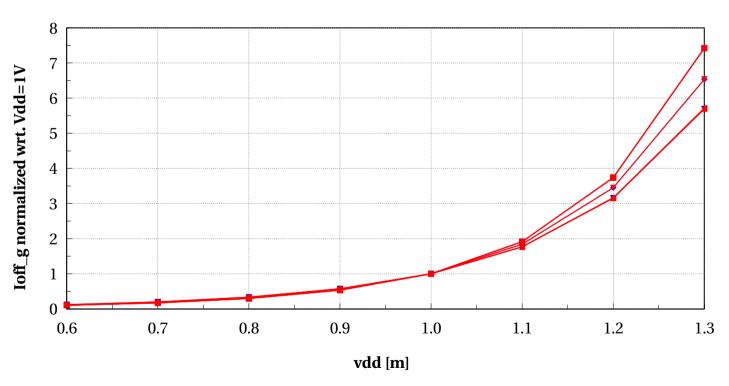






# lvtnfet\_acc, Ioff\_g normalized wrt. Vdd=1V vs vdd [m]











**Vdd effect (l=30e-9w=210e-9)** 



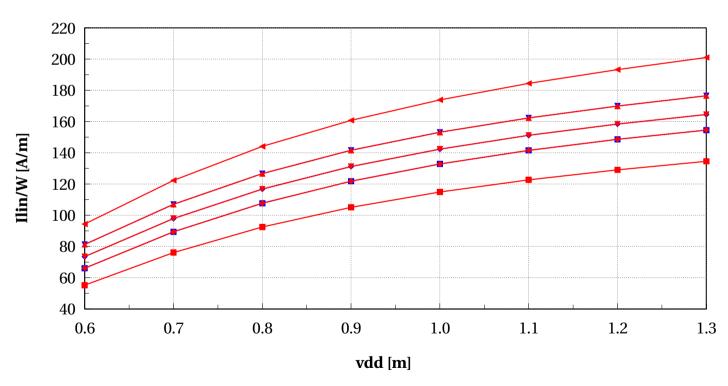


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#### lvtnfet\_acc, Ilin/W [A/m] vs vdd [m]





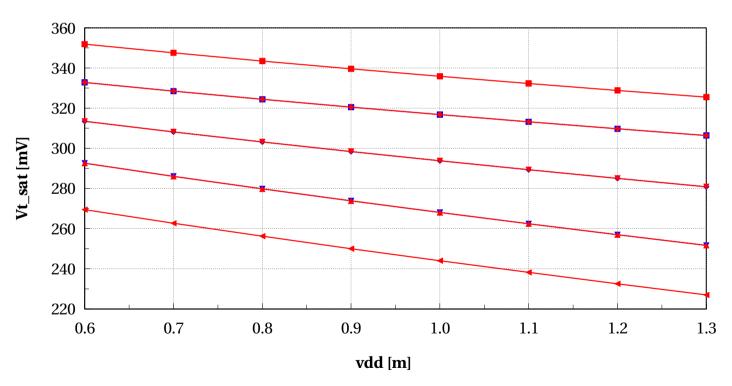






#### lvtnfet\_acc, Vt\_sat [mV] vs vdd [m]



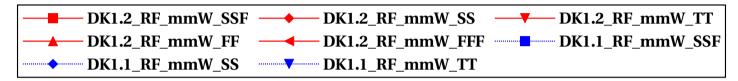


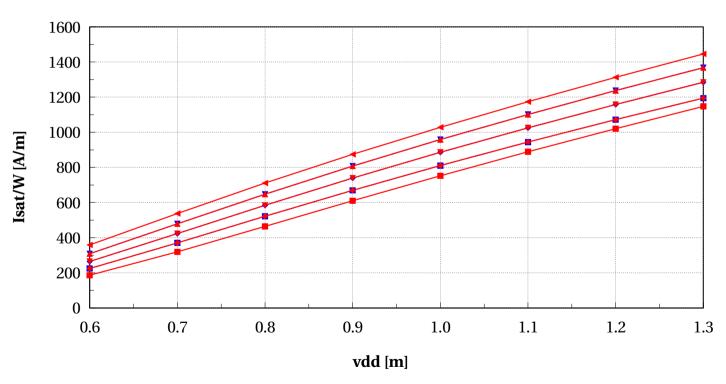






#### lvtnfet\_acc, Isat/W [A/m] vs vdd [m]





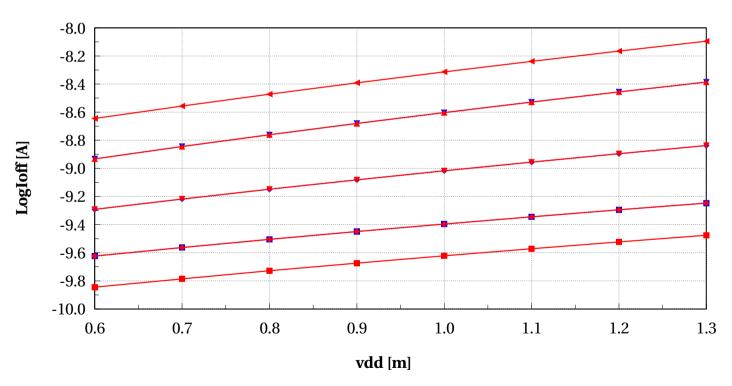






# lvtnfet\_acc, LogIoff [A] vs vdd [m]





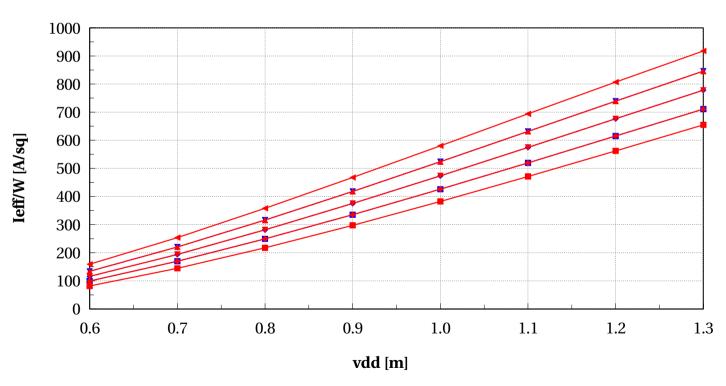






#### lvtnfet\_acc, Ieff/W [A/sq] vs vdd [m]





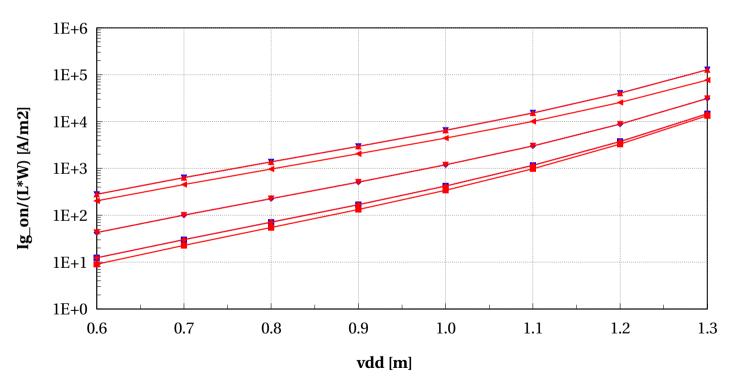






# lvtnfet\_acc, Ig\_on/(L\*W) [A/m2] vs vdd [m]





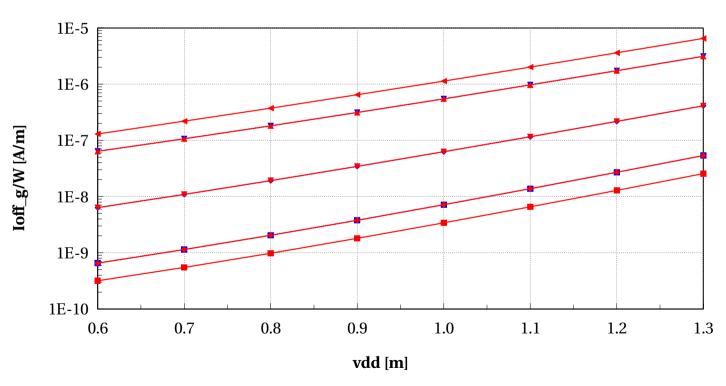






# lvtnfet\_acc, Ioff\_g/W [A/m] vs vdd [m]











# Vdd effect normalized (l=30e-9w=210e-9)



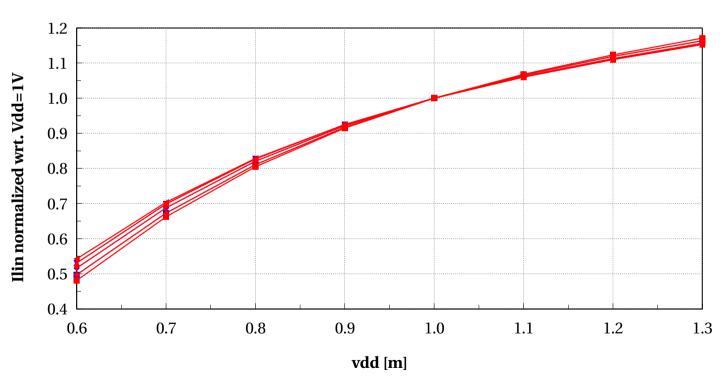


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# lvtnfet\_acc, Ilin normalized wrt. Vdd=1V vs vdd [m]





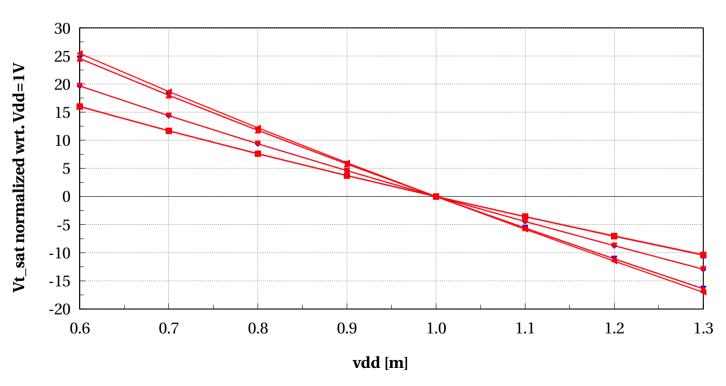






#### lvtnfet\_acc, Vt\_sat normalized wrt. Vdd=1V vs vdd [m]







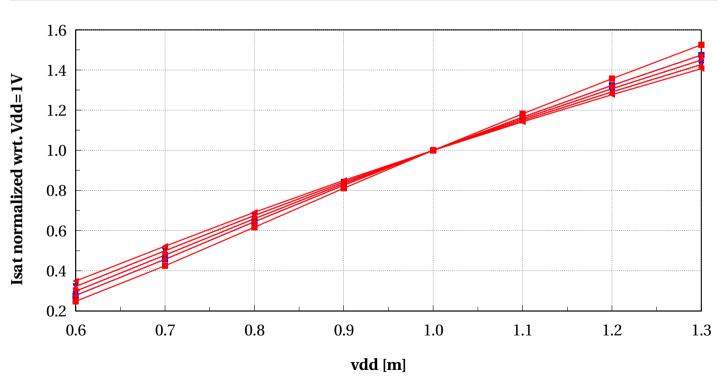




#### lvtnfet\_acc, Isat normalized wrt. Vdd=1V vs vdd [m]

l==30e-9 and w==0.21e-6 and Temp==25 and Vbs==0 and  $p_la==0$ 







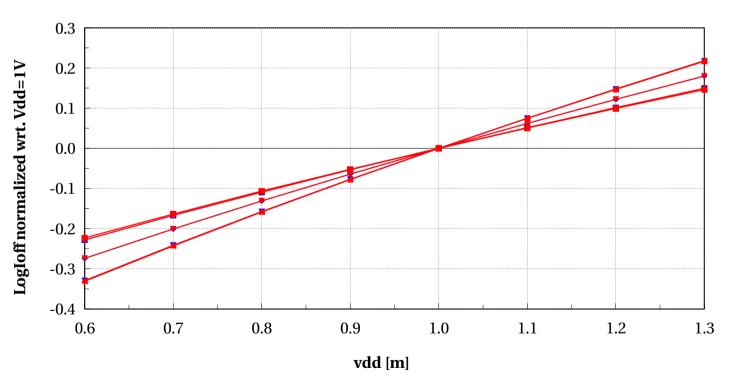


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# lvtnfet\_acc, LogIoff normalized wrt. Vdd=1V vs vdd [m]





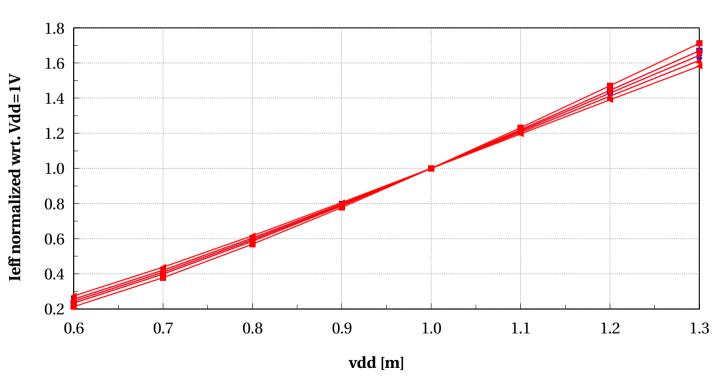






# lvtnfet\_acc, Ieff normalized wrt. Vdd=1V vs vdd [m]





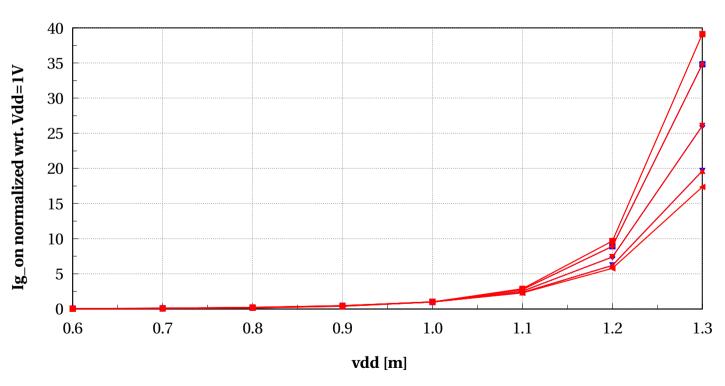






# lvtnfet\_acc, Ig\_on normalized wrt. Vdd=1V vs vdd [m]





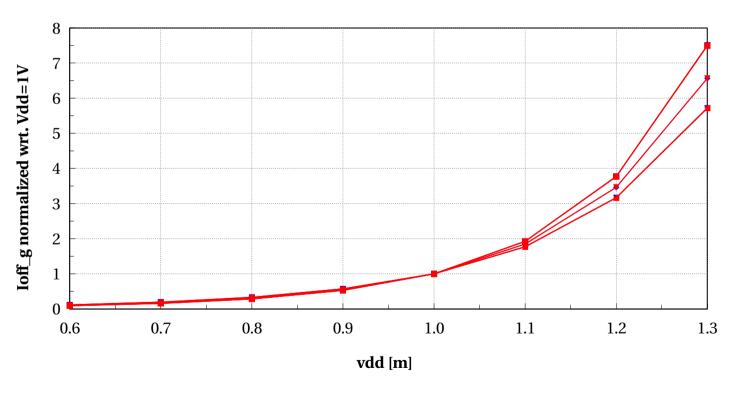






# lvtnfet\_acc, Ioff\_g normalized wrt. Vdd=1V vs vdd [m]











# lvtpfet\_acc Electrical characteristics scaling





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# Scaling versus Vbs (L=1um,W=1um)

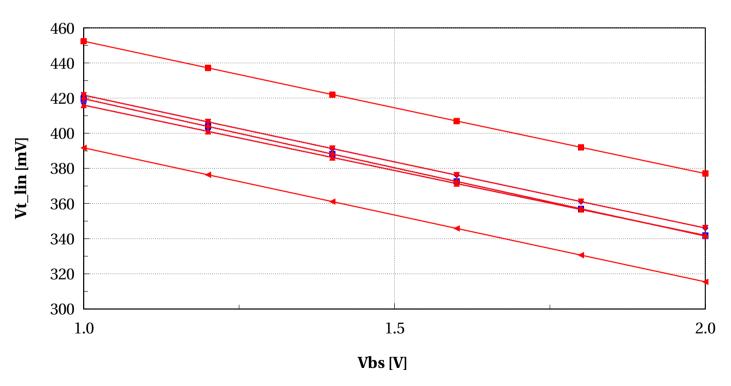






#### lvtpfet\_acc, Vt\_lin [mV] vs Vbs [V]





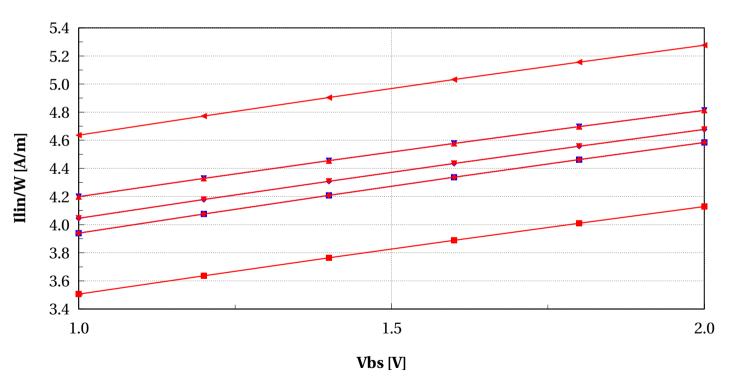






# lvtpfet\_acc, Ilin/W [A/m] vs Vbs [V]





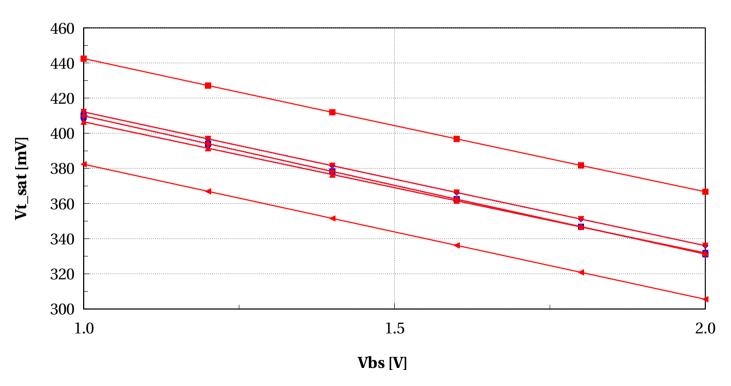






# lvtpfet\_acc, Vt\_sat [mV] vs Vbs [V]





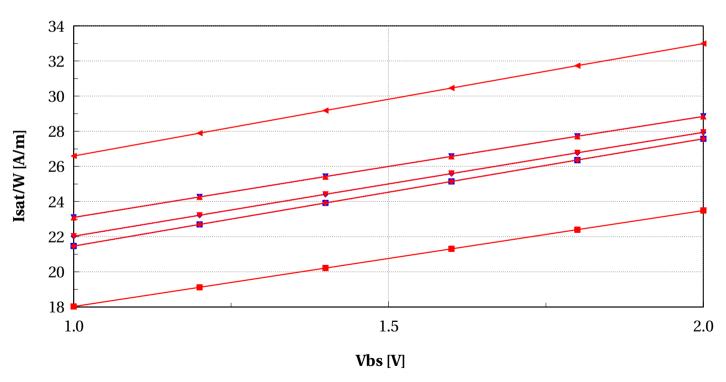






#### lvtpfet\_acc, Isat/W [A/m] vs Vbs [V]





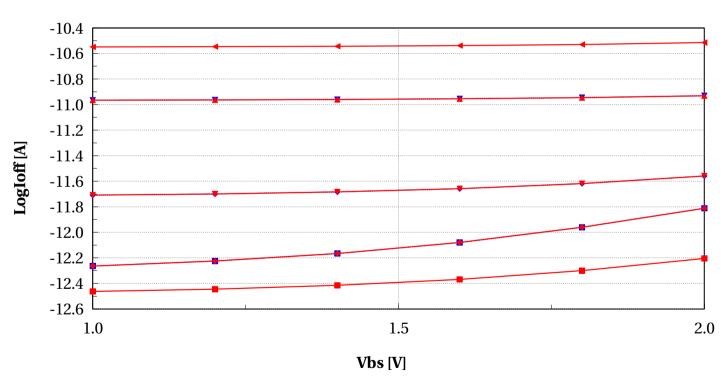






# lvtpfet\_acc, LogIoff [A] vs Vbs [V]







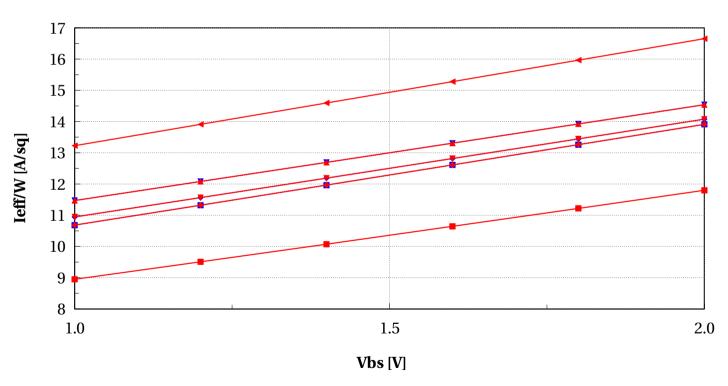




#### lvtpfet\_acc, Ieff/W [A/sq] vs Vbs [V]

w==1e-6 and L==1e-6 and Temp==25 and vbs>0.9 and  $p_la==0$  and Vdd==1









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## Scaling versus Vbs normalized (L=1um,W=1um)

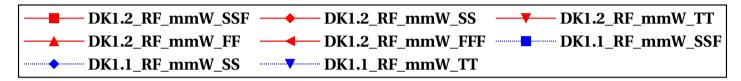


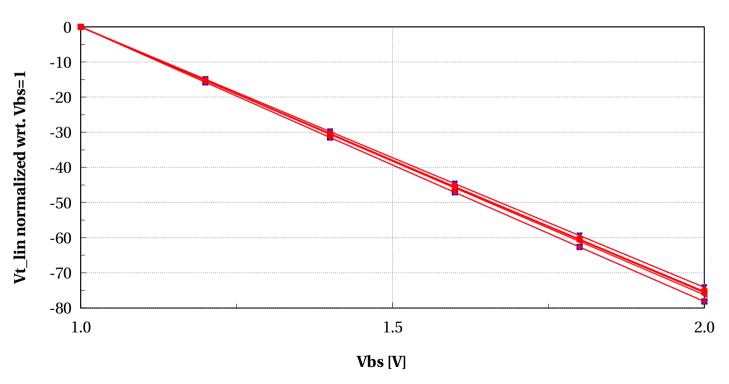


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#### lvtpfet\_acc, Vt\_lin normalized wrt. Vbs=1 vs Vbs [V]







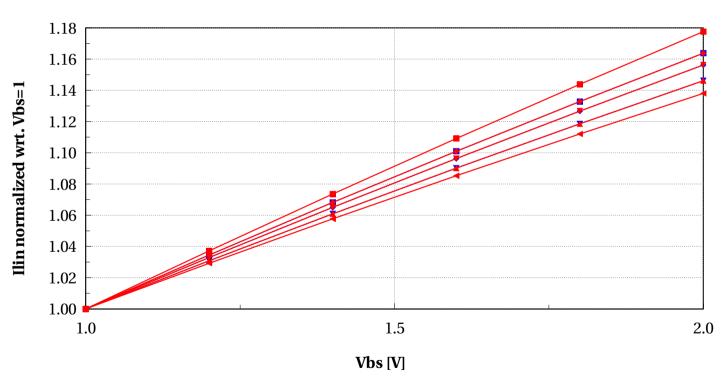




#### lvtpfet\_acc, Ilin normalized wrt. Vbs=1 vs Vbs [V]

w==1e-6 and L==1e-6 and Temp==25 and vbs>0.9 and  $p_la==0$  and Vdd==1







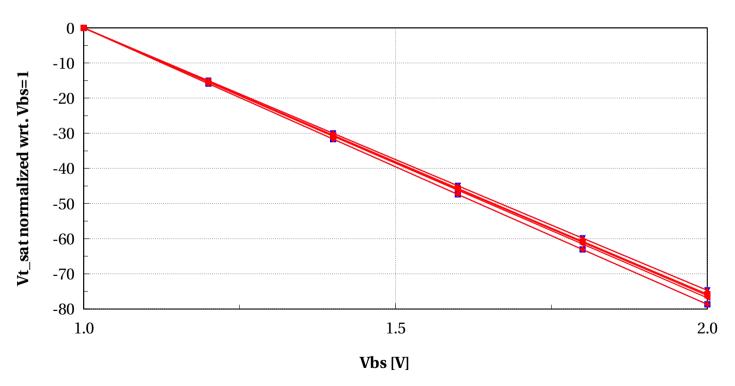


dormieub



#### lvtpfet\_acc, Vt\_sat normalized wrt. Vbs=1 vs Vbs [V]





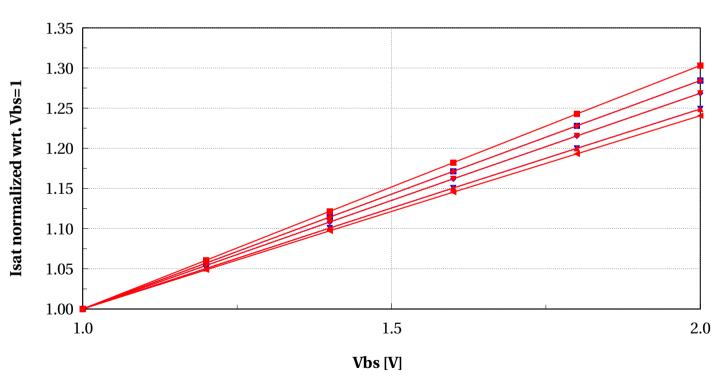






#### lvtpfet\_acc, Isat normalized wrt. Vbs=1 vs Vbs [V]





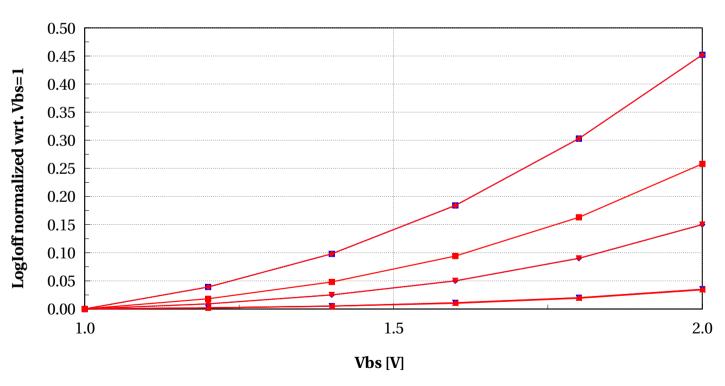






## lvtpfet\_acc, LogIoff normalized wrt. Vbs=1 vs Vbs [V]





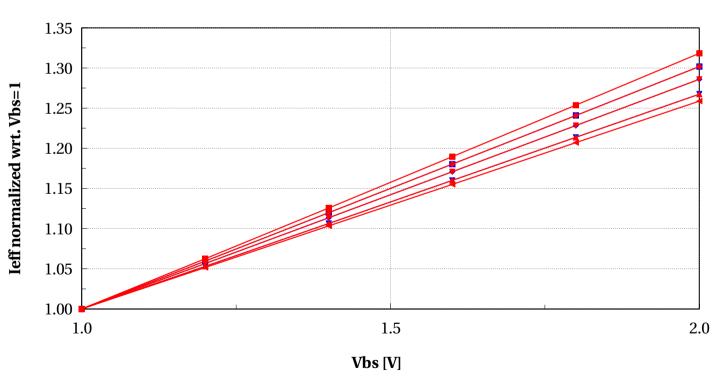






#### lvtpfet\_acc, Ieff normalized wrt. Vbs=1 vs Vbs [V]











# Scaling versus Vbs (L=30nm,W=300nm)

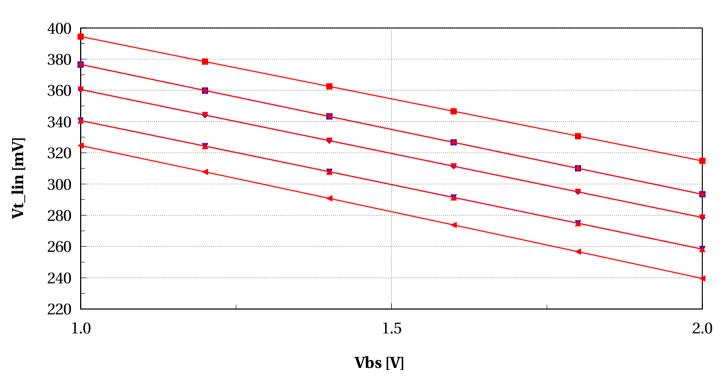






## lvtpfet\_acc, Vt\_lin [mV] vs Vbs [V]





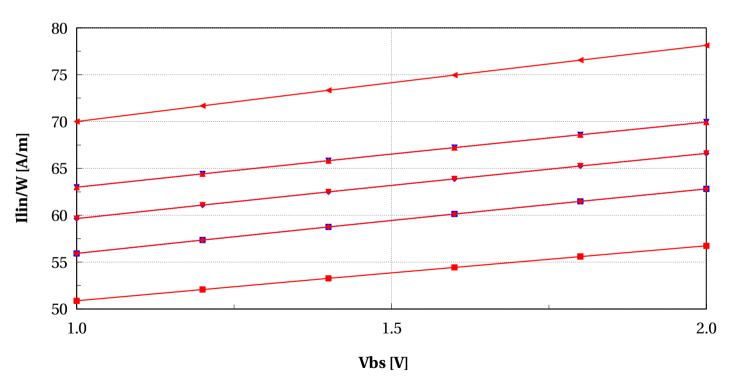






#### lvtpfet\_acc, Ilin/W [A/m] vs Vbs [V]





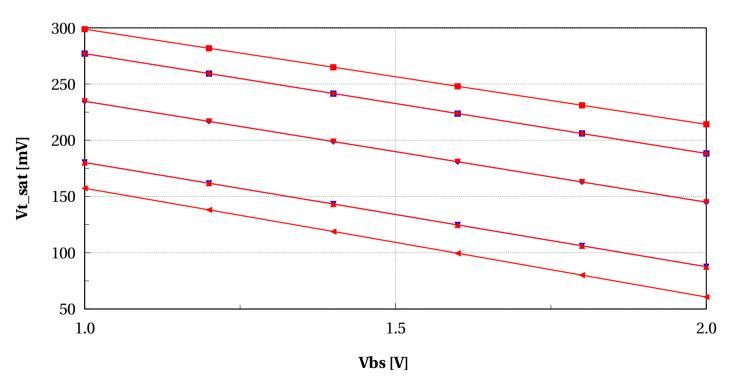






#### lvtpfet\_acc, Vt\_sat [mV] vs Vbs [V]







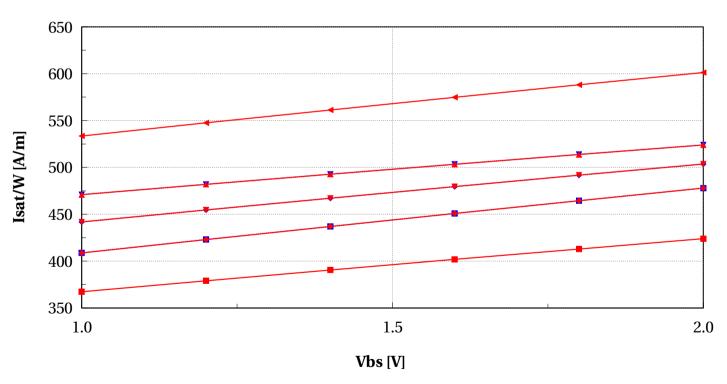




#### lvtpfet\_acc, Isat/W [A/m] vs Vbs [V]

 $w{=}300e{-}9\ and\ L{=}0.03e{-}6\ and\ Temp{=}{=}25\ and\ vbs{>}0.9\ and\ p\_la{=}{=}0\ and\ Vdd{=}{=}1$ 





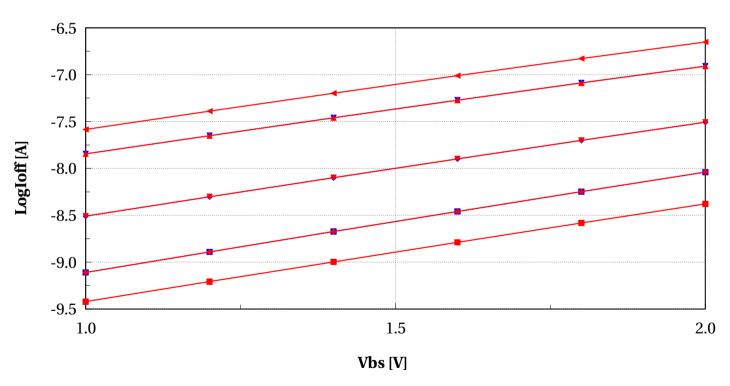






## lvtpfet\_acc, LogIoff [A] vs Vbs [V]







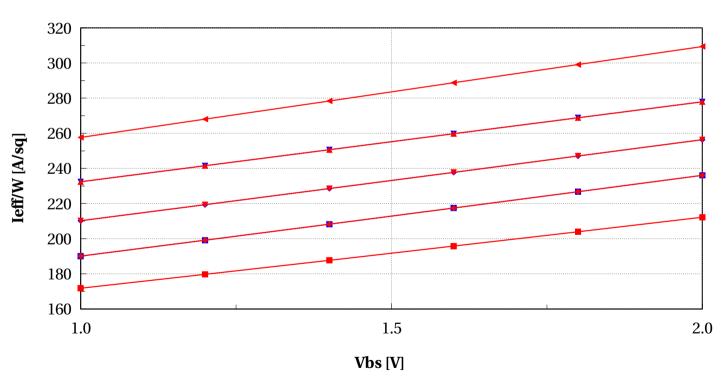




#### lvtpfet\_acc, Ieff/W [A/sq] vs Vbs [V]

 $w{=}300e{-}9~and~L{=}{=}0.03e{-}6~and~Temp{=}{=}25~and~vbs{>}0.9~and~p\_la{=}{=}0~and~Vdd{=}{=}1$ 











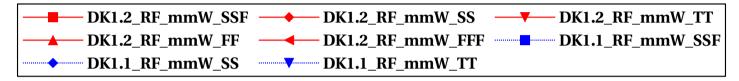
## Scaling versus Vbs normalized (L=30nm,W=300nm)

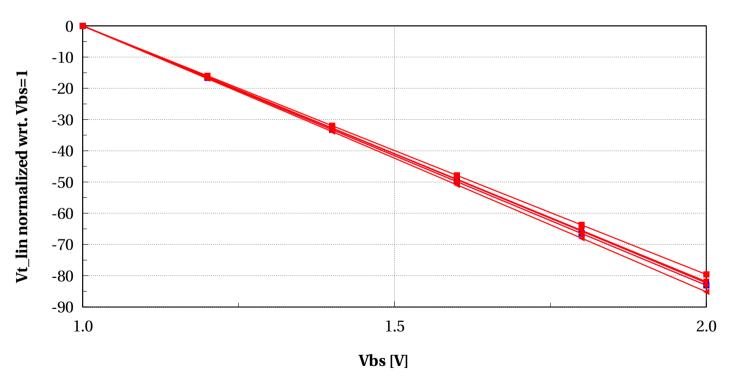




#### lvtpfet\_acc, Vt\_lin normalized wrt. Vbs=1 vs Vbs [V]

 $w{=}300e{-}9~and~L{=}{=}0.03e{-}6~and~Temp{=}{=}25~and~vbs{>}0.9~and~p\_la{=}{=}0~and~Vdd{=}{=}1$ 





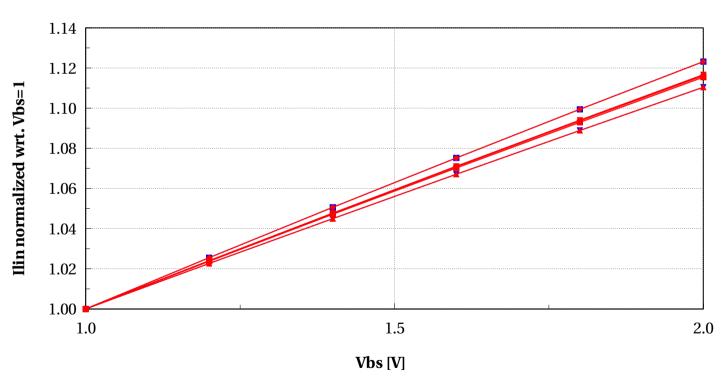






#### lvtpfet\_acc, Ilin normalized wrt. Vbs=1 vs Vbs [V]





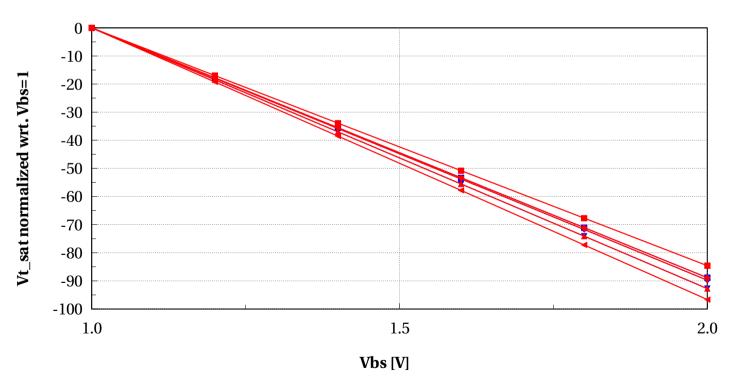






#### lvtpfet\_acc, Vt\_sat normalized wrt. Vbs=1 vs Vbs [V]





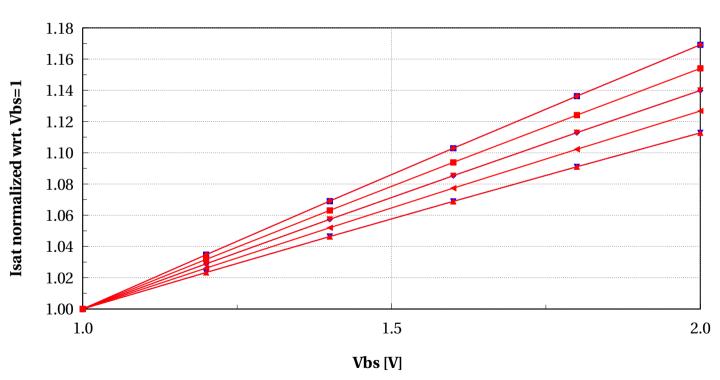






#### lvtpfet\_acc, Isat normalized wrt. Vbs=1 vs Vbs [V]







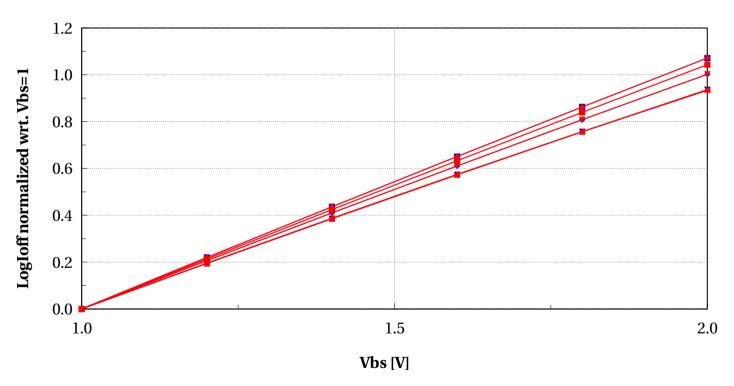




## lvtpfet\_acc, LogIoff normalized wrt. Vbs=1 vs Vbs [V]

 $w{=}300e{-}9\ and\ L{=}0.03e{-}6\ and\ Temp{=}{=}25\ and\ vbs{>}0.9\ and\ p\_la{=}{=}0\ and\ Vdd{=}{=}1$ 







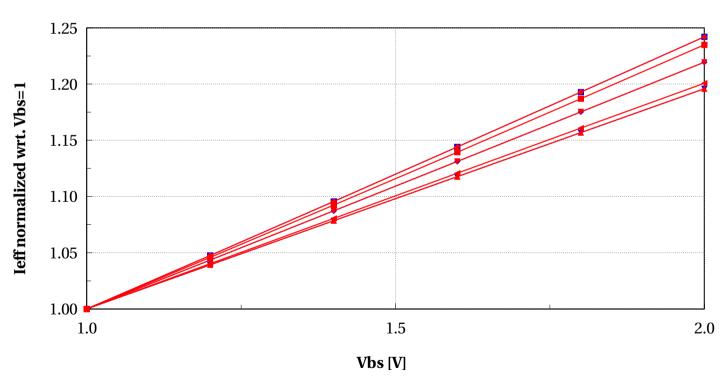




#### lvtpfet\_acc, Ieff normalized wrt. Vbs=1 vs Vbs [V]

 $w{=}300e{-}9~and~L{=}{=}0.03e{-}6~and~Temp{=}{=}25~and~vbs{>}0.9~and~p\_la{=}{=}0~and~Vdd{=}{=}1$ 











## Vdd effect (l=1um,w=1um)



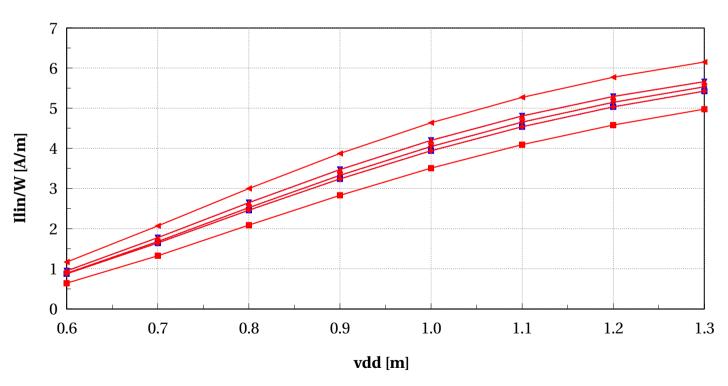


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### lvtpfet\_acc, Ilin/W [A/m] vs vdd [m]



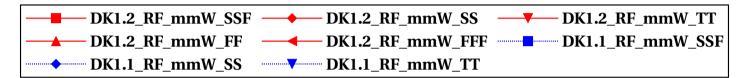


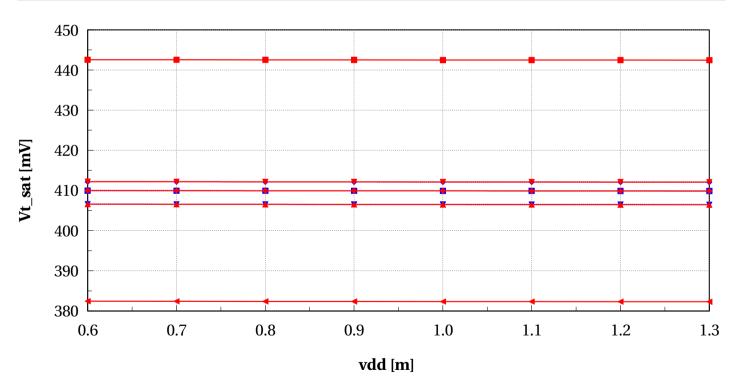






#### lvtpfet\_acc, Vt\_sat [mV] vs vdd [m]





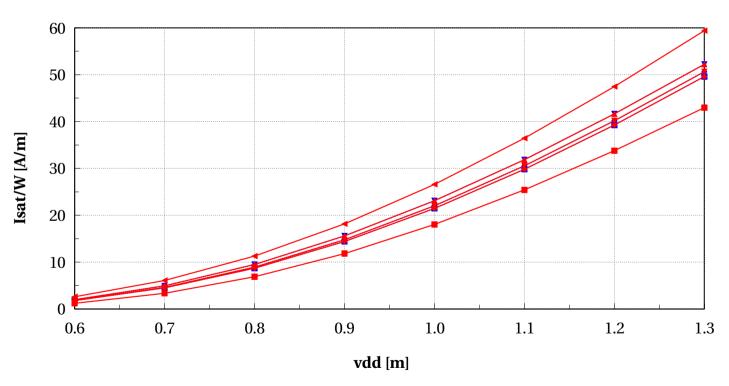






#### lvtpfet\_acc, Isat/W [A/m] vs vdd [m]



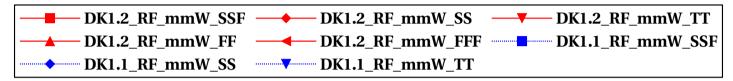


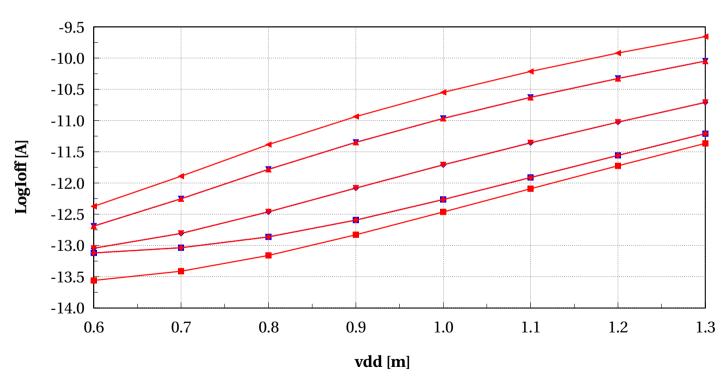






## lvtpfet\_acc, LogIoff [A] vs vdd [m]



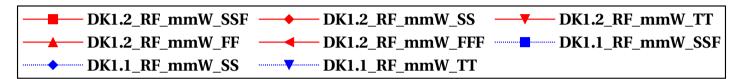


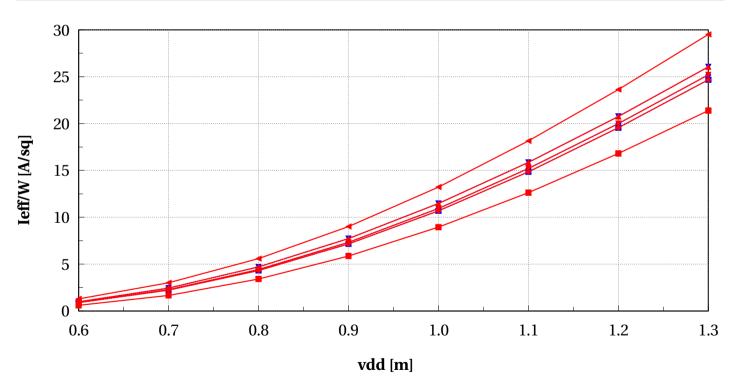






#### lvtpfet\_acc, Ieff/W [A/sq] vs vdd [m]



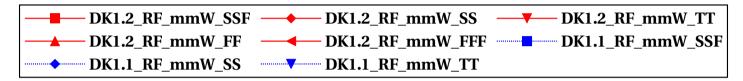


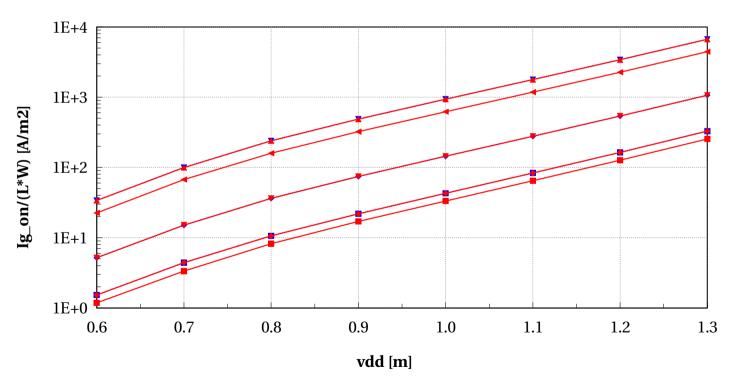






## lvtpfet\_acc, Ig\_on/(L\*W) [A/m2] vs vdd [m]





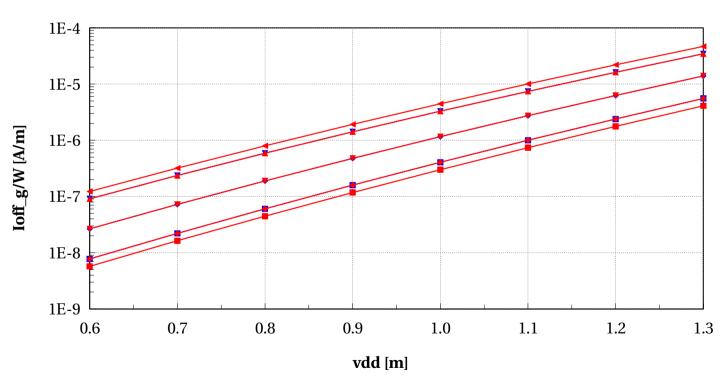






## lvtpfet\_acc, Ioff\_g/W [A/m] vs vdd [m]











## Vdd effect normalized (l=1um,w=1um)

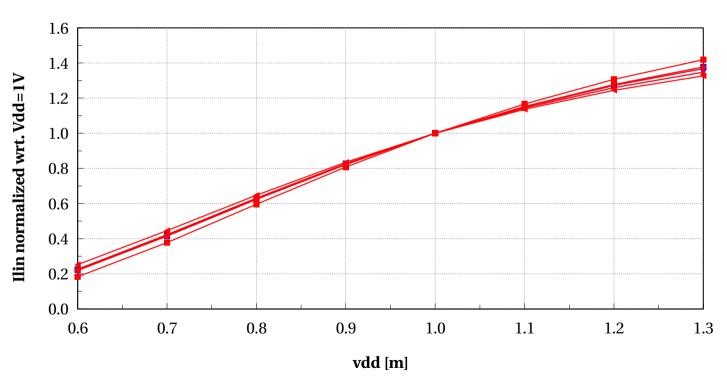






### lvtpfet\_acc, Ilin normalized wrt. Vdd=1V vs vdd [m]



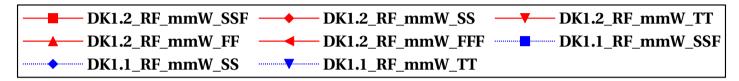


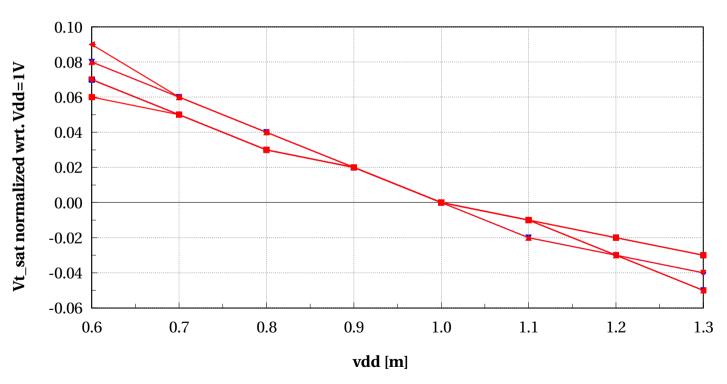






#### lvtpfet\_acc, Vt\_sat normalized wrt. Vdd=1V vs vdd [m]





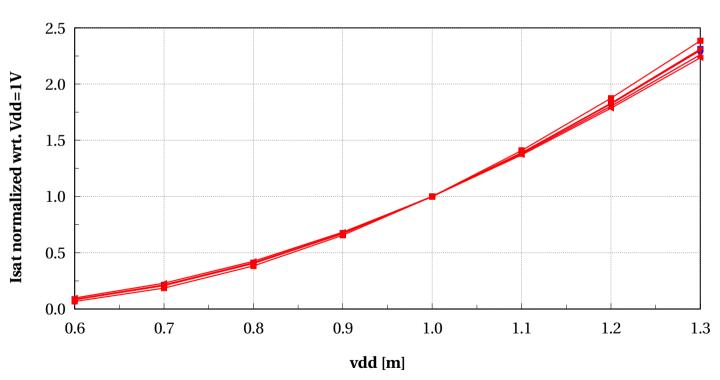






#### lvtpfet\_acc, Isat normalized wrt. Vdd=1V vs vdd [m]





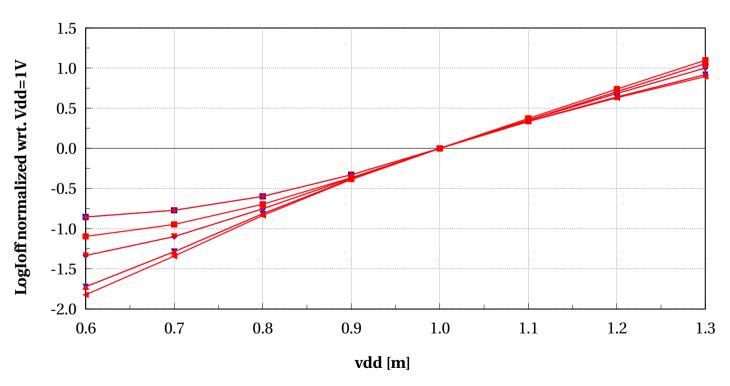






## lvtpfet\_acc, LogIoff normalized wrt. Vdd=1V vs vdd [m]





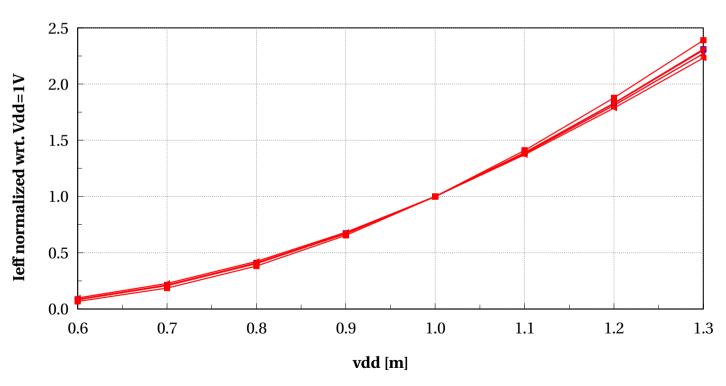






#### lvtpfet\_acc, Ieff normalized wrt. Vdd=1V vs vdd [m]





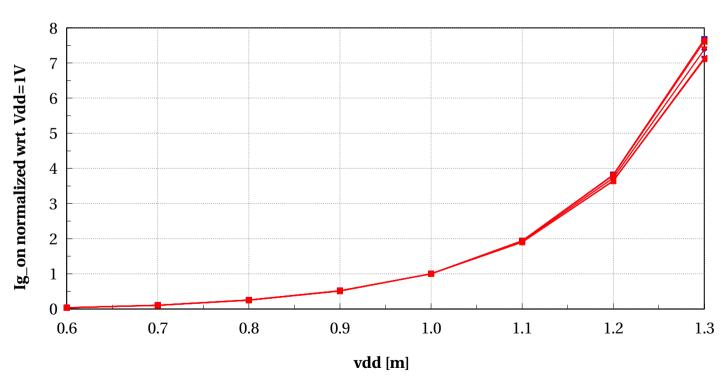






### lvtpfet\_acc, Ig\_on normalized wrt. Vdd=1V vs vdd [m]





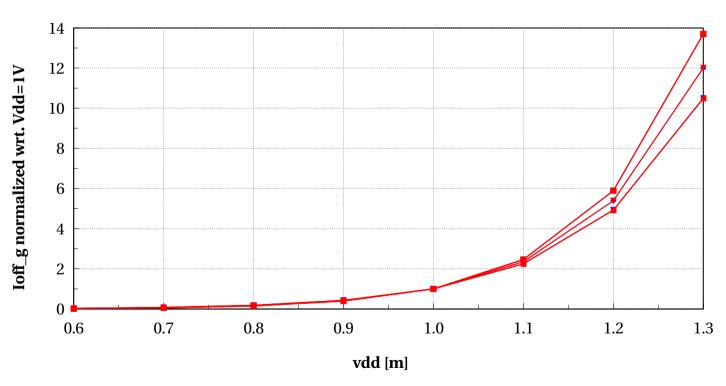






### lvtpfet\_acc, Ioff\_g normalized wrt. Vdd=1V vs vdd [m]











**Vdd effect (l=30e-9w=300e-9)** 



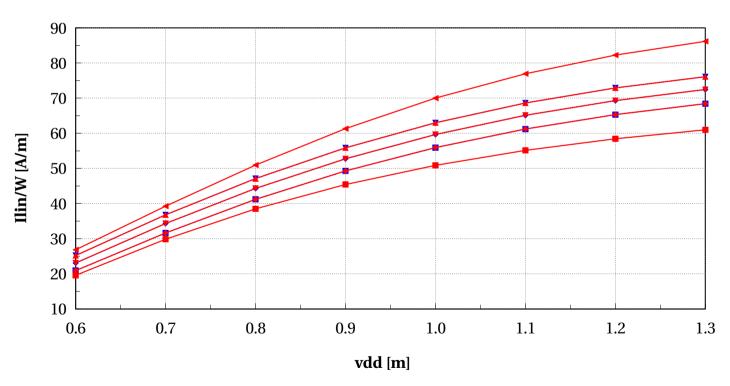


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#### lvtpfet\_acc, Ilin/W [A/m] vs vdd [m]





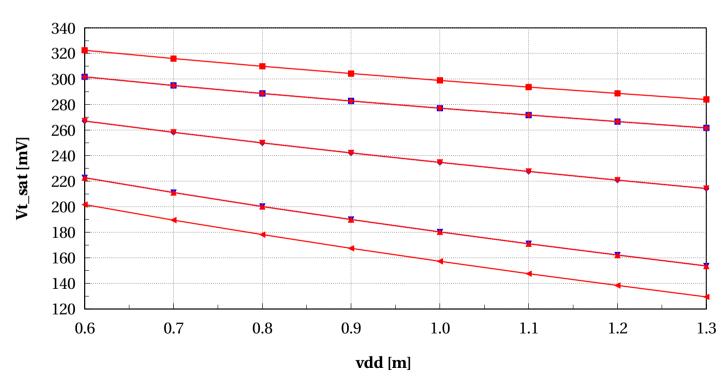






#### lvtpfet\_acc, Vt\_sat [mV] vs vdd [m]



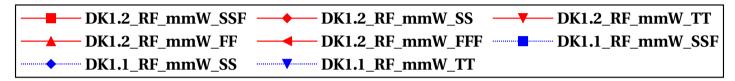


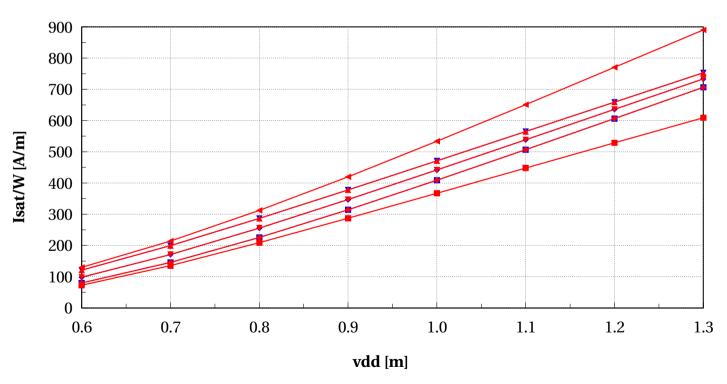






#### lvtpfet\_acc, Isat/W [A/m] vs vdd [m]





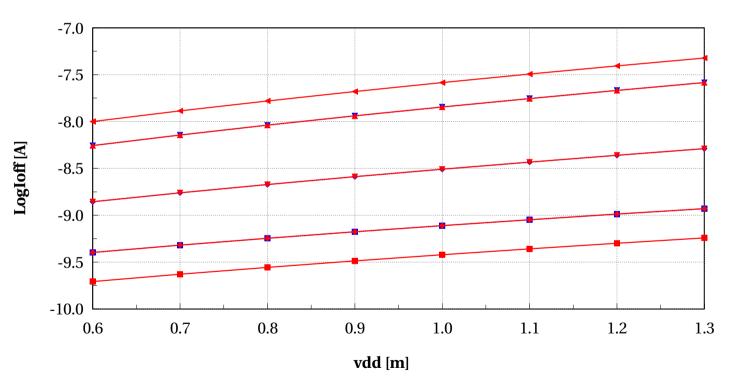






### lvtpfet\_acc, LogIoff [A] vs vdd [m]





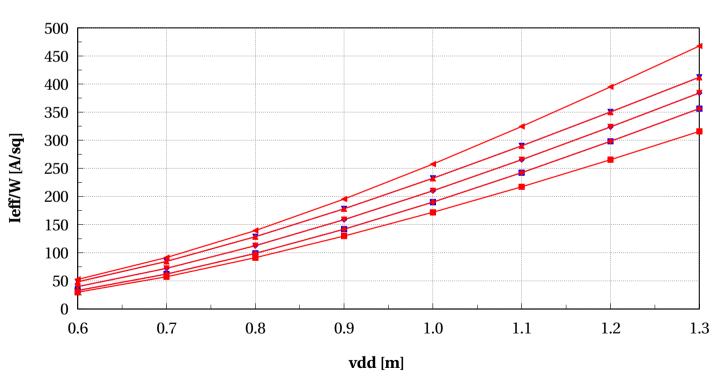






#### lvtpfet\_acc, Ieff/W [A/sq] vs vdd [m]



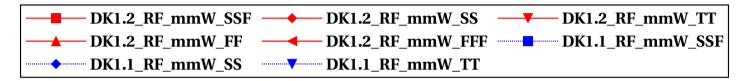


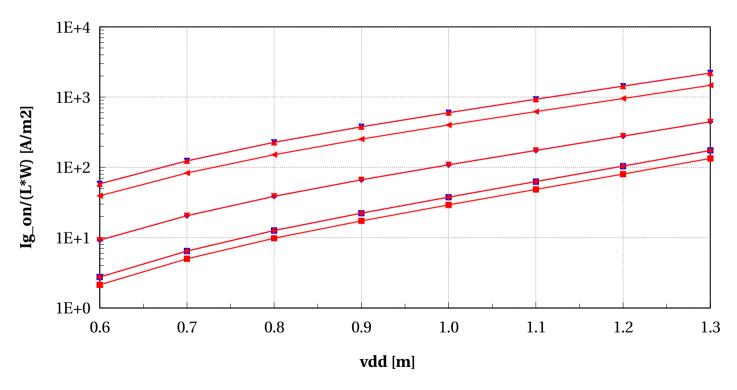






#### lvtpfet\_acc, Ig\_on/(L\*W) [A/m2] vs vdd [m]





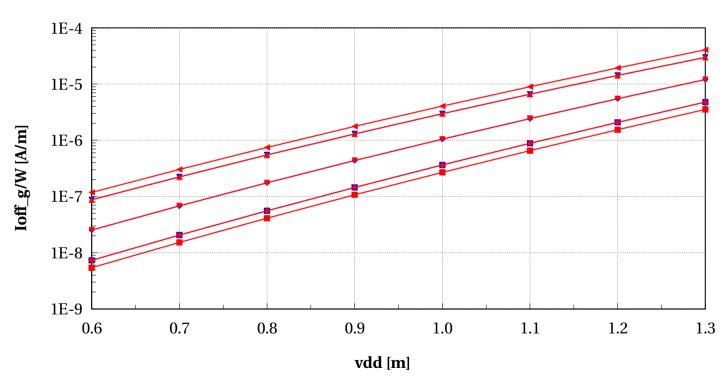






#### lvtpfet\_acc, Ioff\_g/W [A/m] vs vdd [m]











# Vdd effect normalized (l=30e-9w=300e-9)

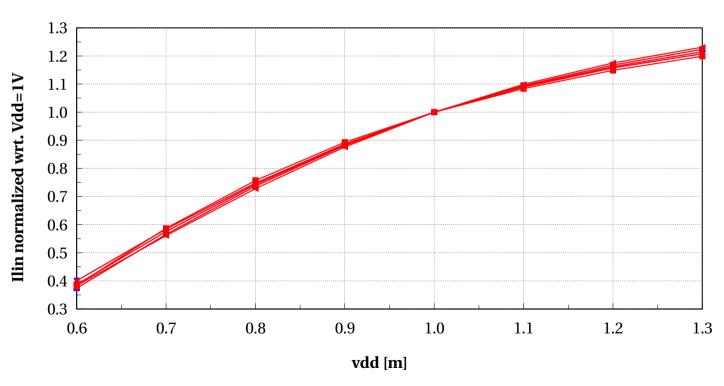


dormieub



#### lvtpfet\_acc, Ilin normalized wrt. Vdd=1V vs vdd [m]





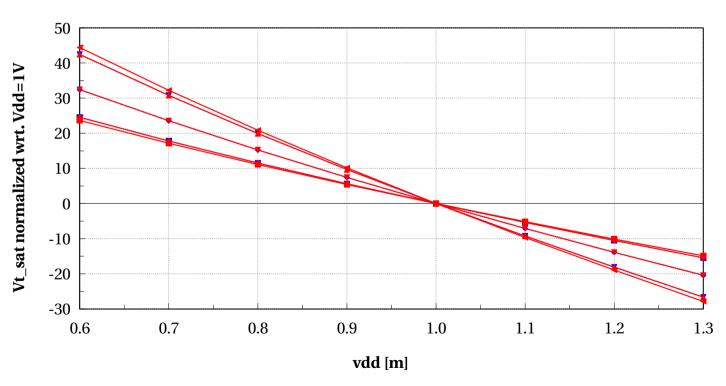






#### lvtpfet\_acc, Vt\_sat normalized wrt. Vdd=1V vs vdd [m]





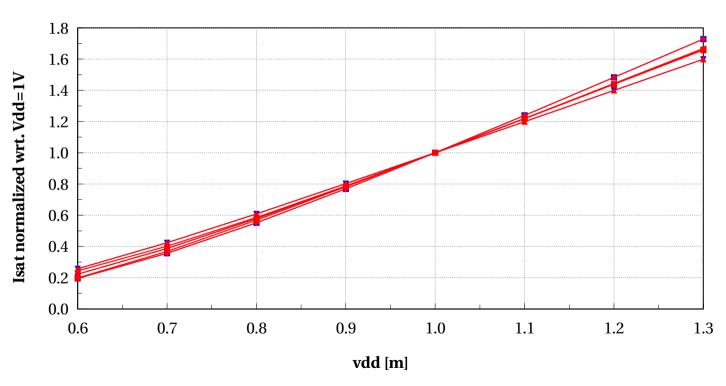






#### lvtpfet\_acc, Isat normalized wrt. Vdd=1V vs vdd [m]





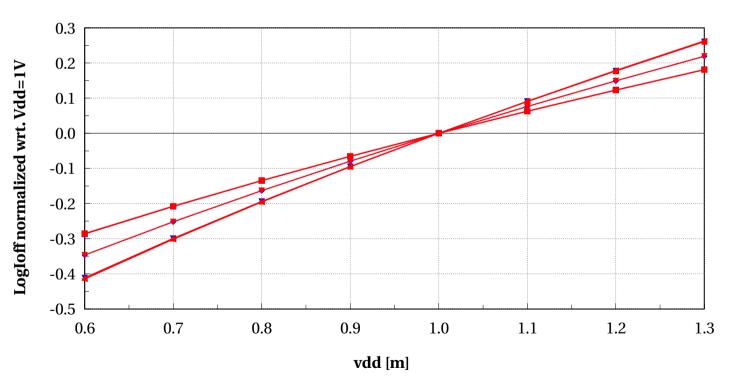






#### lvtpfet\_acc, LogIoff normalized wrt. Vdd=1V vs vdd [m]





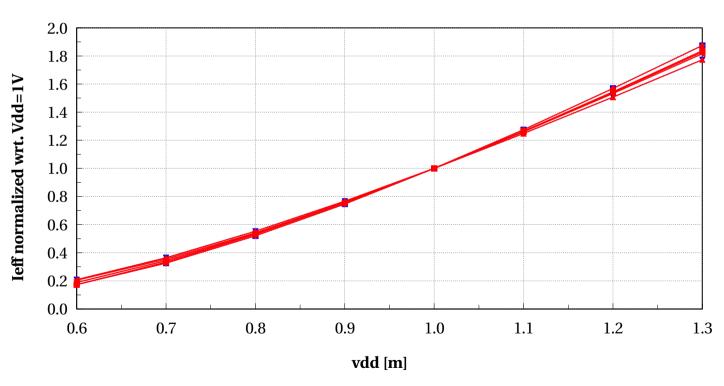






#### lvtpfet\_acc, Ieff normalized wrt. Vdd=1V vs vdd [m]





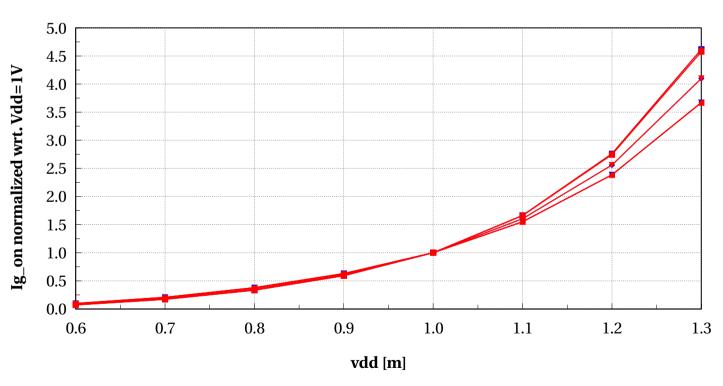






### lvtpfet\_acc, Ig\_on normalized wrt. Vdd=1V vs vdd [m]





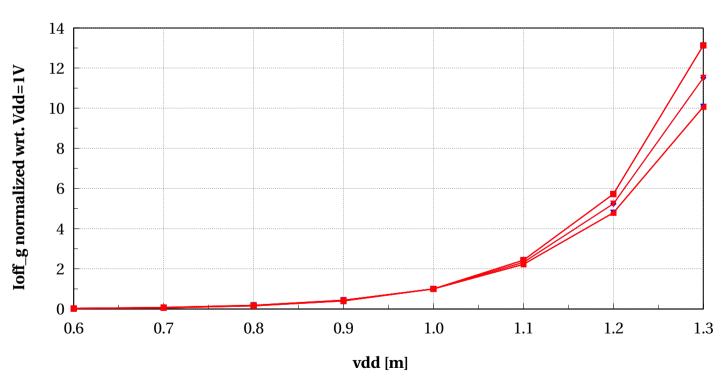






### lvtpfet\_acc, Ioff\_g normalized wrt. Vdd=1V vs vdd [m]











## **Annex**



**ST Confidential** 



#### **Conditions of simulations**

The simulations were done with SBenchLSF Alpha using Eldo simulator 2018.3.

- Model lvtnfet\_acc (DK1.2\_RF\_mmW)
  - ✓ Input Parameters
    - $\times$  vds\_sat = Vdd V
    - $\times$  ivt = 300e-9 A
    - **x** mc\_runs = 1000
    - $\mathbf{X}$  vstep\_ivt = 0.005 V
    - **x** vds\_off = vds\_sat V
    - $\mathbf{x}$  vgs\_off = 0 V
    - $\times$  temp = 25 °C
    - $\times$  vgs\_start = -0.5 V
    - $\mathbf{x}$  mc\_sens = 0
    - $\times$  vds lin = 0.05 V
    - **✗** sbenchlsf\_release = Alpha
    - **✗** plashrink\_ivt = 1
    - $\mathbf{x}$  vbs = 0 V
    - $\times$  ams\_release = 2018.3



- **x** model\_version = 1.3.e
- **x** mc\_nsigma = 3
- $\star$  ithslwi = 10e-9 A
- $\times$  vgs\_stop = vdd V
- **x** shrink\_ivt = 1
- $\times$  vdd = 1 V
- **✗** dlshrink\_ivt = 0
- ✓ Sweep Parameters
  - $\star$  vbs = 0.0, 0.2, 0.4, 0.6, 0.8, 1.0, 1.2, 1.4, 1.6, 1.8, 2.0
  - **x** vdd = 0.6, 0.7, 0.8, 0.9, 1.0, 1.1, 1.2, 1.3
- ✓ Extra parameters
  - $\mathsf{X}$  lvt\_dev = 0
  - **✗** gflag\_\_noisedev\_\_lvt\_\_cmos028fdsoi = 0
- Model lvtpfet\_acc (DK1.2\_RF\_mmW)
  - ✓ Input Parameters
    - $\times$  vds\_sat = Vdd V
    - **x** ivt = 70e-9 A
    - **x** mc\_runs = 1000
    - $\times$  vstep\_ivt = 0.005 V
    - **x** vds\_off = vds\_sat V
    - $\mathbf{x}$  vgs\_off = 0 V
    - $\times$  temp = 25 °C
    - $\times$  vgs\_start = -0.5 V
    - $\mathbf{x}$  mc sens = 0
    - $\times$  vds\_lin = 0.05 V



- **✗** sbenchlsf\_release = Alpha
- **x** plashrink\_ivt = 1
- $\mathbf{x}$  vbs = 1 V
- $\mathbf{x}$  ams\_release = 2018.3
- **x** model\_version = 1.3.e
- **x** mc\_nsigma = 3
- $\star$  ithslwi = 10e-9 A
- $\mathbf{x}$  vgs\_stop = vdd V
- **x** shrink\_ivt = 1
- $\times$  vdd = 1 V
- **✗** dlshrink\_ivt = 0
- ✓ Sweep Parameters
  - $\star$  vbs = 0.0, 0.2, 0.4, 0.6, 0.8, 1.0, 1.2, 1.4, 1.6, 1.8, 2.0
  - **x** vdd = 0.6, 0.7, 0.8, 0.9, 1.0, 1.1, 1.2, 1.3
- ✓ Extra parameters
  - $\mathbf{X}$  lvt dev = 0
  - **✗** gflag\_\_noisedev\_\_lvt\_\_cmos028fdsoi = 0
- Model lvtnfet\_acc (DK1.1\_RF\_mmW)
  - ✓ Input Parameters
    - $\times$  vds\_sat = Vdd V
    - **x** ivt = 300e-9 A
    - **x** mc\_runs = 1000
    - $\mathbf{X}$  vstep\_ivt = 0.005 V
    - **x** vds\_off = vds\_sat V
    - $\mathbf{x}$  vgs\_off = 0 V



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- $\times$  temp = 25 °C
- $\times$  vgs\_start = -0.5 V
- $\mathbf{x}$  mc sens = 0
- $\times$  vds lin = 0.05 V
- **x** sbenchlsf\_release = Alpha
- **x** plashrink\_ivt = 1
- $\mathbf{x}$  vbs = 0 V
- $\mathbf{x}$  ams\_release = 2018.3
- **x** model version = 1.3.d
- **x** mc\_nsigma = 3
- $\star$  ithslwi = 10e-9 A
- $\mathsf{x}$  vgs\_stop = vdd V
- **x** shrink\_ivt = 1
- $\times$  vdd = 1 V
- **✗** dlshrink\_ivt = 0
- ✓ Sweep Parameters
  - $\star$  vbs = 0.0, 0.2, 0.4, 0.6, 0.8, 1.0, 1.2, 1.4, 1.6, 1.8, 2.0
  - **x** vdd = 0.6, 0.7, 0.8, 0.9, 1.0, 1.1, 1.2, 1.3
- ✓ Extra parameters
  - $\mathbf{X}$  lvt dev = 0
  - **✗** gflag\_\_noisedev\_\_lvt\_\_cmos028fdsoi = 0
- Model lvtpfet\_acc (DK1.1\_RF\_mmW)
  - ✓ Input Parameters
    - $\times$  vds\_sat = Vdd V
    - **x** ivt = 70e-9 A



- **x** mc\_runs = 1000
- $\mathbf{X}$  vstep\_ivt = 0.005 V
- **x** vds\_off = vds\_sat V
- $\times$  vgs\_off = 0 V
- **x** temp =  $25 \, ^{\circ}$ C
- $\times$  vgs\_start = -0.5 V
- $\mathbf{x}$  mc\_sens = 0
- $\times$  vds\_lin = 0.05 V
- **x** sbenchlsf\_release = Alpha
- **✗** plashrink\_ivt = 1
- $\star$  vbs = 1 V
- $\mathbf{x}$  ams\_release = 2018.3
- **x** model\_version = 1.3.d
- **x** mc\_nsigma = 3
- $\star$  ithslwi = 10e-9 A
- $\times$  vgs\_stop = vdd V
- **x** shrink\_ivt = 1
- $\times$  vdd = 1 V
- $\mathsf{X}$  dlshrink ivt = 0
- ✓ Sweep Parameters
  - $\star$  vbs = 0.0, 0.2, 0.4, 0.6, 0.8, 1.0, 1.2, 1.4, 1.6, 1.8, 2.0
  - **x** vdd = 0.6, 0.7, 0.8, 0.9, 1.0, 1.1, 1.2, 1.3
- ✓ Extra parameters
  - $\mathsf{X}$  lvt\_dev = 0
  - **✗** gflag\_\_noisedev\_\_lvt\_\_cmos028fdsoi = 0

