



cmos028fdsoi Technology

Resistors models

DK1.2\_RF\_mmW

Comparison with DK1.1\_RF\_mmW model(s)

Please use the bookmark to navigate

Sep 21, 2018

Technology R&D Crolles Site – TDP/TDS/SPICE Modeling

Unauthorized reproduction and communication strictly prohibited

dormieub

**ST Confidential**

## General information on Resistors models

- Maximum supply voltage is - V.
- Validity domain is defined as follows:
  - ✓ Drawn gate length varies from 0.4um to 100um.
  - ✓ Drawn transistor width varies from 0.15um to 10um.
  - ✓ Device temperature varies from -40 °C to 125 °C.

# Output parameters definitions

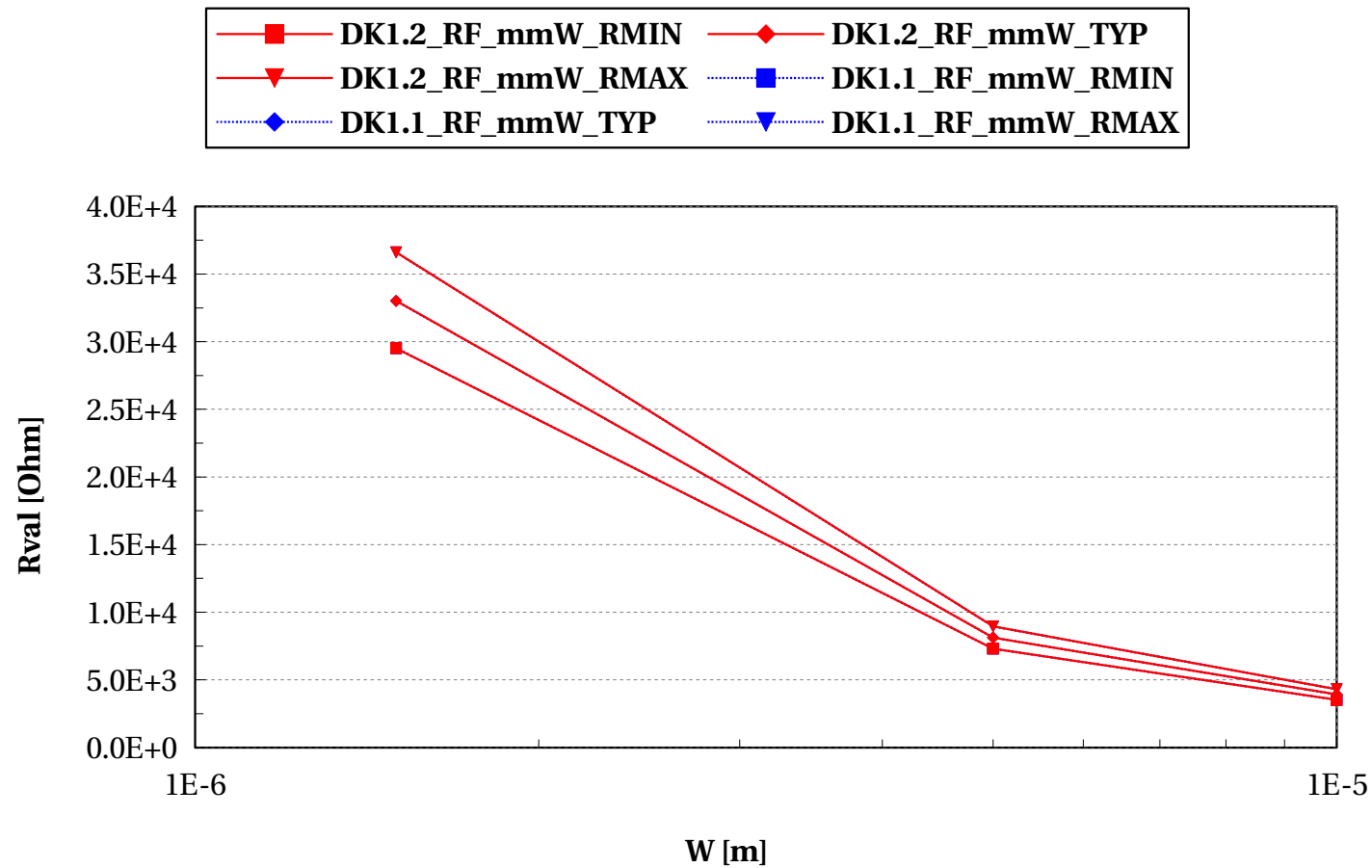
- Model(s): nwres
  - ✓ Rval : Resistance at  $V_{res} = 50e-3V$

# **nwres**

## **Electrical characteristics scaling**

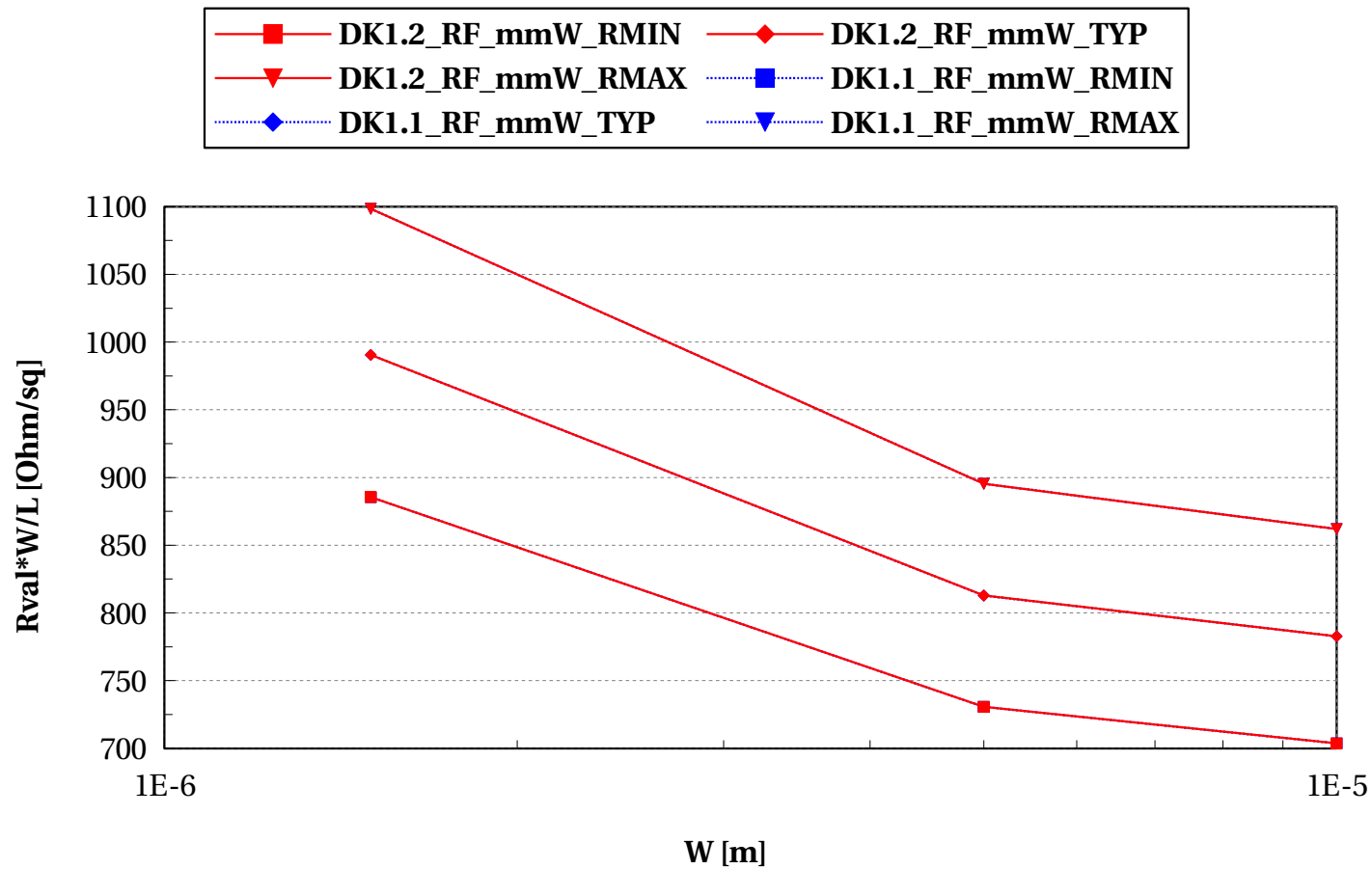
# nwres, Rval [Ohm] vs W [m]

Temp==25 and Vres==50e-3 and l==50e-6 and w>0.18e-6



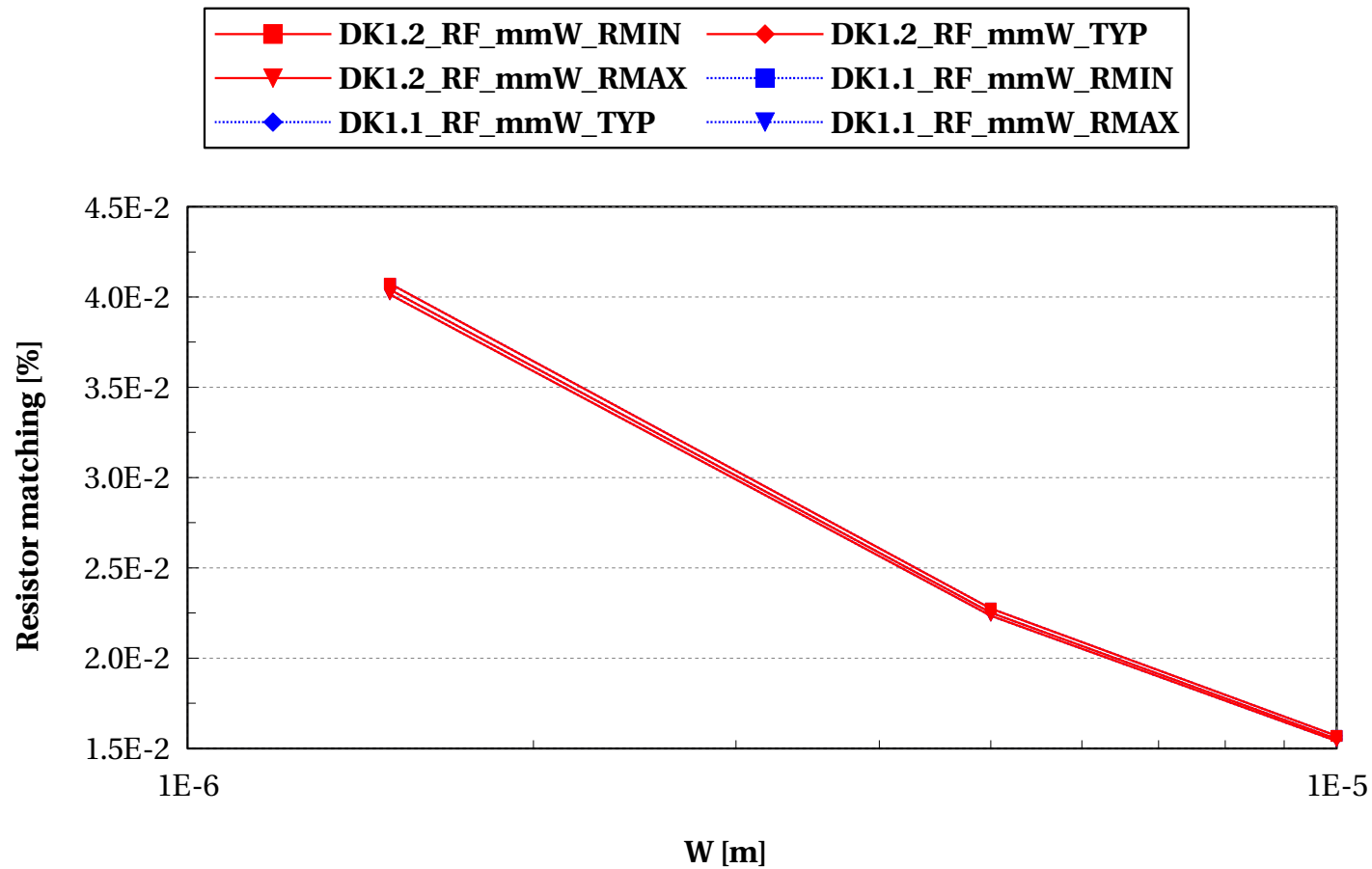
## nwres, Rval\*W/L [Ohm/sq] vs W [m]

Temp==25 and Vres==50e-3 and l==50e-6 and w>0.18e-6



## nwres, Resistor matching [%] vs W [m]

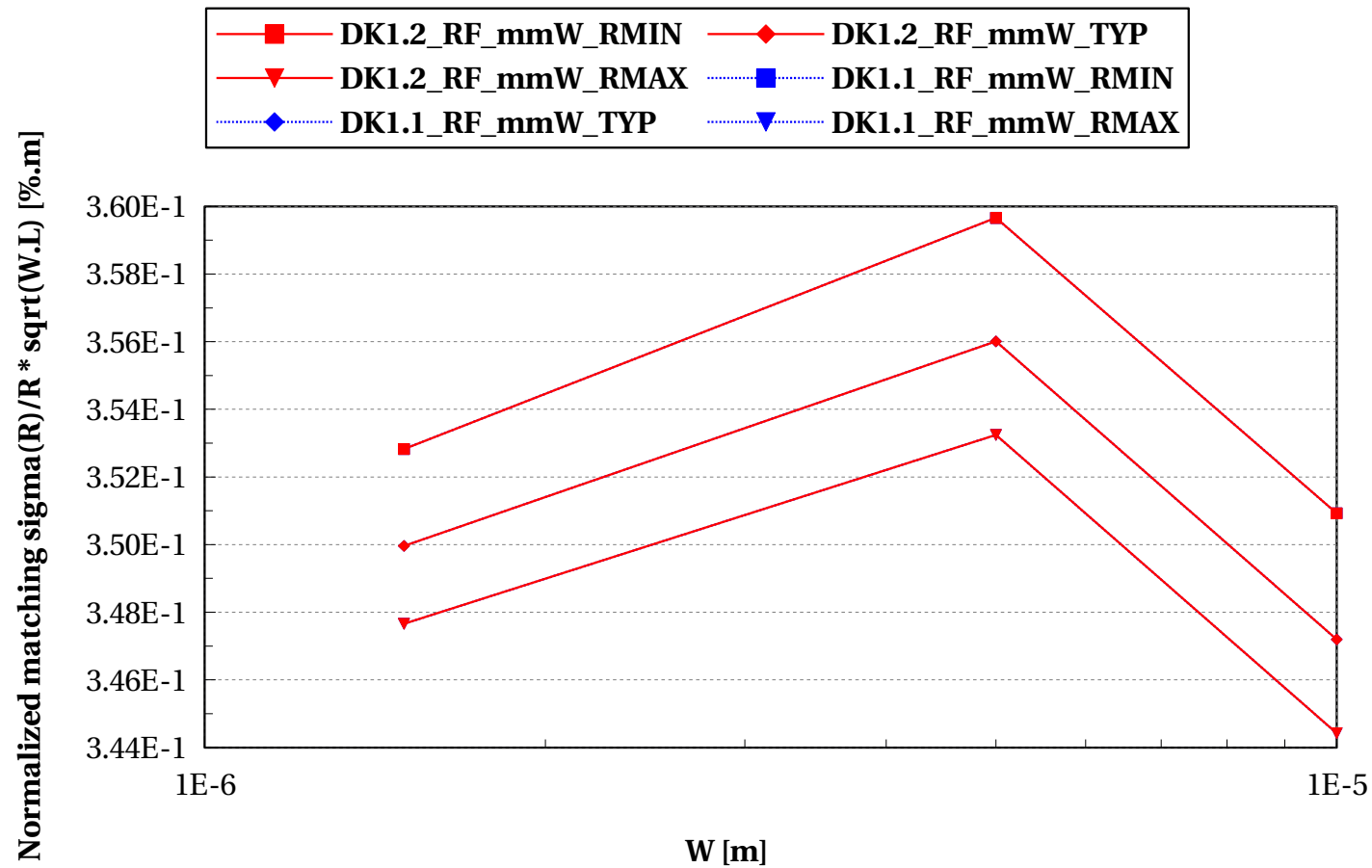
Temp==25 and Vres==50e-3 and I==50e-6 and w>0.18e-6





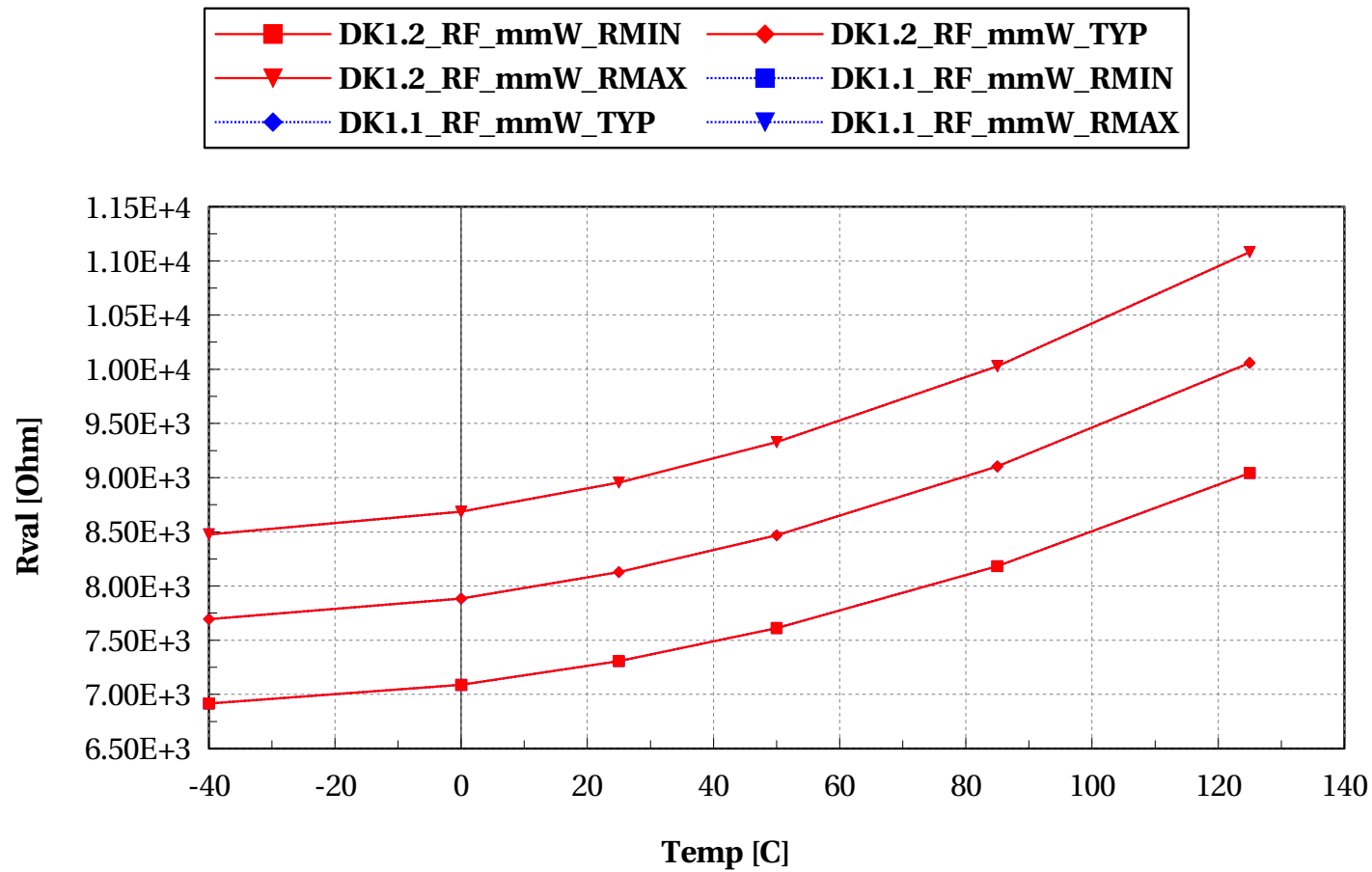
# nwres, Normalized matching sigma(R)/R \* sqrt(W.L) [%.m] vs W [m]

Temp==25 and Vres==50e-3 and l==50e-6 and w>0.18e-6



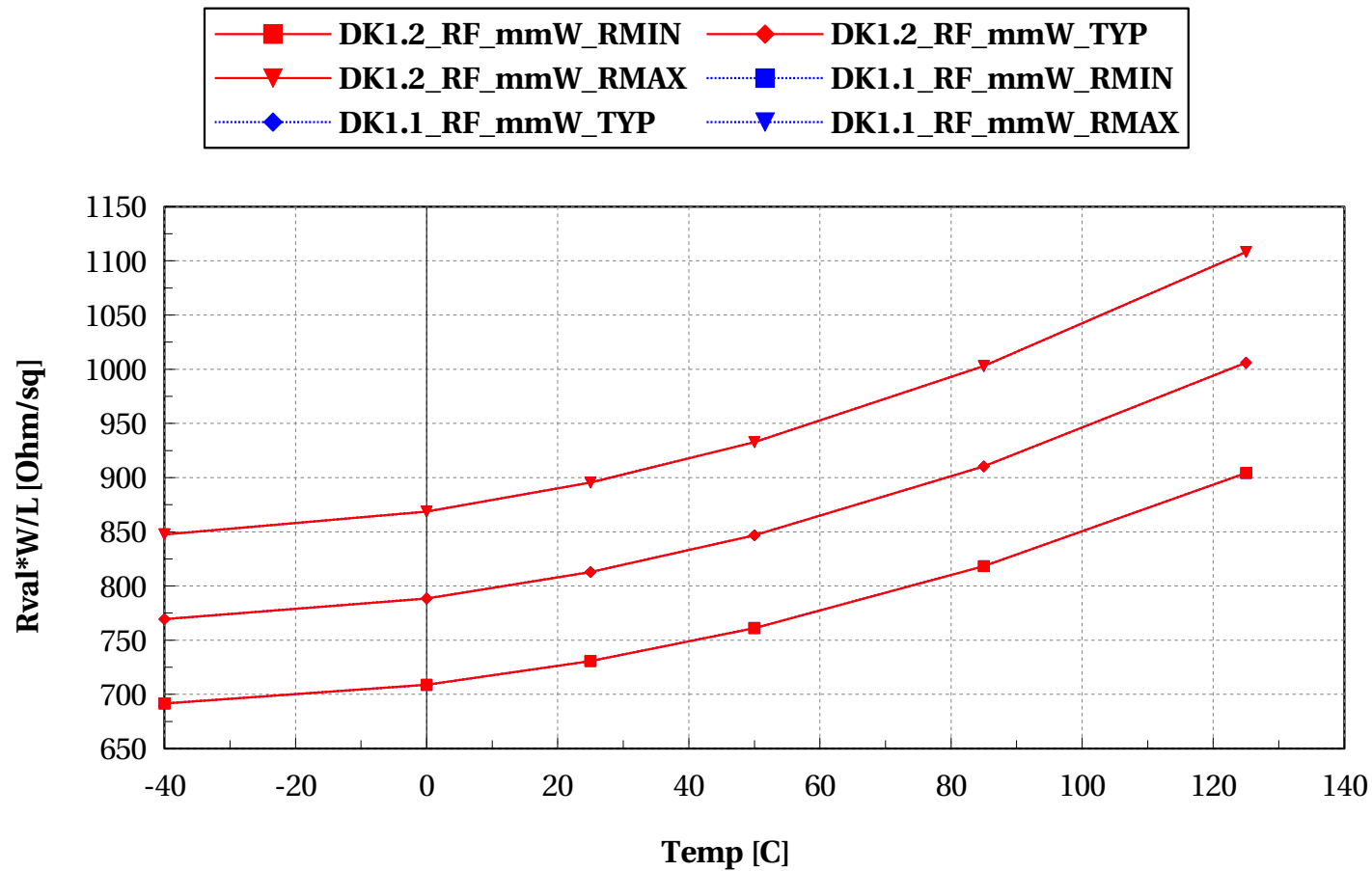
# nwres, Rval [Ohm] vs Temp [C]

w==5e-6 and Vres==50e-3 and l==50e-6



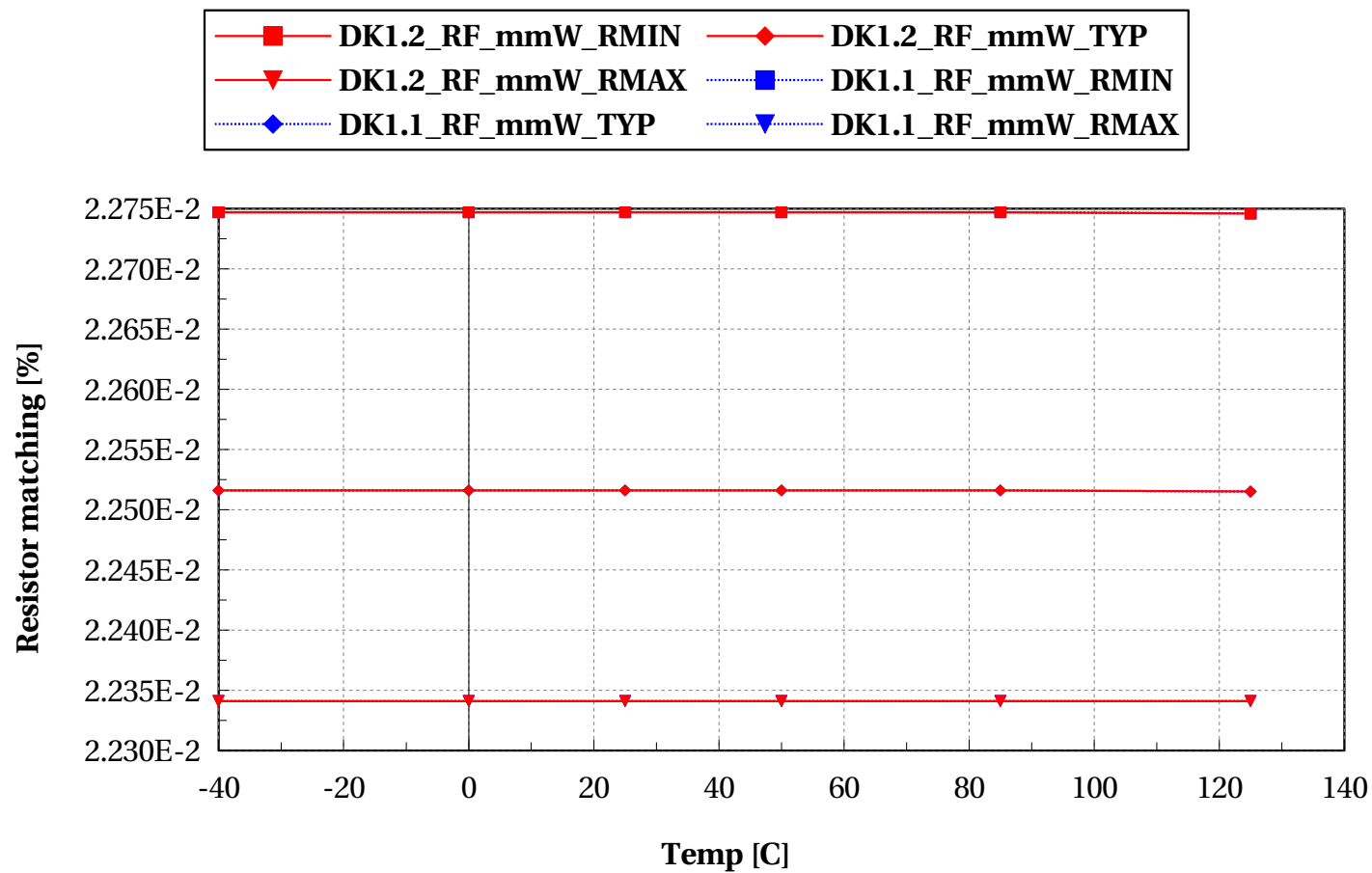
# nwres, Rval\*W/L [Ohm/sq] vs Temp [C]

w==5e-6 and Vres==50e-3 and l==50e-6



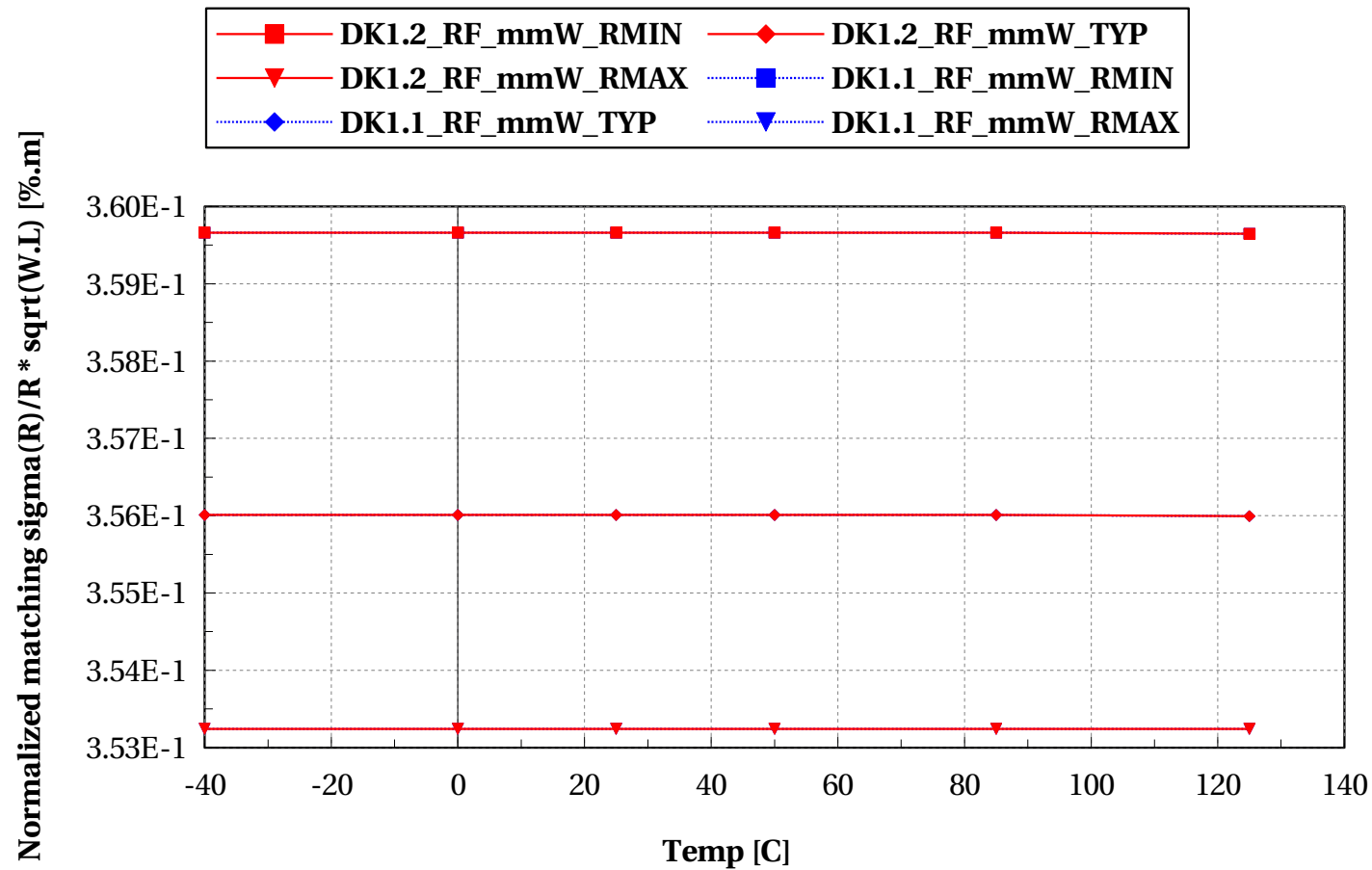
# nwres, Resistor matching [%] vs Temp [C]

$w=5e-6$  and  $V_{res}=50e-3$  and  $l=50e-6$



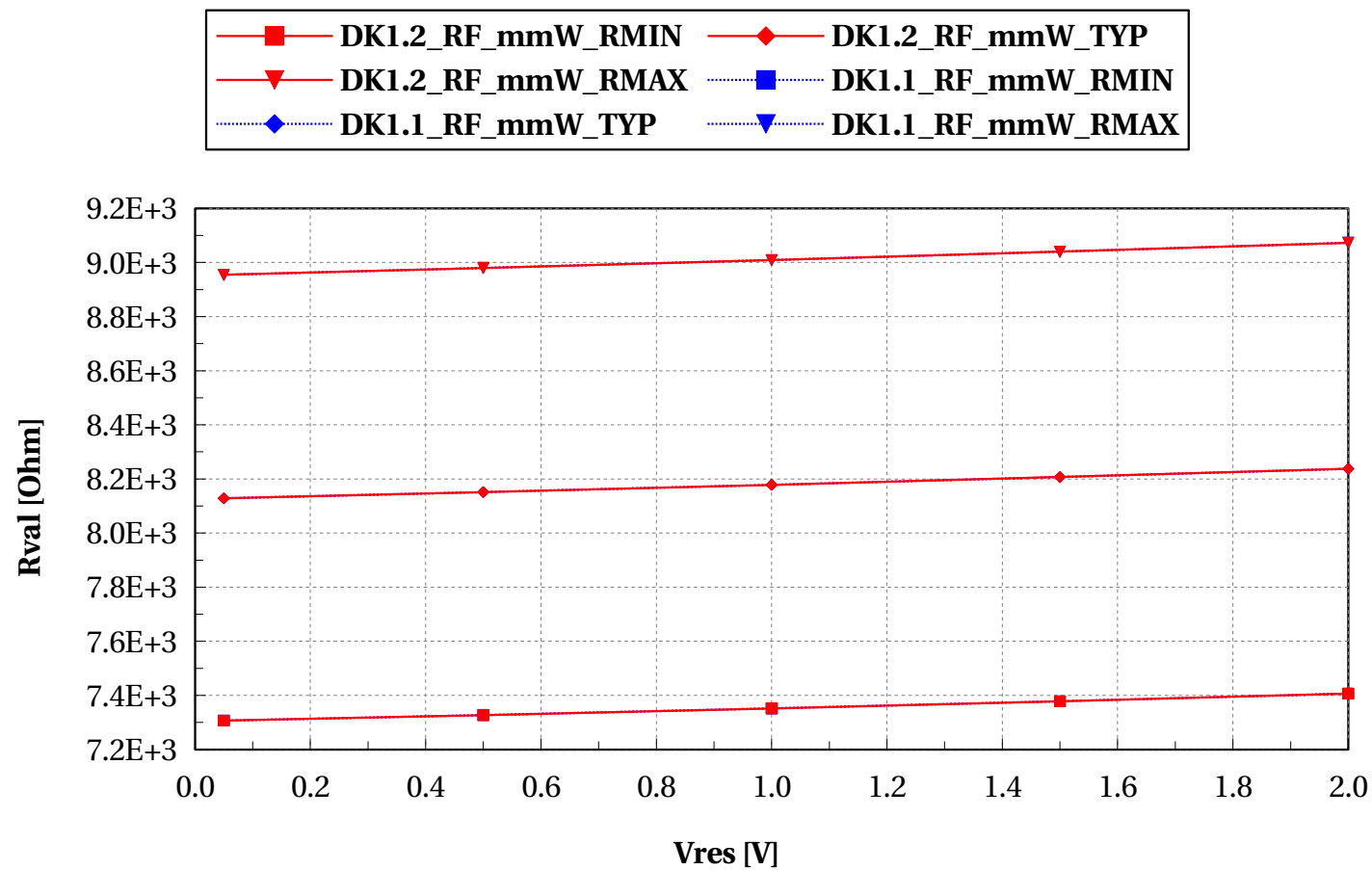
# nwres, Normalized matching sigma(R)/R \* sqrt(W.L) [%.m] vs Temp [C]

w==5e-6 and Vres==50e-3 and l==50e-6



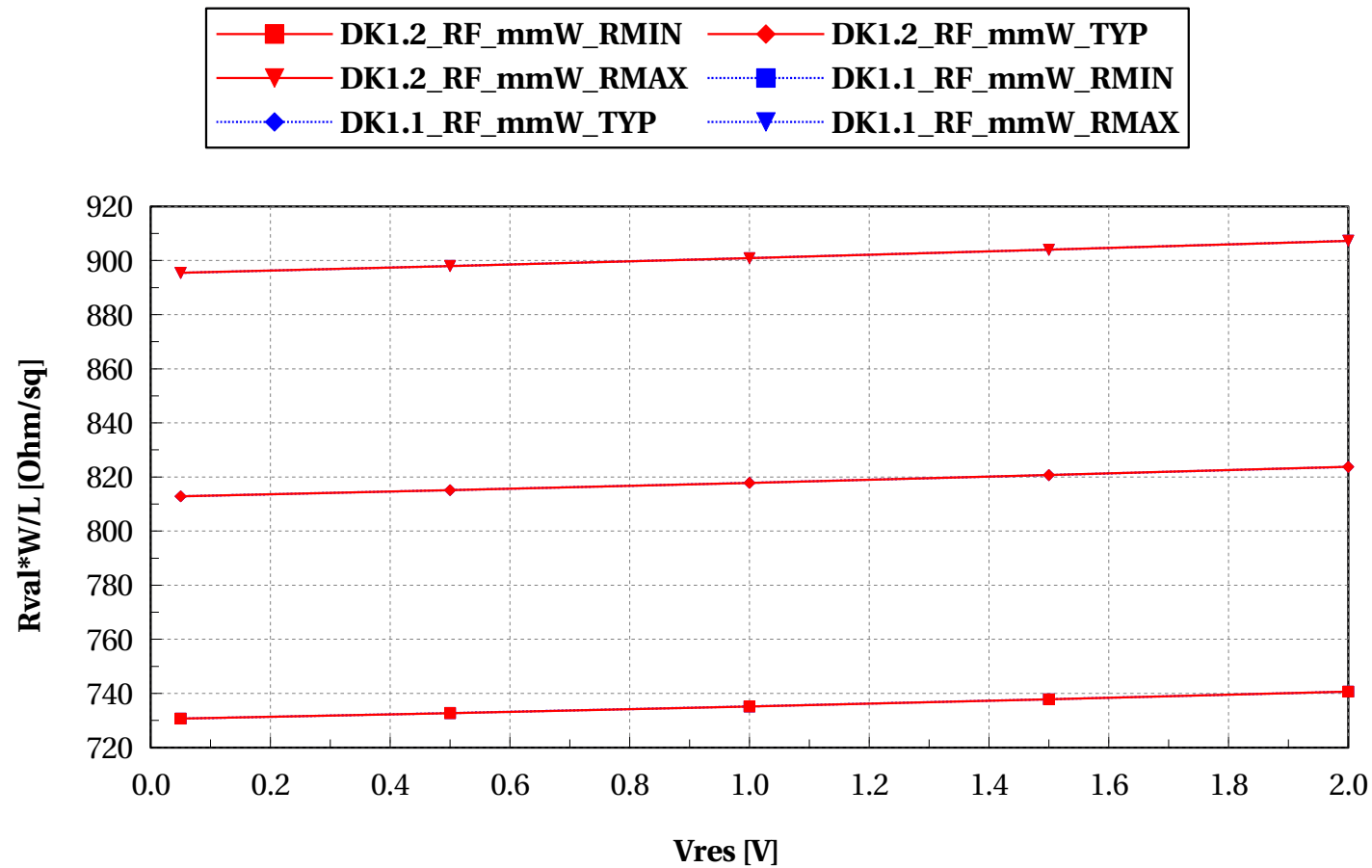
# nwres, Rval [Ohm] vs Vres [V]

w==5e-6 and Temp==25 and l==50e-6



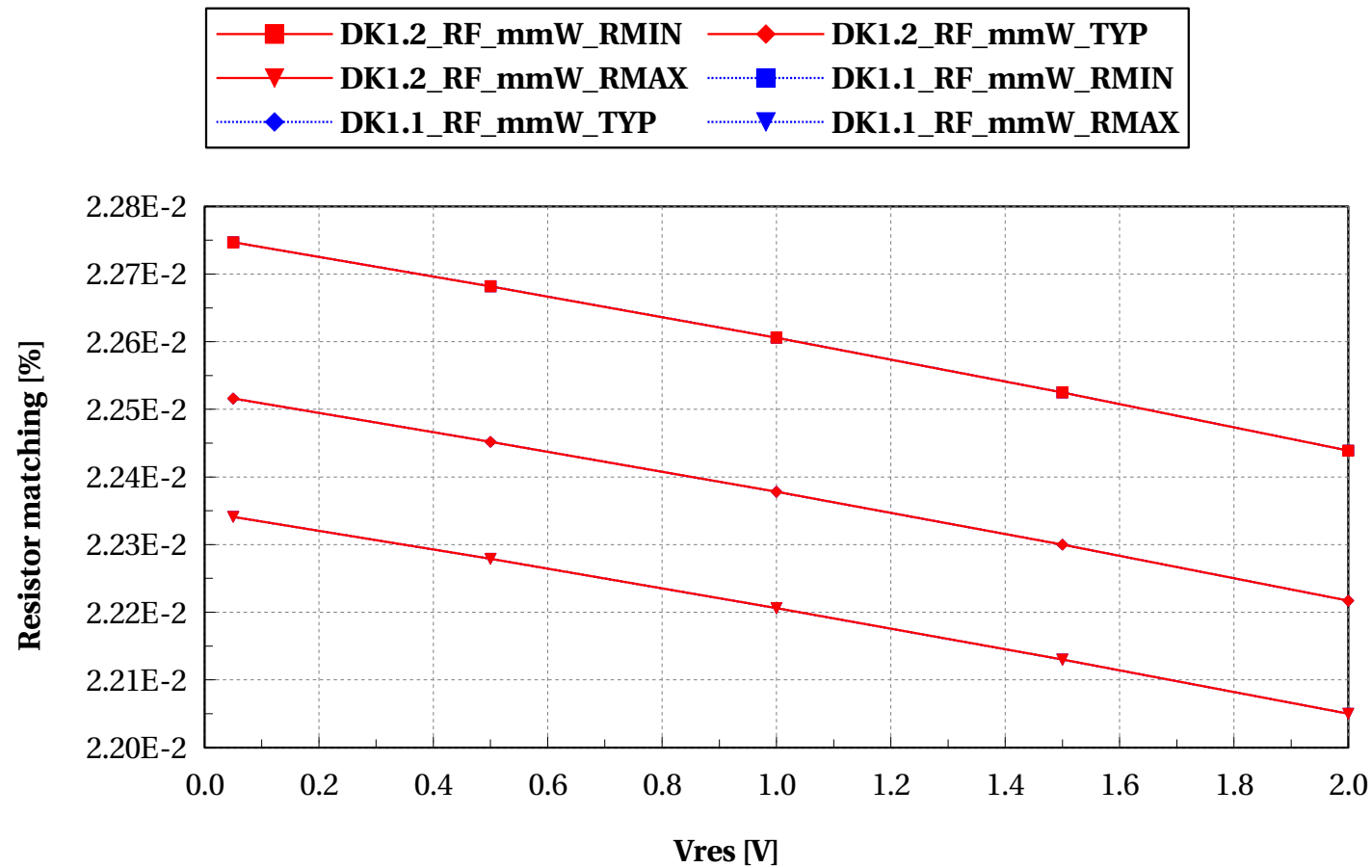
# nwres, Rval\*W/L [Ohm/sq] vs Vres [V]

w==5e-6 and Temp==25 and l==50e-6



# nwres, Resistor matching [%] vs Vres [V]

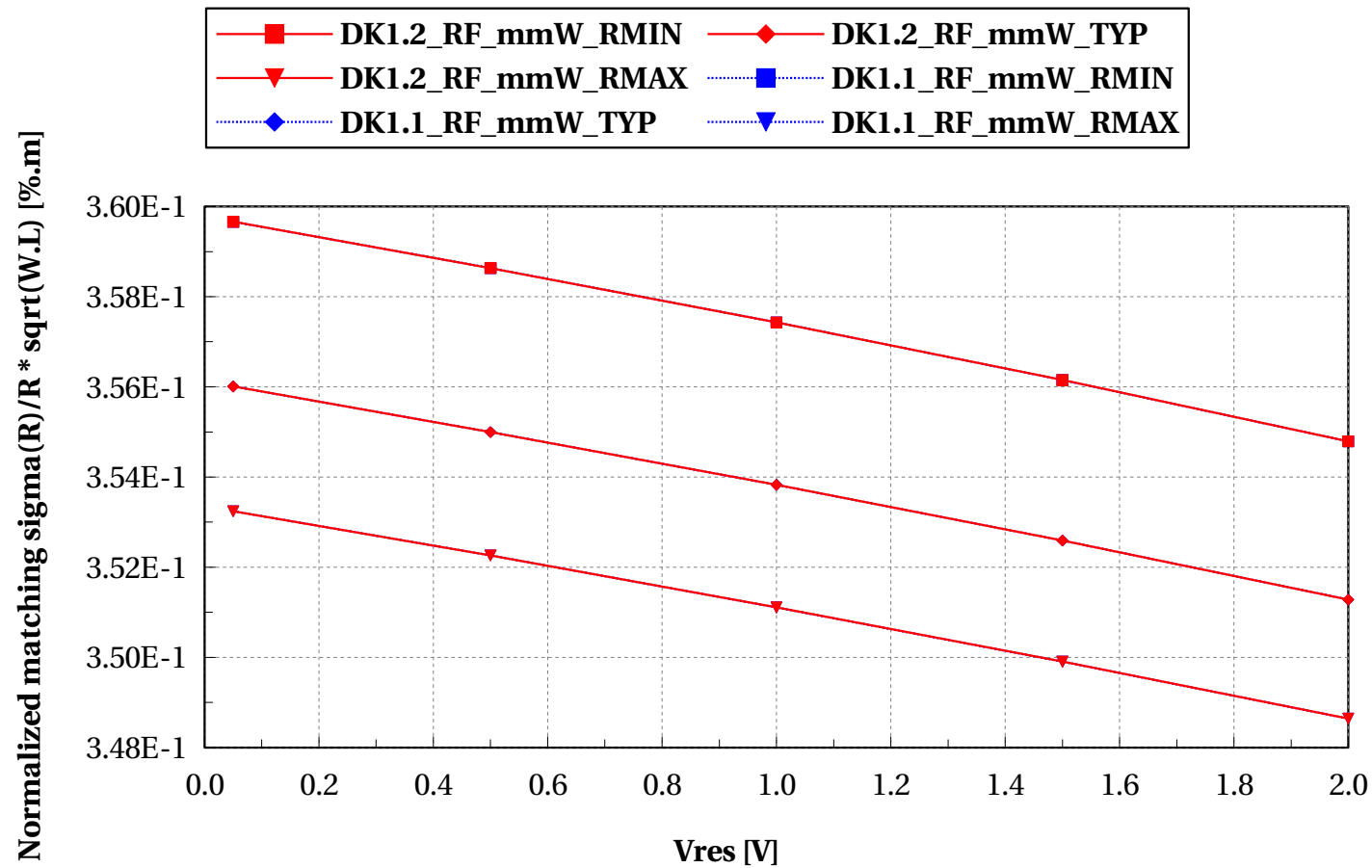
w==5e-6 and Temp==25 and l==50e-6





# nwres, Normalized matching sigma(R)/R \* sqrt(W.L) [%.m] vs Vres [V]

w==5e-6 and Temp==25 and l==50e-6



# Annex

# Conditions of simulations

The simulations were done with SBenchLSF Alpha using Eldo simulator 2018.3.

- Model nwres (DK1.2\_RF\_mmW)
  - ✓ Input Parameters
    - ✗ mc\_runs = 1000
    - ✗ vsub1 = 0
    - ✗ temp = 25 °C
    - ✗ vres = 50e-3 V
    - ✗ mc\_sens = 0
    - ✗ sbenchlsf\_release = Alpha
    - ✗ ams\_release = 2018.3
    - ✗ model\_version = 1.3.a
    - ✗ mc\_nsigma = 3
  - ✓ Sweep Parameters
    - ✗ vres = 0.05, 0.5, 1.0, 1.5, 2.0
    - ✗ temp = 25.0, -40.0, 0.0, 50.0, 85.0, 125.0
  - ✓ Extra parameters
    - ✗ rnwell\_dev = 1

- Model nwres (DK1.1\_RF\_mmW)

- ✓ Input Parameters

- ✗ mc\_runs = 1000

- ✗ vsub1 = 0

- ✗ temp = 25 °C

- ✗ vres = 50e-3 V

- ✗ mc\_sens = 0

- ✗ sbenchlsf\_release = Alpha

- ✗ ams\_release = 2018.3

- ✗ model\_version = 1.3.a

- ✗ mc\_nsigma = 3

- ✓ Sweep Parameters

- ✗ vres = 0.05, 0.5, 1.0, 1.5, 2.0

- ✗ temp = 25.0, -40.0, 0.0, 50.0, 85.0, 125.0

- ✓ Extra parameters

- ✗ rnwell\_dev = 1