

## **Circuit Theory and Electronics Fundamentals**

Department of Electrical and Computer Engineering, Técnico, University of Lisbon  
Mestrado em Engenharia Aeroespacial

Laboratory 2 Report

Group 7

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# 1 Introduction

The objective of this laboratory assignment is to study a circuit containing a dependent voltage source ( $V_c$ ) and independent ( $I_d$ ,  $V_a$ ) current and voltage sources, connected to resistors ( $R_1$  to  $R_7$ ) and to a capacitor ( $C$ ). The circuit and its organization can be seen in Figure ??.

In Section ??, a theoretical analysis of the circuit is presented. In Section ??, the circuit is analysed by simulation, and the results are compared to the theoretical results obtained in Section ??. The conclusions of this study are outlined in Section ??.

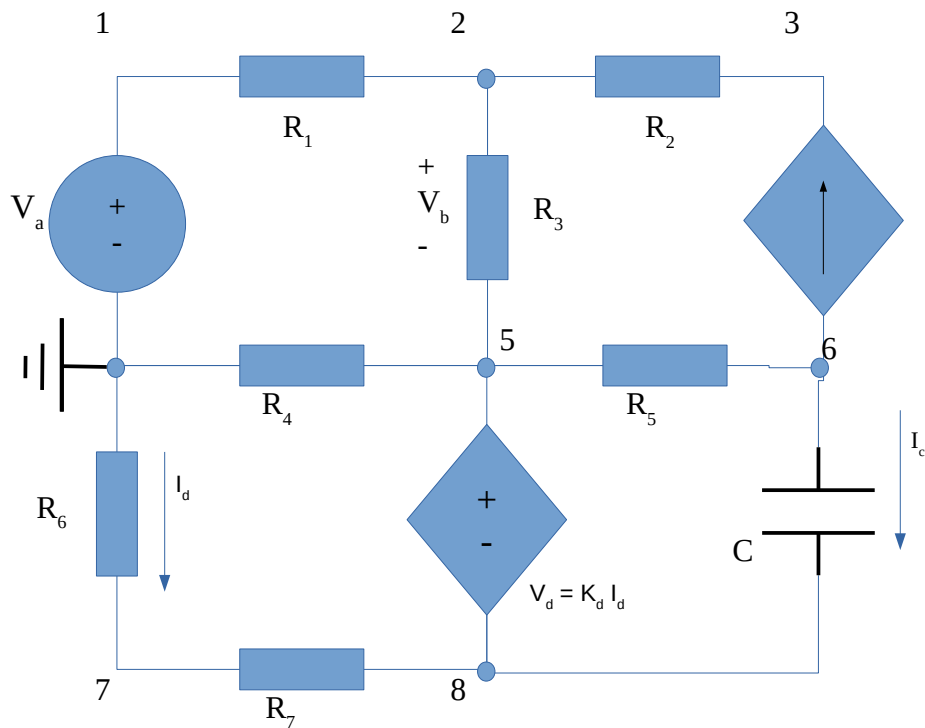


Figure 1: Circuit topography