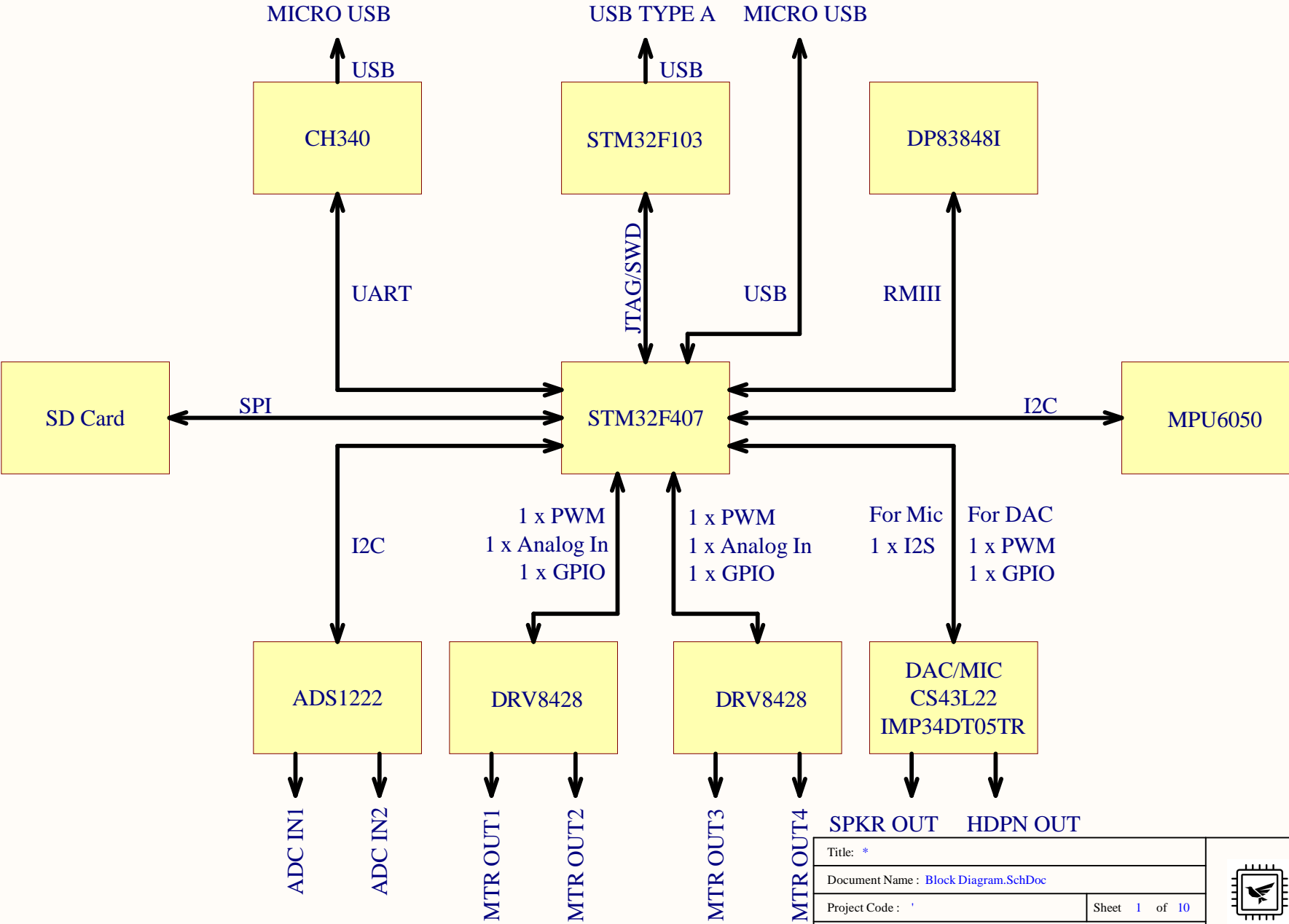


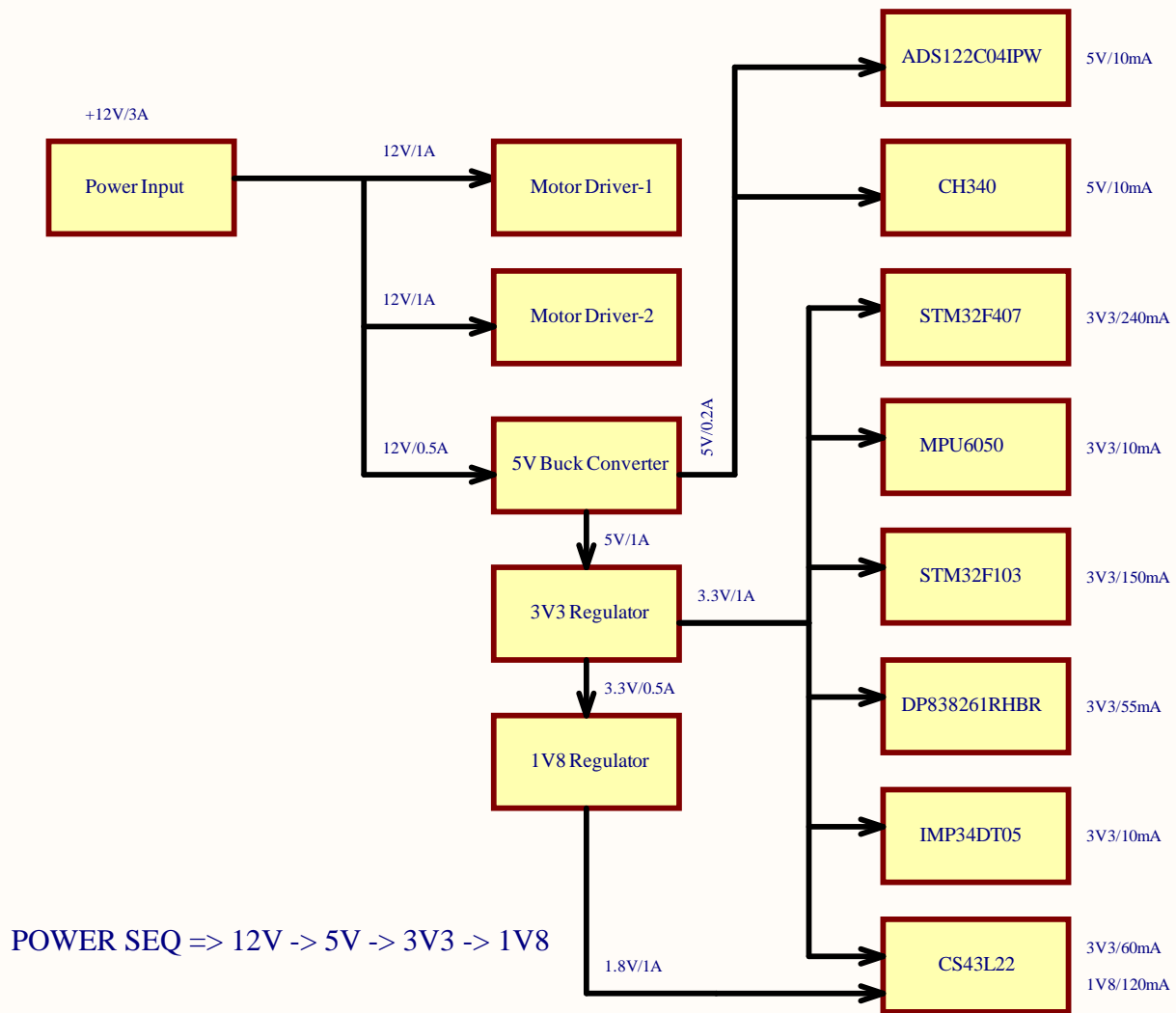
BLOCK DIAGRAM



Title: *	
Document Name : Block Diagram.SchDoc	
Project Code : '	Sheet 1 of 10
Author: *	



# POWER BUDGET AND SEQUENCING



Title: \*

Document Name : Power Budget.SchDoc

Project Code : '

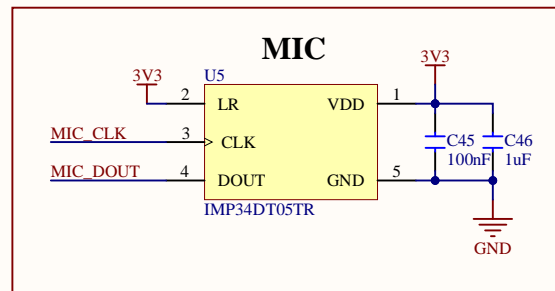
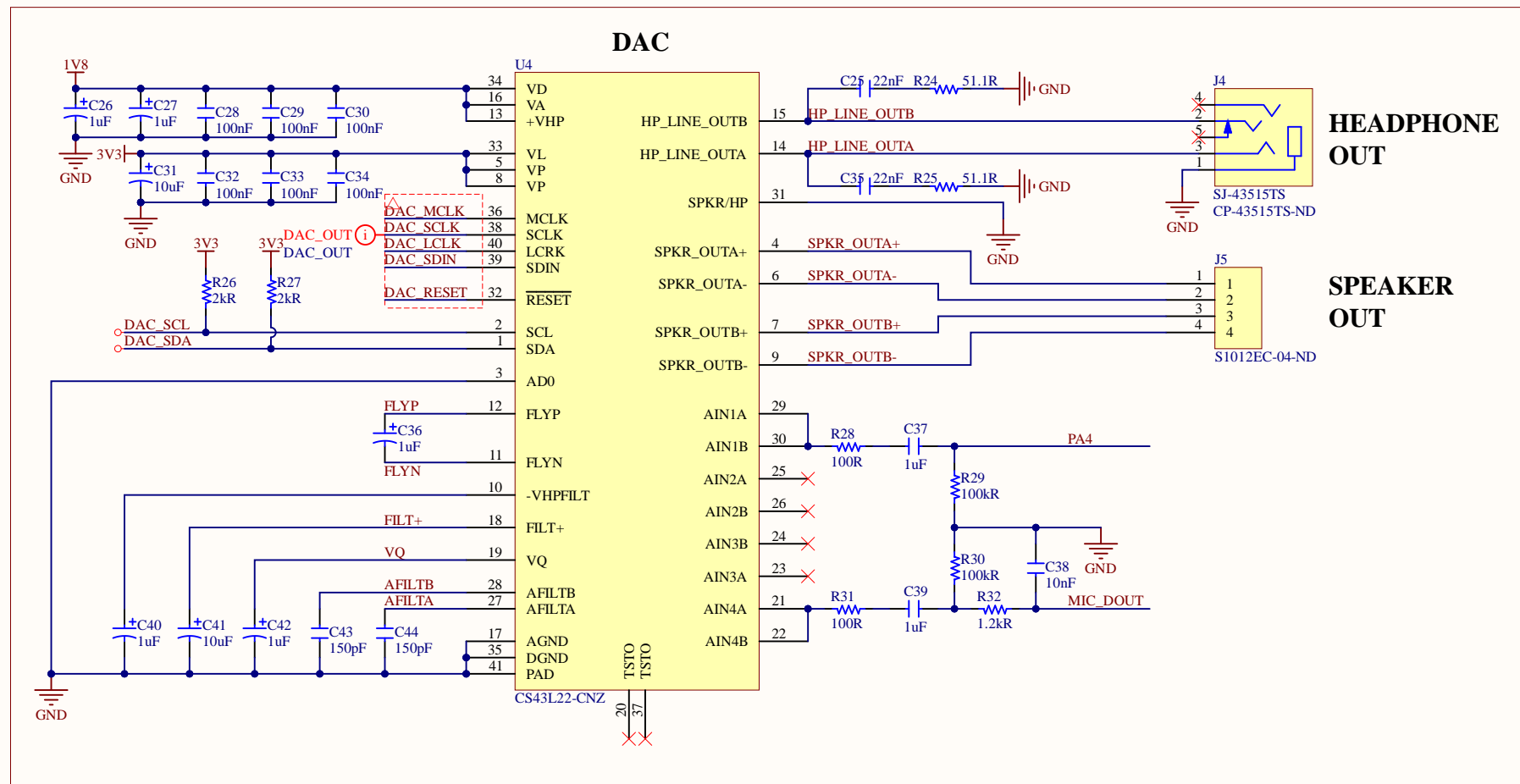
Author: \*

Sheet 2 of 10






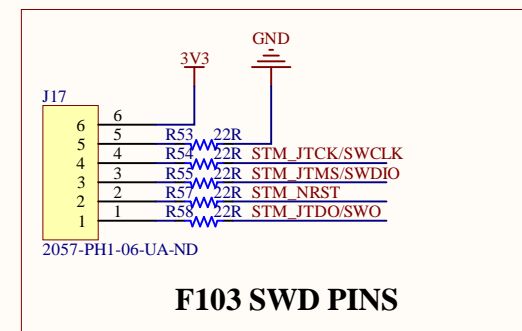
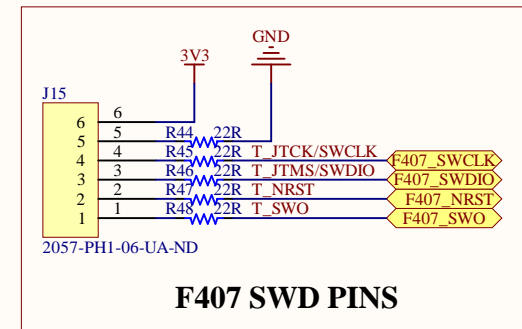
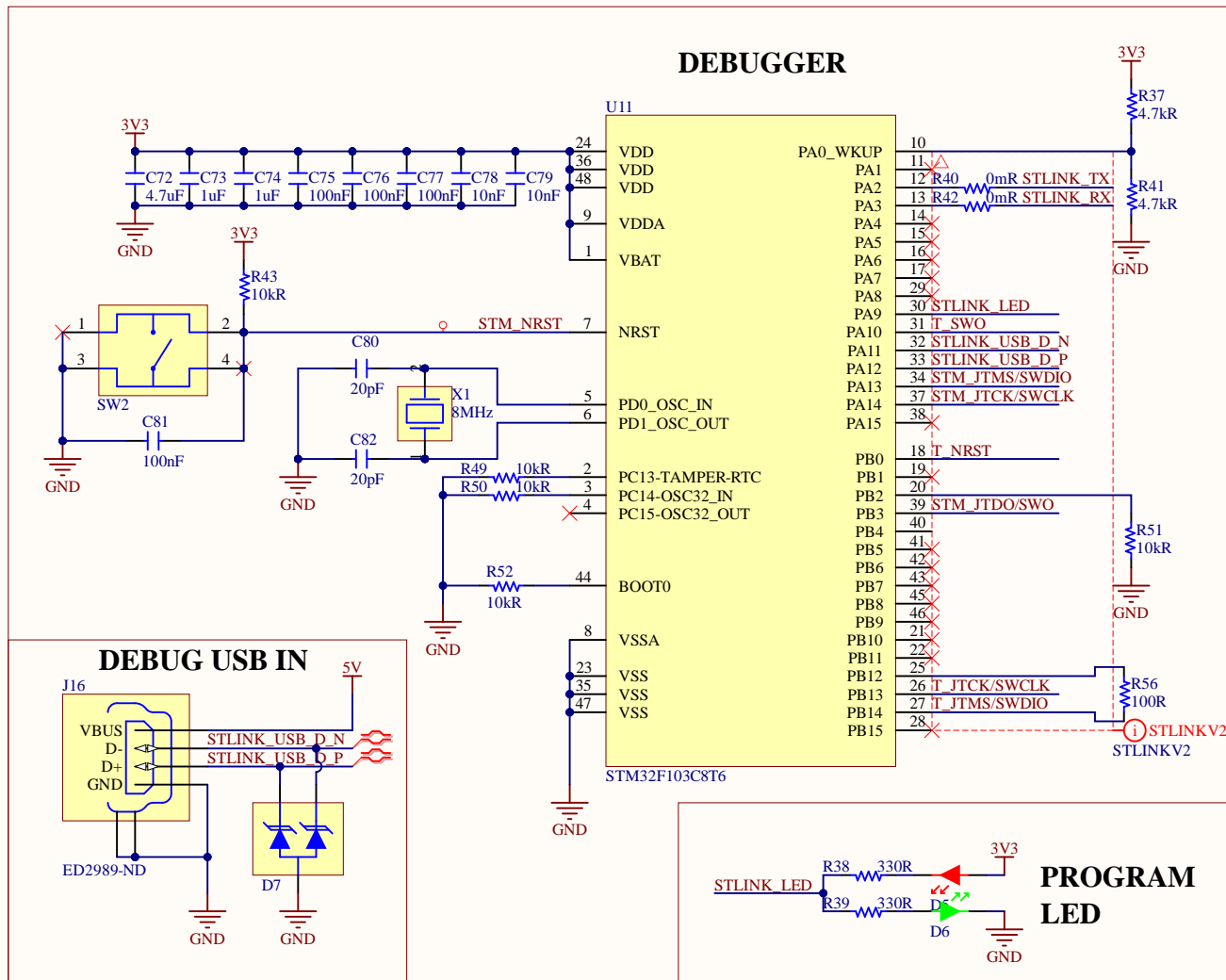




Title: *	
Document Name : DAC and MIC.SchDoc	
Project Code : '	Sheet 5 of 10
Author: *	

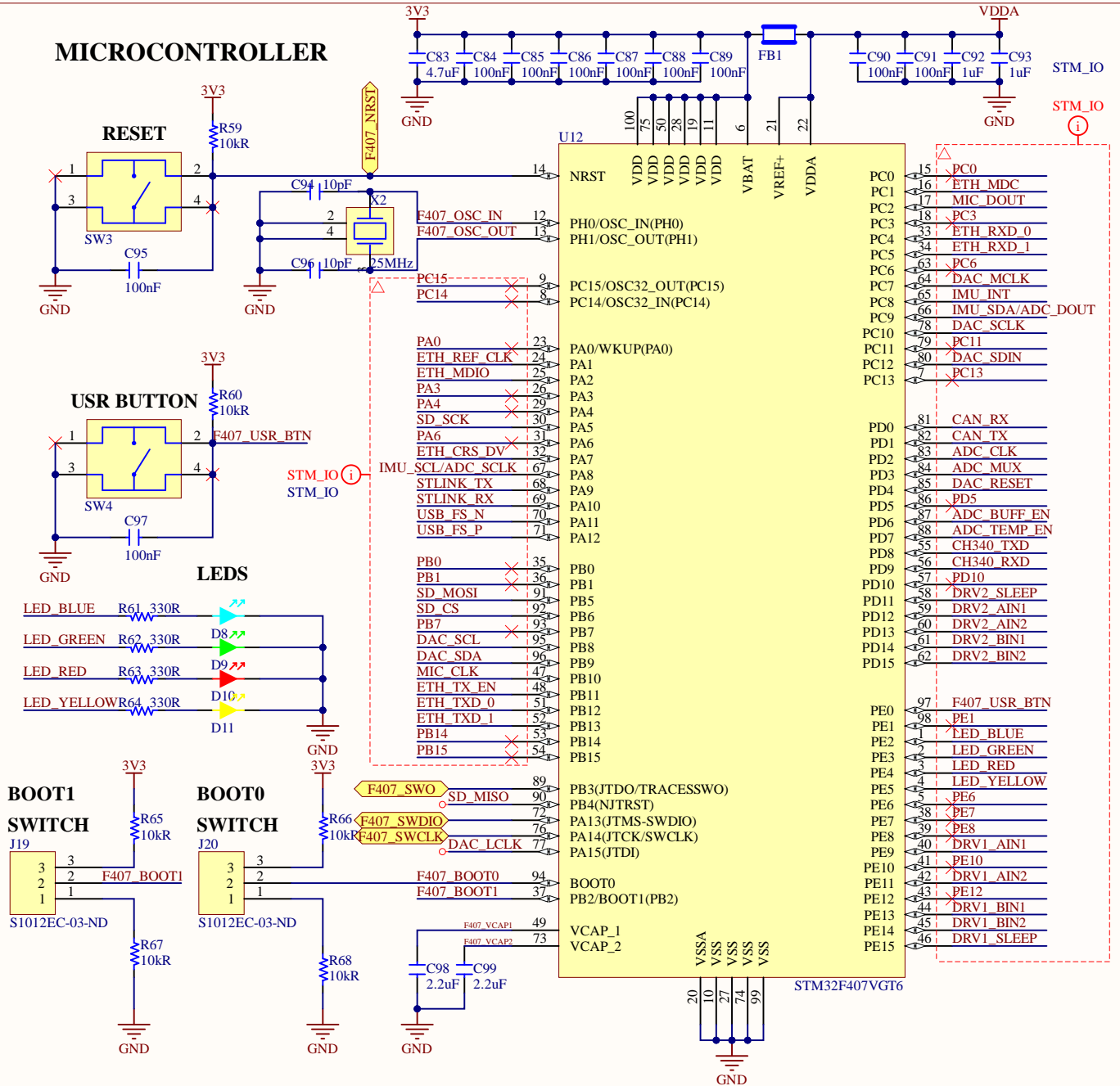

**GÜVERCİN  
ELECTRONICS**



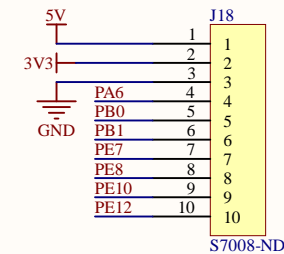


Title		
Size	Number	Revision
A4		
Date:	7.13.2022	Sheet of
File:	C:\Users\...\STM32F103 MCU.SchDoc	Drawn By:

## MICROCONTROLLER

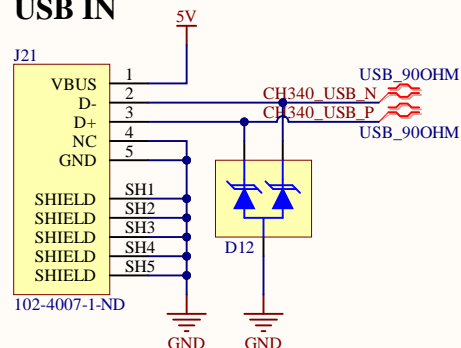


## MICROCONTROLLER GPIO

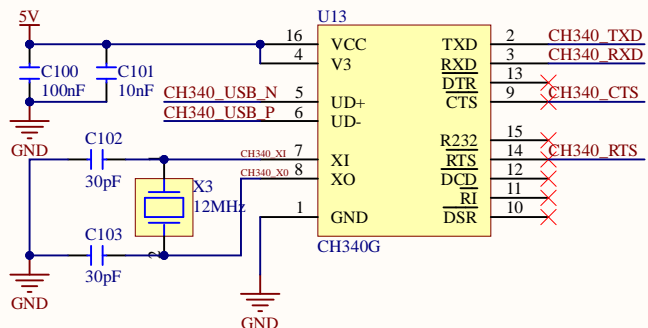


Title		
Size	Number	Revision
A4		
Date:	7.13.2022	Sheet of
File:	C:\Users\...\STM32F407 MCU.SchDoc	Drawn By:

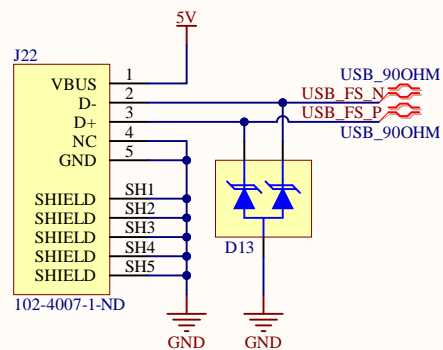
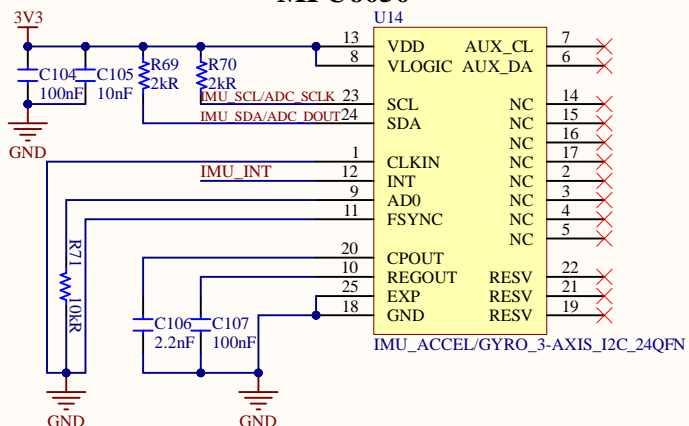




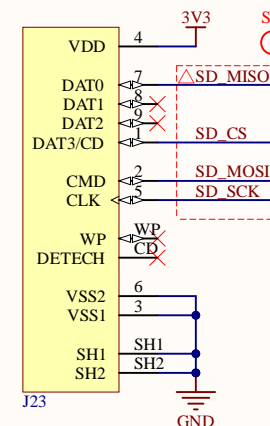
## USB/UART CONVERTER



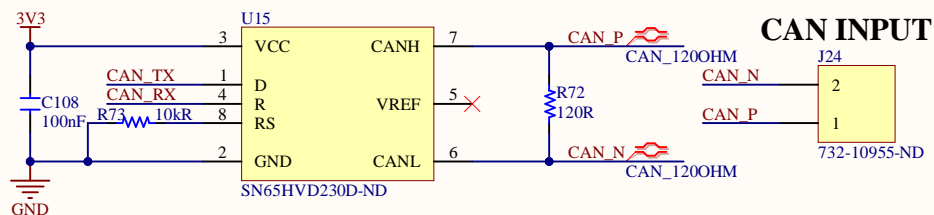
## USB VIRTUAL COM PORT

**MPU6050**

## SD CARD CONNECTOR



## CAN TRANCIEVER

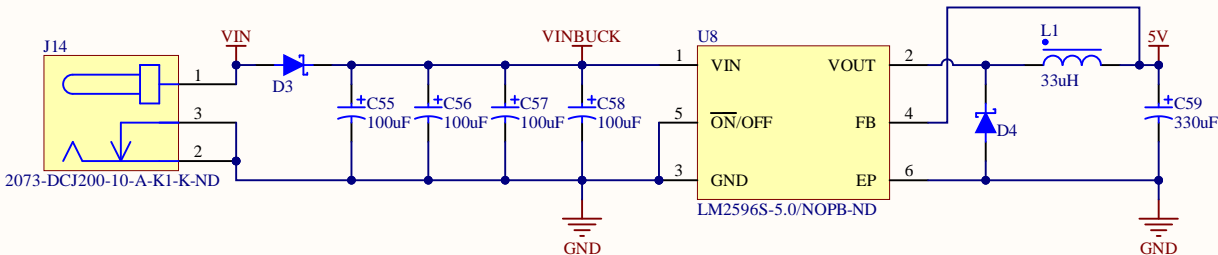


## CAN INPUT

Title		
Size A4	Number	Revision
Date:	7.13.2022	Sheet of
File:	C:\Users\...\IMU-SD Card-CAN-CH340.S	Drawn By:

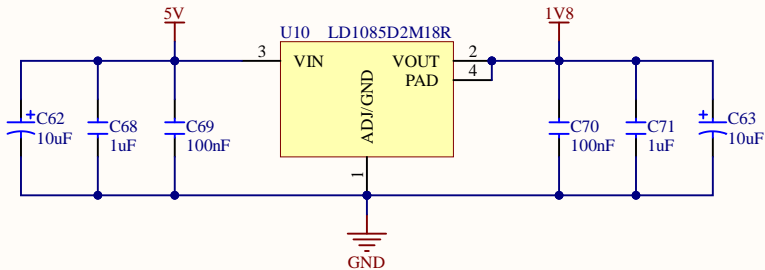
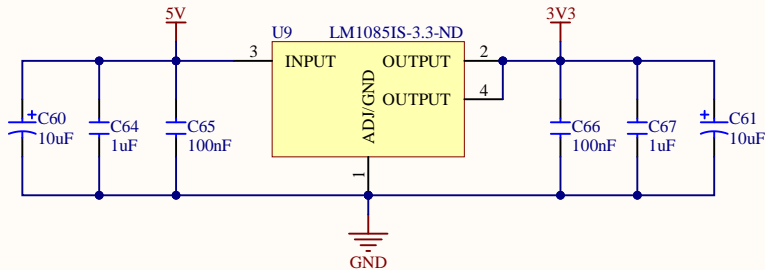
POWER INPUT

5V BUCK CONVERTER



3V3 REGULATOR

1V8 REGULATOR



Title		
Size	Number	Revision
A4		
Date:	7.13.2022	Sheet of
File:	C:\Users\...\Power Input and Buck Converter	Dr. Sch. Bgc

