Open Hardware DSP Platform — www.ohdsp.org
DAC-AK4458 with Passive Output Filters
Revision 1.0

I2S/TDM Clocks
Data Input 1

Ext
Clock

+5V 0V   0V +9V

I2S/TDM
data input 2

I2S/TDM
data input 3

I2S/TDM
data input 4

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SPI Control

Audio
outputs

Audio
outputs

External
reset

DZF/
SMUTE
CAD1/
DCHAIN
TDM0
TDM1
DIF
PS/CAD0_SPI
I2C

Open = High
Short = Low
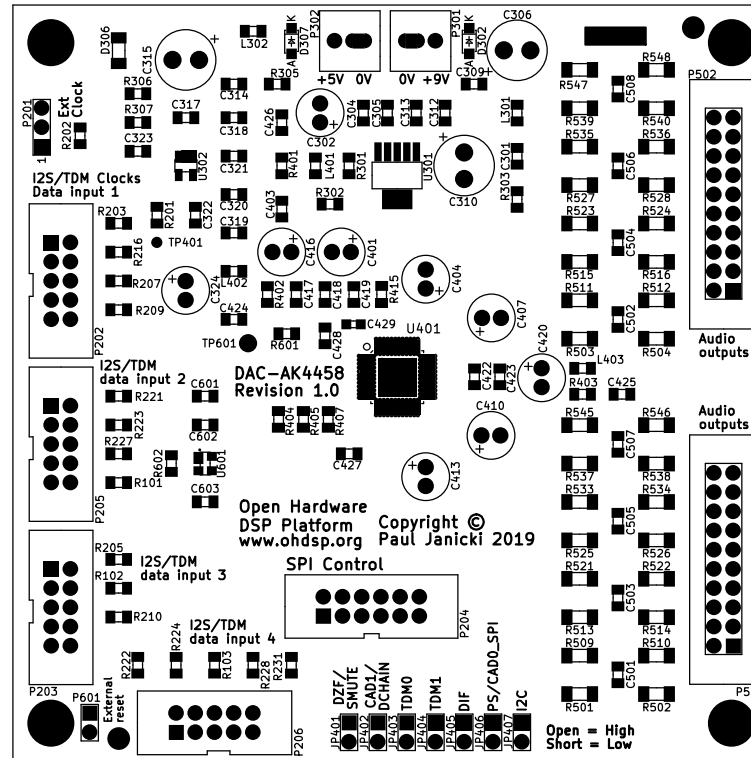
Notes:

Dirty PCB 4 layer stackup:
1oz copper
0.2mm FR4
0.5oz copper
1.2mm FR4
0.5oz copper
0.2mm FR4
1oz copper

50ohm traces with ground plane:
Trace 0.35mm, Clearance 0.2mm

75ohm traces with ground plane:
Trace 0.18mm, Clearance 0.5mm

89ohm traces with ground plane:
Trace 0.135 to 0.155mm Clearance 0.5mm to 1mm (to other traces)

Use 26AWG ribbon cables to match PCB impedances (approx 89ohm)

Sheet:
File: DAC-AK4458-1.0.kicad_pcb

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KiCad E.D.A. kicad (5.1.0)-1   Id: 1/1