





Connectors 12S Inputs See ADAU1452 datasheet for details Master SPI Port Inputs are highly flexible P406 CONN_4×1 MCLK DMCLK R414 P405 CONN_5x1 R413 -**△**M_SPI_MISO R402 **-D**ADC_LRCLK0 -**D**M_SPI_SCLK -**D**M_SPI_MOSI **⊸**ADC_BCLK0 P407 CONN_4×1 R415 22 **⊸**ADC_SDATA0 **−D**M_SPI_SS -DM RST →DM_MUTE P402 CONN_5x1 GNDD R405 ADC_SDATA1 GNDD 12S Outputs See ADAU1452 datasheet for details Outputs are highly flexible R406 MCLK —DDAC_LRCLKO R408 P403 CONN_5x1 **→**DAC_BCLK0 R409 —**D**DAC_SDATA0 P401 CONN_5×1

R410 O ______ODAC_SDATA1

DAC_SDATA2

→DAC_SDATA3

GNDD

P404 PIN 1

P408

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