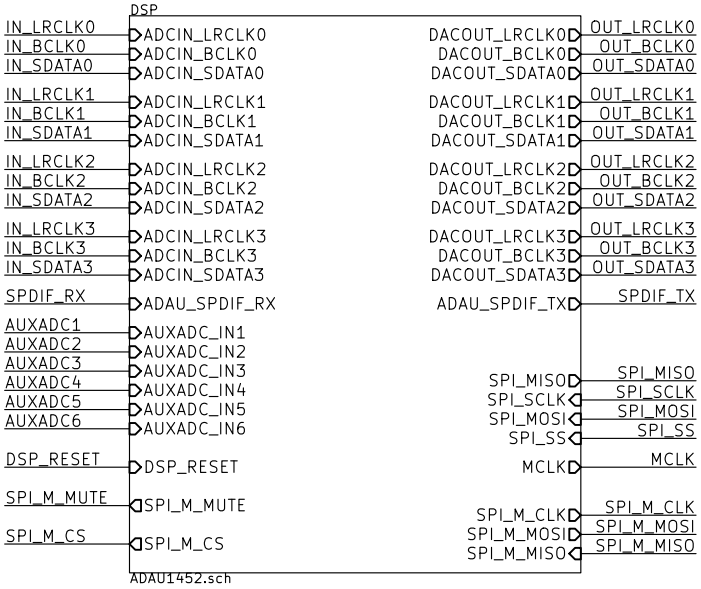
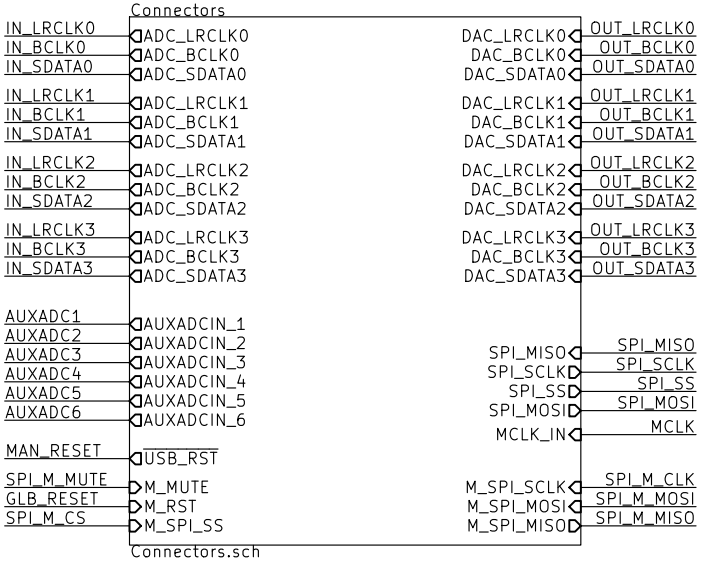
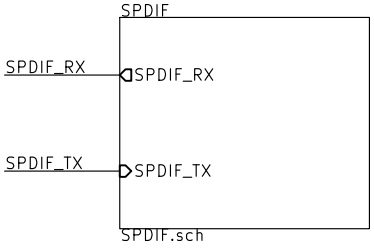
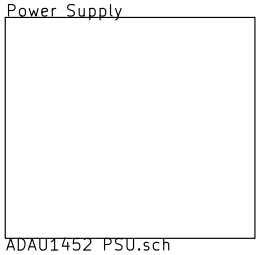
Open Hardware DSP Platform – www.ohdsp.org
uDSP 1.0

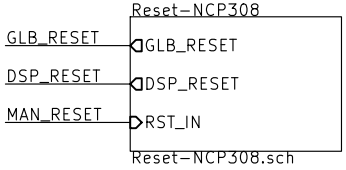
A 5cm x 5cm DSP board using Analog Device SigmaDSP devices
Supports ADAU1466, ADAU1462, ADAU1452, ADAU1451, ADAU1450
Selfboot and external control supported
All I/O routed out











Notes:

All digital I/O is 3V3. Use outside this voltage can cause damage.

See bill of materials for detailed parts information.

Sheet: /
File: uDSP.sch

Title: uDSP

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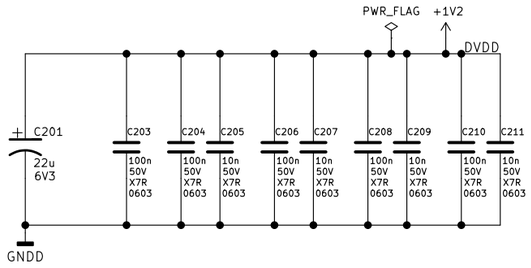
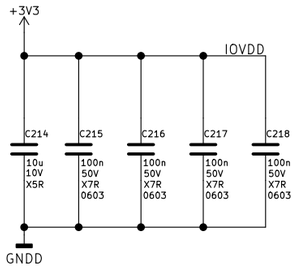
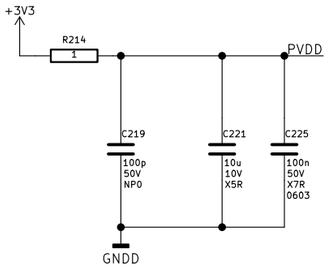
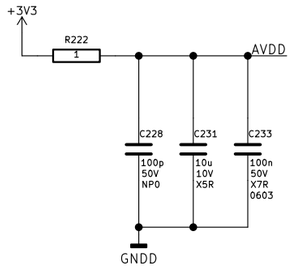
DSP (ADAU1466,ADAU1462,ADAU1452,ADAU1451,ADAU1450)





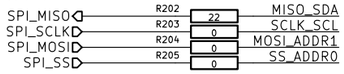


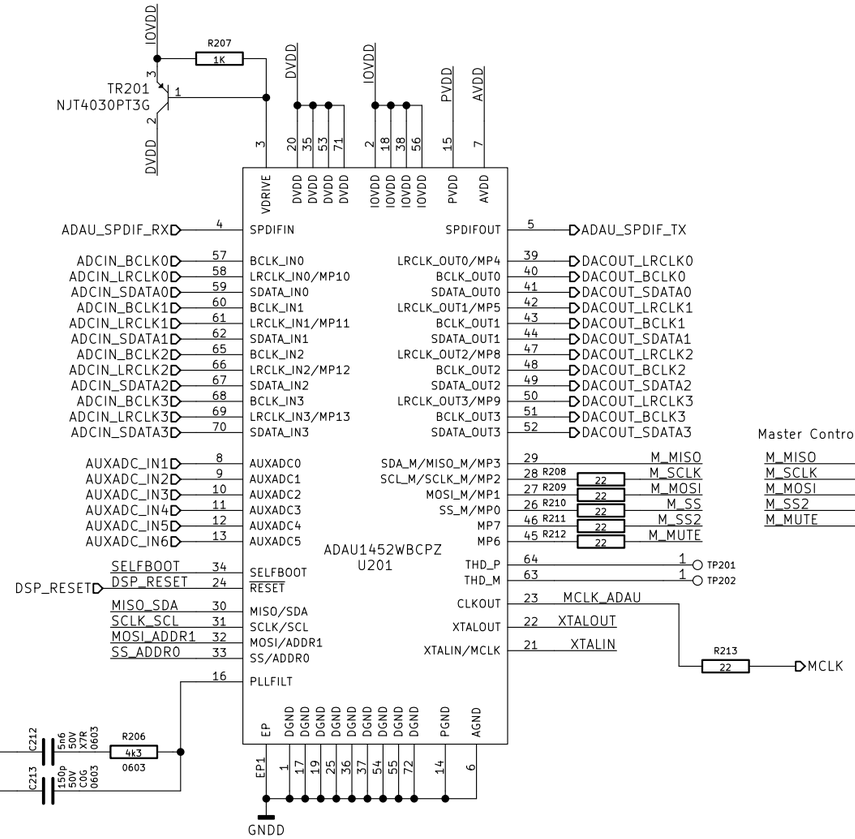


Slave control port

Used for external control and programming.

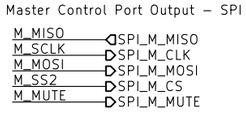
SPI





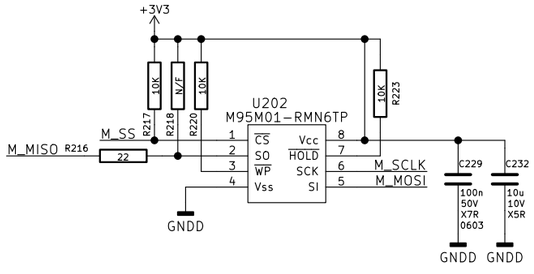
U201 can be ADAU1466, ADAU1462, ADUA1452, ADAU1451, or ADAU1450.
SPDIF is not support on the ADAU1450.

Master Control Port Output – SPI



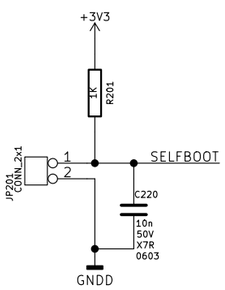
SPI EEPROM

Connects to master serial port
Fit an EEPROM such as the 24AA1024 to allow selfboot operation



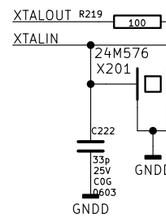
Selfboot Jumper

Leave JP201 open for selfboot.
Short JP201 to disable self boot and program the EEPROM.
Short JP201 to use SPI external control instead of selfboot.

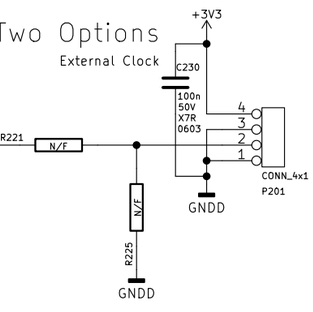


Master Clock – Two Options

Standard Crystal



External Clock



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File: ADAU1452.sch

Title: uDSP

Size: A3	Date: 2019-03-11	**Rev: 1.0**
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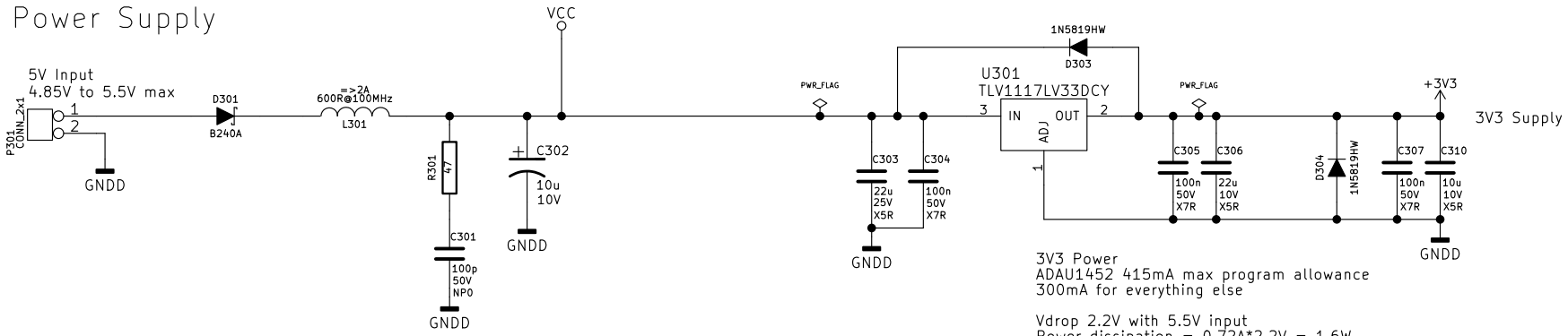
Power Supply



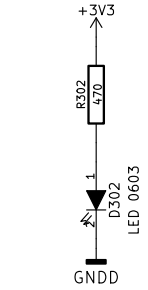
3V3 Power
ADAU1452 415mA max program allowance
300mA for everything else

Vdrop 2.2V with 5.5V input
Power dissipation = 0.72A*2.2V = 1.6W
Temperature rise = 63C/W * 1.6W = 100C

Vdrop 1.7V with 5V input
Power dissipation = 0.72A*1.7V = 1.2W
Temperature rise = 63C/W * 1.6W = 75C

Excluding PCB copper heatsink effect

Power LED



Mounting Holes and Fiducials

MNT301 MNT303 FID301 FID303

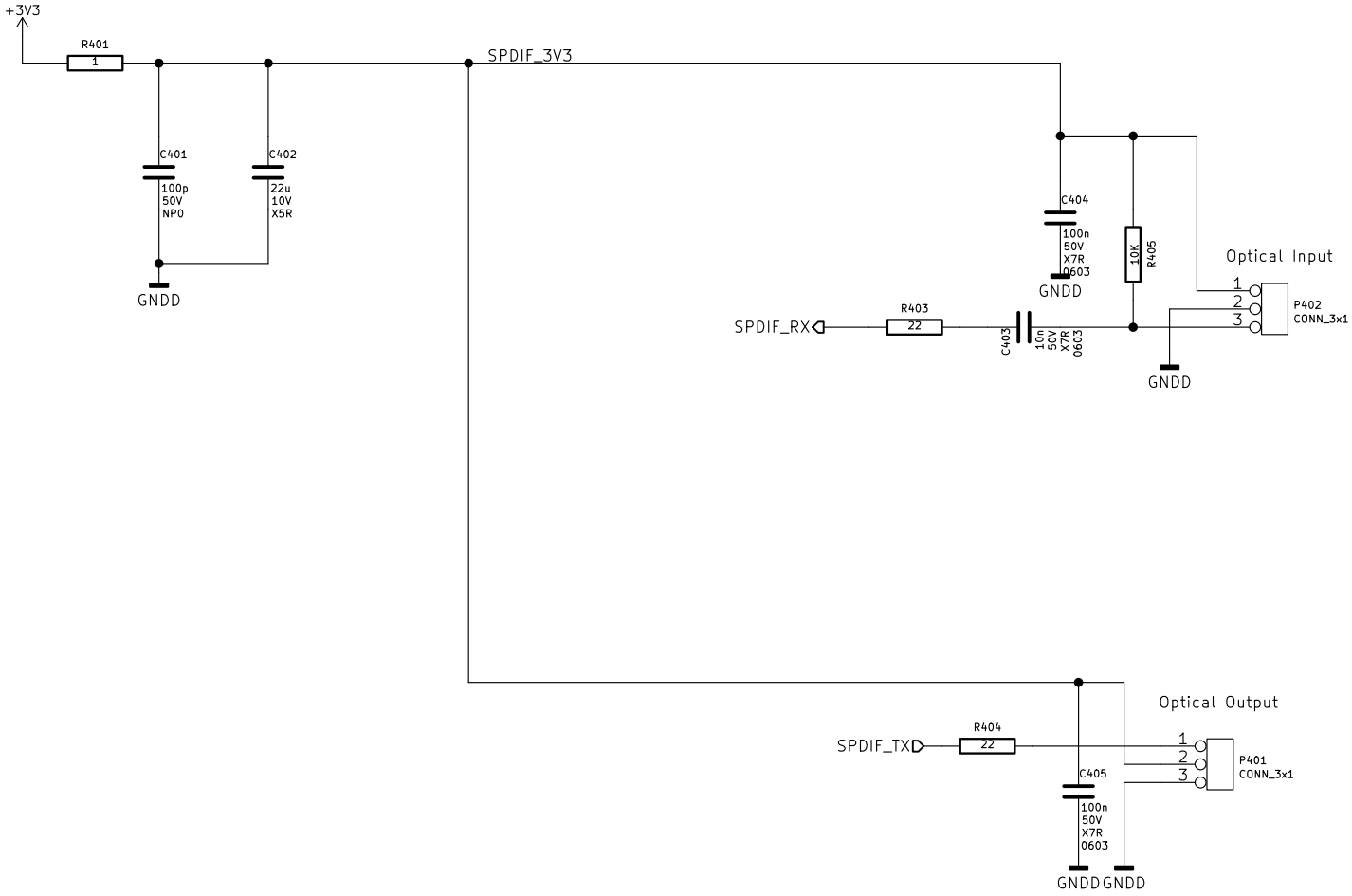
MNT302 MNT304 FID302 FID304

Sheet: /Power Supply/
File: ADAU1452 PSU.sch

Title: uDSP

Size: A4	Date: 2019-03-11	**Rev: 1.0**
KiCad E.D.A. kicad (5.0.2)-1		Id: 3/6

SPDIF Optical Input and Output – Mounted off-board



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File: SPDIF.sch

Title: uDSP

Size: A4	Date: 2019-03-11	**Rev: 1.0**
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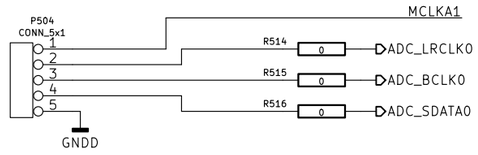
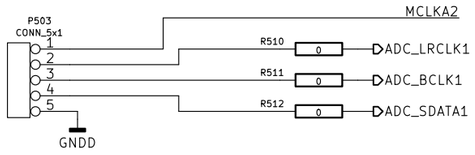
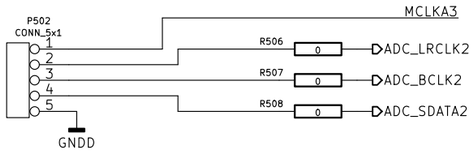
Connectors

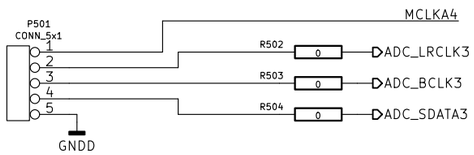
I2S Inputs

See ADAU14xx datasheet for details
Inputs are highly flexible



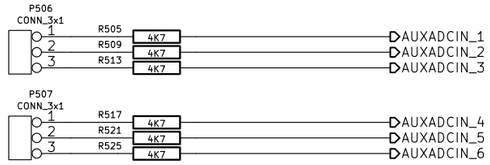






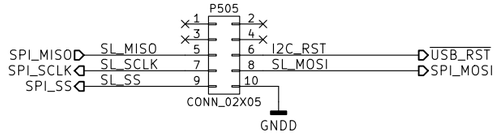
AUX ADC Inputs

These are 3V3 max input
They have limited protection

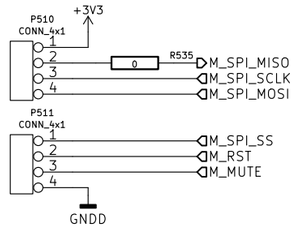


ADI EVAL-ADUSB2Z USB/I2C Programming/Control

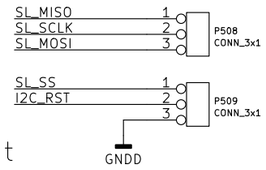
These go to the slave port
These are 3V3 lines only!



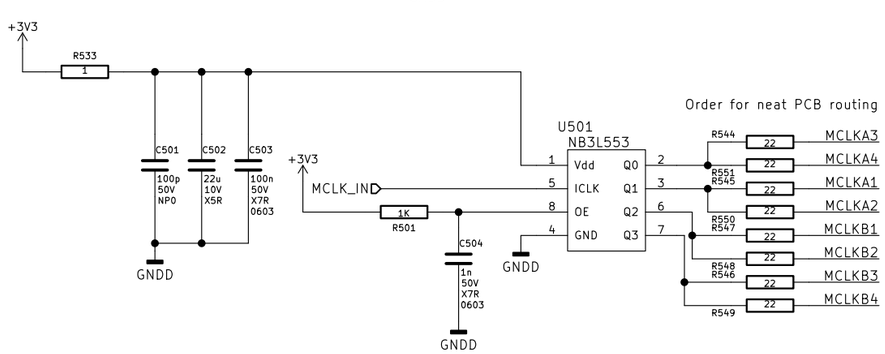
Master SPI Port



Slave SPI Port

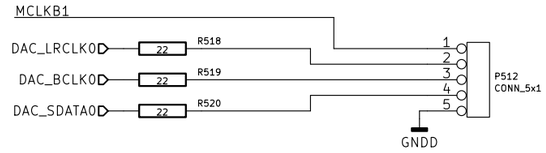


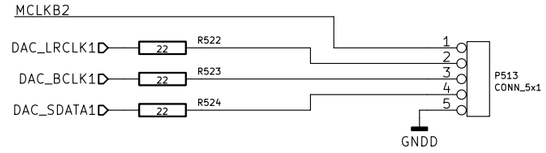
Master Clock Fan Out

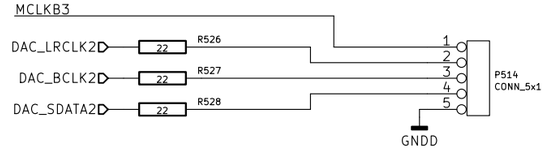


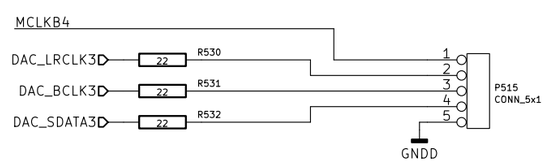
I2S Outputs

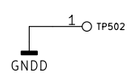
See ADAU14xx datasheet for details
Outputs are highly flexible











Sheet: /Connectors/
File: Connectors.sch

Title: uDSP

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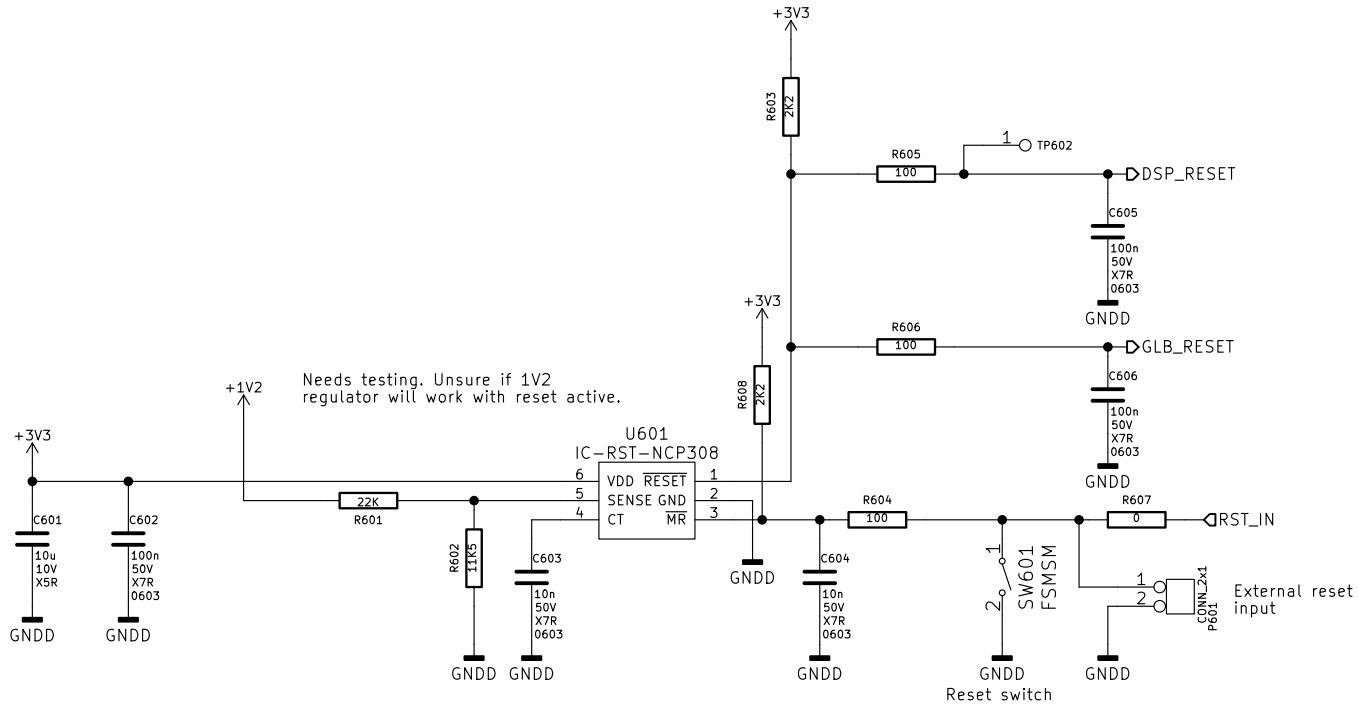
Voltage Monitor Reset

Generate a global reset based on 1V2 voltage rail.
Use NCP308SNADJT1G adjustable version.
Supports external reset signal.



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Title: uDSP

Size: A4	Date: 2019-03-11	**Rev: 1.0**
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