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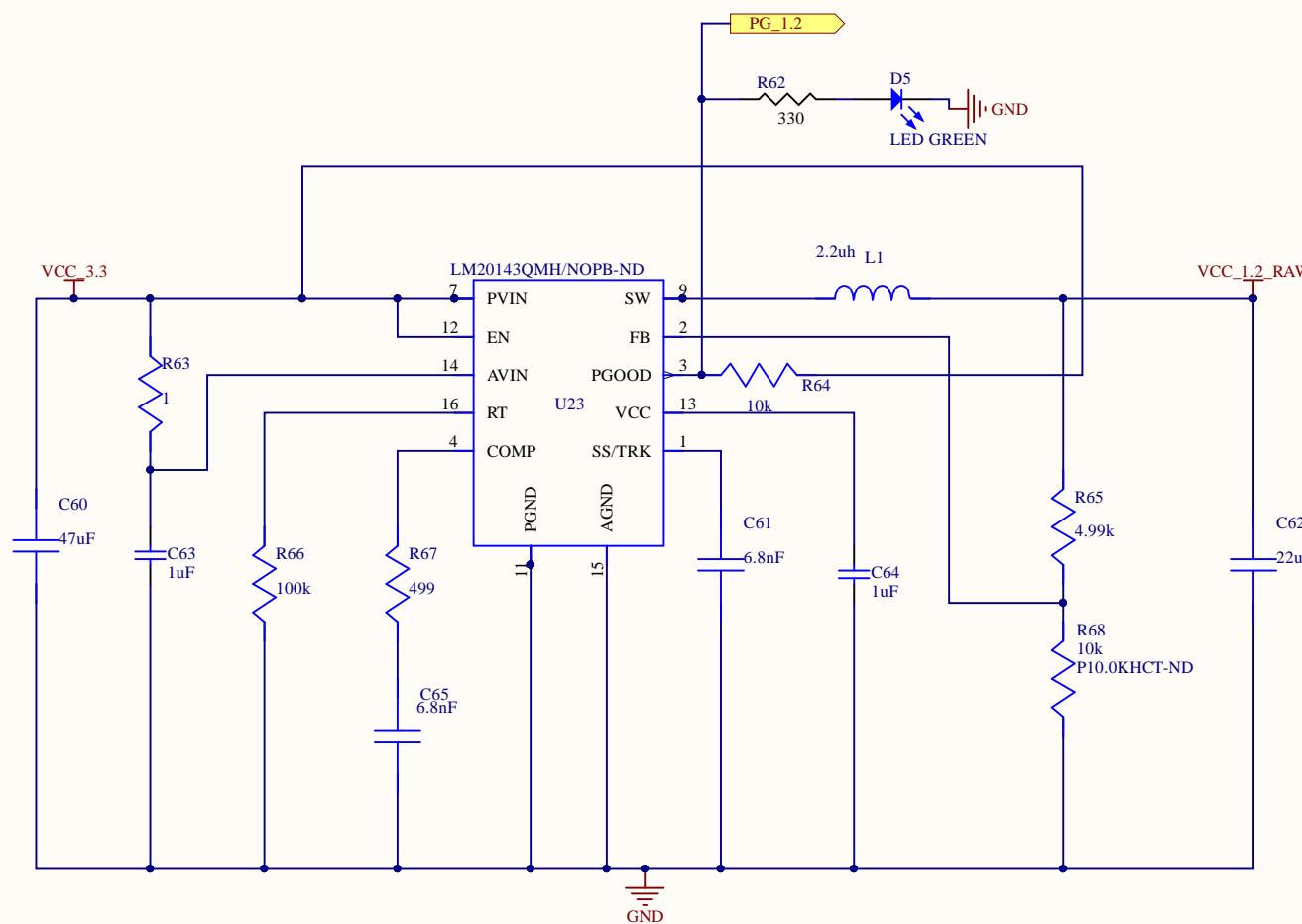
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Title **POWER IV2**Size: **B** Number: **2** Revision: **F**Date: **11/2/2019** Time: **12:58:38 PM** Sheet **2** of **35**File: **C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\PS_1.2V.SchDoc****Altium**

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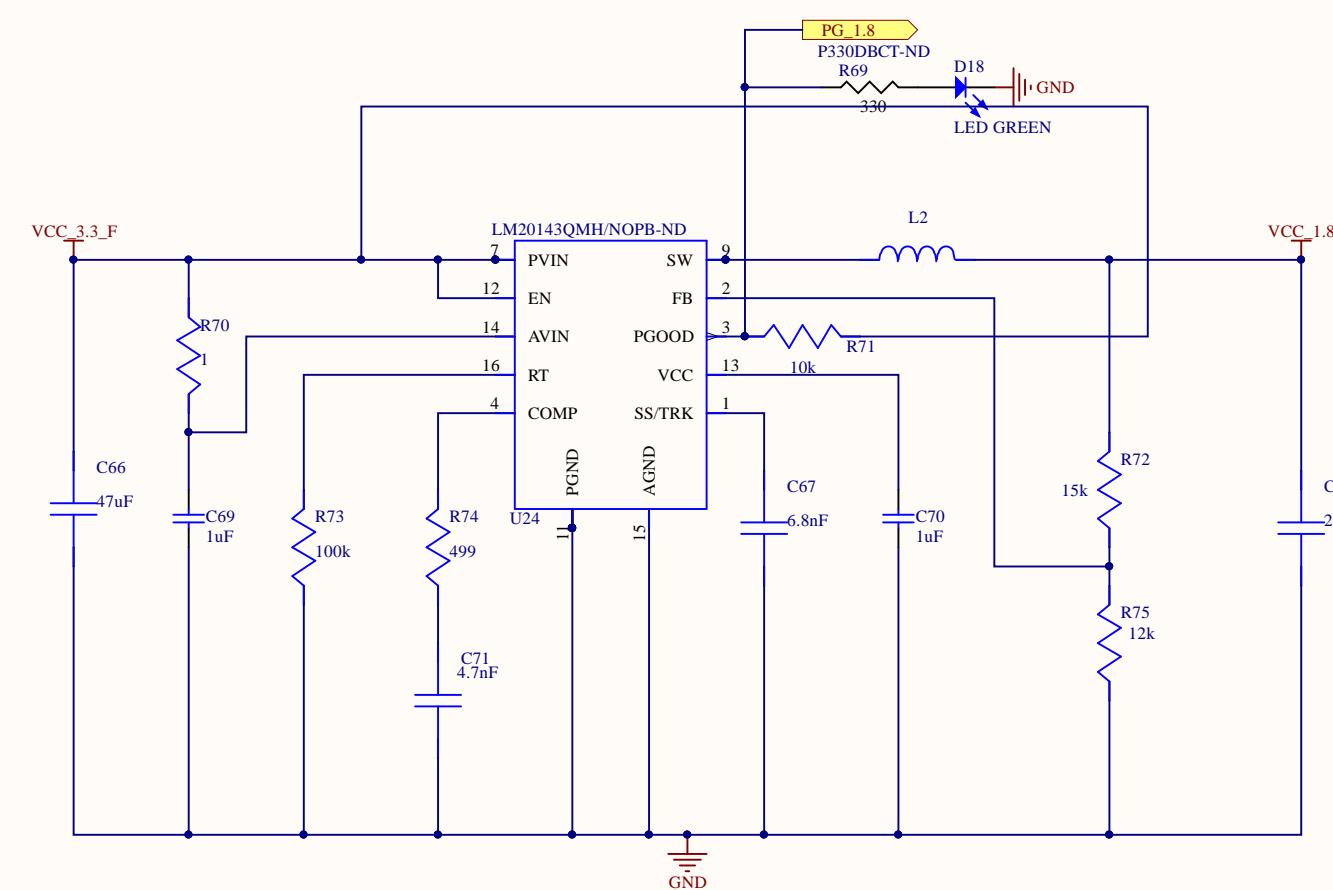
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Title **POWER IV8**

Size: B Number:3 Revision F

Date: 11/2/2019 Time: 12:58:38 PM Sheet 3 of 35

File: C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\PS_1.8V.SchDoc

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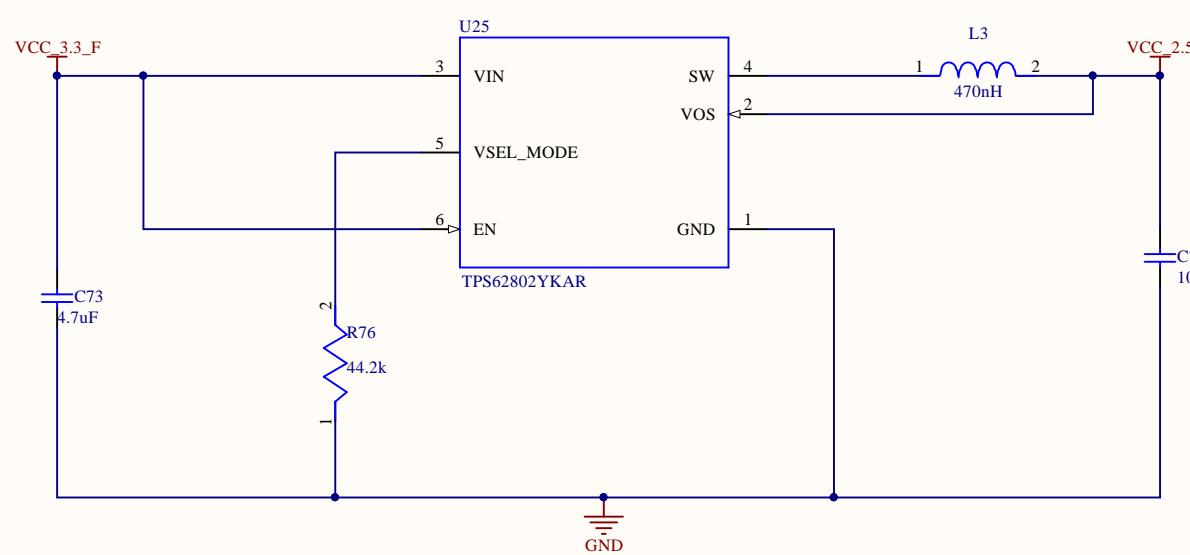
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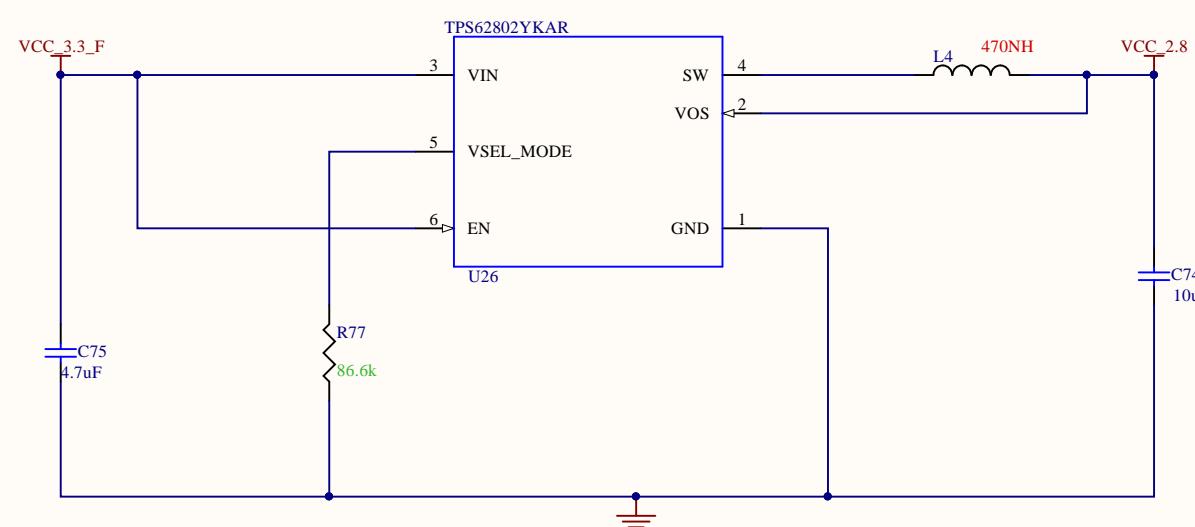
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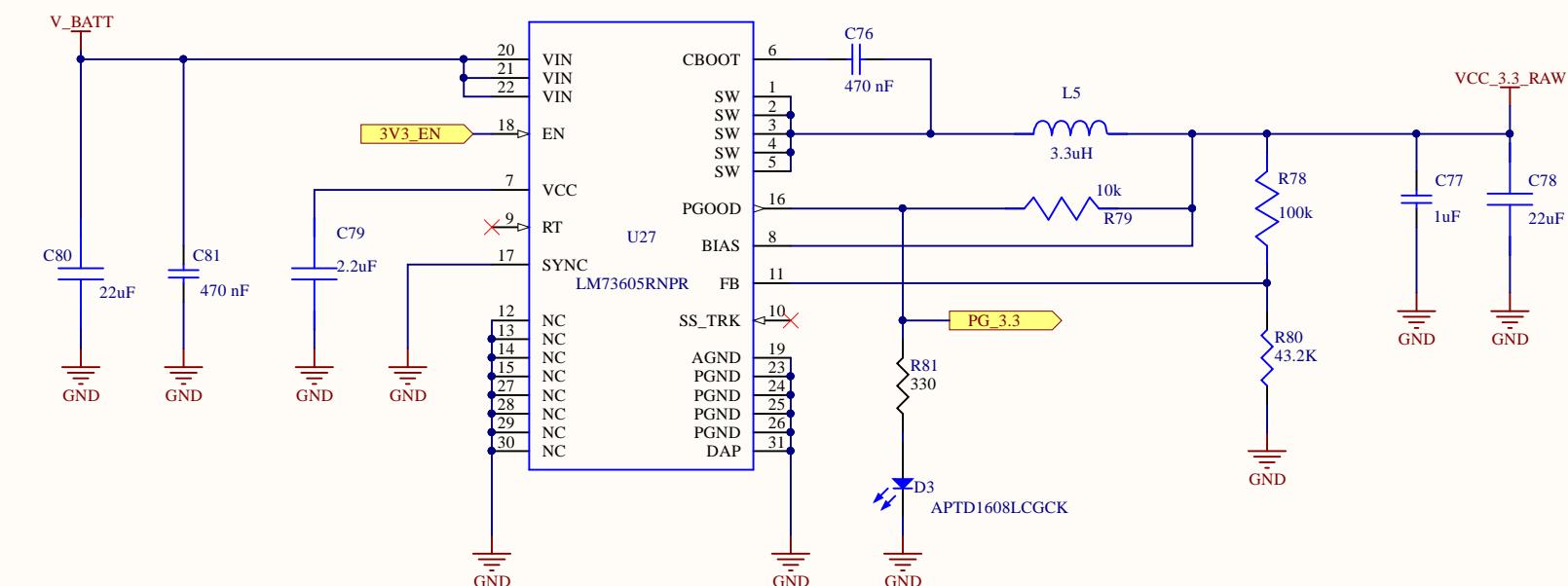
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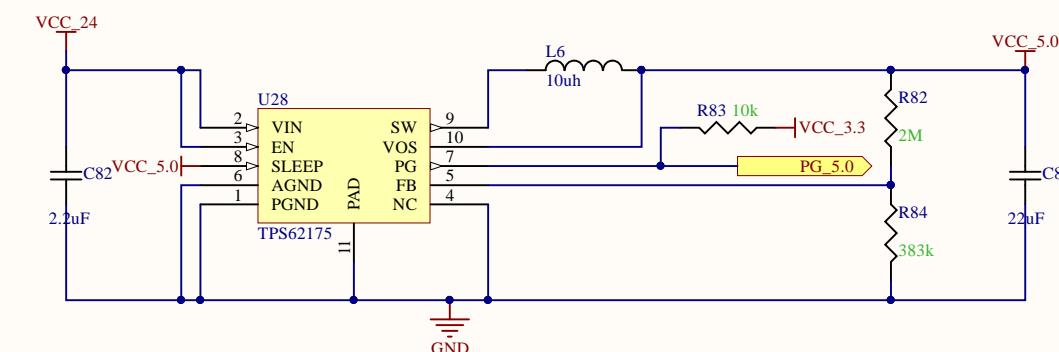
D

Title **POWER 3V3**

Size: B	Number: 6	Revision: F
Date: 11/2/2019	Time: 12:58:38 PM	Sheet 6 of 35
File: C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\PS_3.3V.SchDoc		

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Title **POWER 5V0**

Size: B Number: 7 Revision: F

Date: 11/2/2019 Time: 12:58:38 PM Sheet 7 of 35

File: C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\PS_5.0V.SchDoc

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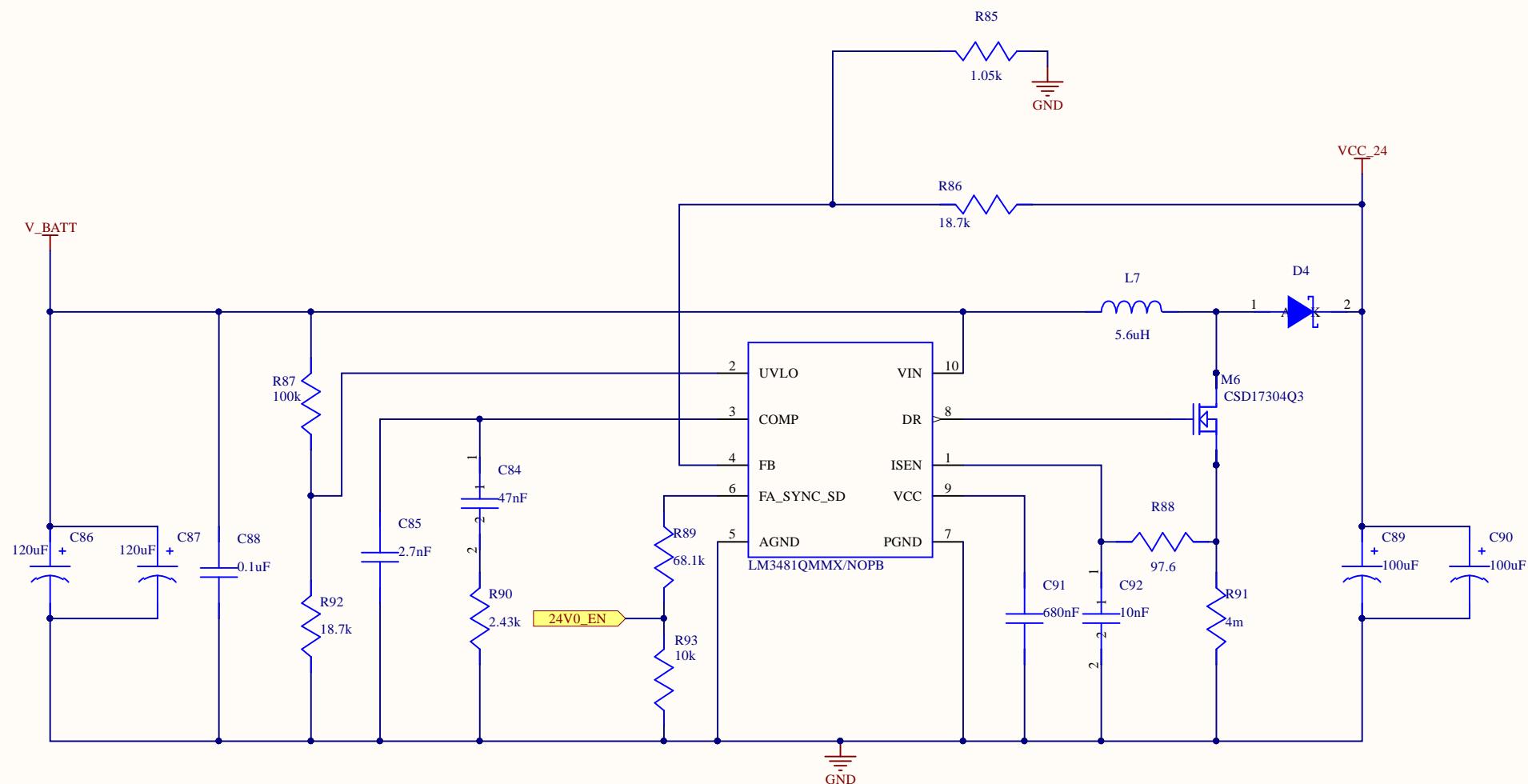
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Title **POWER 24V0**

Size: B Number:8 Revision F

Date: 11/2/2019 Time: 12:58:38 PM Sheet 8 of 35

File: C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\PS_24V.SchDoc

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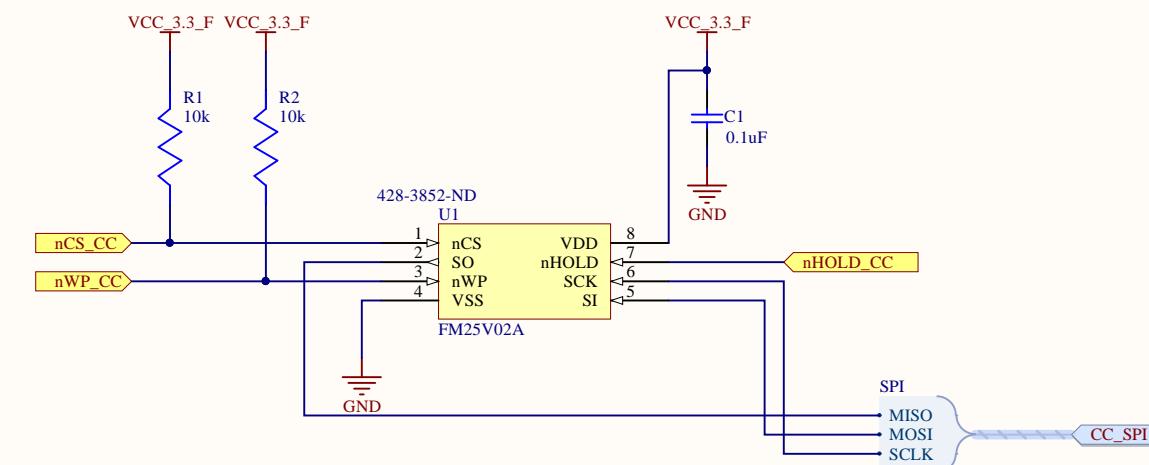
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Title **CAMERA CONFIG. MEMORY**

Size: B Number:9 Revision F

Date: 11/2/2019 Time: 12:58:38 PM Sheet 9 of 35

File: C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\Cam_Config_Mem.SchDoc

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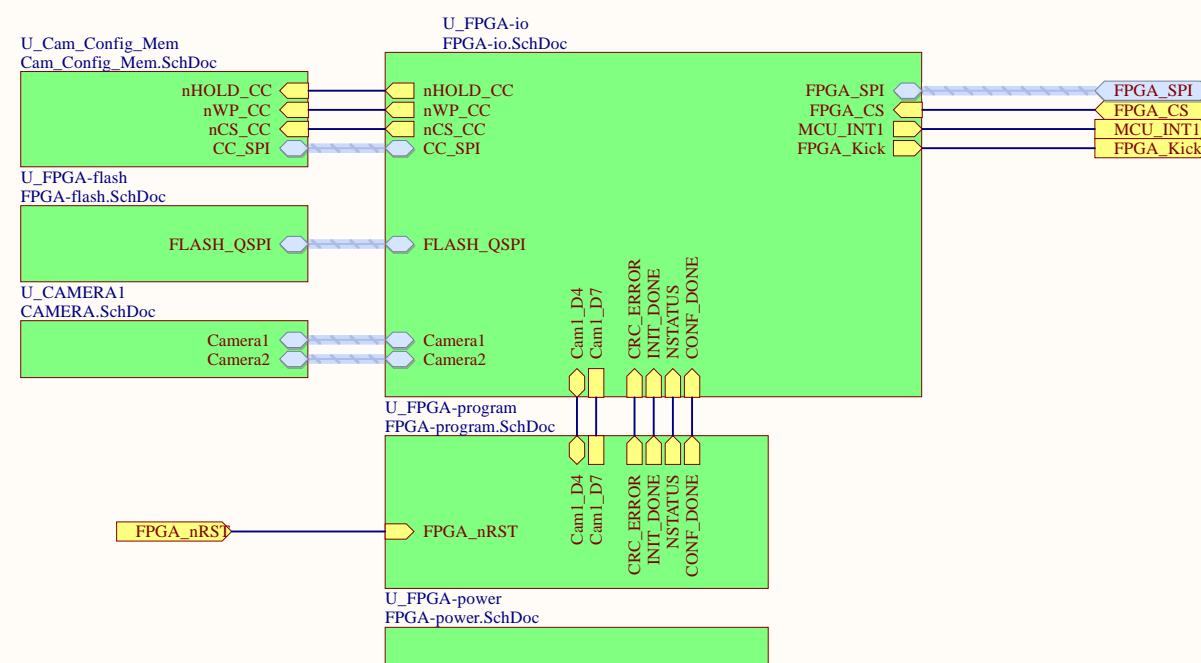
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Title **CAMERA SYSTEM**Size: **B** Number:10 Revision:**F**

Date: 11/2/2019 Time: 12:58:38 PM Sheet 10 of 35

File: C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\CameraSystem.SchDoc

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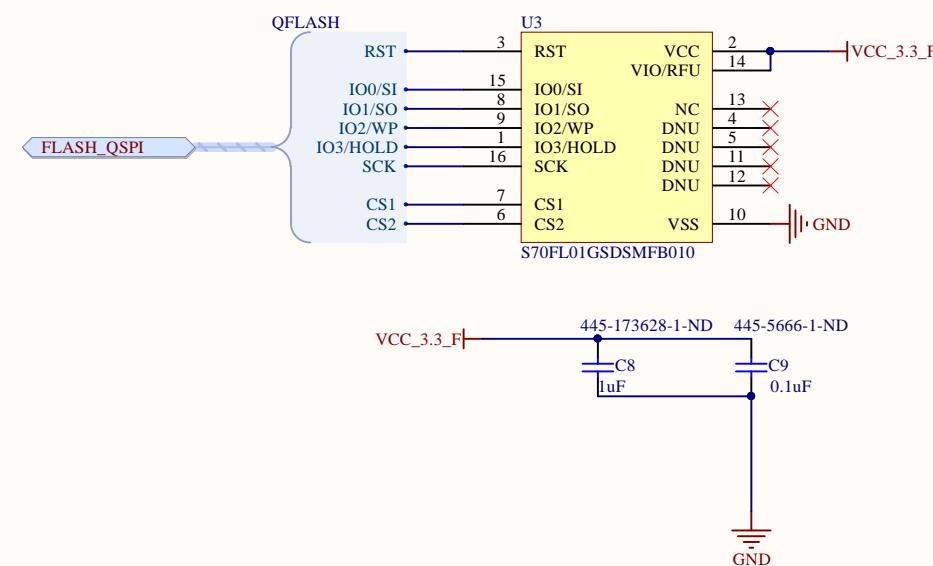
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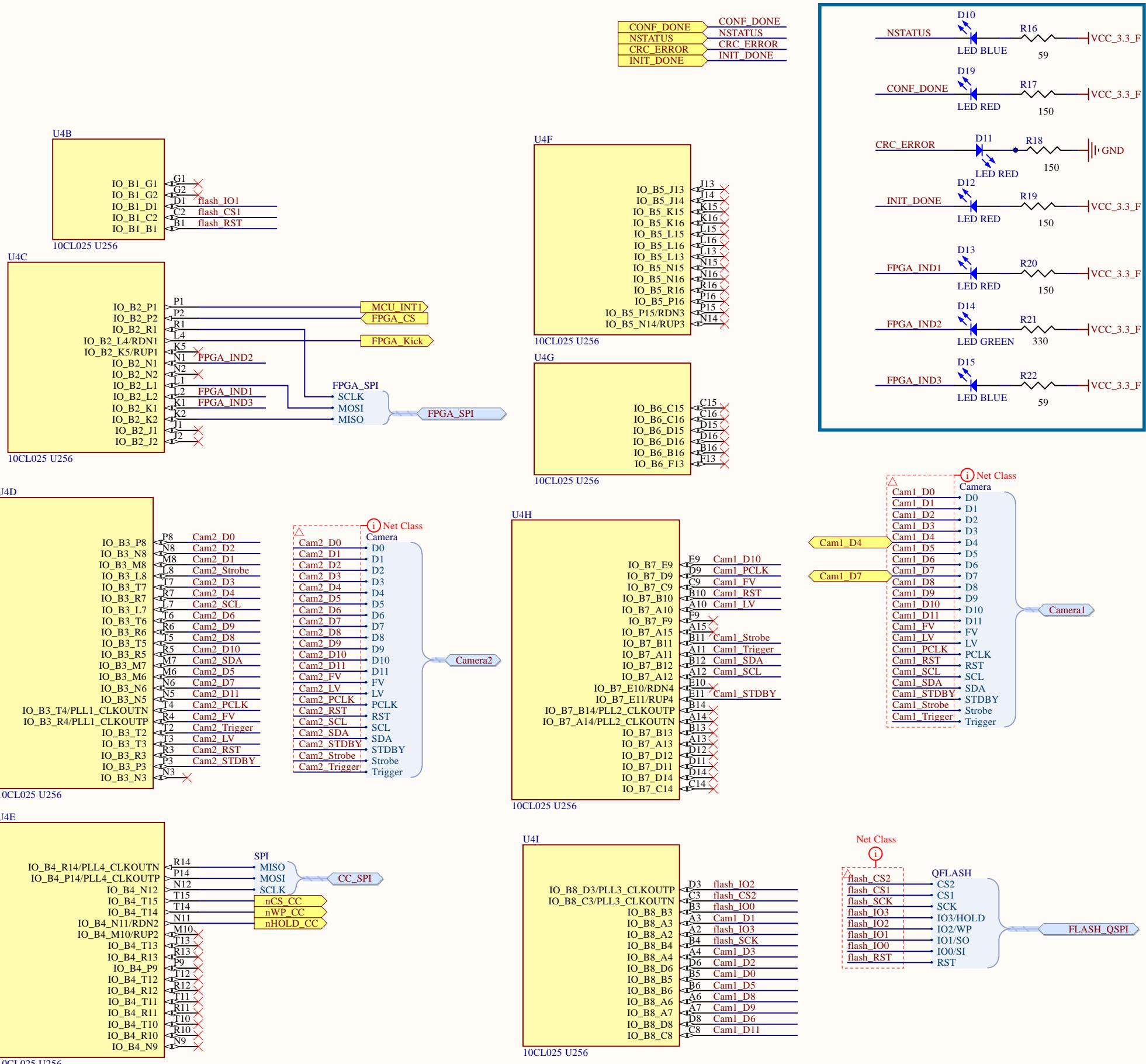
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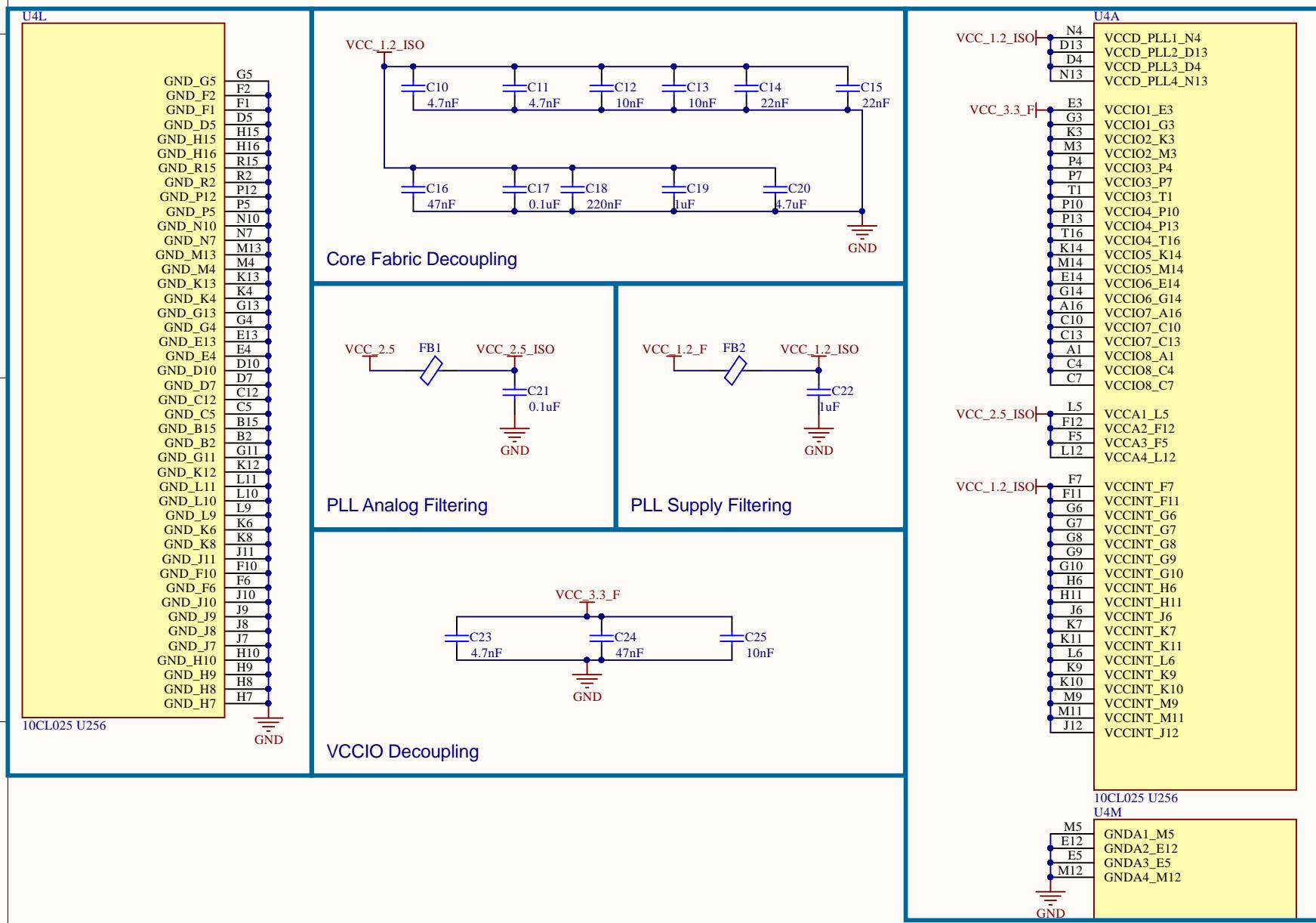
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Title **FPGA POWER**

Size: B Number:13 Revision F

Date: 11/2/2019 Time: 12:58:38 PM Sheet 13 of 35

File: C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\FPGA-power.SchDoc

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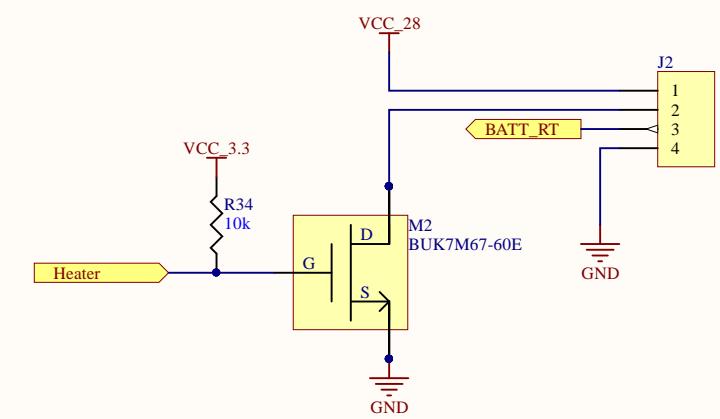
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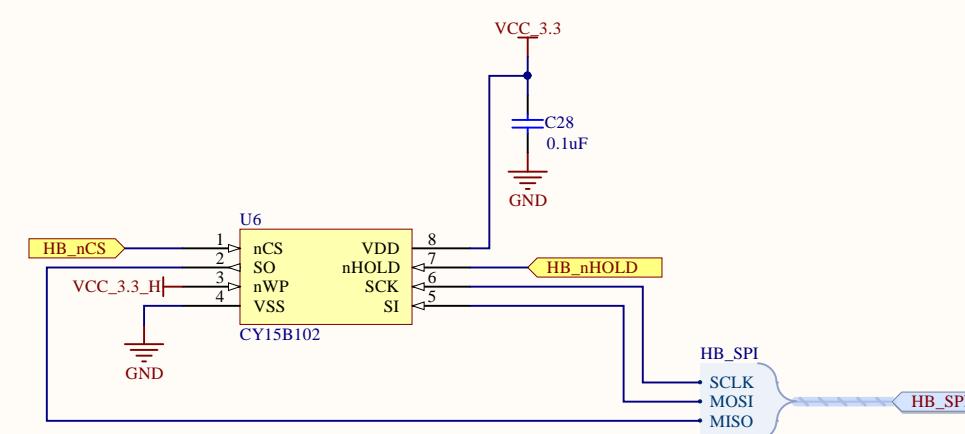
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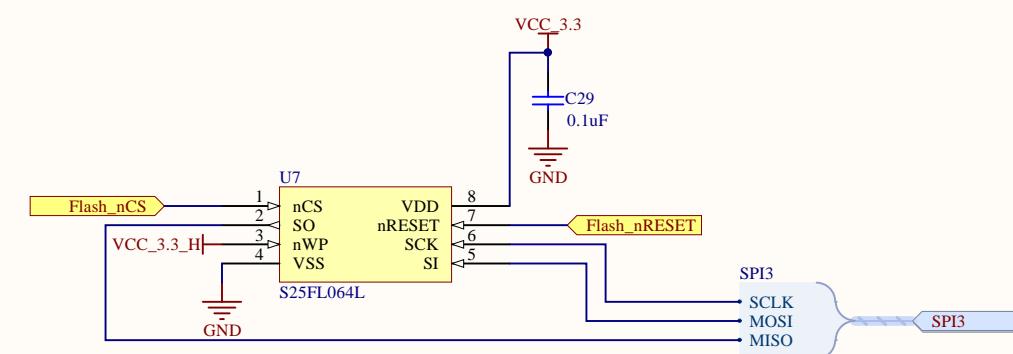
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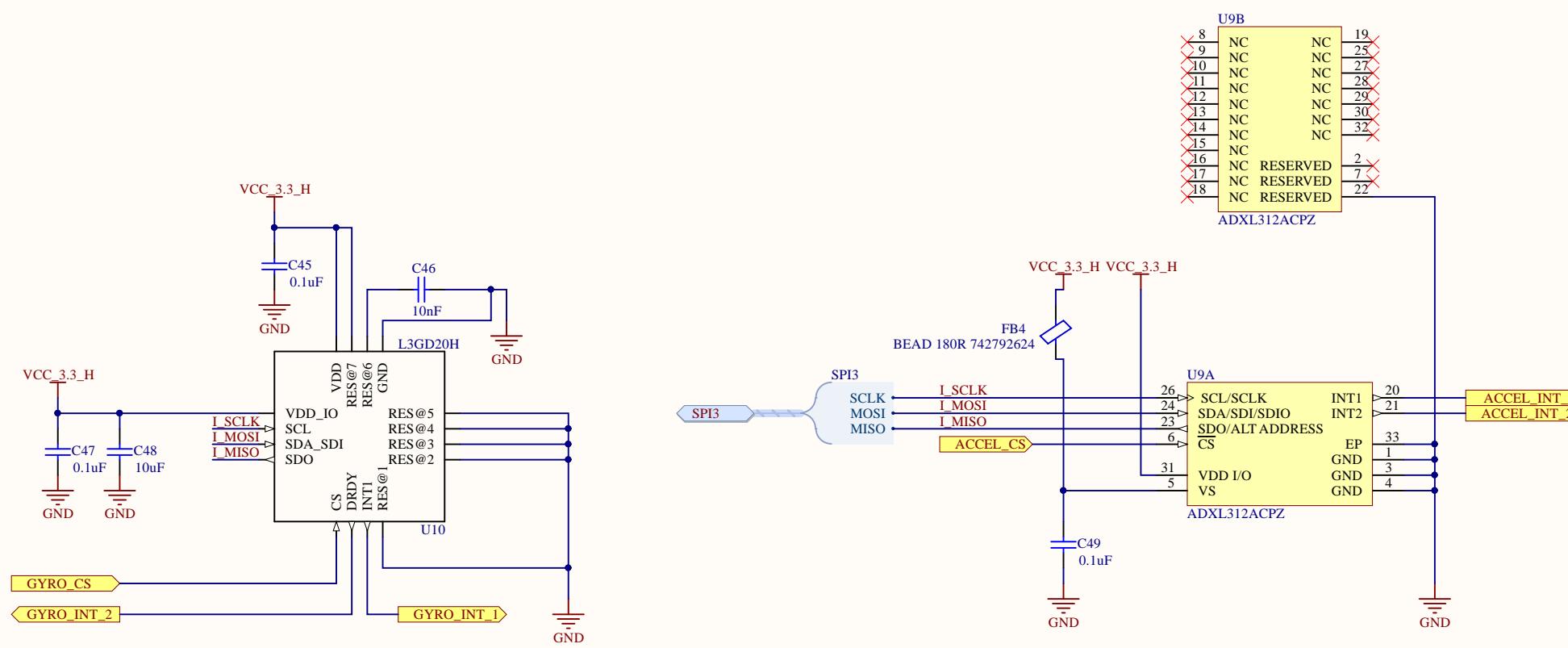
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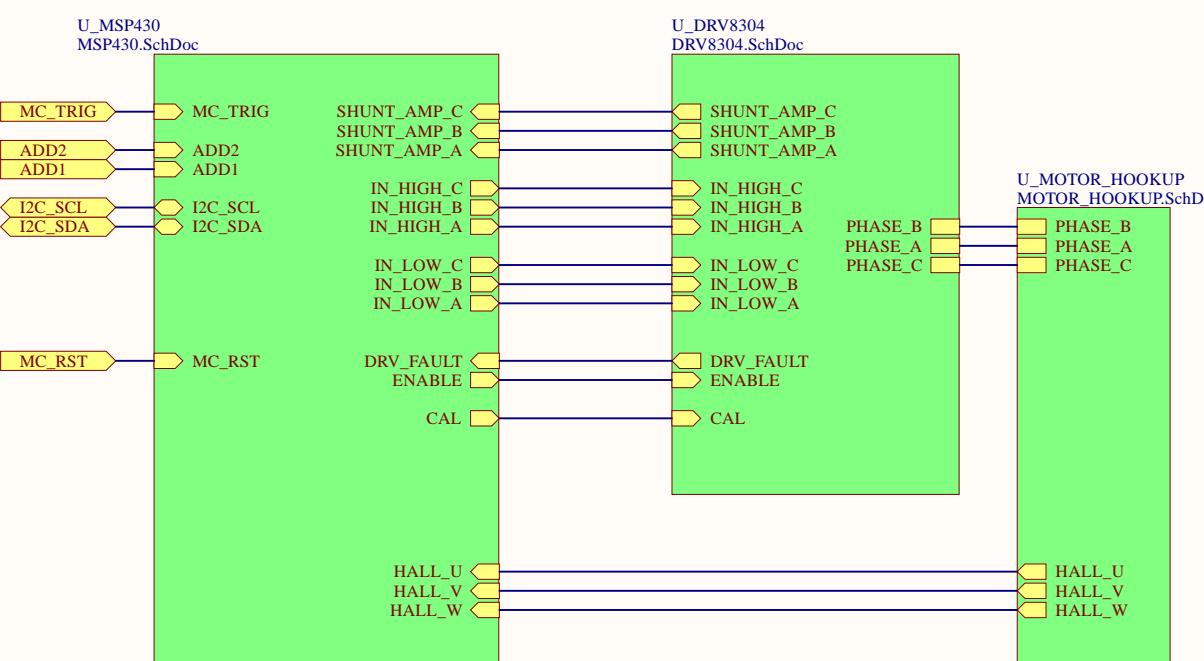
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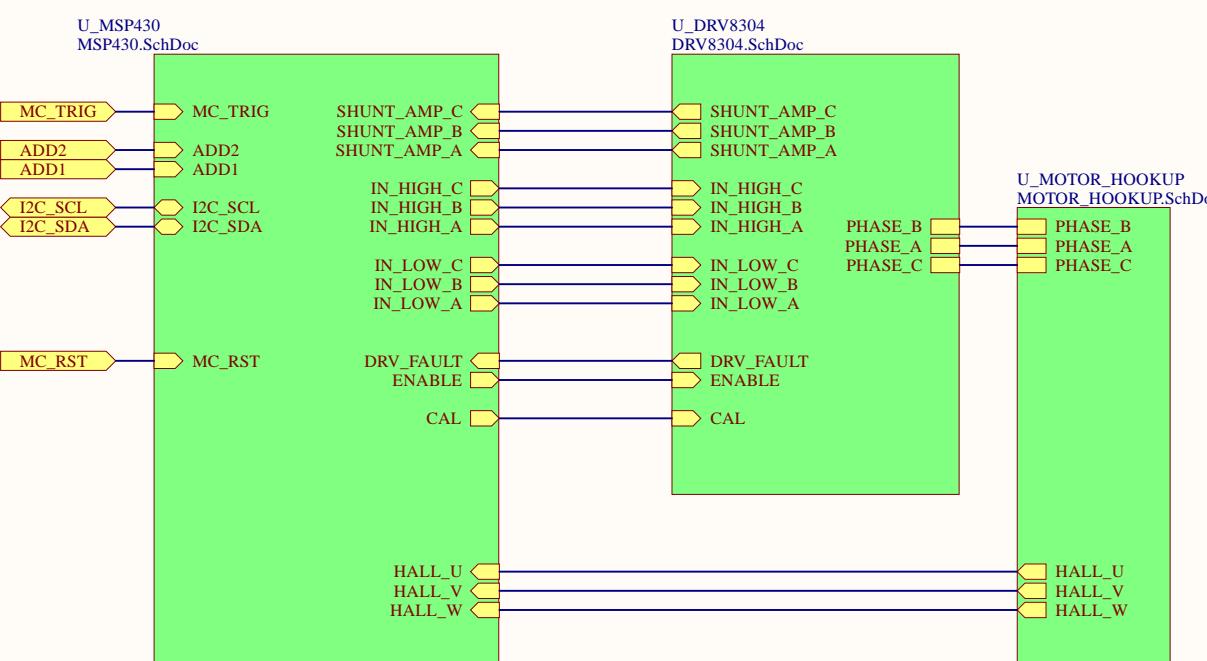
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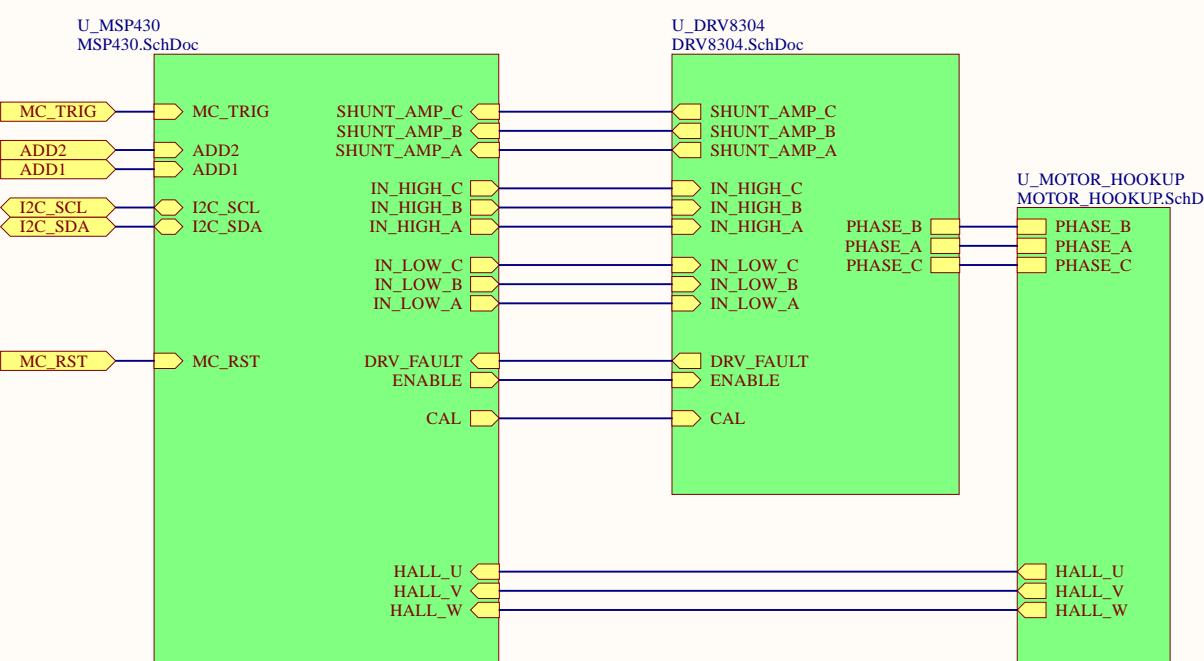
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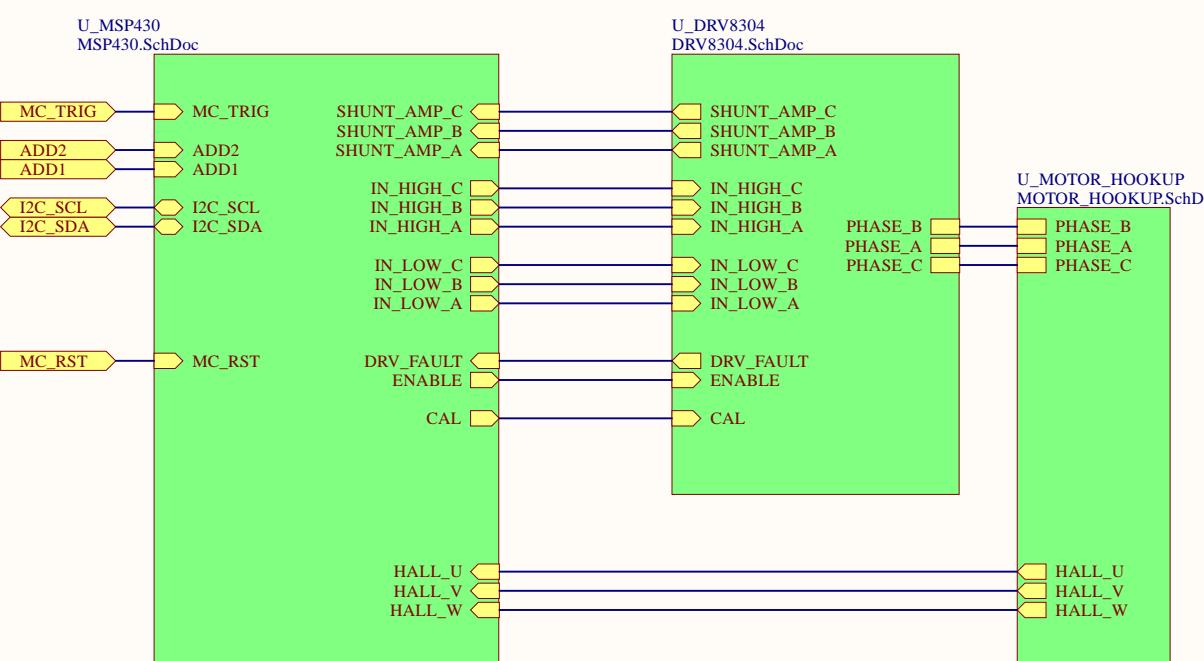
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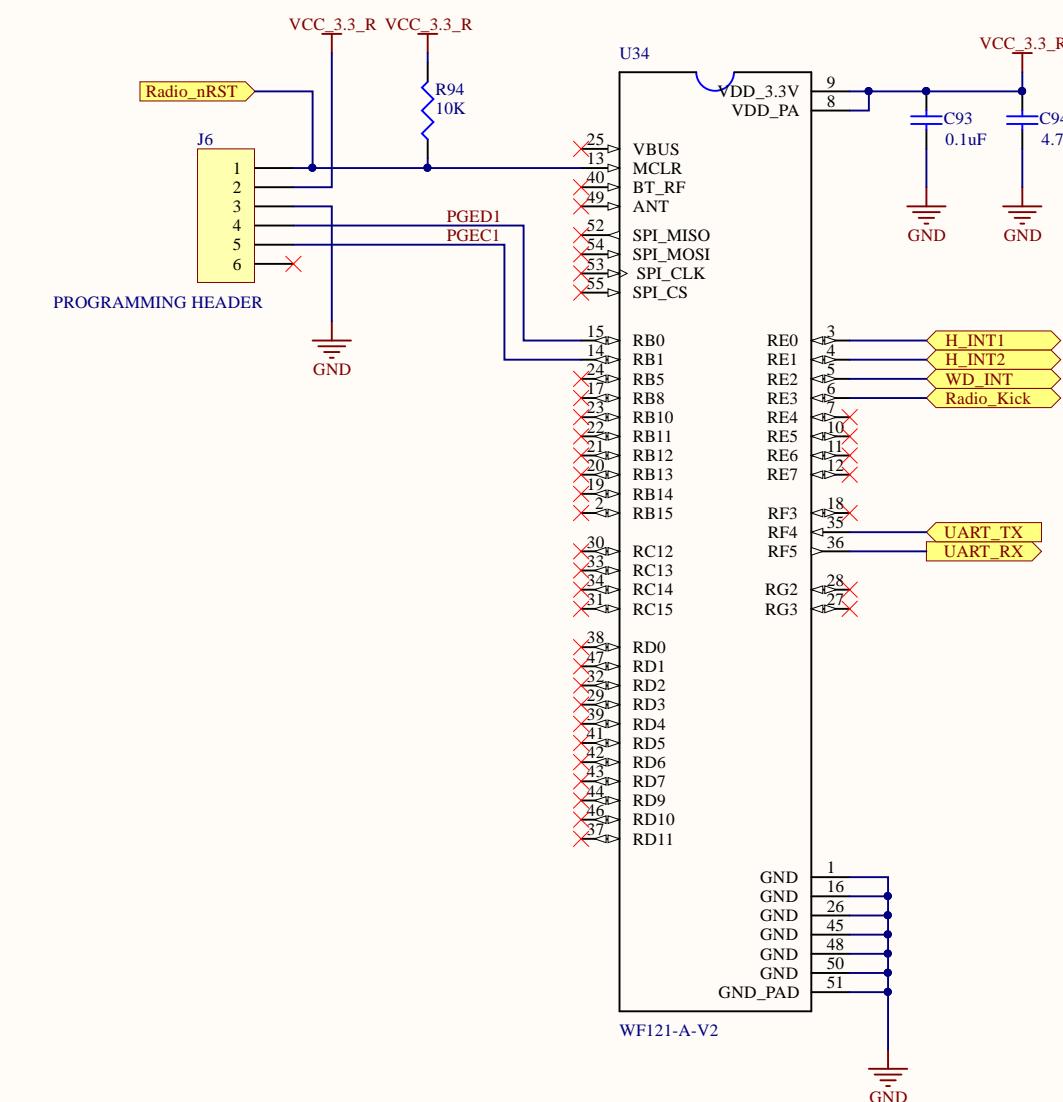
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Title **RADIO**Size: **B** Number:19 Revision:**F**

Date: 11/2/2019 Time: 12:58:39 PM Sheet 19 of 35

File: C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\Radio.SchDoc

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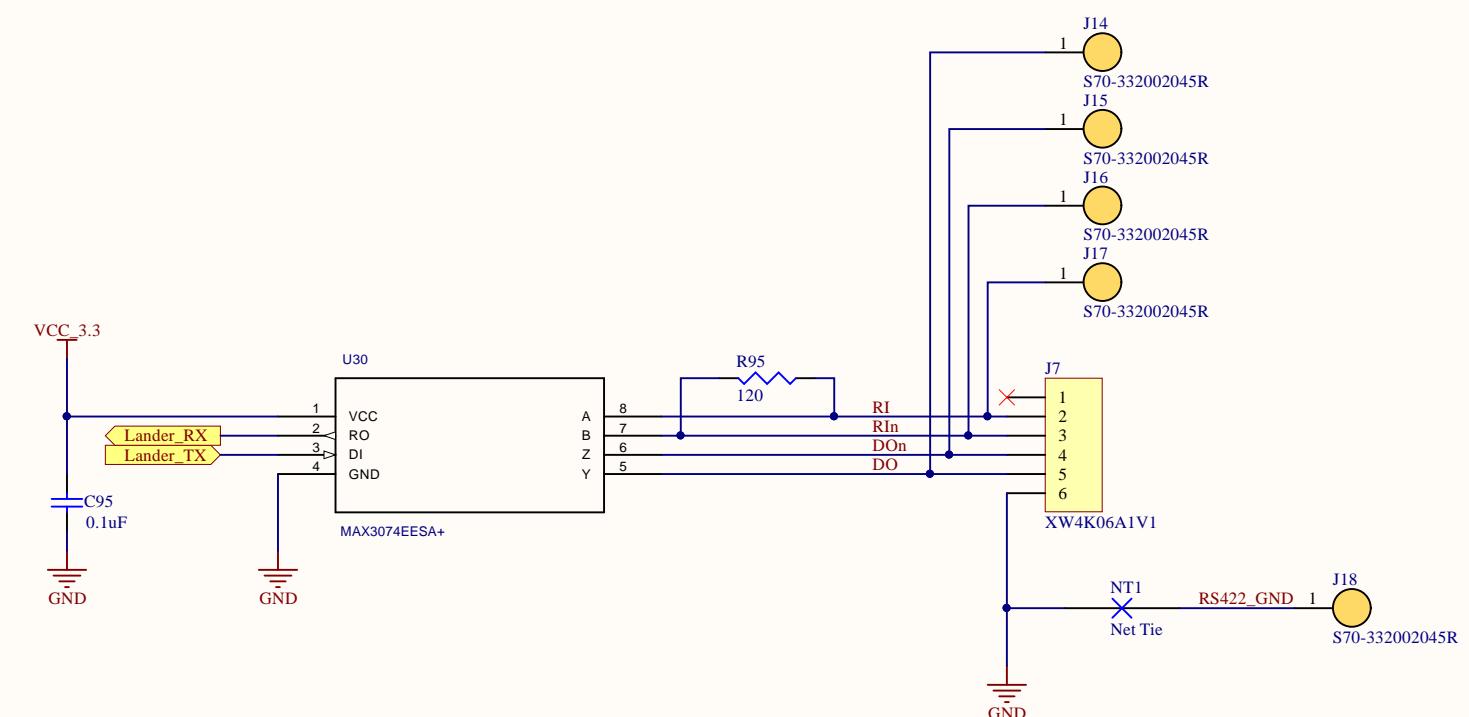
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Title **RS-422**

Size: B Number:20 Revision F

Date: 11/2/2019 Time: 12:58:39 PM Sheet 20 of 35

File: C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\RS422.SchDoc

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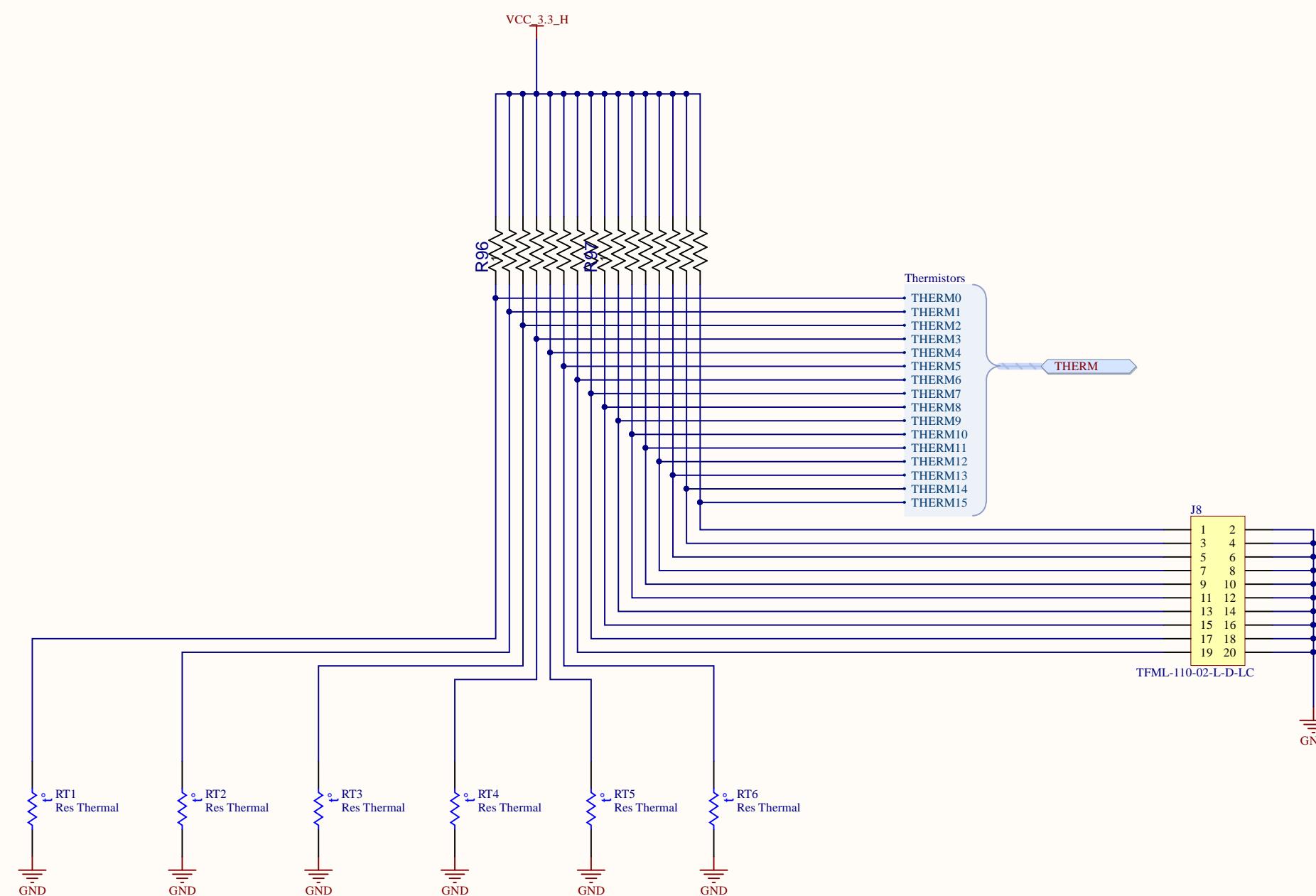
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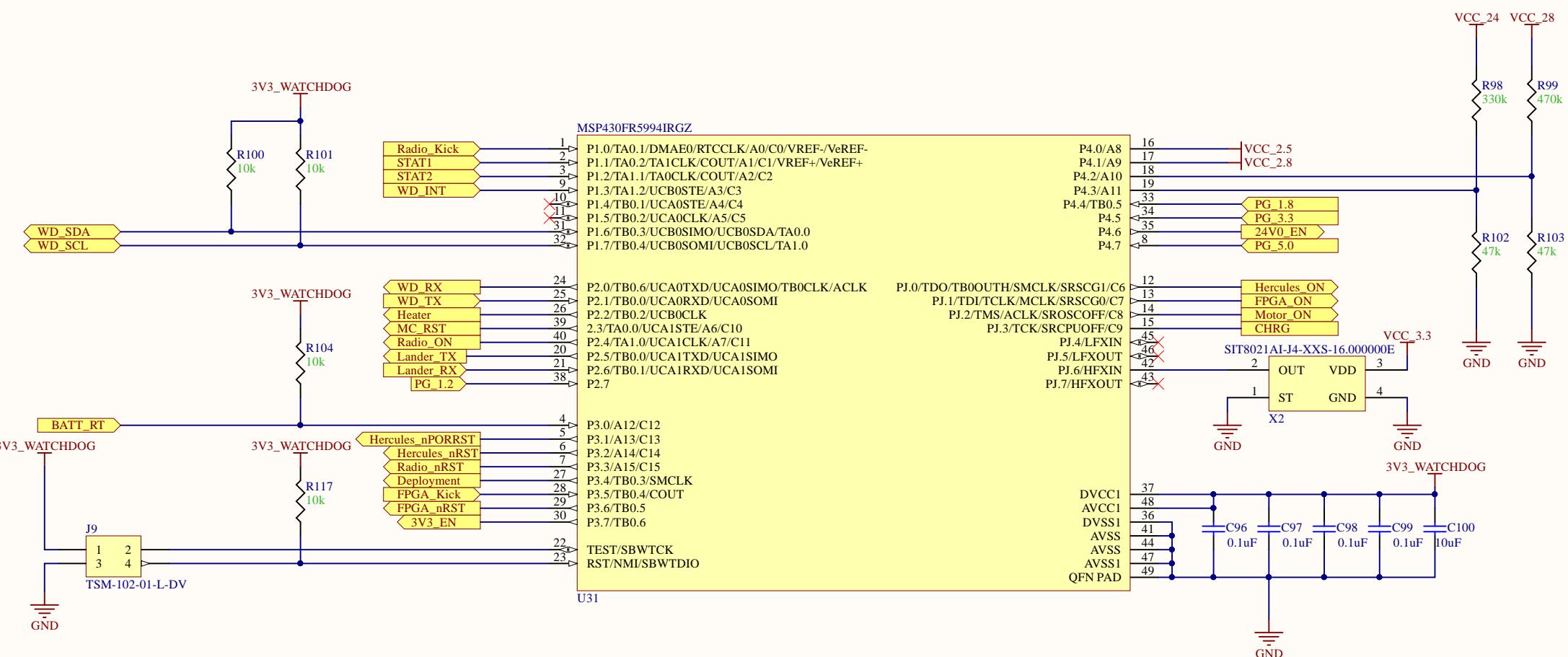
Size: B	Number: 22	Revision: F
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Date: 11/2/2019 Time: 12:58:39 PM Sheet 22 of 35

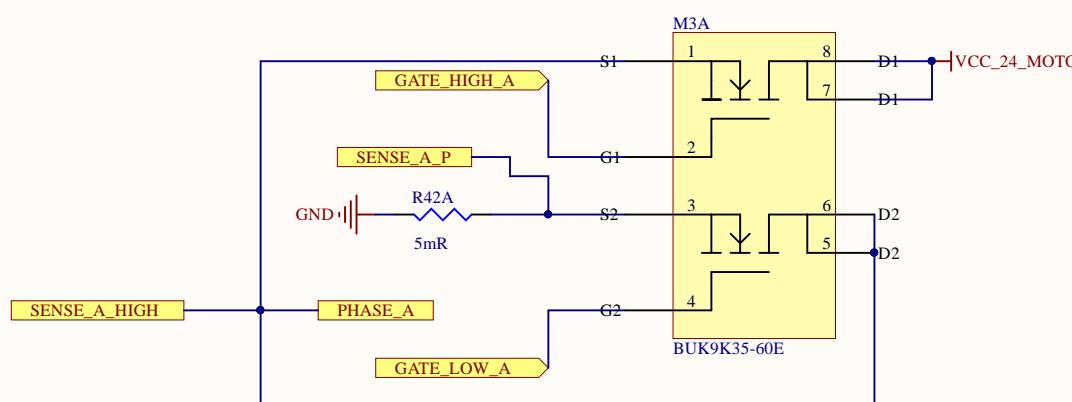
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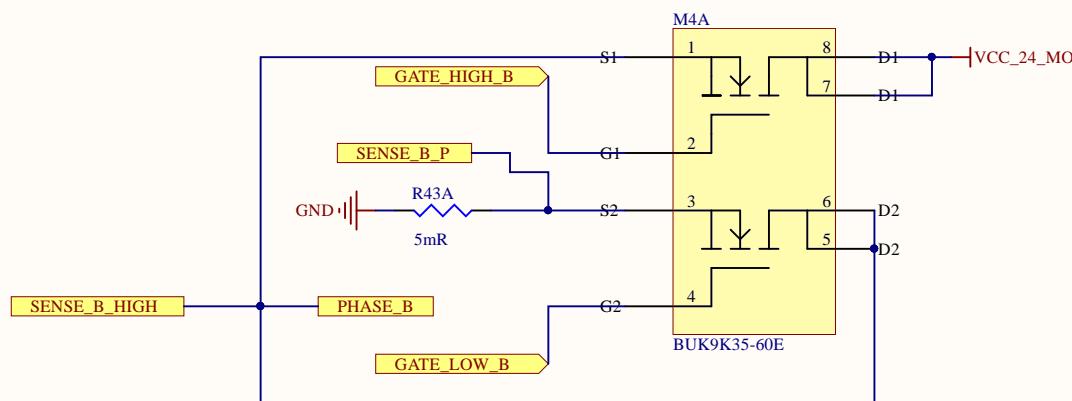
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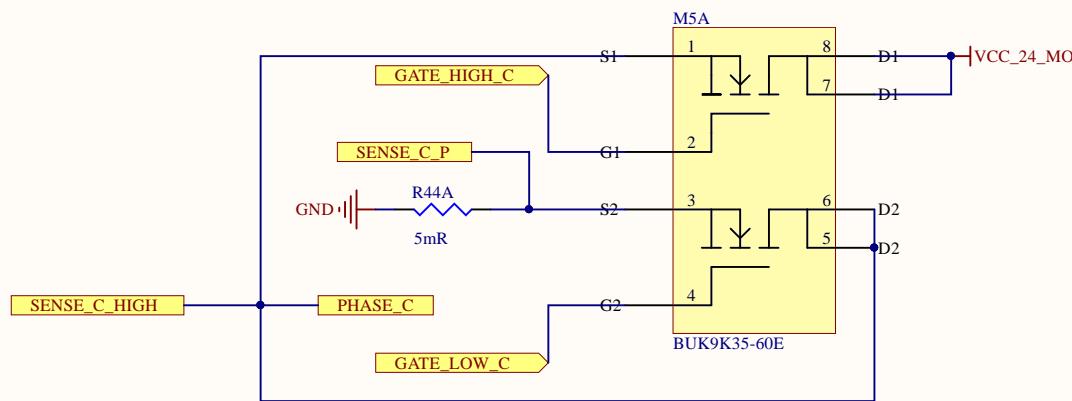
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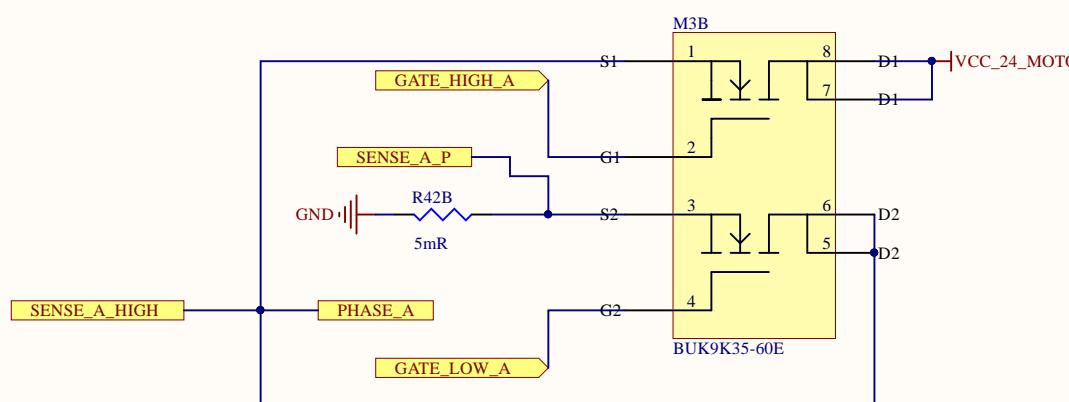


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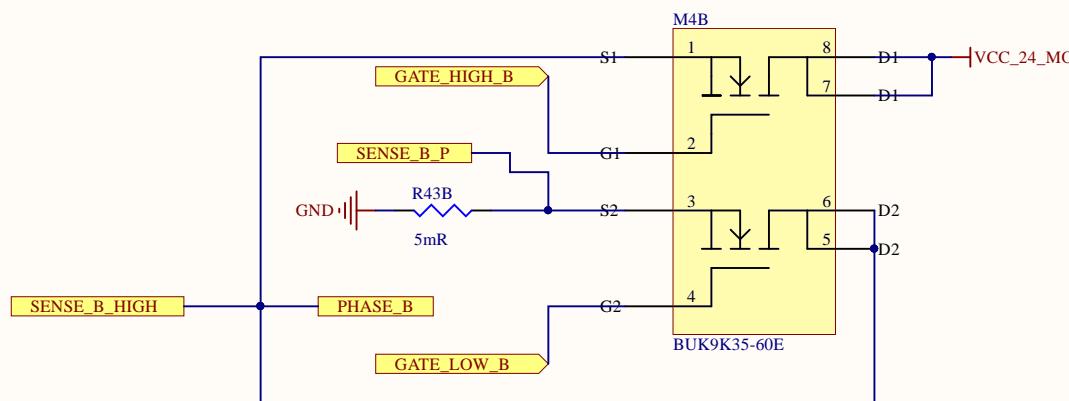


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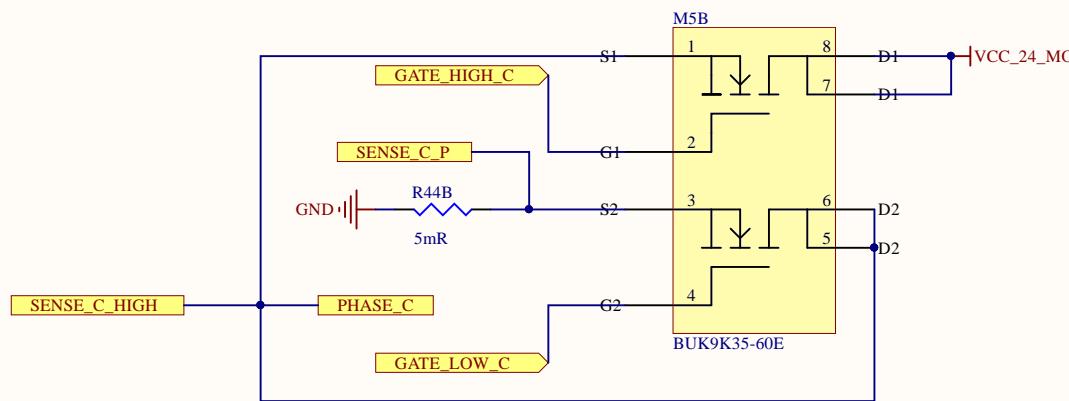
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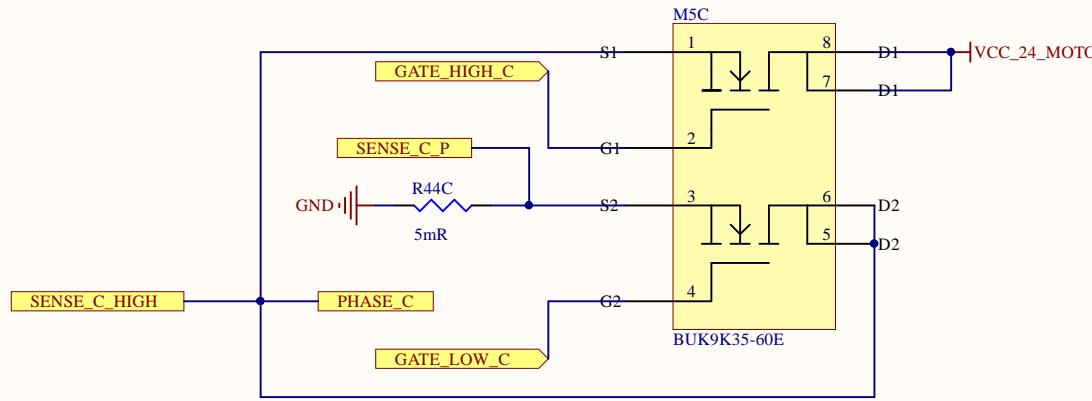
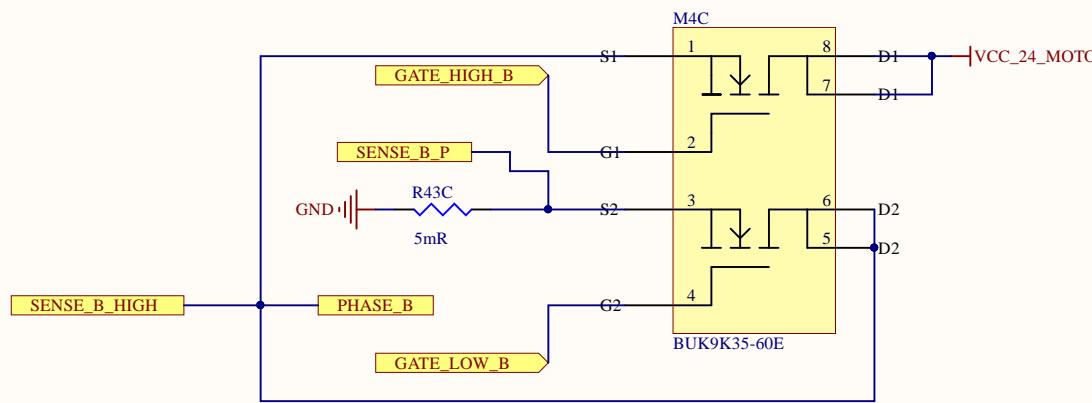
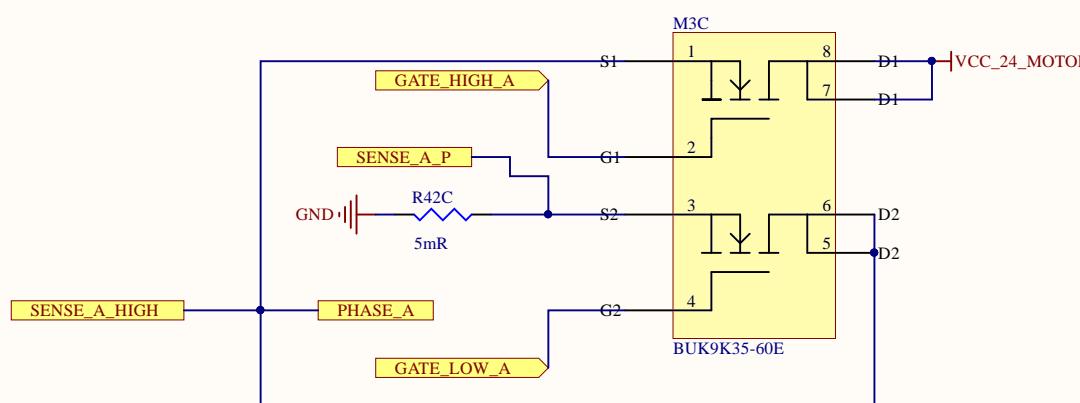
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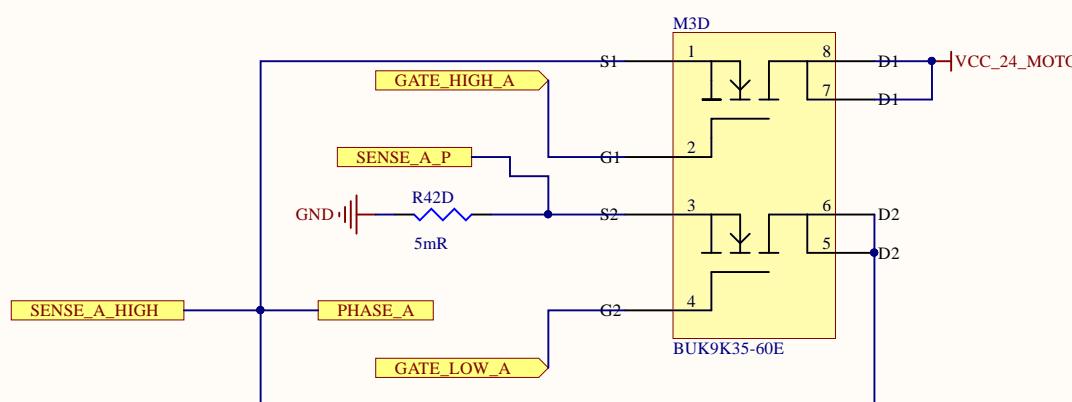
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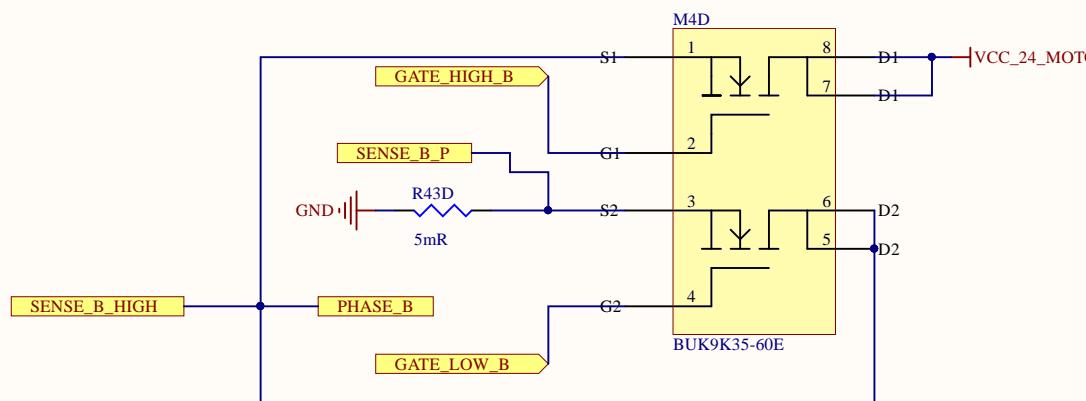


Title MOSFETS		
Size: B	Number: 24	Revision F
Date: 11/2/2019	Time: 12:58:39 PM	Sheet 24 of 35
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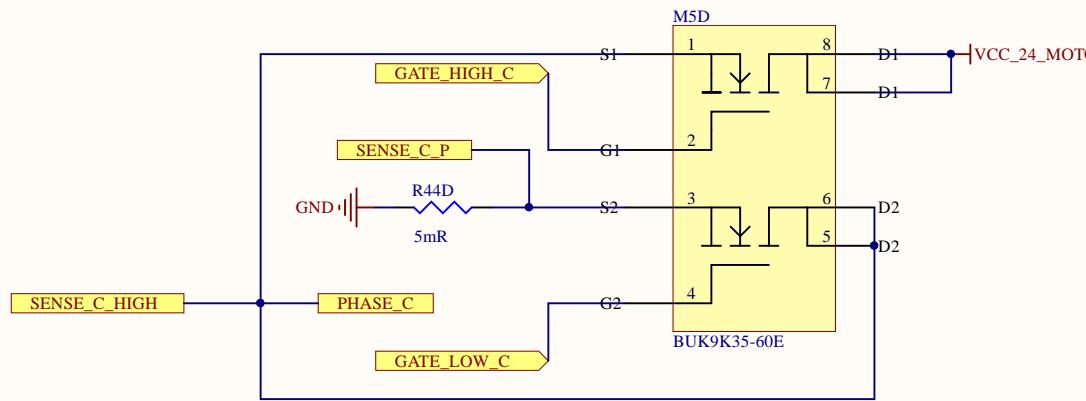
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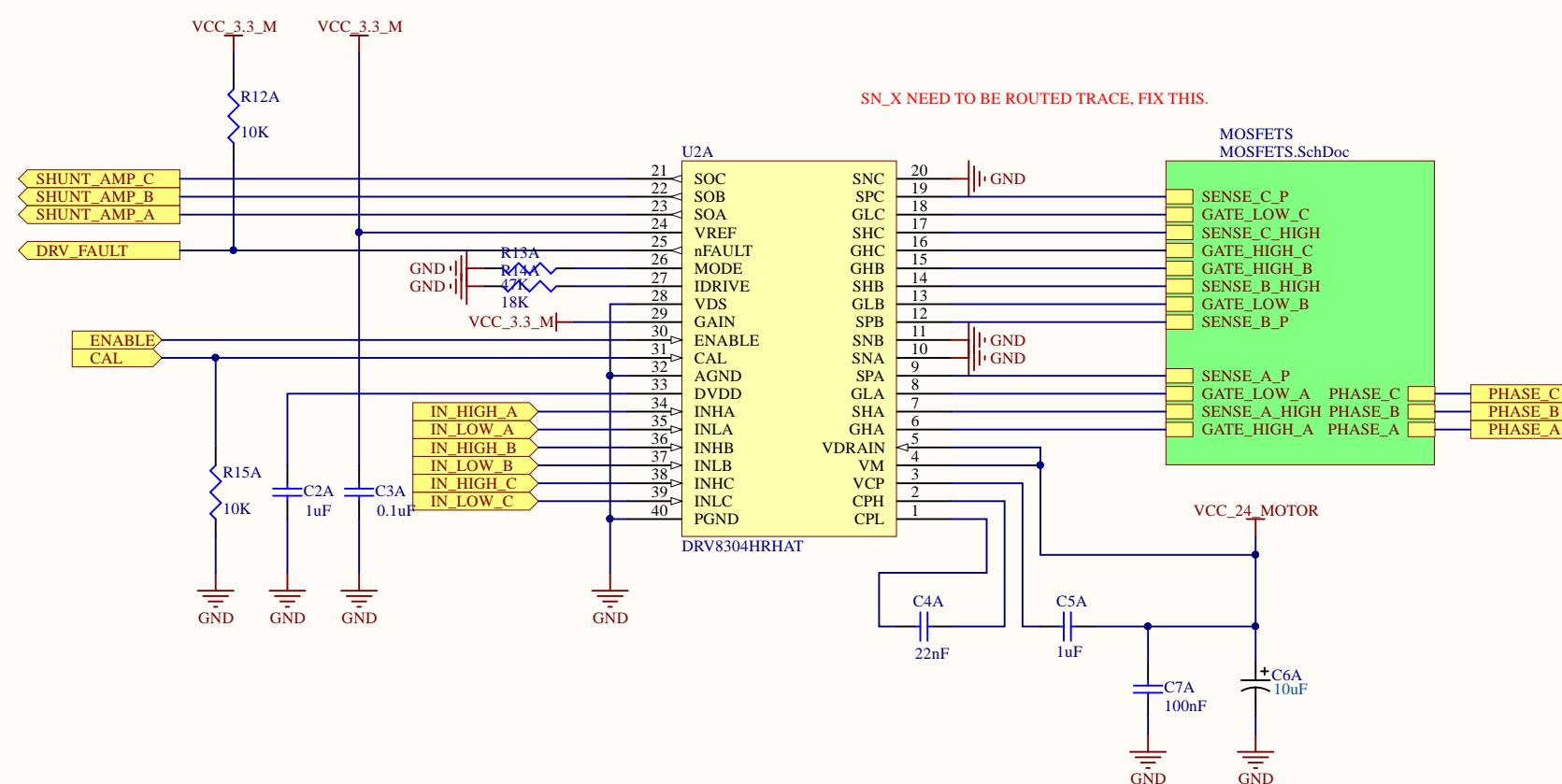
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Title **DRV8304**

Size: B Number:25 Revision: F

Date: 11/2/2019 Time: 12:58:40 PM Sheet 25 of 35

File: C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\DRV8304.SchDoc

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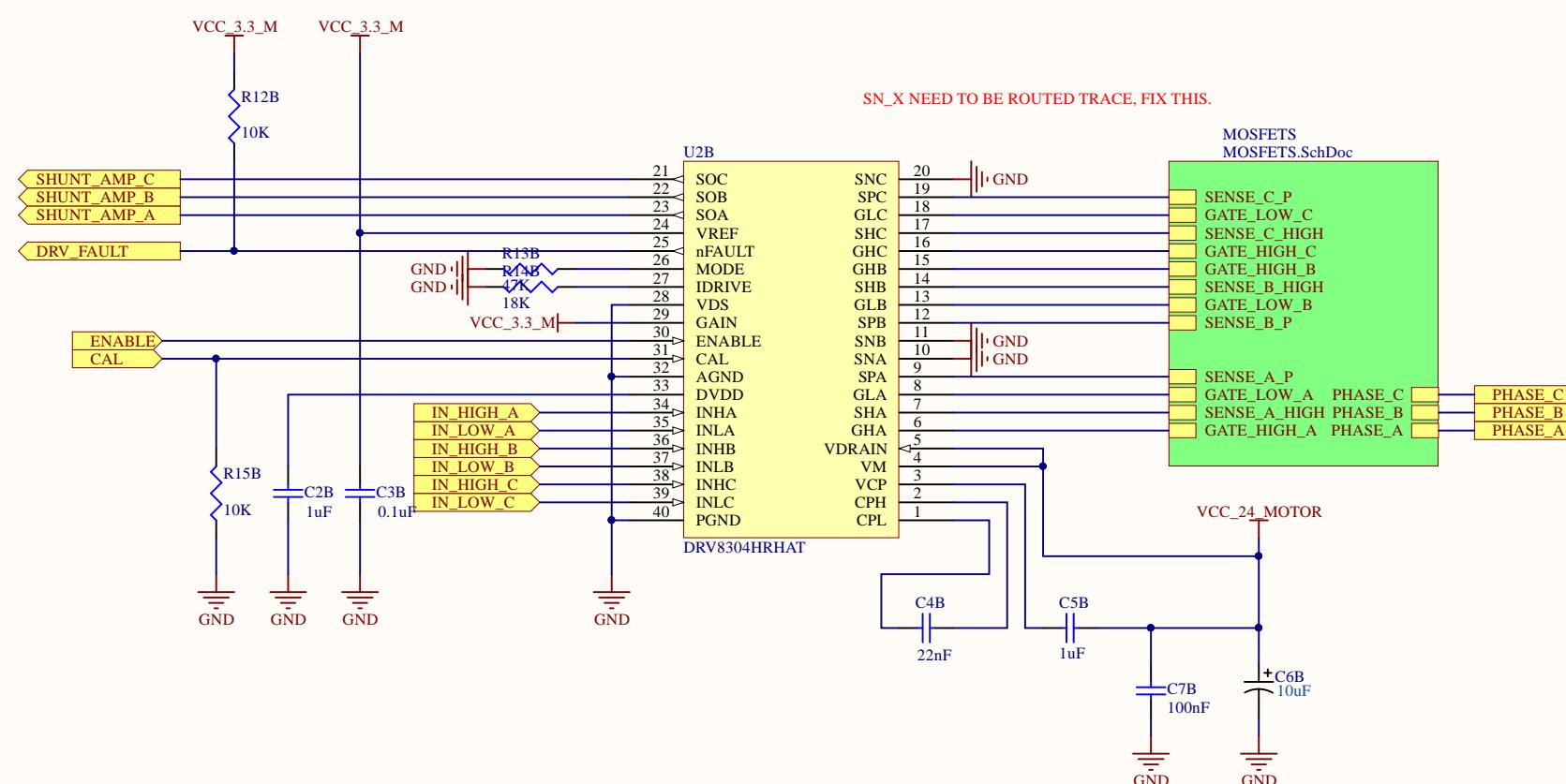
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Title **DRV8304**

Size: B Number:25 Revision: F

Date: 11/2/2019 Time: 12:58:40 PM Sheet 25 of 35

File: C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\DRV8304.SchDoc

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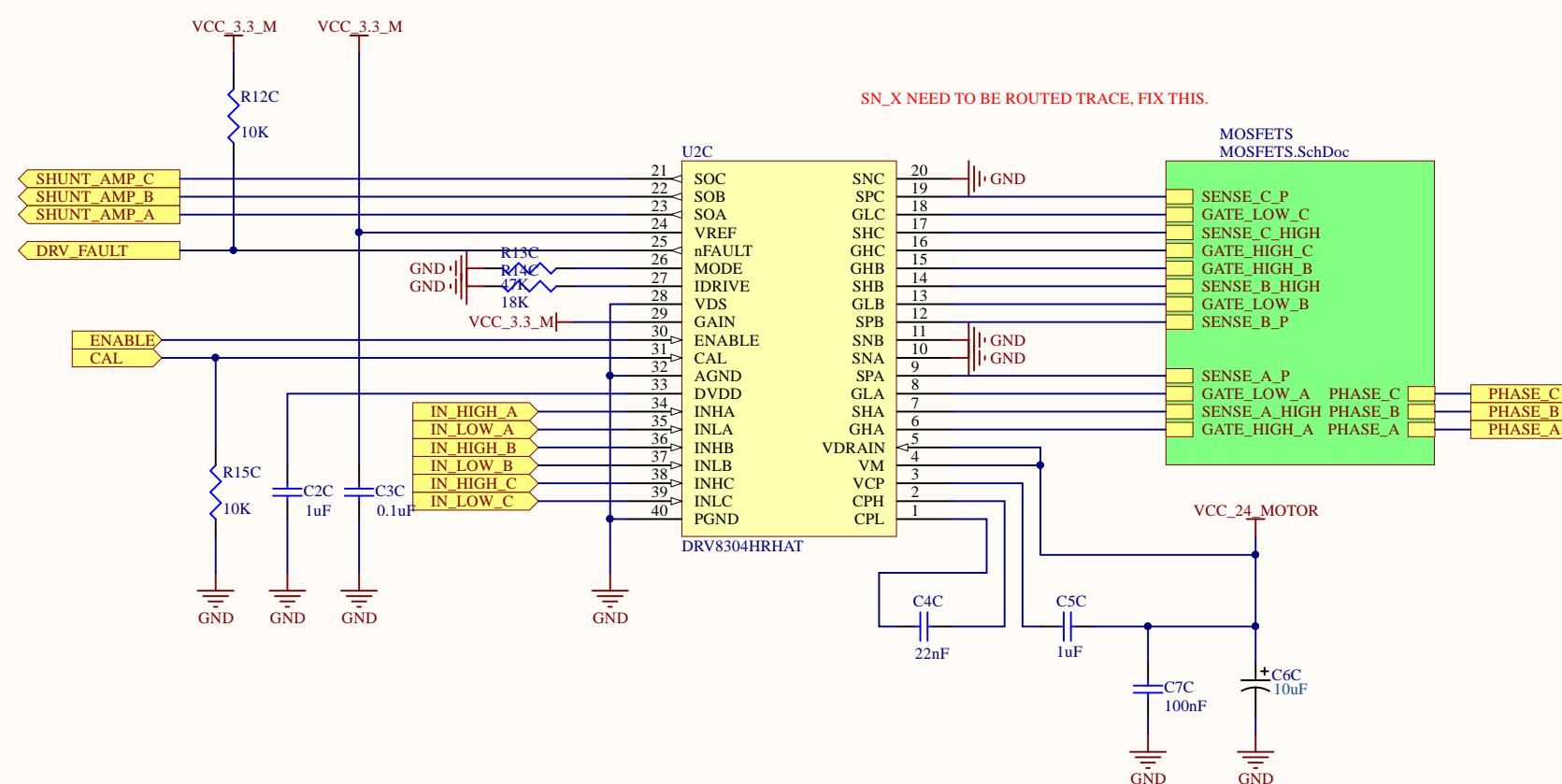
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Title: **DRV8304**Size: **B** Number: **25** Revision: **F**Date: **11/2/2019** Time: **12:58:40 PM** Sheet **25** of **35**File: **C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\DRV8304.SchDoc****Altium**

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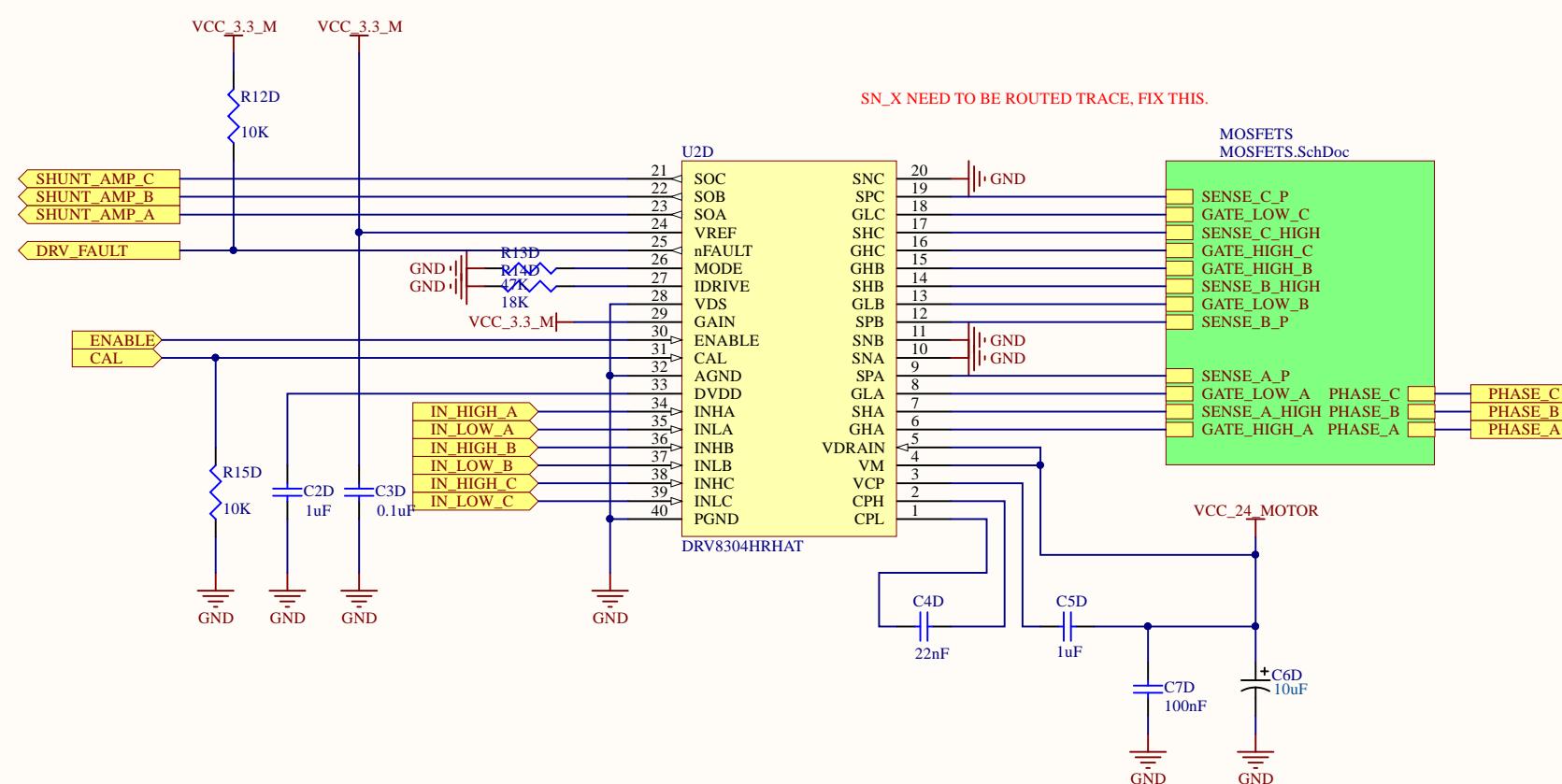
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Title **DRV8304**

Size: B Number:25 Revision: F

Date: 11/2/2019 Time: 12:58:40 PM Sheet 25 of 35

File: C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\DRV8304.SchDoc

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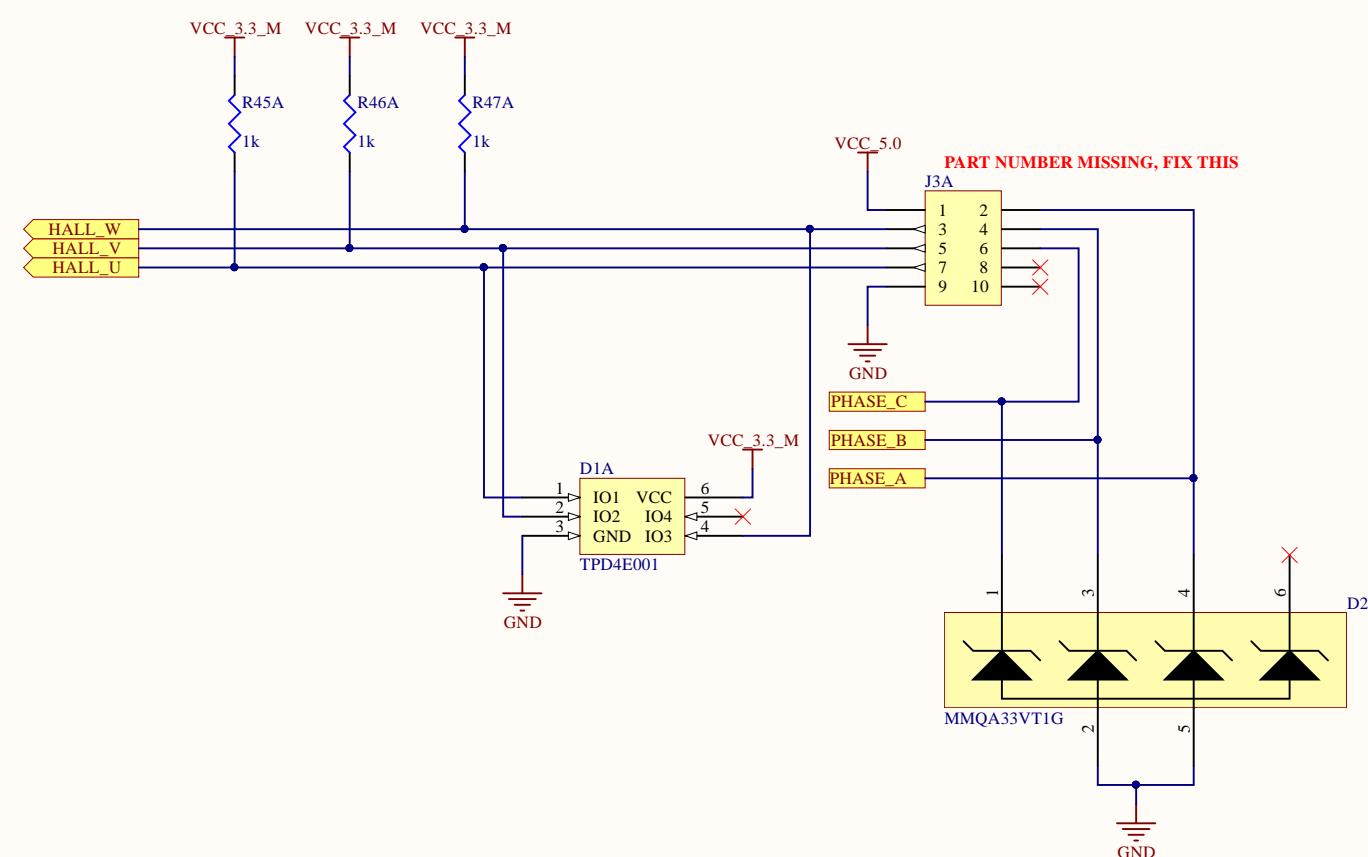
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Title **Motor Interface**

Size: B Number:26 Revision F

Date: 11/2/2019 Time: 12:58:40 PM Sheet 26 of 35

File: C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\MOTOR_HOOKUP.SchDoc

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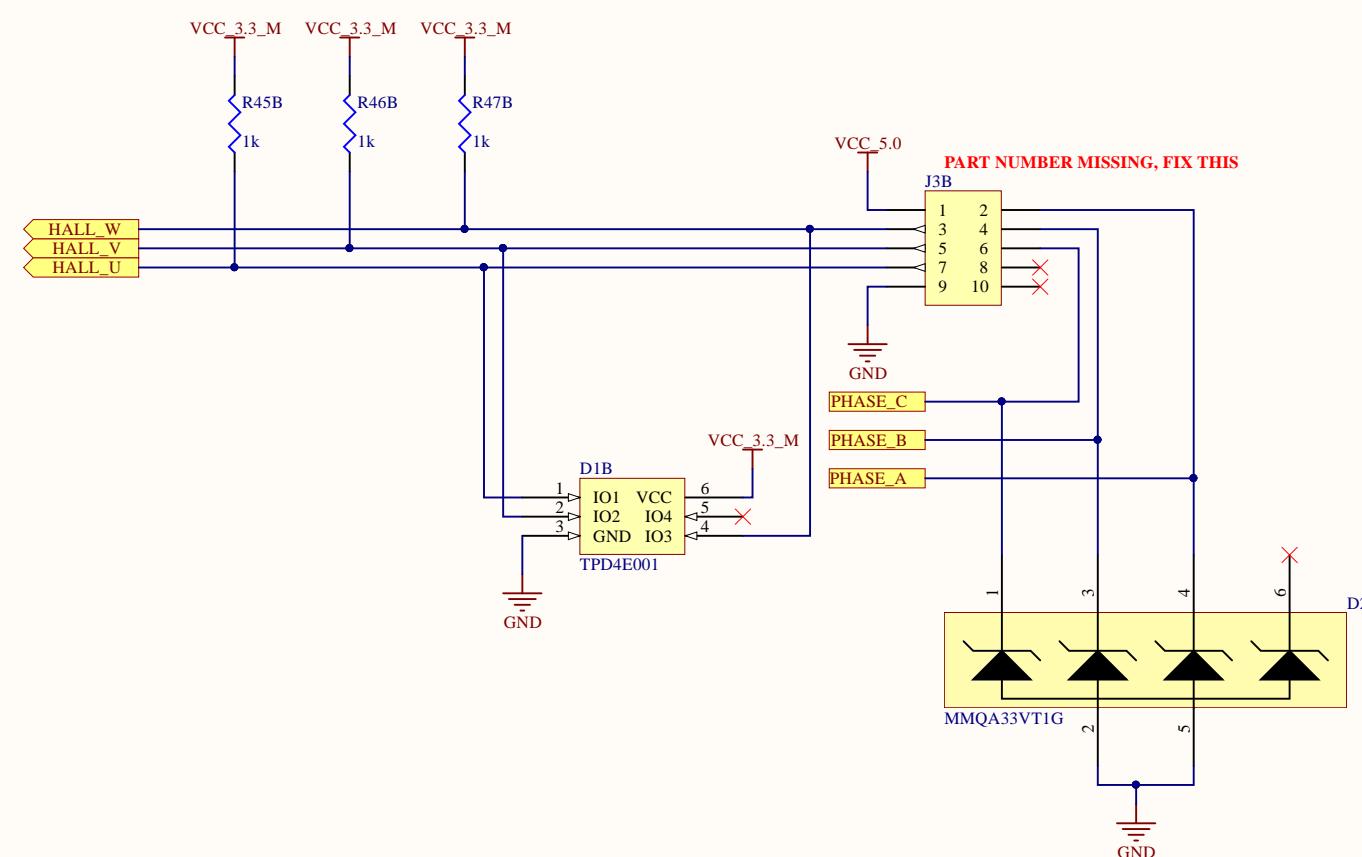
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Title **Motor Interface**

Size: B Number:26 Revision F

Date: 11/2/2019 Time: 12:58:40 PM Sheet 26 of 35

File: C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\MOTOR_HOOKUP.SchDoc

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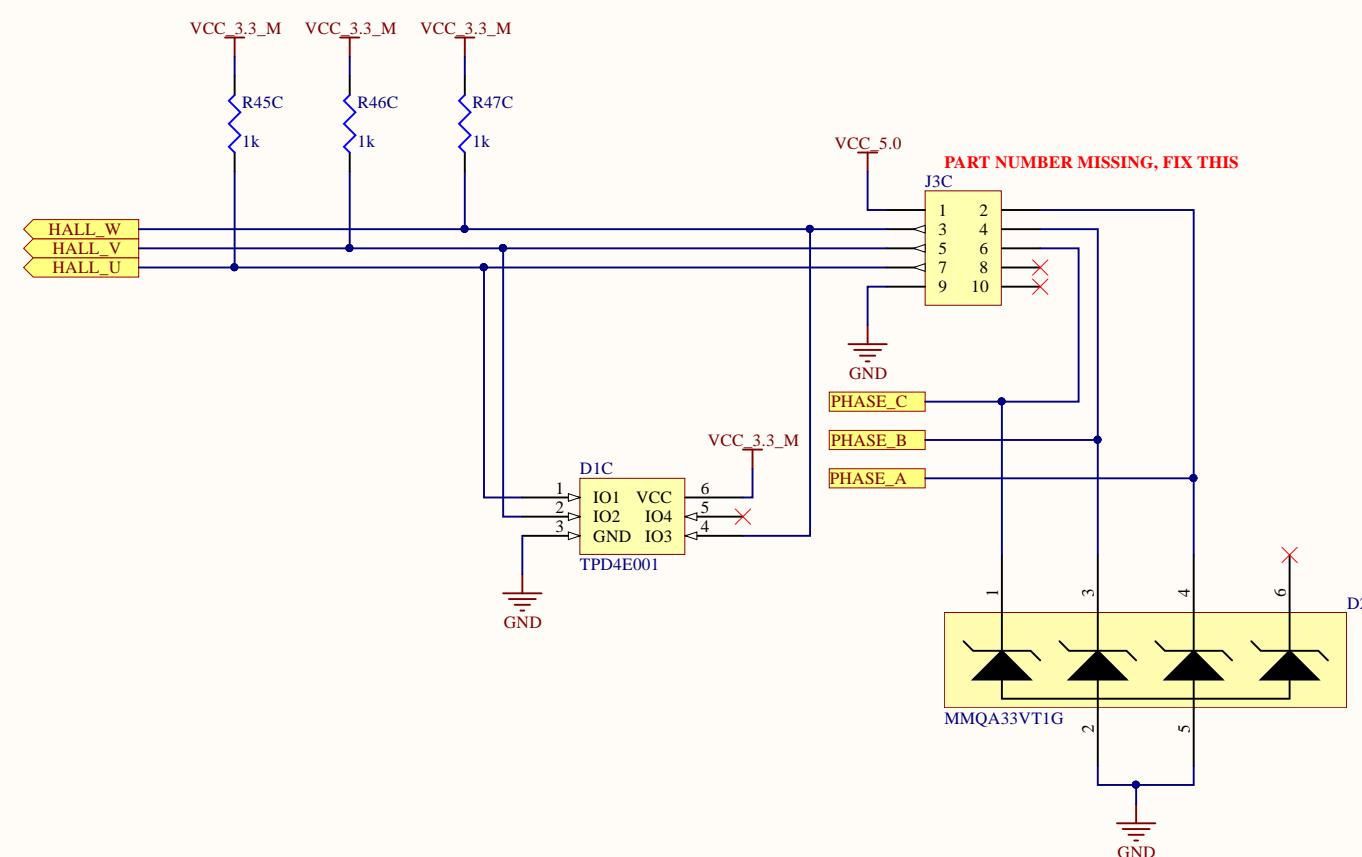
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Title **Motor Interface**

Size: B Number:26 Revision: F

Date: 11/2/2019 Time: 12:58:40 PM Sheet 26 of 35

File: C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\MOTOR_HOOKUP.SchDoc

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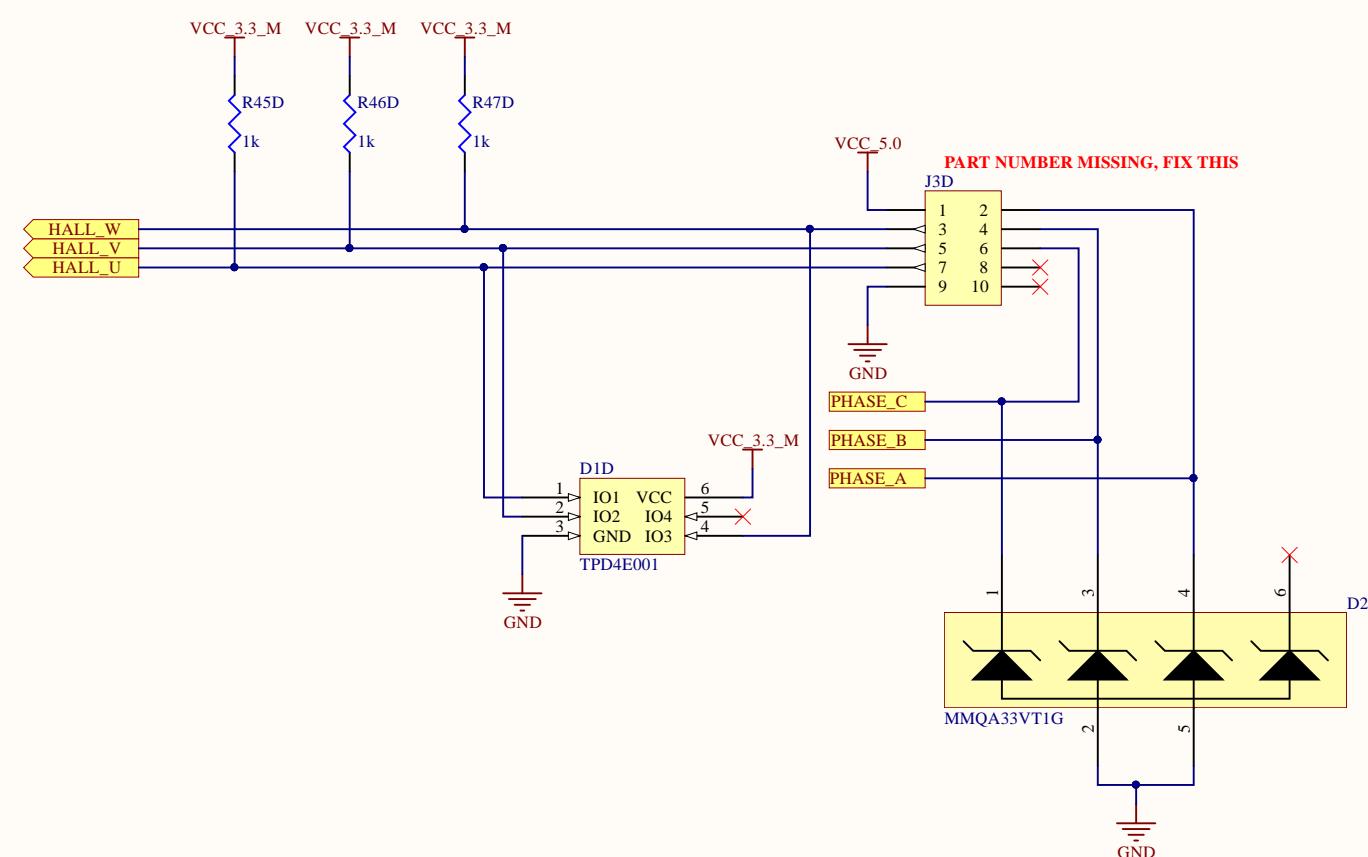
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Title Motor Interface			*	
Size: B	Number:26	Revision F	*	
Date: 11/2/2019 Time: 12:58:40 PM			Sheet 26 of 35	
File: C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\MOTOR_HOOKUP.SchDoc				

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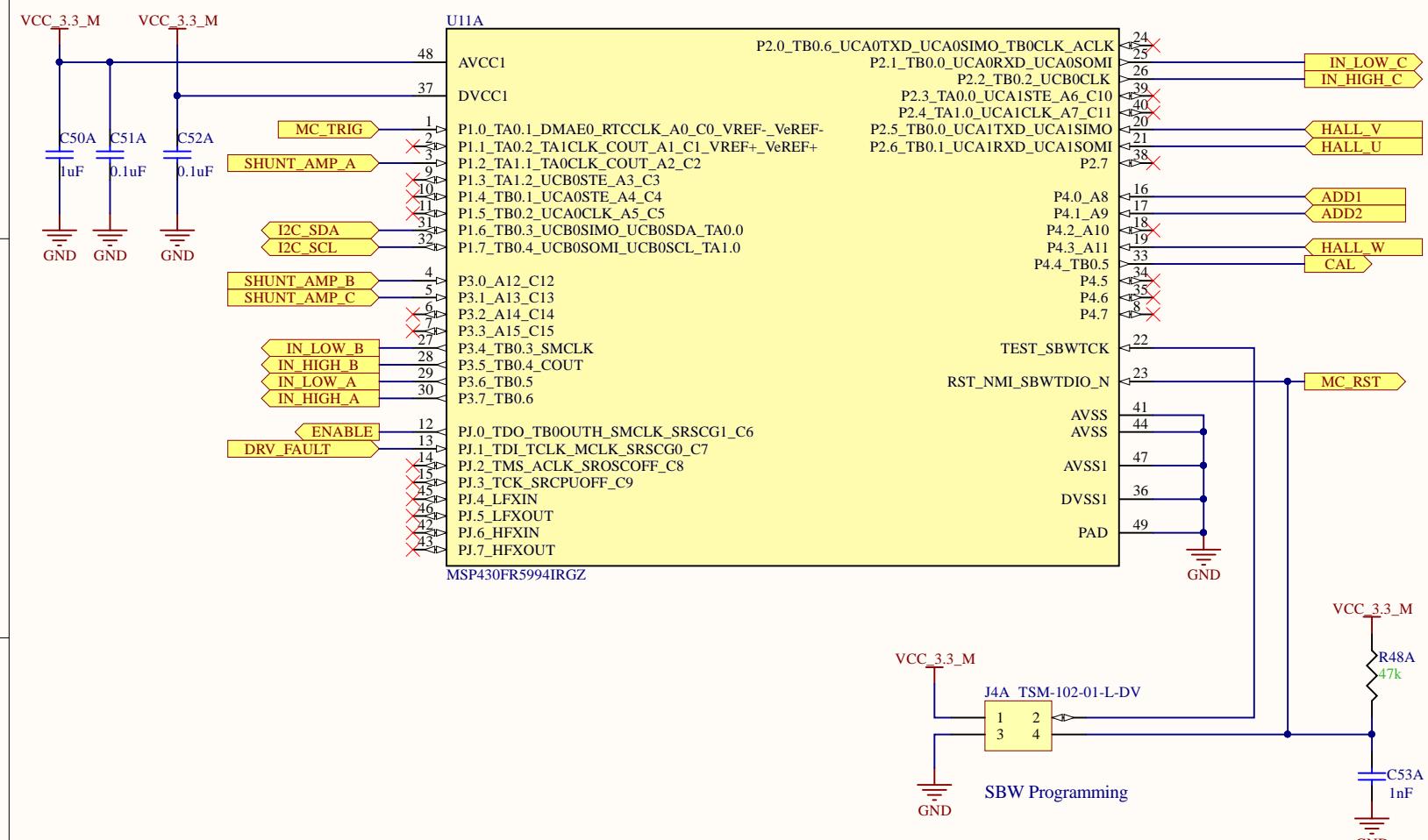
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Title **WATCHDOG**

CMU/ASTROBOTIC

Size: B Number:27 Revision: F

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Date: 11/2/2019 Time: 12:58:40 PM Sheet 27 of 35

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File: C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\MSP430.SchDoc

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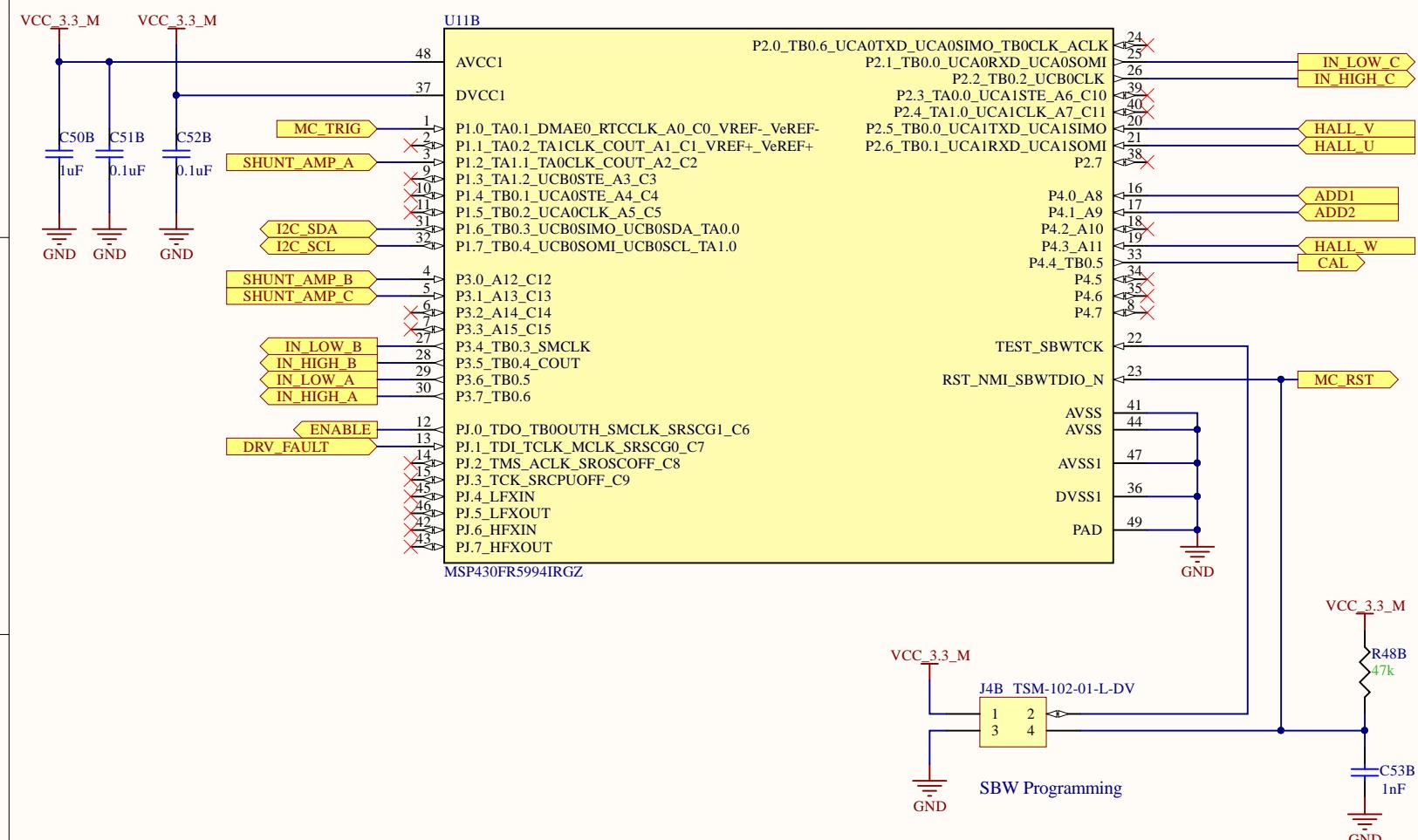
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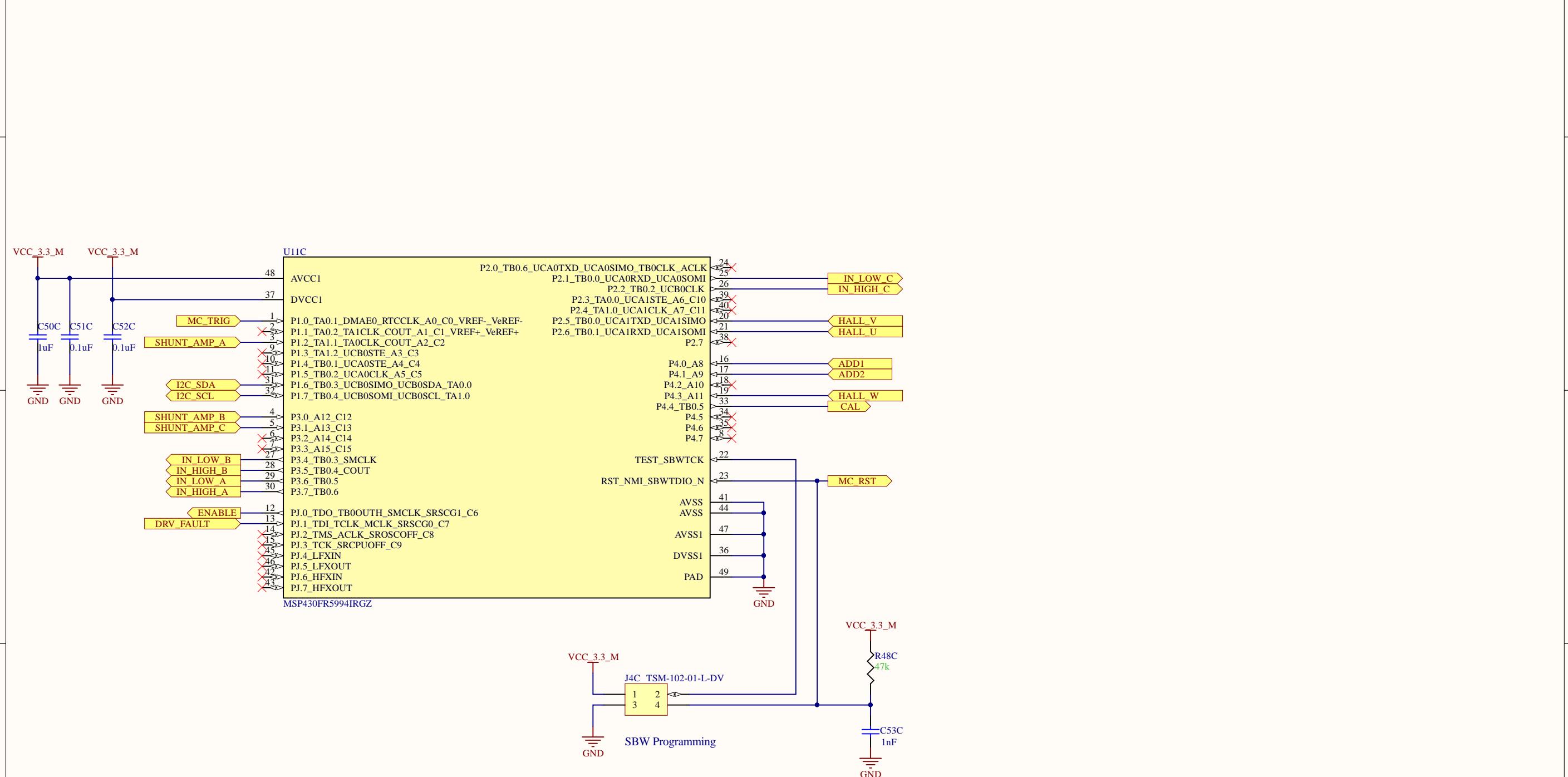
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Title **WATCHDOG**

Size: B	Number: 27	Revision: F
Date: 11/2/2019	Time: 12:58:40 PM	Sheet 27 of 35
File: C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\MSP430.SchDoc		

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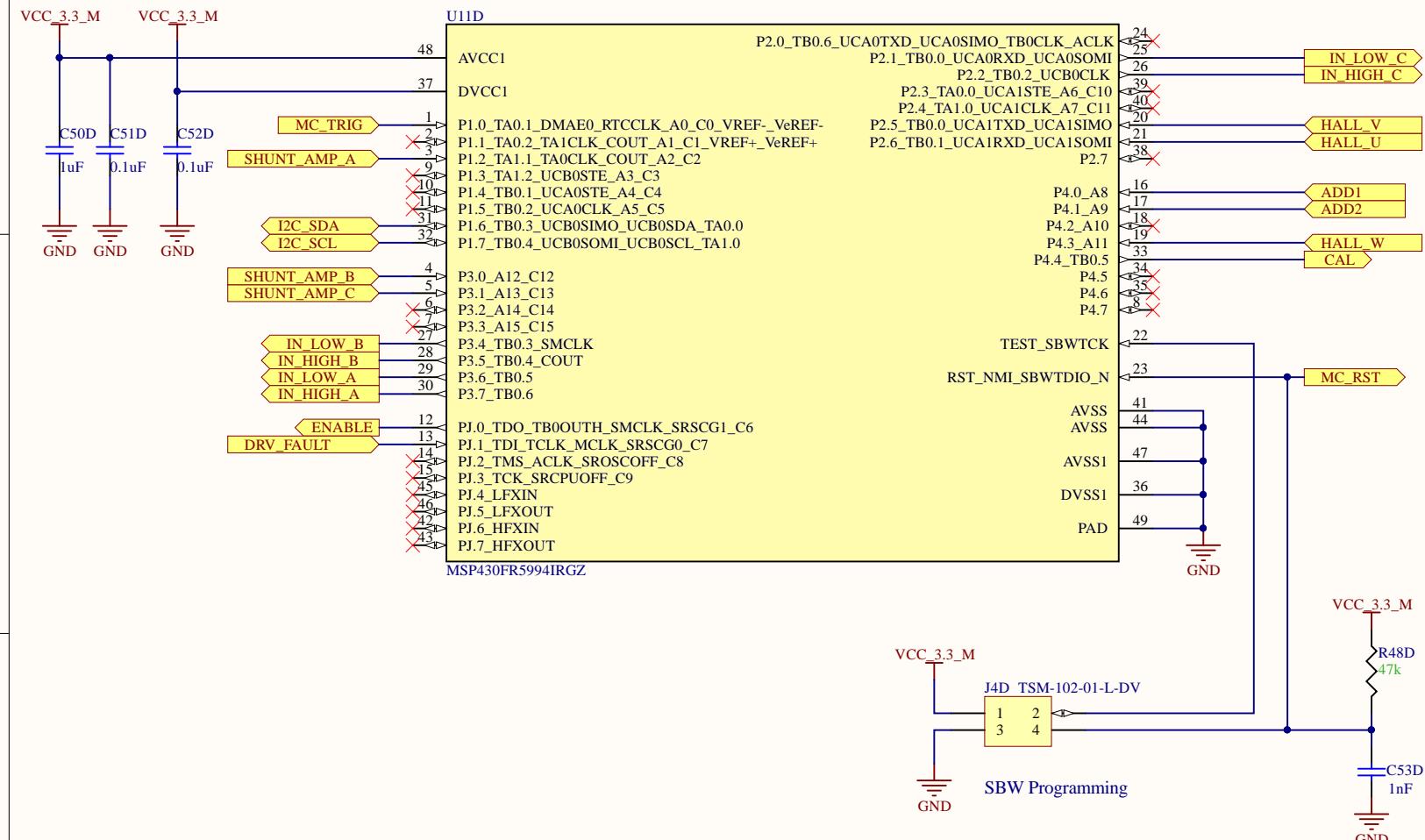
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Title **WATCHDOG**

CMU/ASTROBOTIC

Size: B Number:27 Revision: F

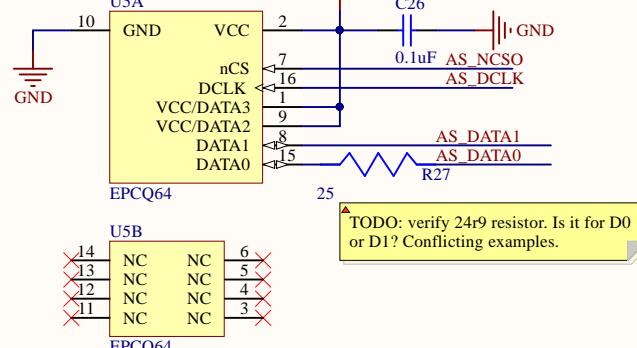
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Date: 11/2/2019 Time: 12:58:41 PM Sheet 27 of 35

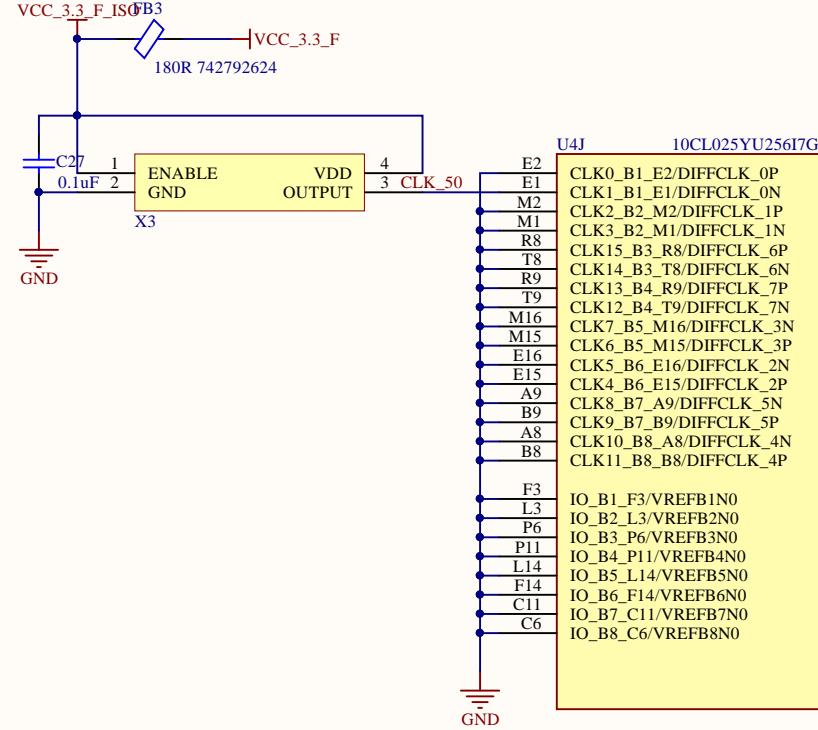
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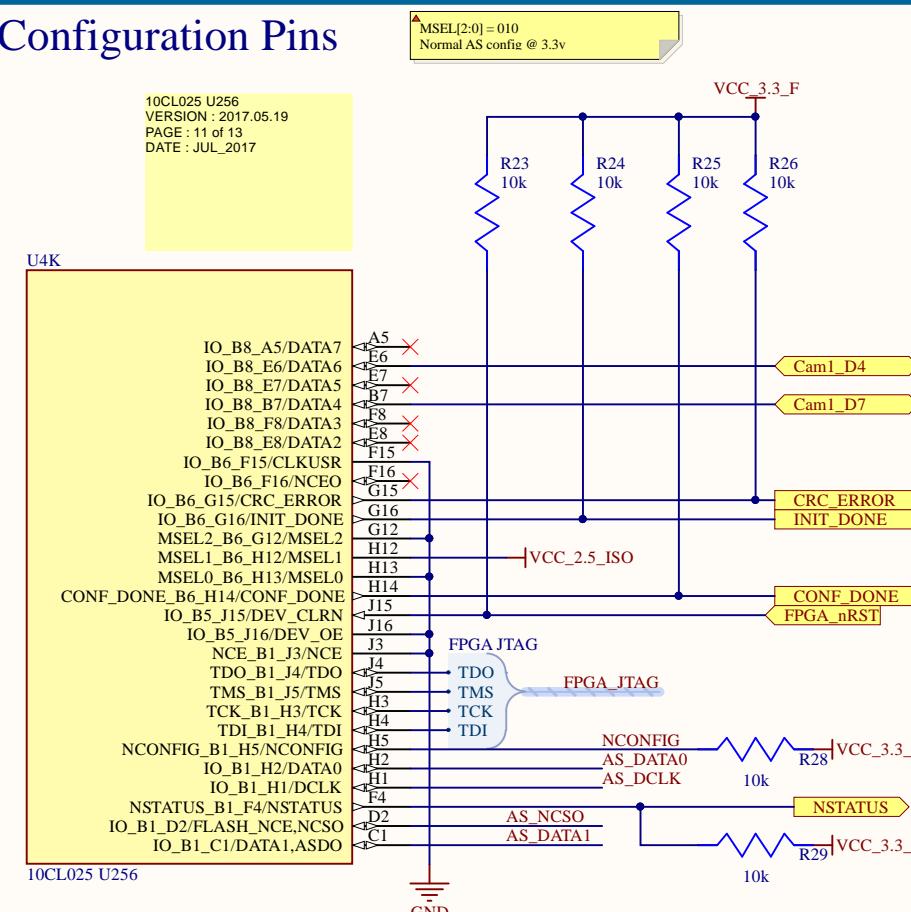
FPGA Configuration Flash



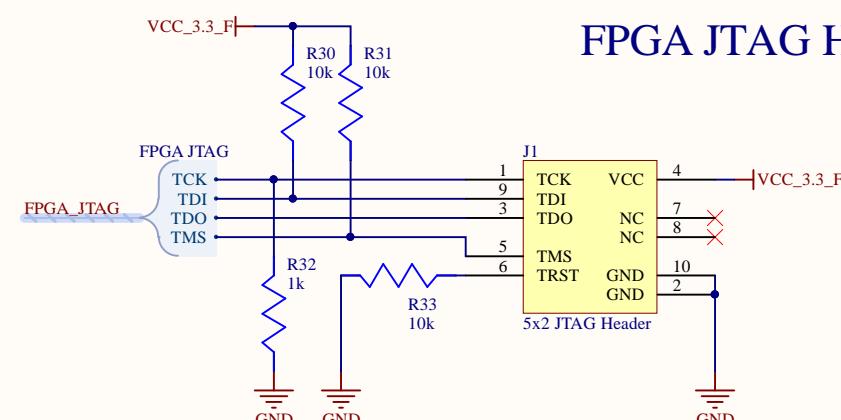
FPGA Clock/VREF

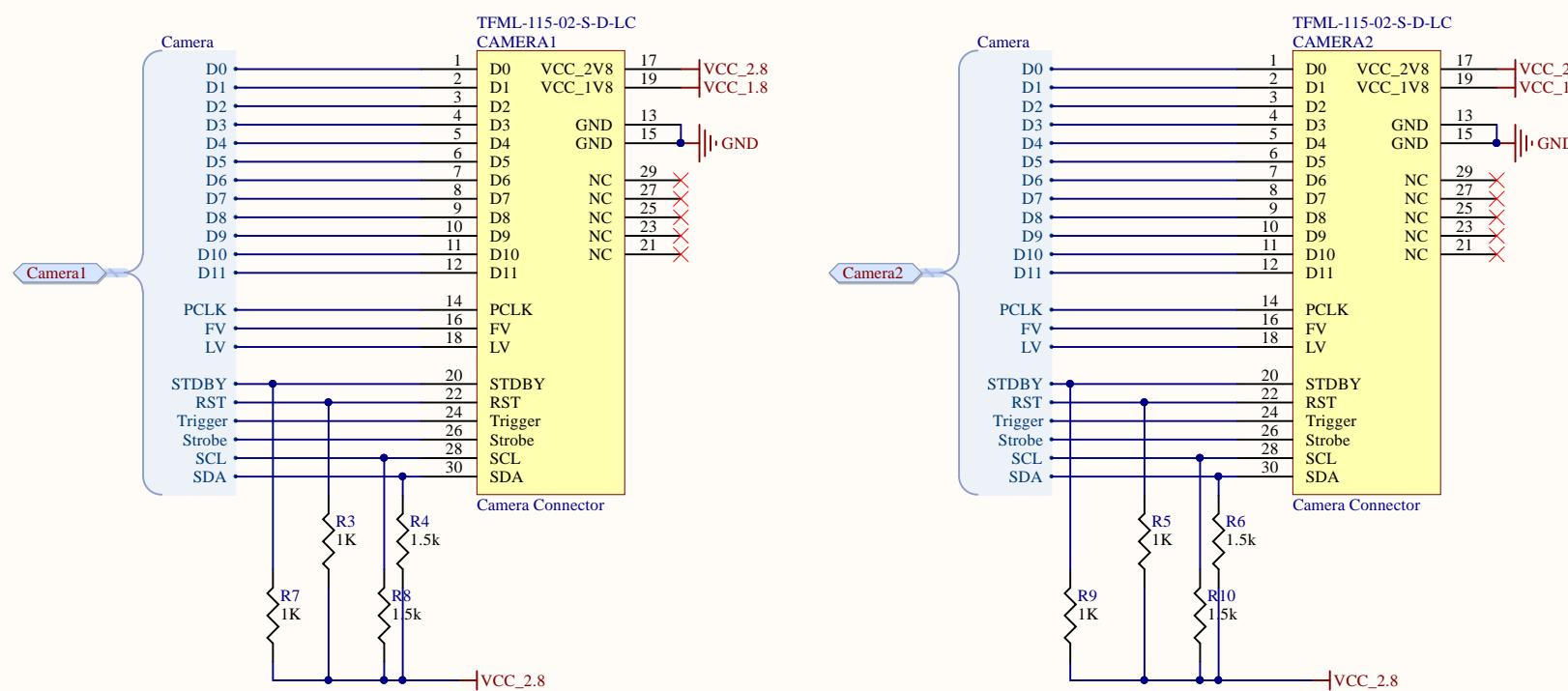


FPGA Configuration Pins



FPGA JTAG Header





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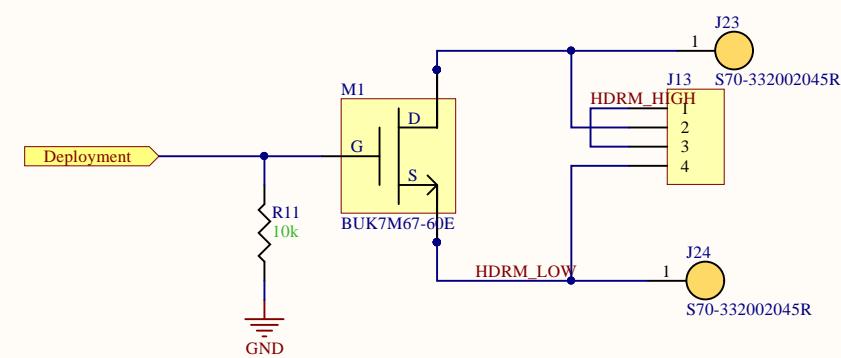
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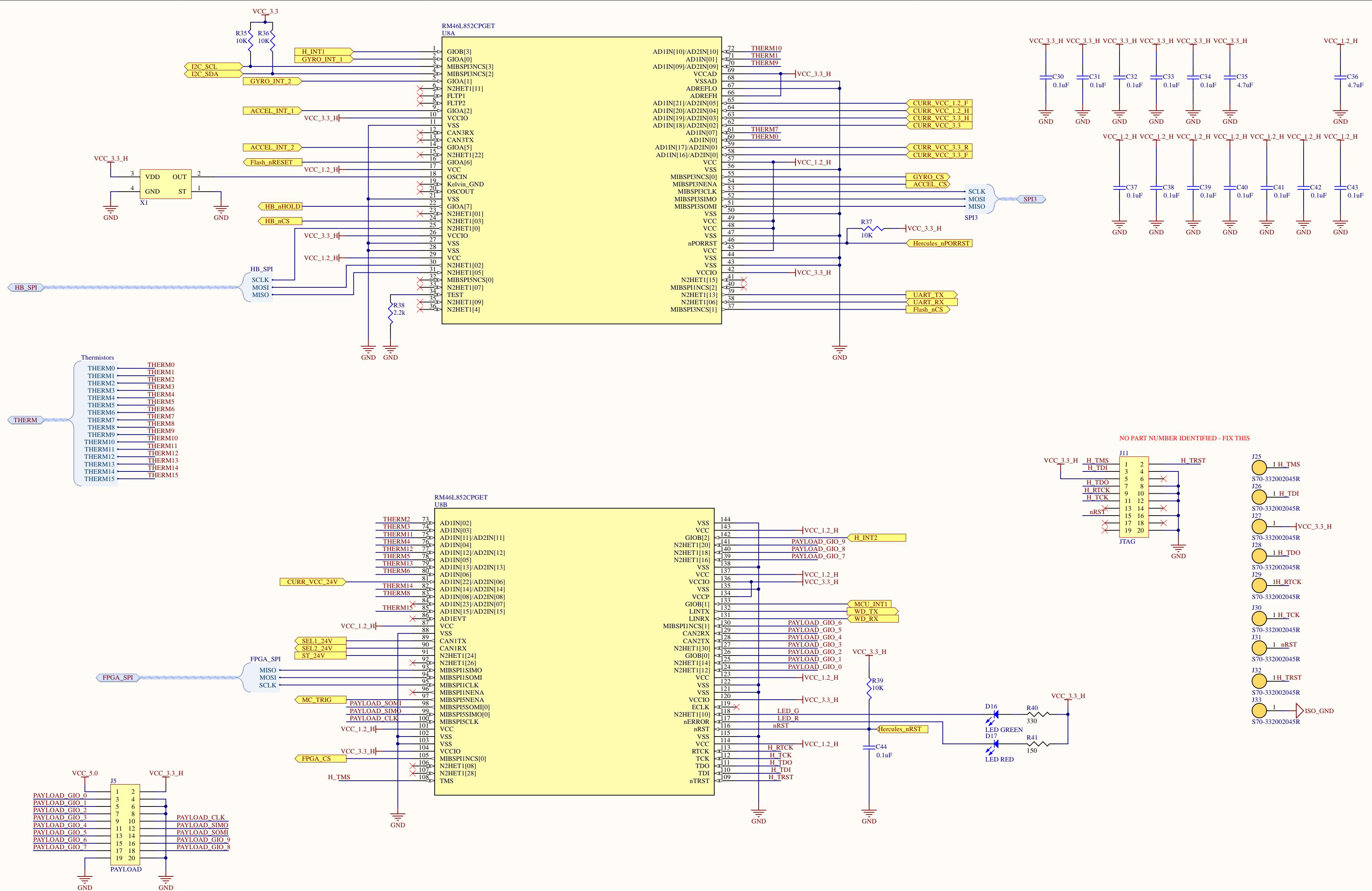
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Title DEPLOYMENT			*
Size: B	Number: 30	Revision: F	*
Date: 11/2/2019	Time: 12:58:41 PM	Sheet 30 of 35	*
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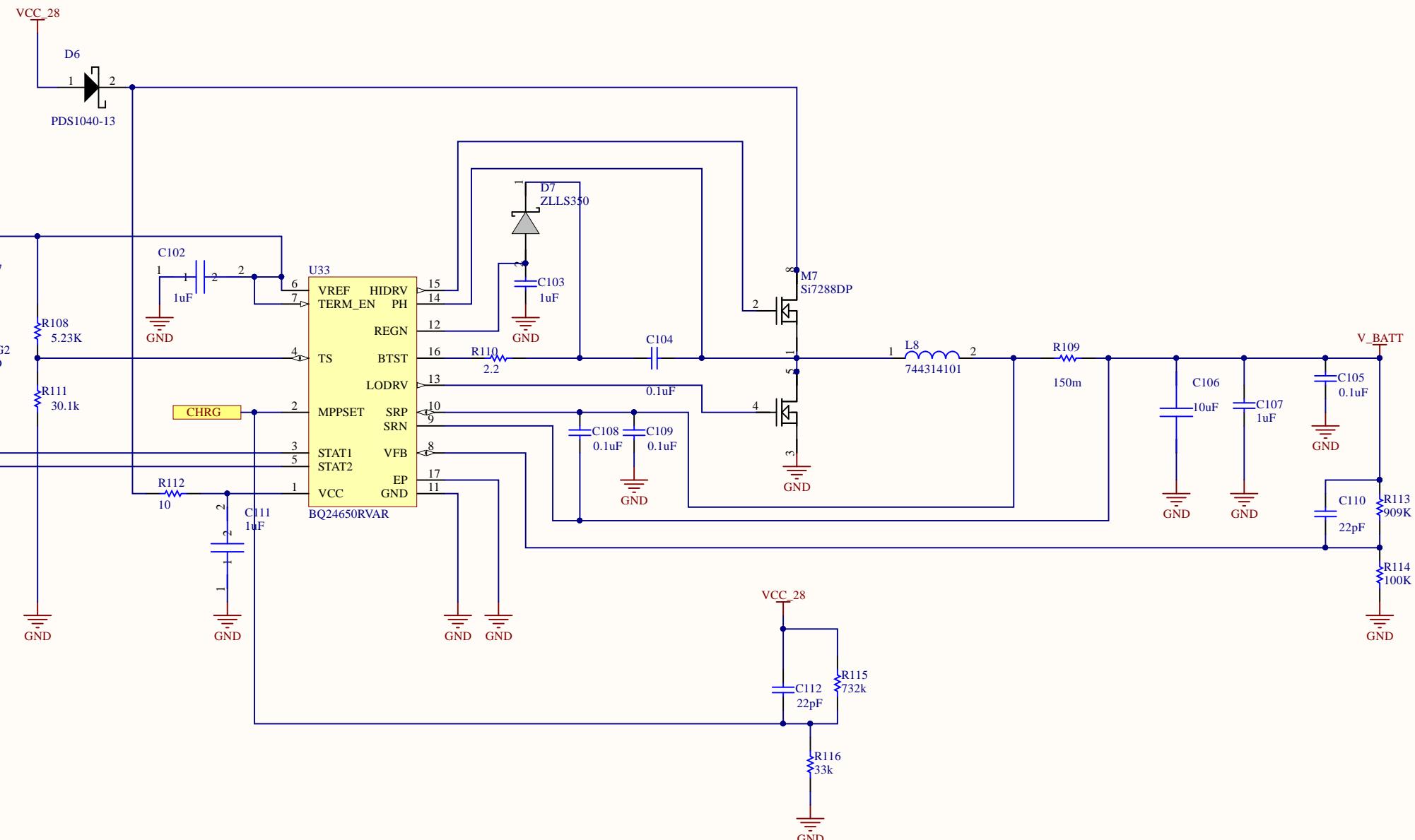
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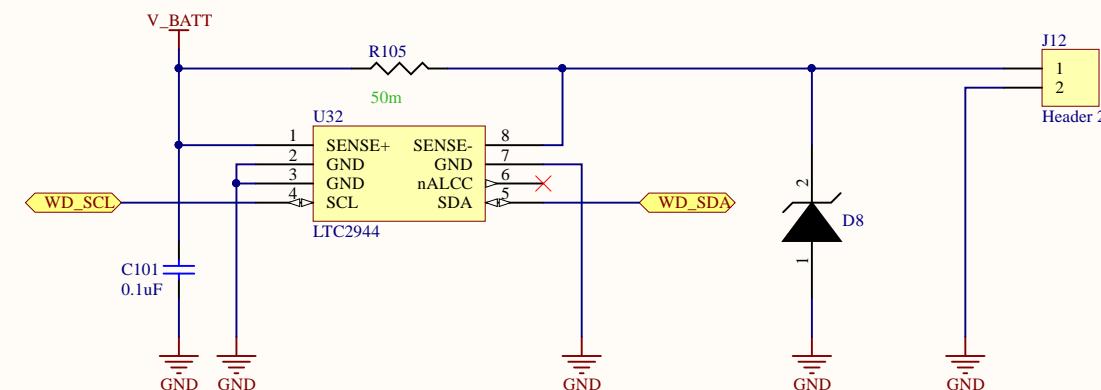
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Title **FUEL GAUGE**

Size: B Number:33 Revision F

Date: 11/2/2019 Time: 12:58:41 PM Sheet 33 of 35

File: C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\FuelGauge.SchDoc

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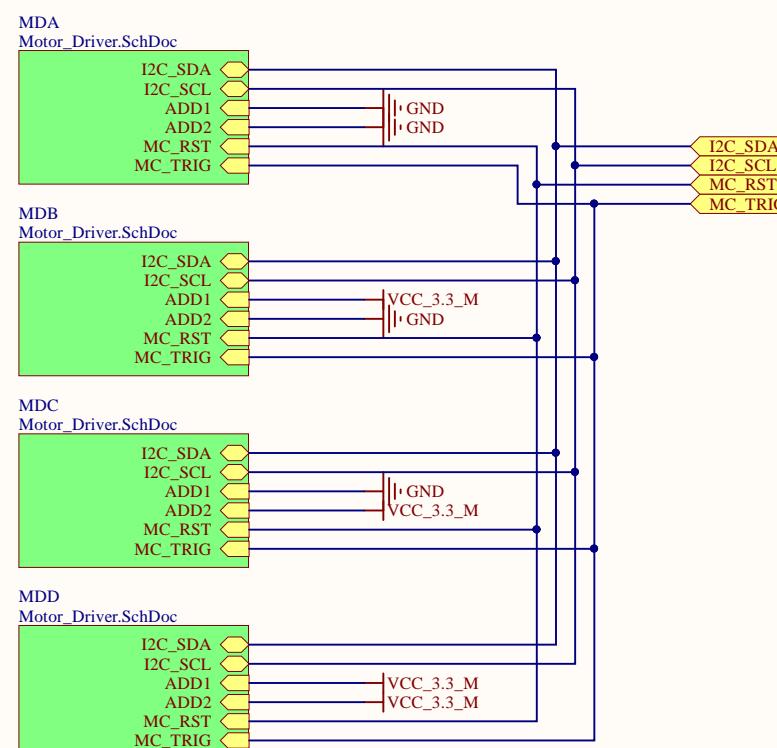
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Title **MOTORS**

Size: B Number:34 Revision F

Date: 11/2/2019 Time: 12:58:41 PM Sheet 34 of 35

File: C:\Users\Cedric.Corpadelafuen\OneDrive\Altium\CMU Repo\Tmp\Motors.SchDoc

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