

# Schottky Barrier Diodes BAT54T1G, SBAT54T1G

These Schottky barrier diodes are designed for high speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction loss. Miniature surface mount package is excellent for hand held and portable applications where space is limited.

#### **Features**

- Extremely Fast Switching Speed
- Low Forward Voltage 0.35 Volts (Typ) @  $I_F = 10 \text{ mAdc}$
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

# **MAXIMUM RATINGS** (T<sub>J</sub> = 125°C unless otherwise noted)

Rating	Symbol	Value	Unit
Reverse Voltage	V <sub>R</sub>	30	V
Forward Power Dissipation, FR–5 Board (Note 1)  @ T <sub>A</sub> = 25°C Derate above 25°C	P <sub>F</sub>	400 4.0	mW mW/°C
Thermal Resistance, Junction-to-Case	$R_{ heta JL}$	174	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	492	°C/W
Forward Current (DC)	IF	200 Max	mA
Non-Repetitive Peak Forward Current t <sub>p</sub> < 10 msec	I <sub>FSM</sub>	600	mA
Repetitive Peak Forward Current Pulse Wave = 1 sec, Duty Cycle = 66%	I <sub>FRM</sub>	300	mA
Junction Temperature	TJ	-55 to 125	°C
Storage Temperature Range	T <sub>stg</sub>	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

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# 30 VOLT SCHOTTKY BARRIER DETECTOR AND SWITCHING DIODES





SOD-123 CASE 425 STYLE 1

#### **MARKING DIAGRAM**



SB = Device Code

M = Date Code

Pb-Free Package

(Note: Microdot may be in either location)

## **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
BAT54T1G	SOD-123 (Pb-Free)	3000 / Tape & Reel
SBAT54T1G	SOD-123 (Pb-Free)	3000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

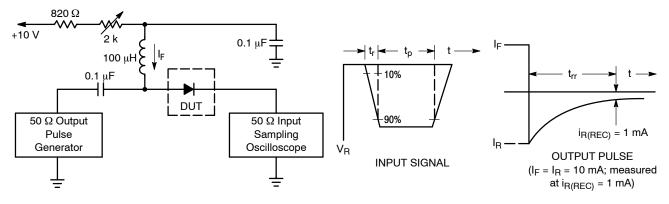
<sup>1.</sup>  $FR-5 = 1.0 \times 0.75 \times 0.062$  in.

# BAT54T1G, SBAT54T1G

# **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Breakdown Voltage $(I_R = 10 \mu A)$	V <sub>(BR)R</sub>	30	-	-	V
Total Capacitance (V <sub>R</sub> = 1.0 V, f = 1.0 MHz)	C <sub>T</sub>	-	7.6	10	pF
Reverse Leakage (V <sub>R</sub> = 25 V)	I <sub>R</sub>	-	0.2	2.0	μAdc
Forward Voltage (I <sub>F</sub> = 0.1 mAdc)	V <sub>F</sub>	-	0.22	0.24	Vdc
Forward Voltage (I <sub>F</sub> = 30 mAdc)	V <sub>F</sub>	-	0.41	0.5	Vdc
Forward Voltage (I <sub>F</sub> = 100 mAdc)	V <sub>F</sub>	-	0.52	0.8	Vdc
Reverse Recovery Time $(I_F = I_R = 10 \text{ mAdc}, I_{R(REC)} = 1.0 \text{ mAdc}, Figure 1)$	t <sub>rr</sub>	-	-	5.0	ns
Forward Voltage (I <sub>F</sub> = 1.0 mAdc)	V <sub>F</sub>	-	0.29	0.32	Vdc
Forward Voltage (I <sub>F</sub> = 10 mAdc)	V <sub>F</sub>	-	0.35	0.40	Vdc

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



Notes: 1. A 2.0 k $\Omega$  variable resistor adjusted for a Forward Current (I<sub>F</sub>) of 10 mA.

- 2. Input pulse is adjusted so  $I_{R(peak)}$  is equal to 10 mA.
- 3.  $t_p \gg t_{rr}$

Figure 1. Recovery Time Equivalent Test Circuit

# BAT54T1G, SBAT54T1G

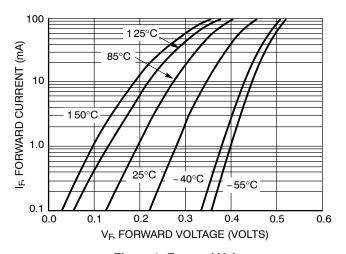


Figure 2. Forward Voltage

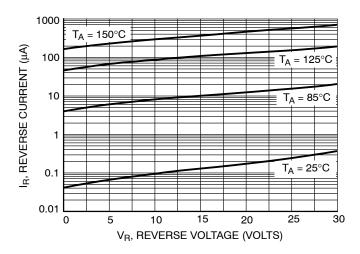


Figure 3. Leakage Current

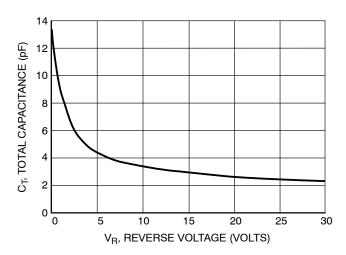


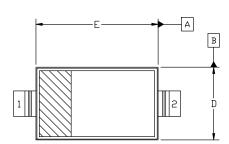
Figure 4. Total Capacitance



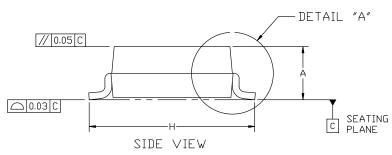


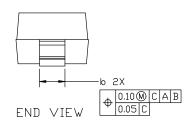
# SOD-123 2-LEAD, 1.60x2.69x1.16 **CASE 425 ISSUE H**

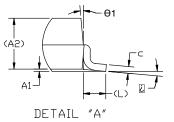
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TOP VIEW



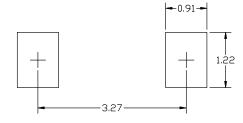




### NOTES:

- DIMENSION AND TOLERANCING PER ASME Y14.5M, 2018
- 2. CONTROLLING DIMENSION: MILLIMETERS

	MILLIMETER			
DIM	MIN.	N□M.	MAX.	
А	0.94	1.17	1.35	
A1	0.00	0.05	0.10	
A2	1.16 REF.			
b	0.51	0.61	0.71	
C	_	_	0.15	
D	1.40	1.60	1.80	
Е	2.54	2.69	2.84	
Н	3.56	3.68	3,86	
L	0.25 REF.			
<u>S</u>	0°		10°	
θ1	0°		10°	



RECOMMENDED MOUNTING FOOTPRINT \*For additional information on or Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference manual SDLDERRM/D.

# **GENERIC MARKING DIAGRAM\***



XXX = Specific Device Code

= Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

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DESCRIPTION:	SOD-123 2-LEAD, 1.60x2.69x1.16		PAGE 1 OF 1	

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