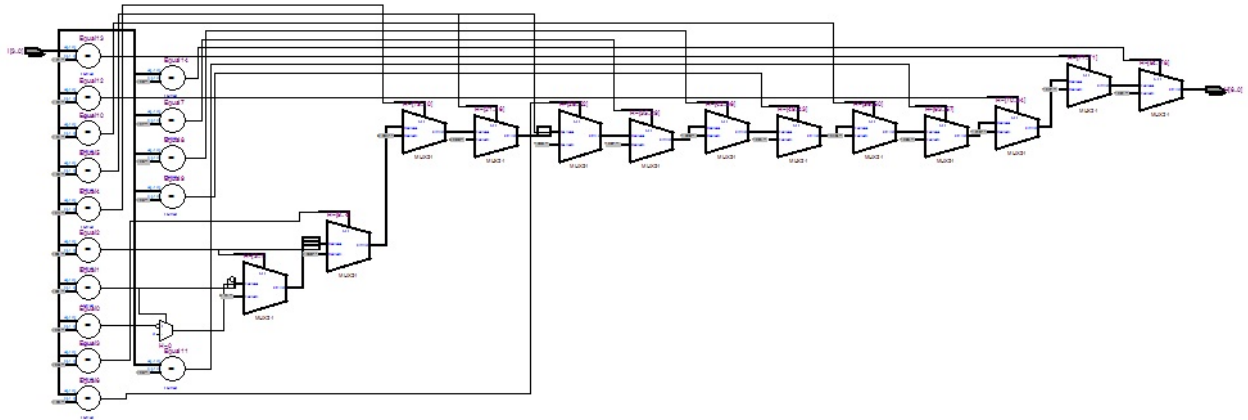
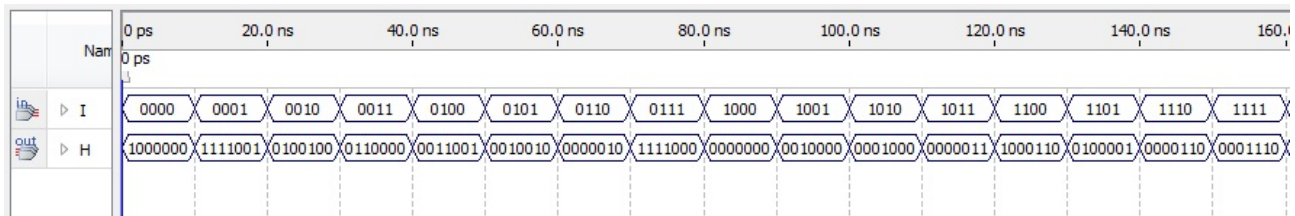


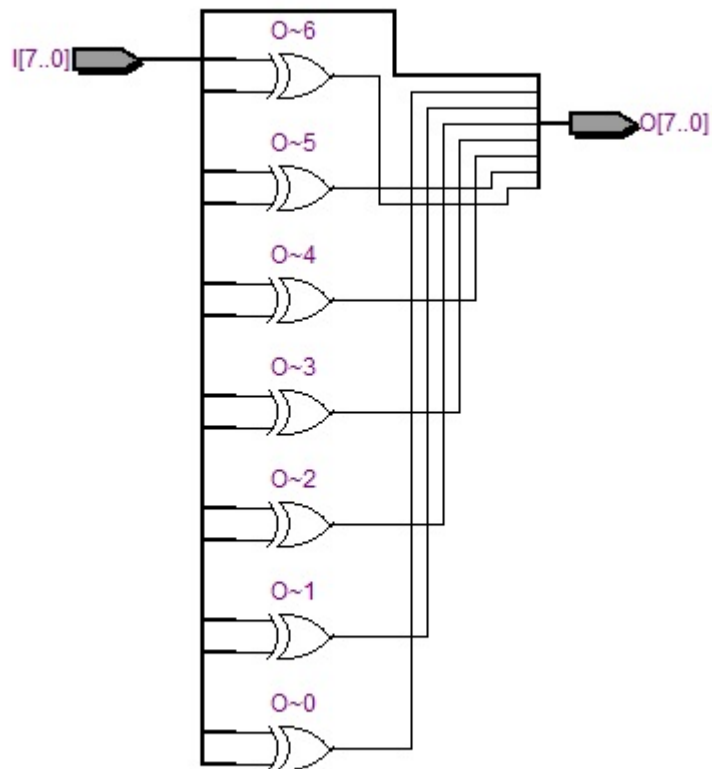
RTL Diagram for Binary Coded Decimal to Seven Segment Display Decoder:



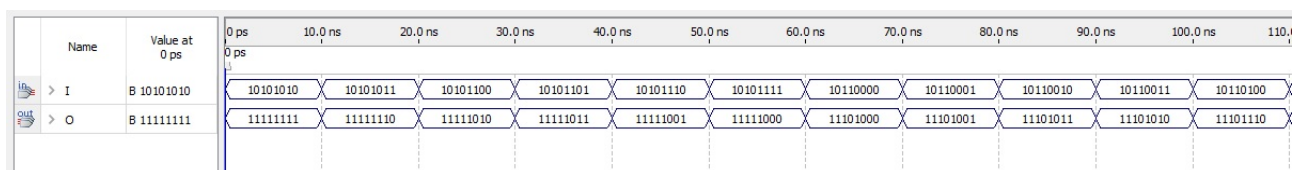
Waveform for Binary Coded Decimal to Seven Segment Display Decoder:



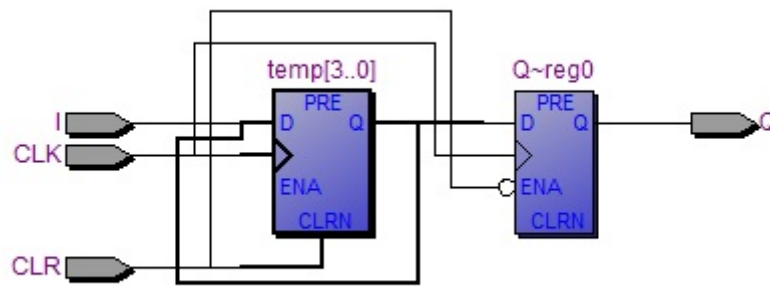
RTL Diagram for Binary to Gray Code Convertor:



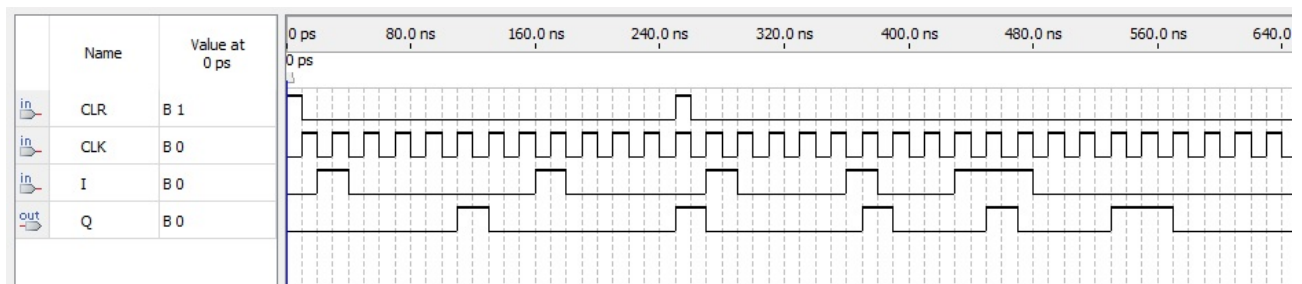
Output Waveform for Binary to Gray Code Convertor:



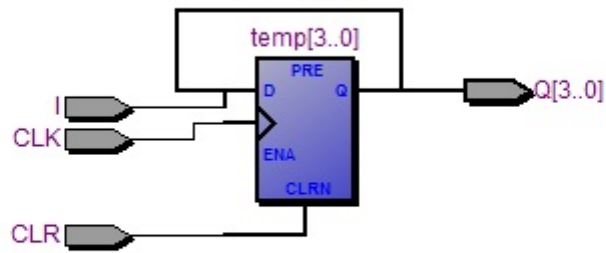
RTL Diagram for Serial Input Serial Output (SISO) Shift Register:



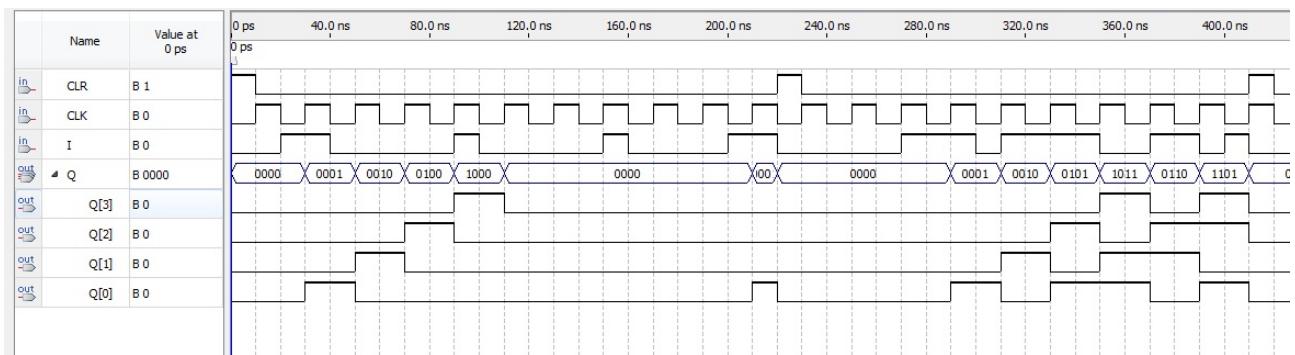
Output Waveform for Serial Input Serial Output (SISO) Shift Register:



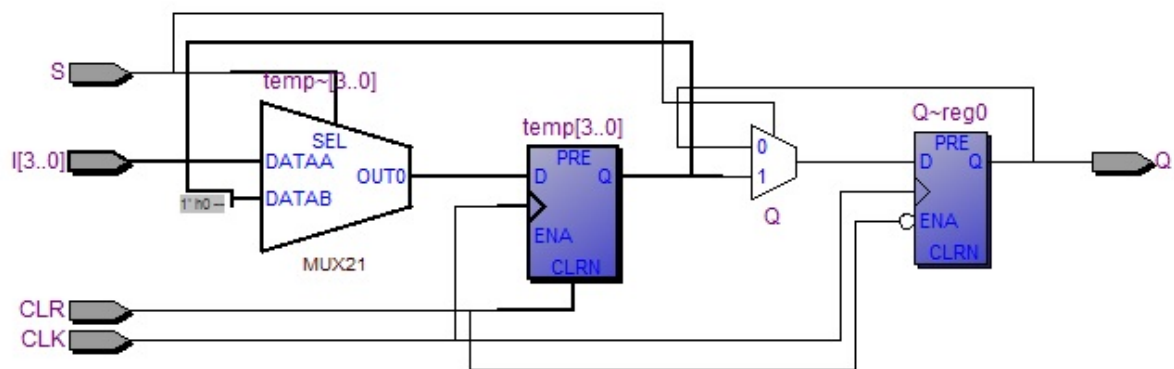
RTL Diagram for Serial Input Parallel Output (SIPO) Shift Register:



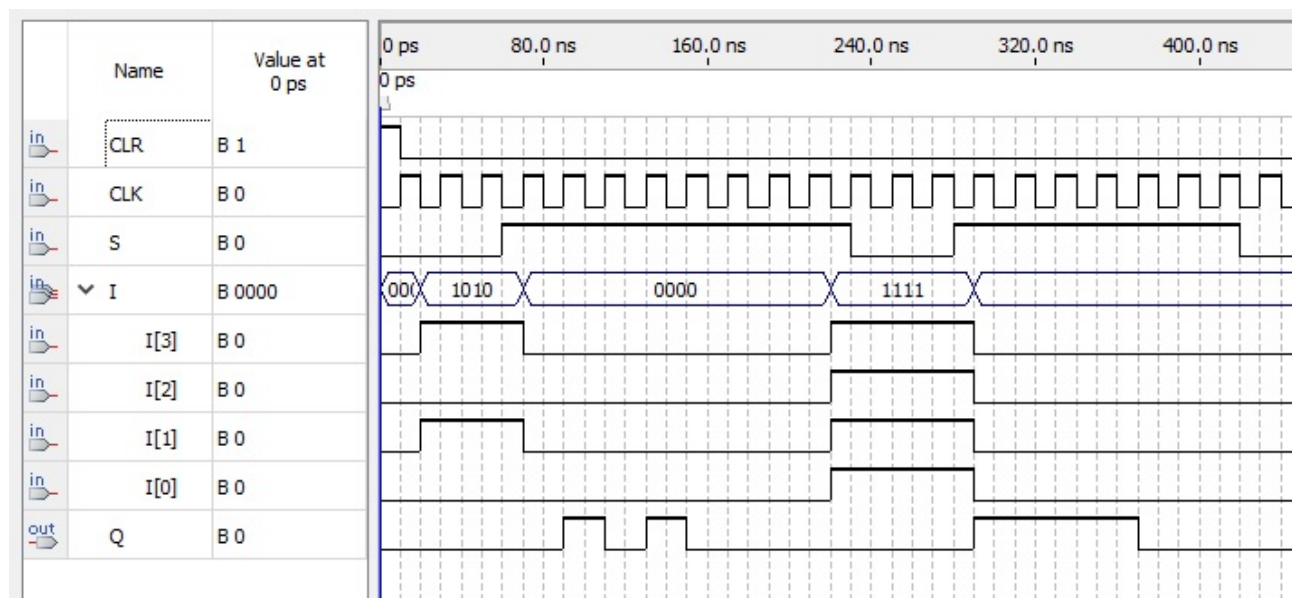
Output Waveform for Serial Input Parallel Output (SIPO) Shift Register:



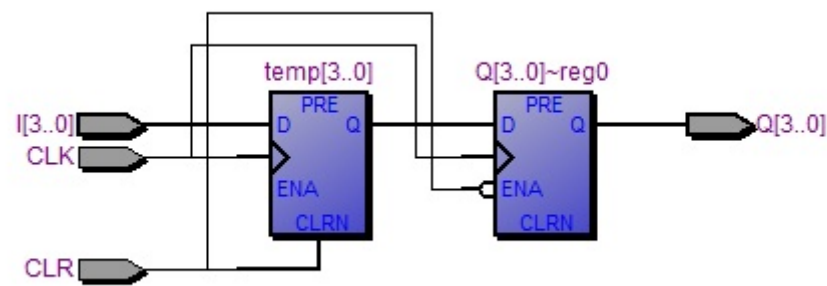
RTL Diagram for Parallel Input Serial Output (PISO) Shift Register:



Output Waveform for Parallel Input Serial Output (PISO) Shift Register:



RTL Diagram for Parallel Input Parallel Output (PIPO) Shift Register:



Output Waveform for Parallel Input Parallel Output (PIPO) Shift Register:

