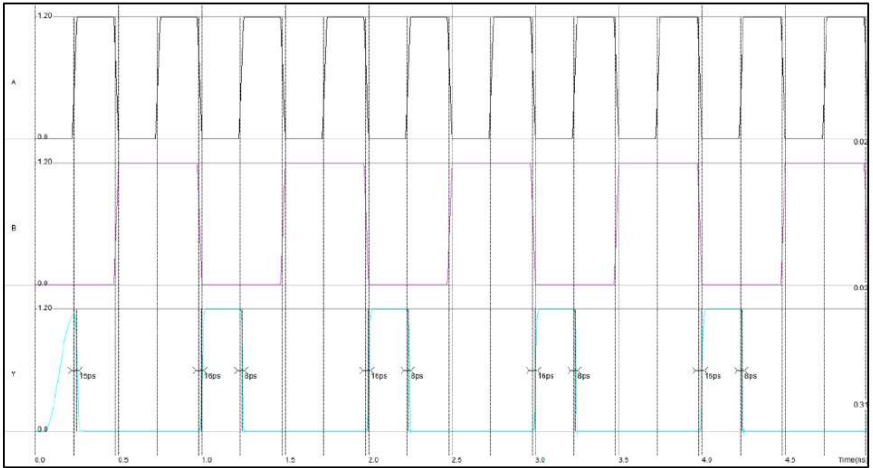
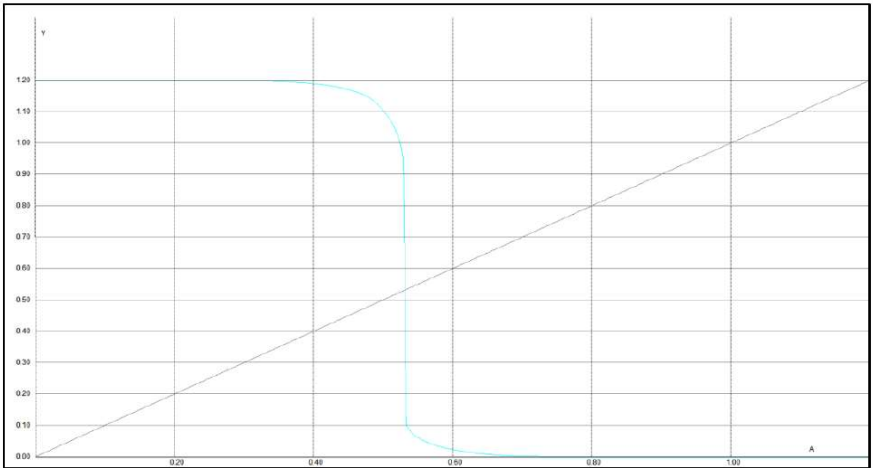


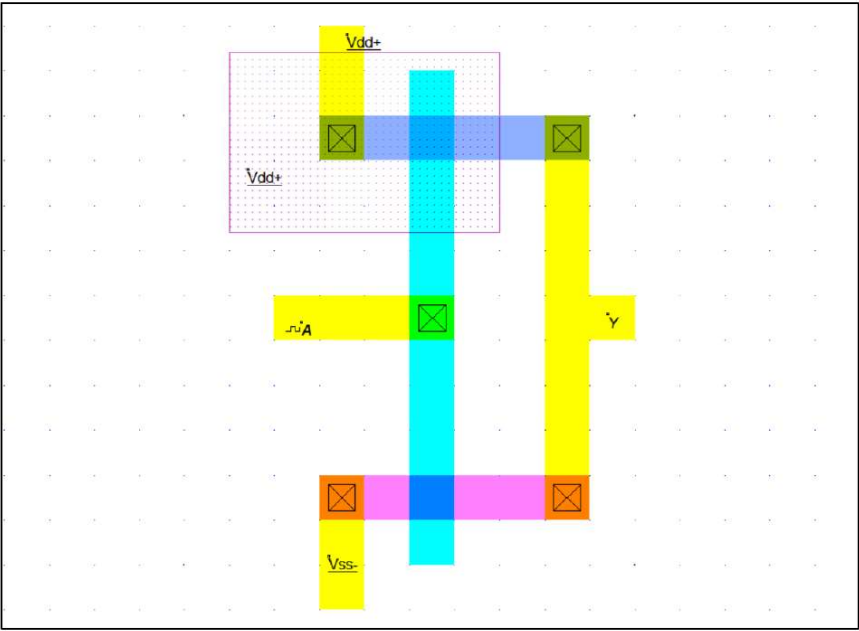
NOR GATE



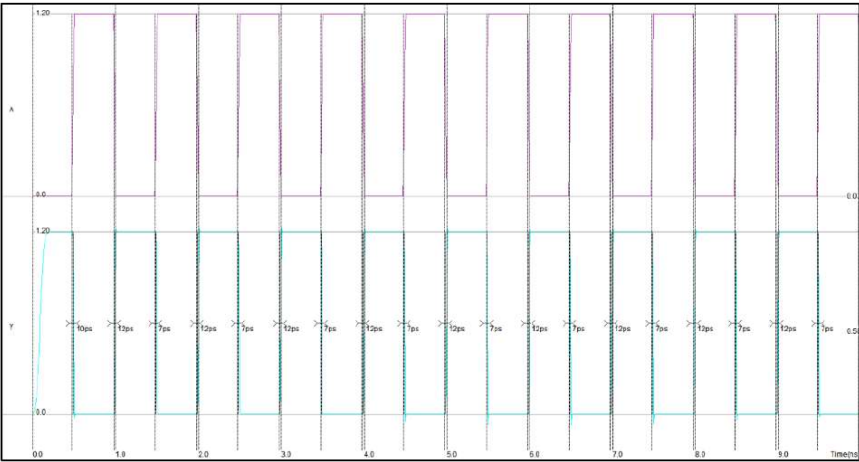
VOLTAGE VS TIME



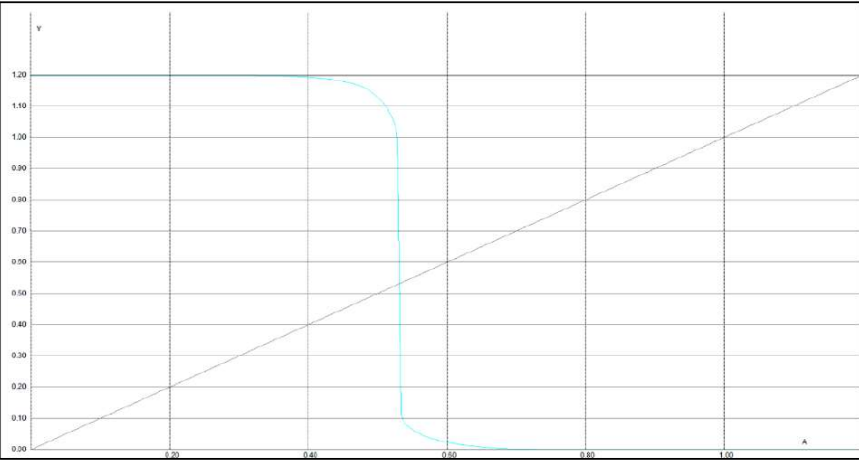
VOLTAGE VS VOLTAGE



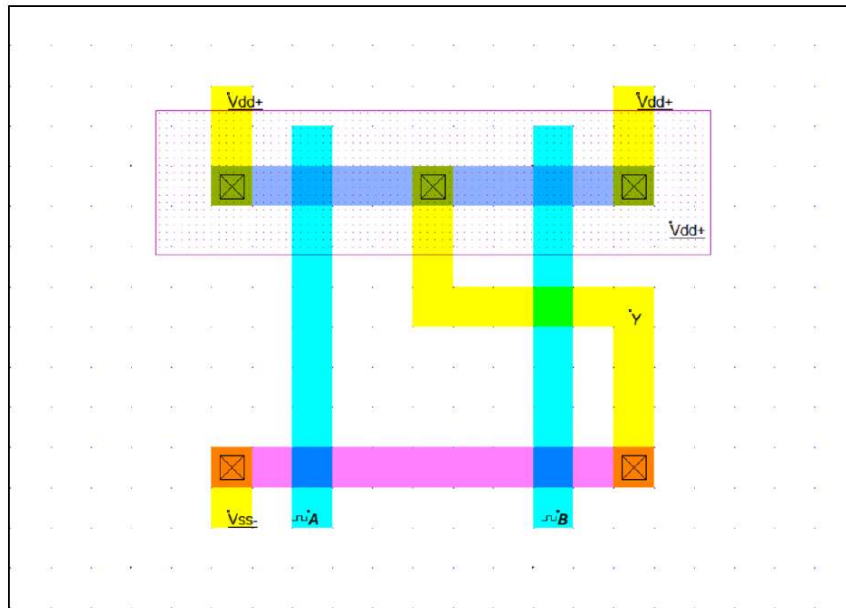
INVERTER



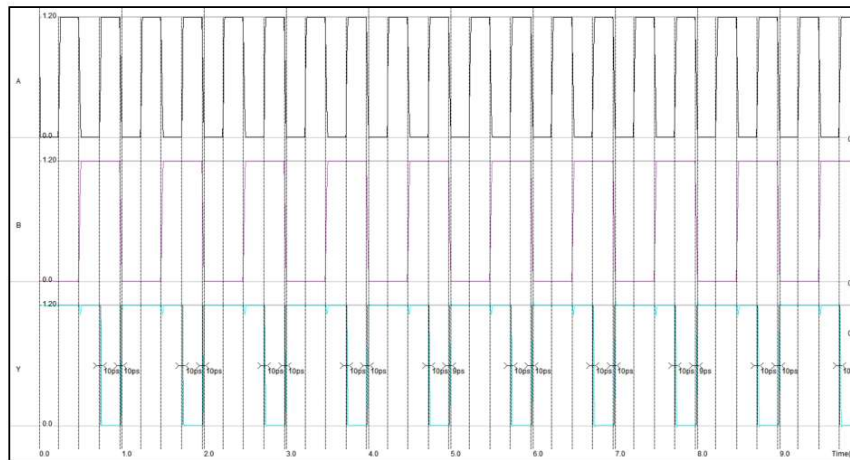
VOLTAGE VS TIME



VOLTAGE VS VOLTAGE



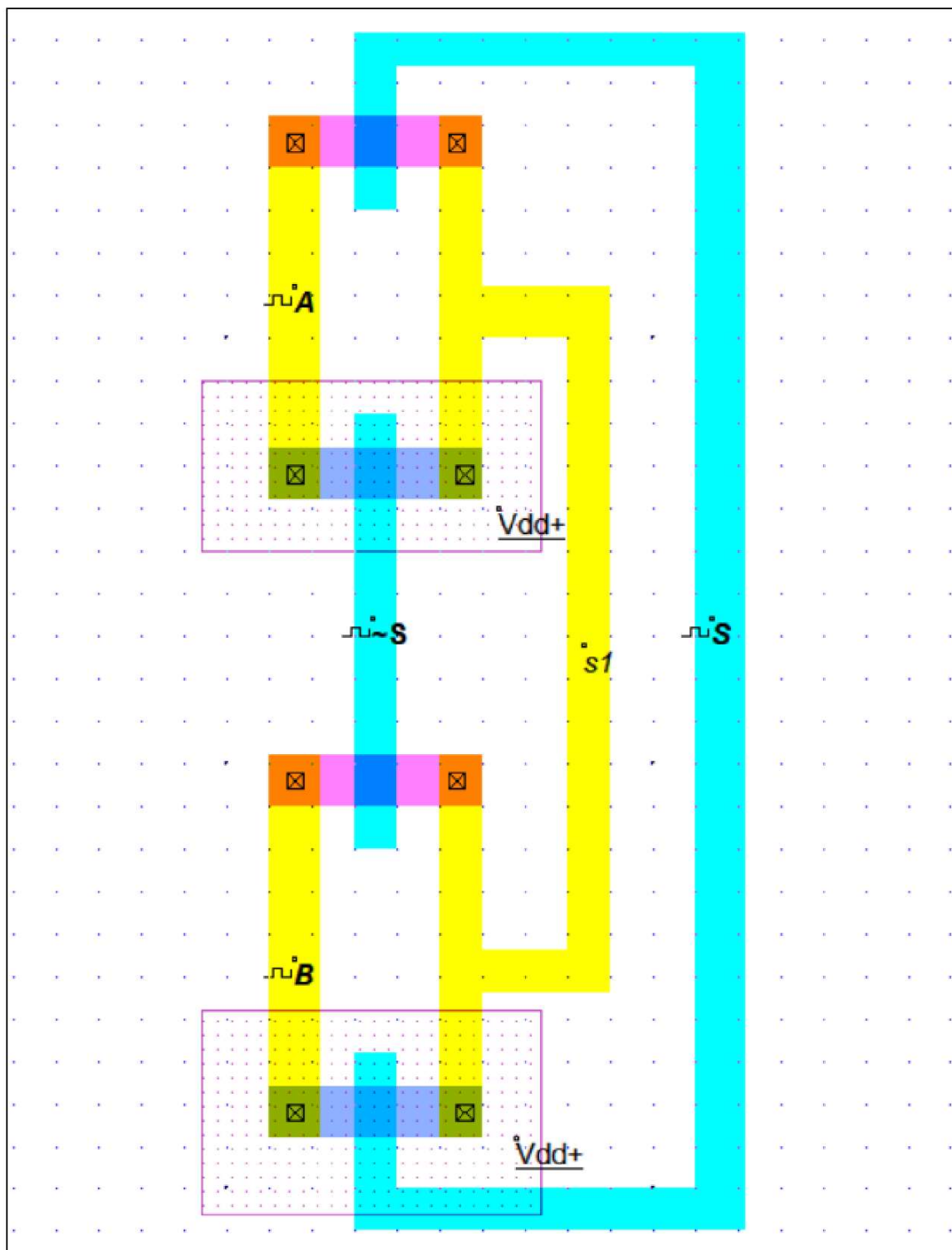
NAND GATE



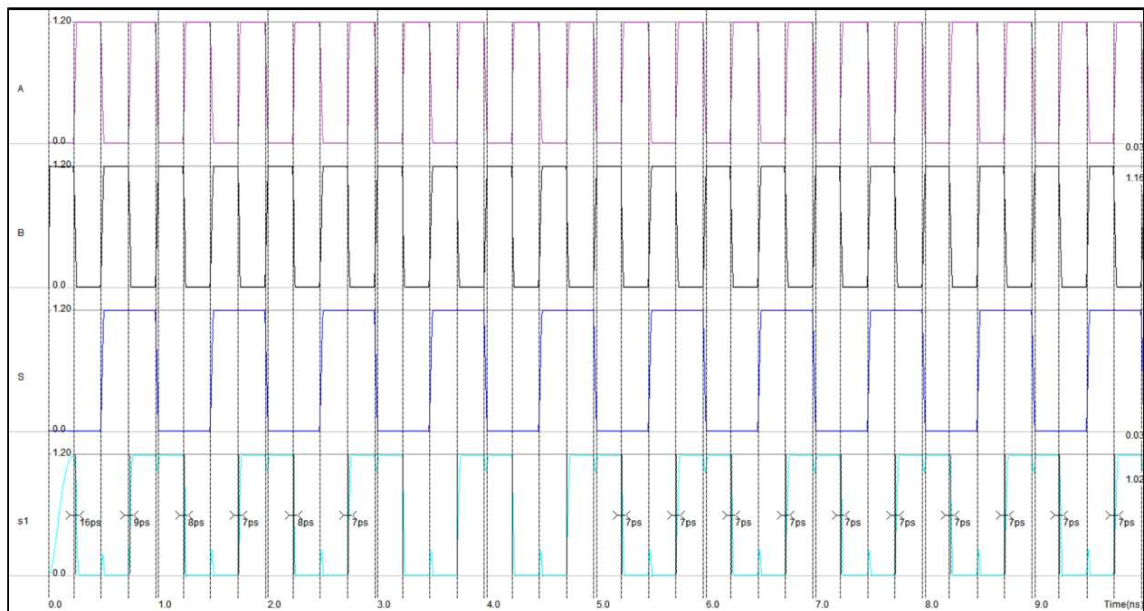
VOLTAGE VS TIME



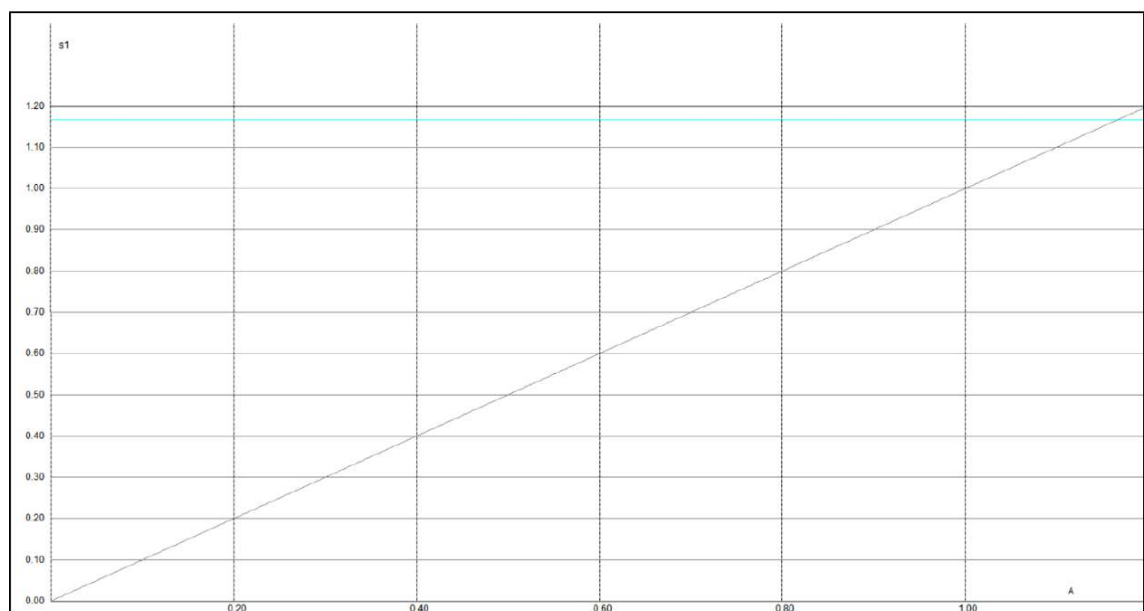
VOLTAGE VS VOLTAGE



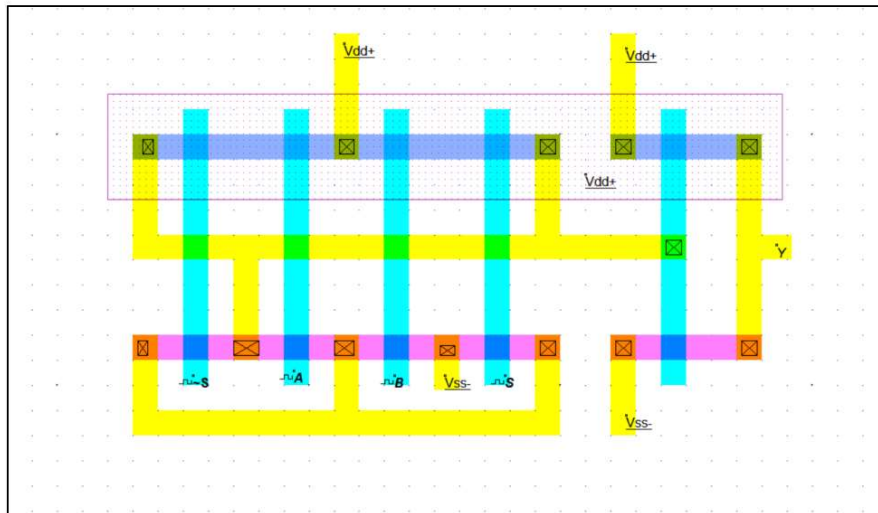
2:1 MUX USING TRANSMISSION GATES



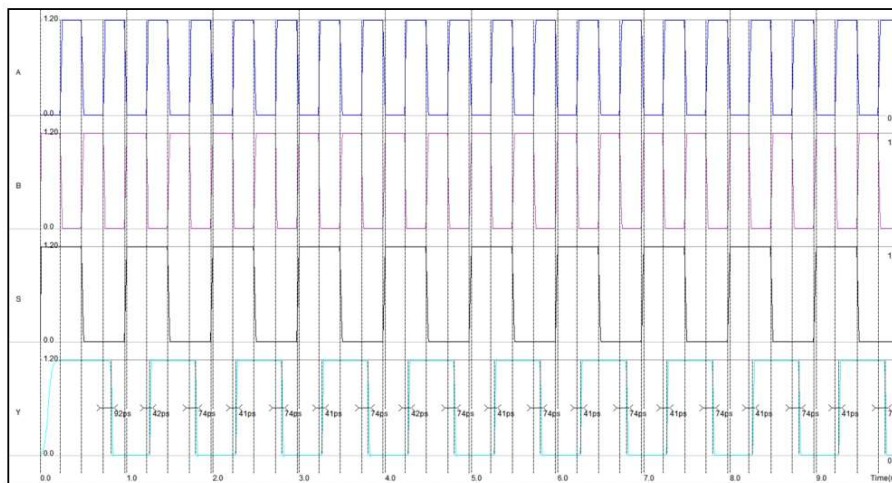
VOLTAGE VS TIME



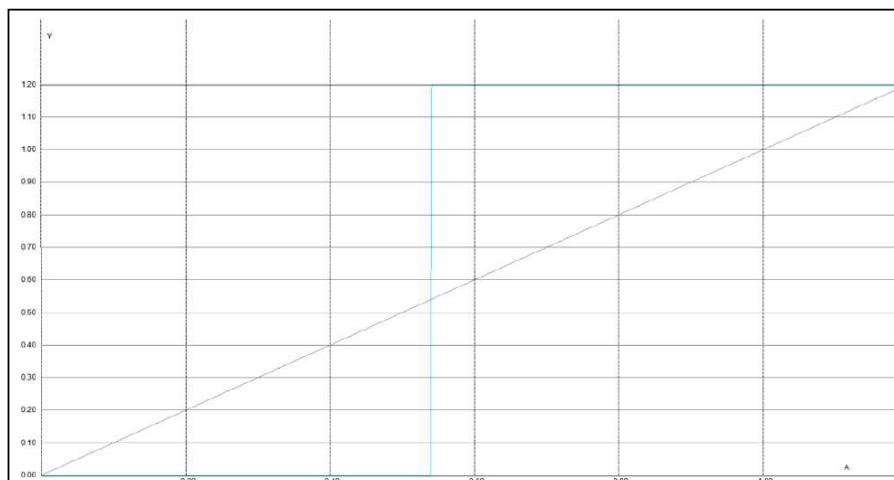
VOLTAGE VS VOLTAGE



2:1 MUX USING CONVENTIONAL METHOD



VOLTAGE VS TIME



VOLTAGE VS VOLTAGE