

// SPI and I2C serial mode defines

```
#define SPI_SS                PB2
#define SPI_SS_PORT          PORTB
#define SPI_SS_PIN           PINB
#define SPI_SS_DDR           DDRB

#define SPI_MOSI              PB3
#define SPI_MOSI_PORT        PORTB
#define SPI_MOSI_PIN         PINB
#define SPI_MOSI_DDR         DDRB

#define SPI_MISO              PB4
#define SPI_MISO_PORT        PORTB
#define SPI_MISO_PIN         PINB
#define SPI_MISO_DDR         DDRB

#define SPI_SCK               PB5
#define SPI_SCK_PORT          PORTB
#define SPI_SCK_PIN           PINB
#define SPI_SCK_DDR           DDRB

#define I2C_SDA               PC4
#define I2C_SDA_PORT          PORTC
#define I2C_SDA_PIN           PINC
#define I2C_SDA_DDR           DDRC

#define I2C_SCL               PC5
#define I2C_SCL_PORT          PORTC
#define I2C_SCL_PIN           PINC
#define I2C_SCL_DDR           DDRC
```