

A

ZYNQ_TOP_LEVEL
ZYNQ_TOP_LEVEL.SchDoc

PS_POR_B

PA_EN

LM_SPI

ZYNQ_LMS_DIG_IO

HMC_VCTL

ANTSW_CTL_2

ANTSW_CTL_1

GigEthernet_1

GigEthernet_2

PS_SPI

GPS_D

UART_0

UART_1

JTAG

I2C

PS_SRST_B

DDR3

Power_Distribution_TopLevel
Power_Distribution_TopLevel.SchDoc

PS_POR_B

PA_EN

LM_SPI

ZYNQ_LMS_DIG_IO

HMC_VCTL

ANTSW_CTL_2

ANTSW_CTL_1

ETHERNET_TOP_LEVEL
ETHERNET_TOP_LEVEL.SchDoc

Gig_Ethernet_1

Gig_Ethernet_2

PS_POR_B

L1_OK

PERIPHERALS
PERIPHERALS.SchDoc

PS_SPI

GPS_D

UART_0

UART_1

JTAG

I2C

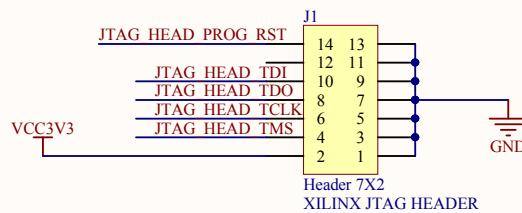
PS_SRST_B

DDR3_TOP_LEVEL
DDR3_TOP_LEVEL.SchDoc

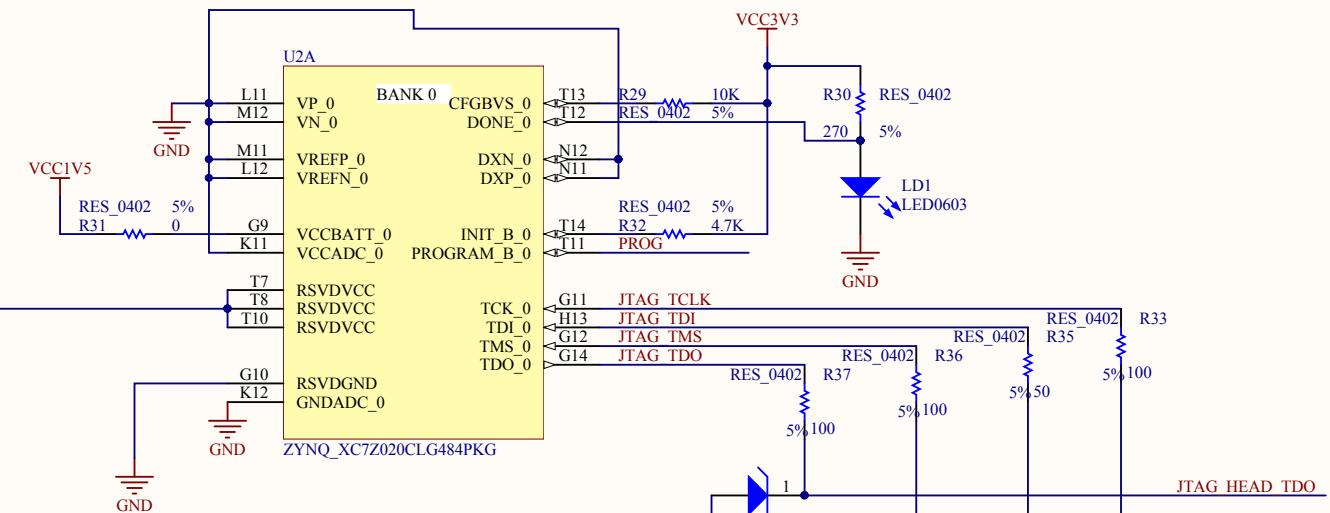
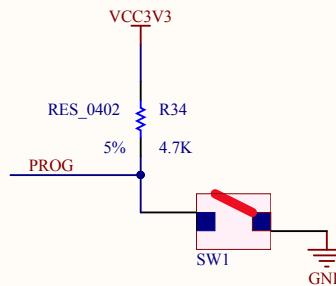
DDR3

Title		
Size A4	Number	Revision
Date: 2/6/2013	Sheet of	
File: C:\Users\...\COMRAD_TOP LEVEL.SchDoc	Drawn By:	

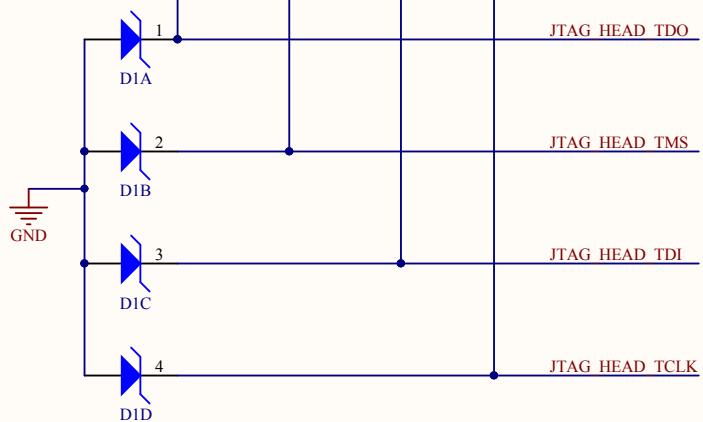
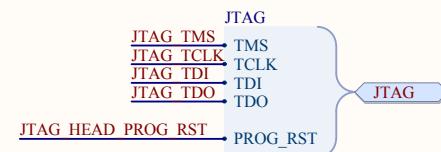
A



B

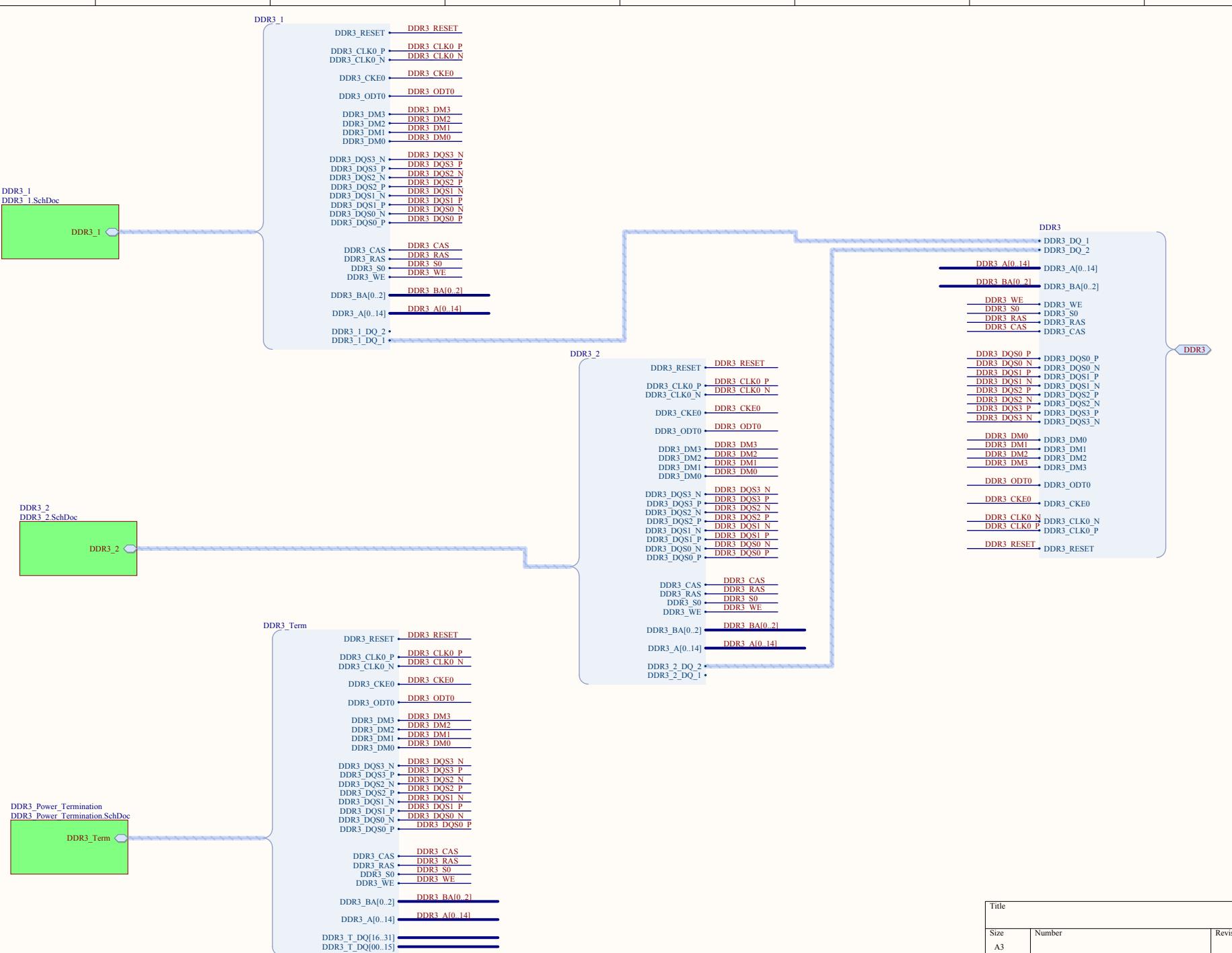


C



D

Title		
Size A4	Number	Revision
Date: 2/6/2013	Sheet of	
File: C:\Users\...\ZYNQ_CONFIG.SchDoc	Drawn By:	



A

A

B

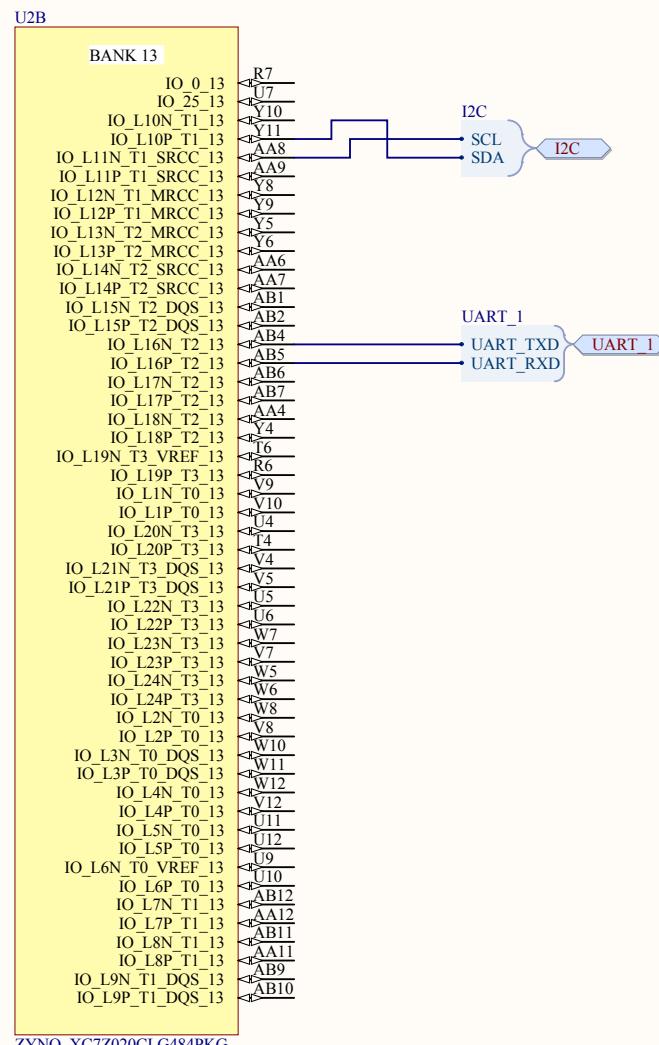
B

C

C

D

D



Title		
Size A4	Number	Revision
Date: 2/6/2013	Sheet of	
File: C:\Users\ZYNQ_PL_Bank_13.SchDoc	Drawn By:	

A

U2J

BANK 33

IO_0_33
 IO_25_33
 IO_L10N_T1_33
 IO_L10P_T1_33
 IO_L11N_T1_SRCC_33
 IO_L11P_T1_SRCC_33
 IO_L12N_T1_MRCC_33
 IO_L12P_T1_MRCC_33
 IO_L13N_T2_MRCC_33
 IO_L13P_T2_MRCC_33
 IO_L14N_T2_SRCC_33
 IO_L14P_T2_SRCC_33
 IO_L15N_T2_DQS_33
 IO_L15P_T2_DQS_33
 IO_L16N_T2_33
 IO_L16P_T2_33
 IO_L17N_T2_33
 IO_L17P_T2_33
 IO_L18N_T2_33
 IO_L18P_T2_33
 IO_L19N_T3_VREF_33
 IO_L19P_T3_33
 IO_LIN_T0_33
 IO_LIP_T0_33
 IO_L20N_T3_33
 IO_L20P_T3_33
 IO_L21N_T3_DQS_33
 IO_L21P_T3_DQS_33
 IO_L22N_T3_33
 IO_L22P_T3_33
 IO_L23N_T3_33
 IO_L23P_T3_33
 IO_L24N_T3_33
 IO_L24P_T3_33
 IO_L2N_T0_33
 IO_L2P_T0_33
 IO_L3N_T0_DQS_33
 IO_L3P_T0_DQS_33
 IO_L4N_T0_33
 IO_L4P_T0_33
 IO_L5N_T0_33
 IO_L5P_T0_33
 IO_L6N_T0_VREF_33
 IO_L6P_T0_33
 IO_L7N_T1_33
 IO_L7P_T1_33
 IO_L8N_T1_33
 IO_L8P_T1_33
 IO_L9N_T1_DQS_33
 IO_L9P_T1_DQS_33

ZYNQ_XC7Z020CLG484PKG



B

A

B

C

C

D

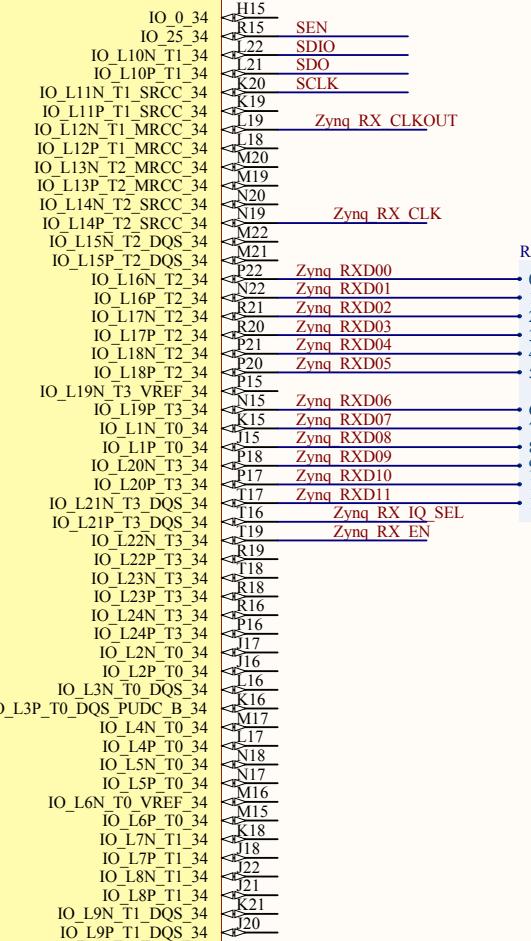
D

Title		
Size	Number	Revision
A4		
Date:	2/6/2013	Sheet of
File:	C:\Users\.\ZYNQ_PL_Bank_33.SchDoc	Drawn By:

A

U2C

BANK 34



ZYNQ_XC7Z020CLG484PKG

Title

Size

A4

Number

Revision

Date: 2/6/2013

Sheet of

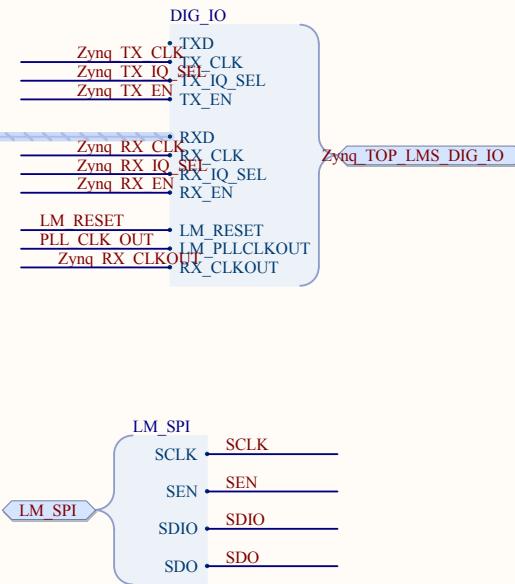
File: C:\Users\...\ZYNQ_PL_Bank_34.SchDoc Drawn By:

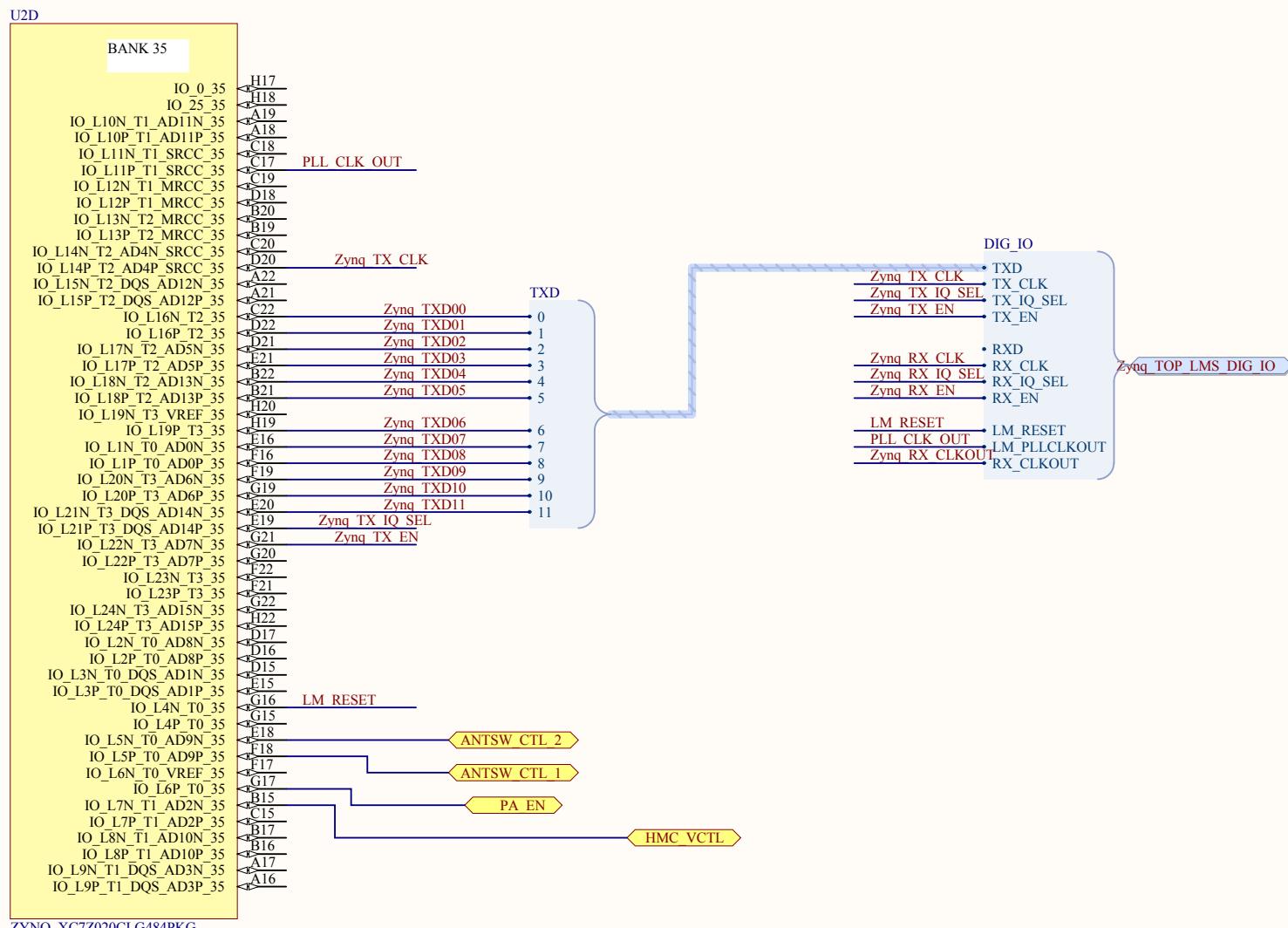
A

B

C

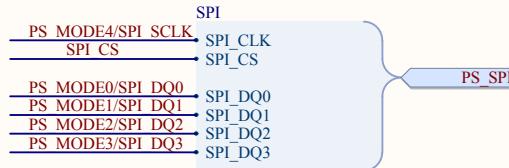
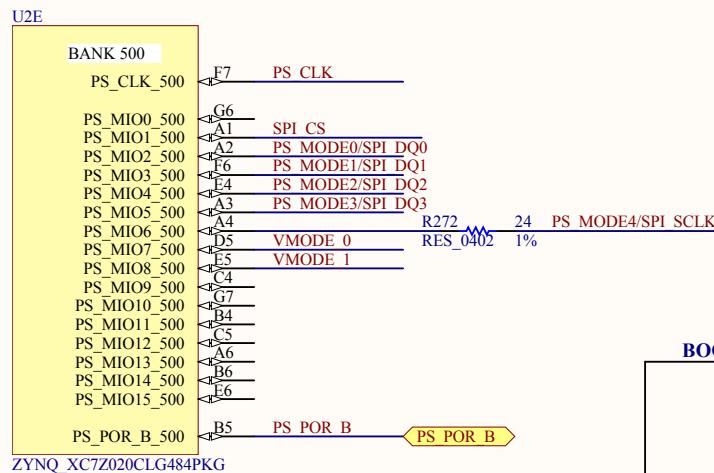
D





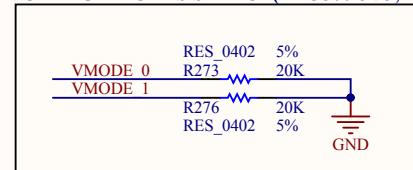
Title		
Size	Number	Revision
A4		
Date: 2/6/2013	Sheet of	
File: C:\Users\ZYNQ_PL_Bank_35.SchDoc	Drawn By:	

A



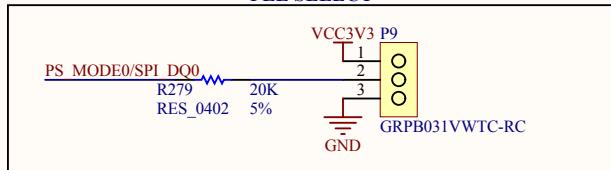
B

MIO VOLTAGE MODES SELECT(MIO500-3V3;MIO501-3V3)

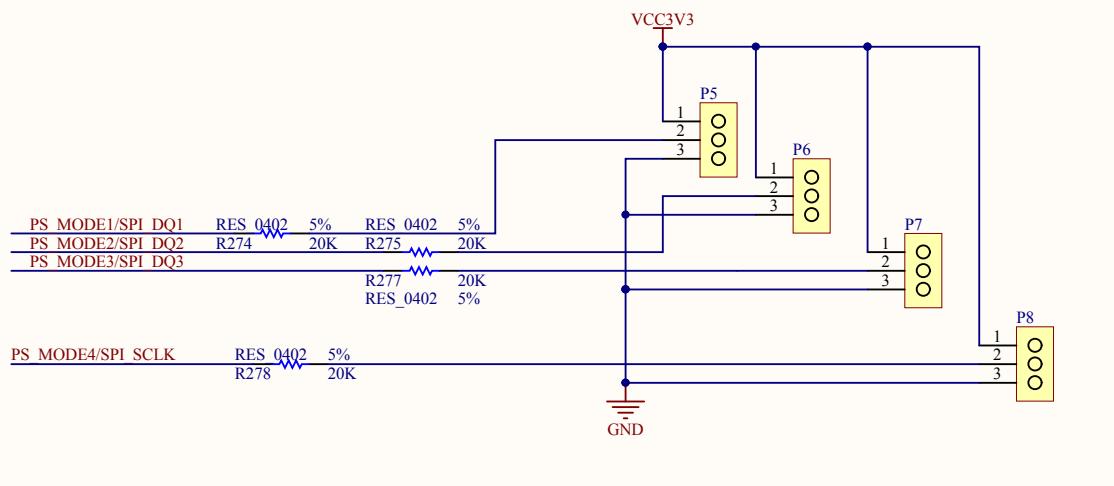


C

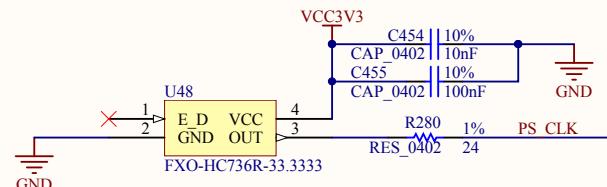
PLL SELECT



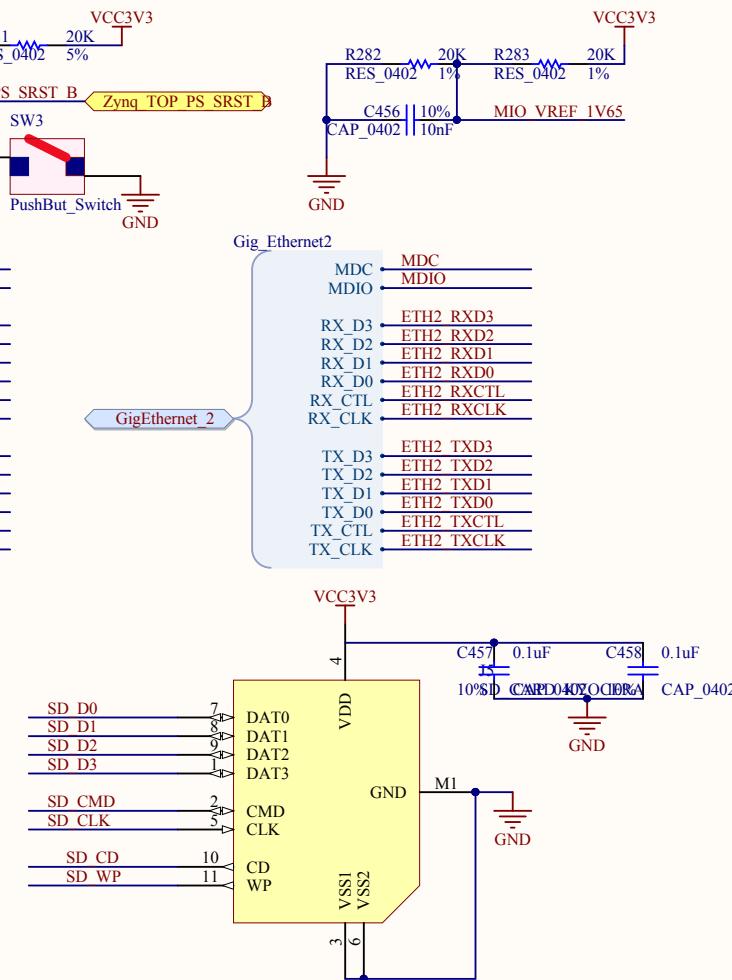
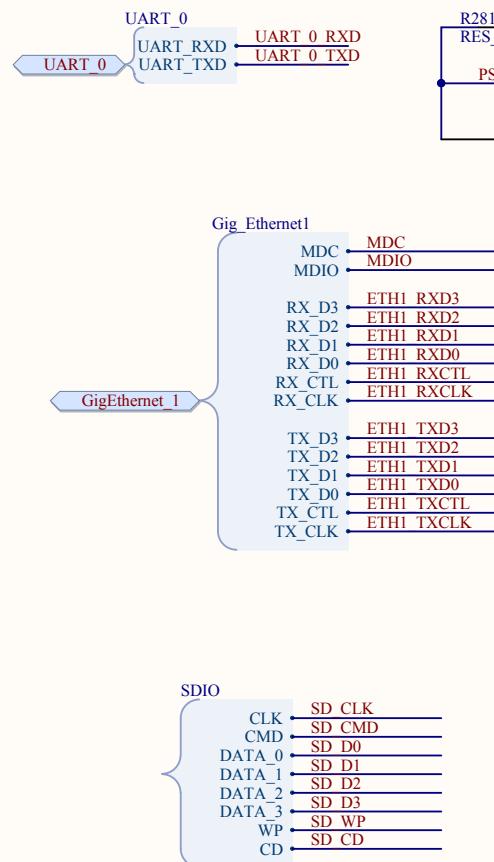
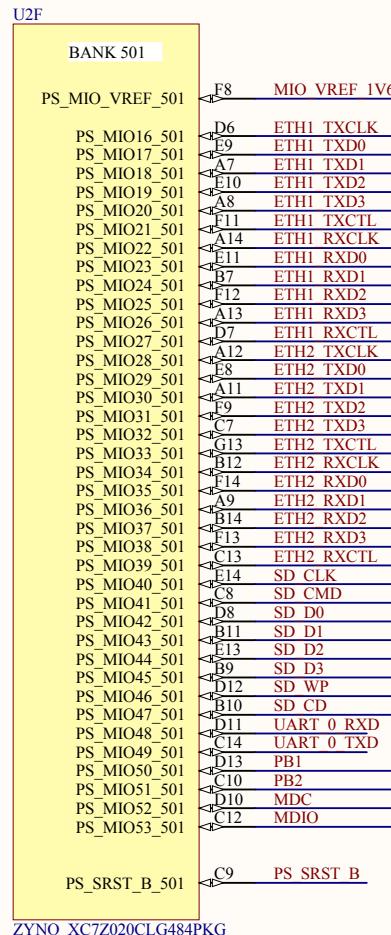
BOOT ROM SELECT



D

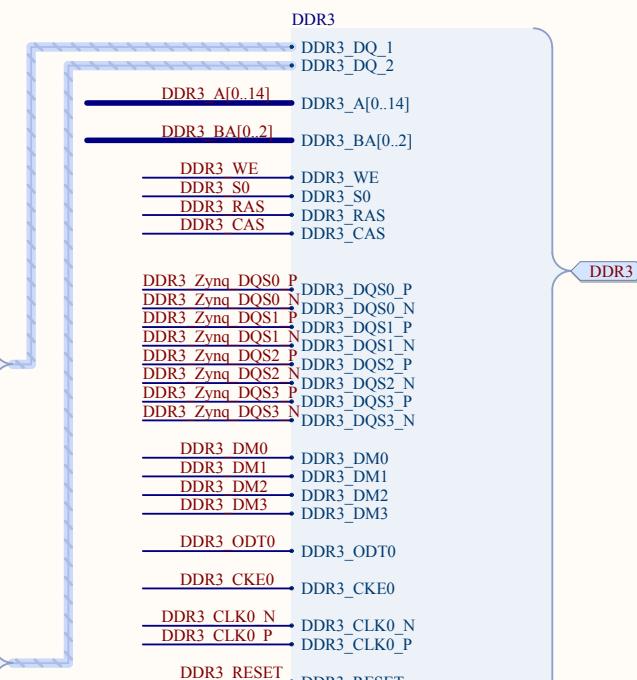
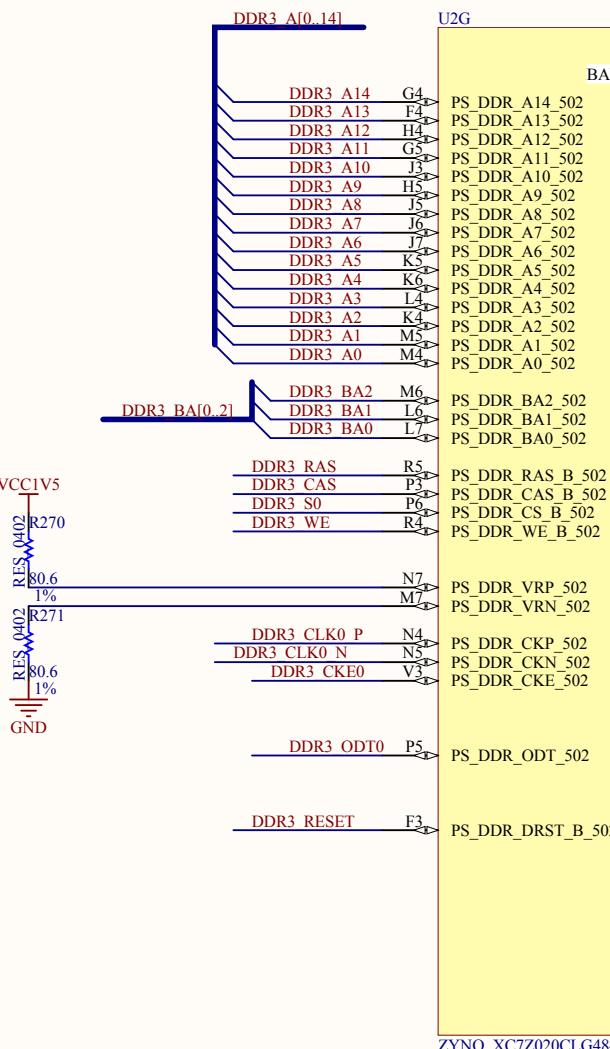


Title		
Size A4	Number	Revision
Date: 2/6/2013	Sheet of	
File: C:\Users\...\ZYNQ_MIOBank_500.SchDoc		



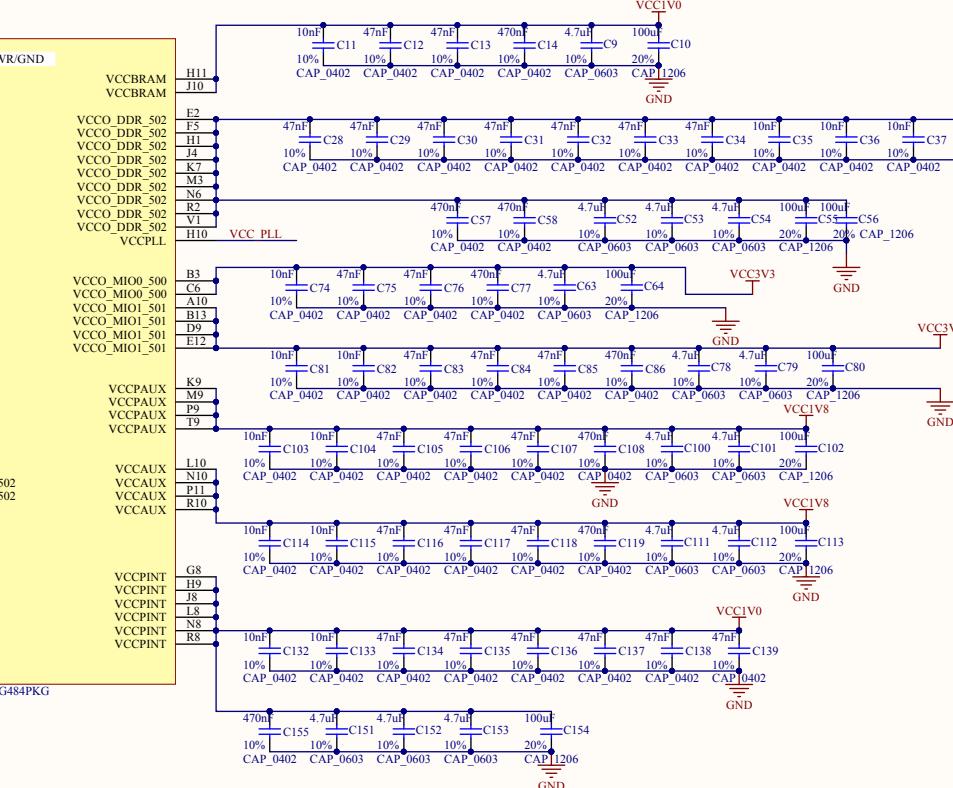
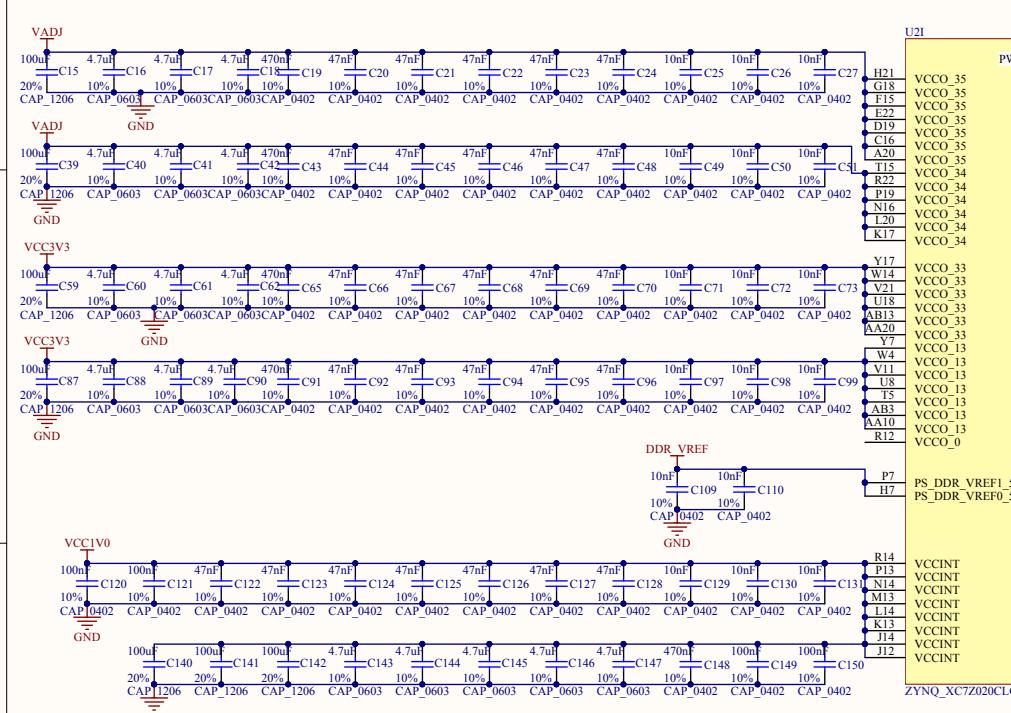
D

Title		
Size A4	Number	Revision
Date: 2/6/2013	Sheet of	
File: C:\Users\...\ZYNQ_MIOBank_501.SchDocDrawn By:		

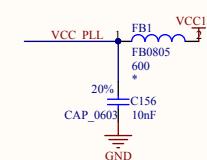
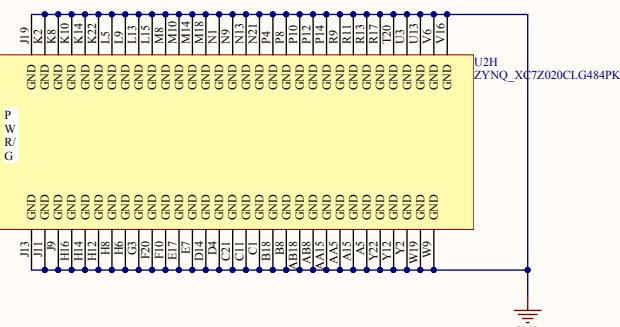


Title		
Size	Number	Revision
A4		
Date:	2/6/2013	Sheet of
File:	C:\Users\ZYNQ_DDR3_Bank_502.Sch	Drawn By:

A



B



C

Title		
Size	Number	Revision
A3		
Date:	2/6/2013	Sheet of
File:	C:\Users\...\ZYNQ Power SchDoc	Drawn By:

D

A

A

B

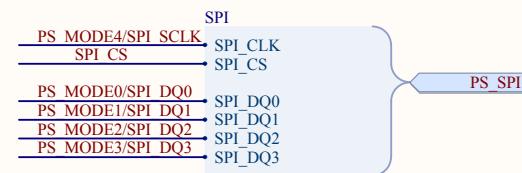
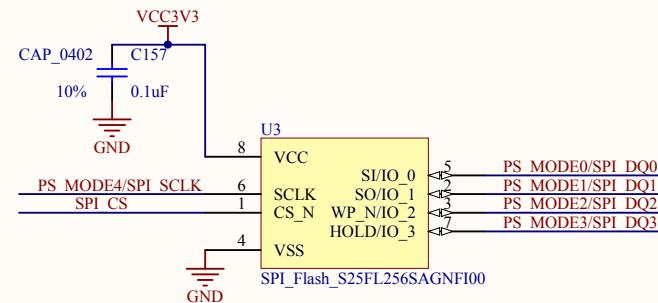
B

C

C

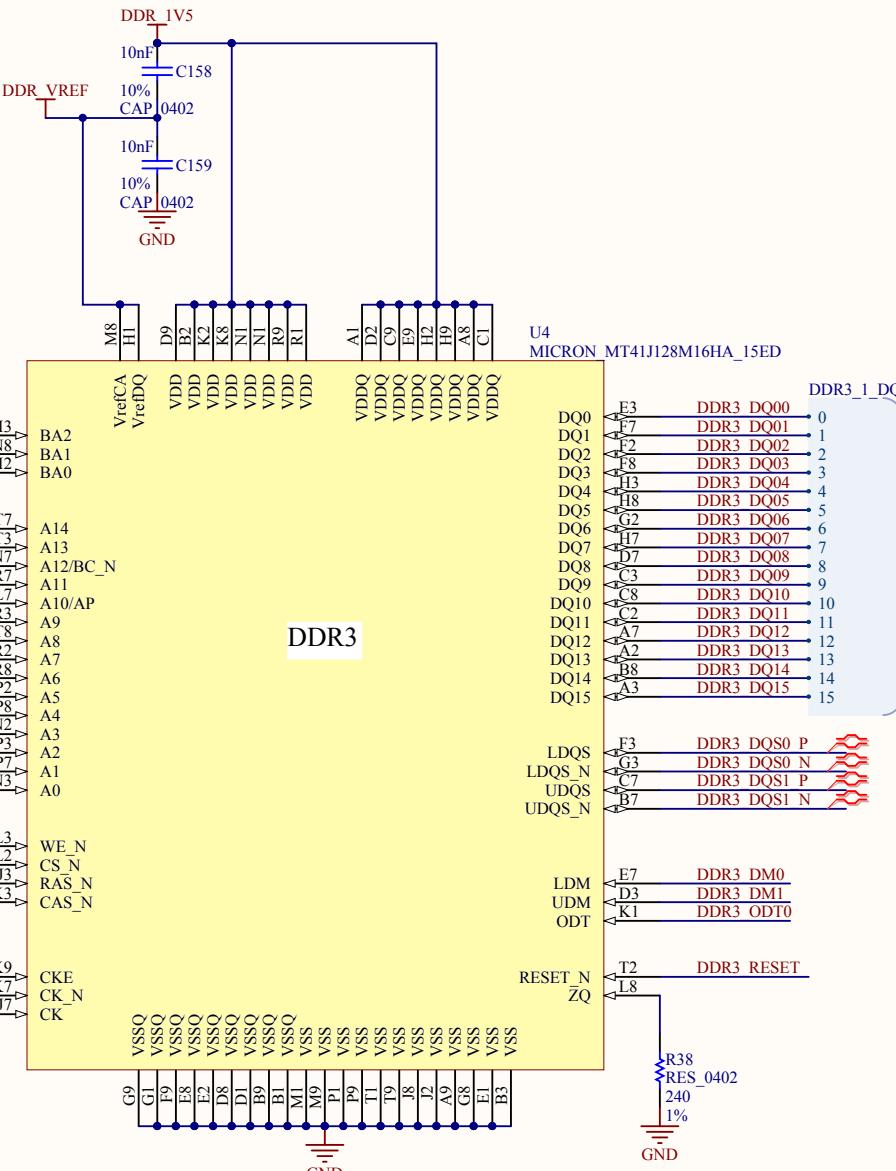
D

D

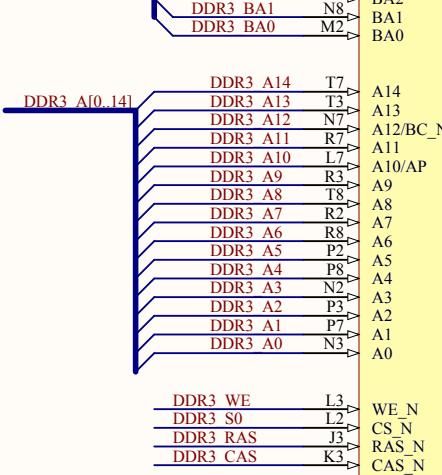


Title		
Size	Number	Revision
A4		
Date:	2/6/2013	Sheet of
File:	C:\Users\...\BootFlash_SPI.SchDoc	Drawn By:

A

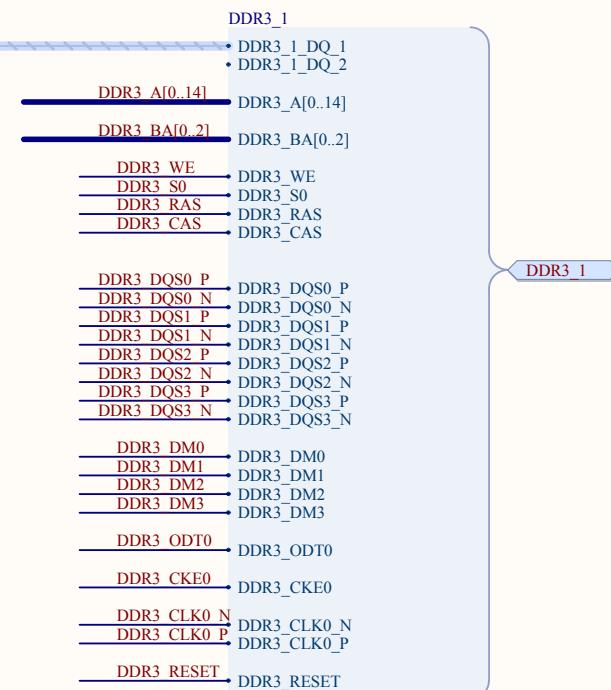


B



DDR3

C



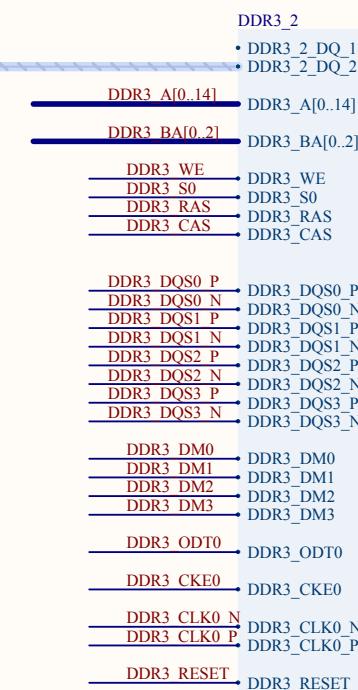
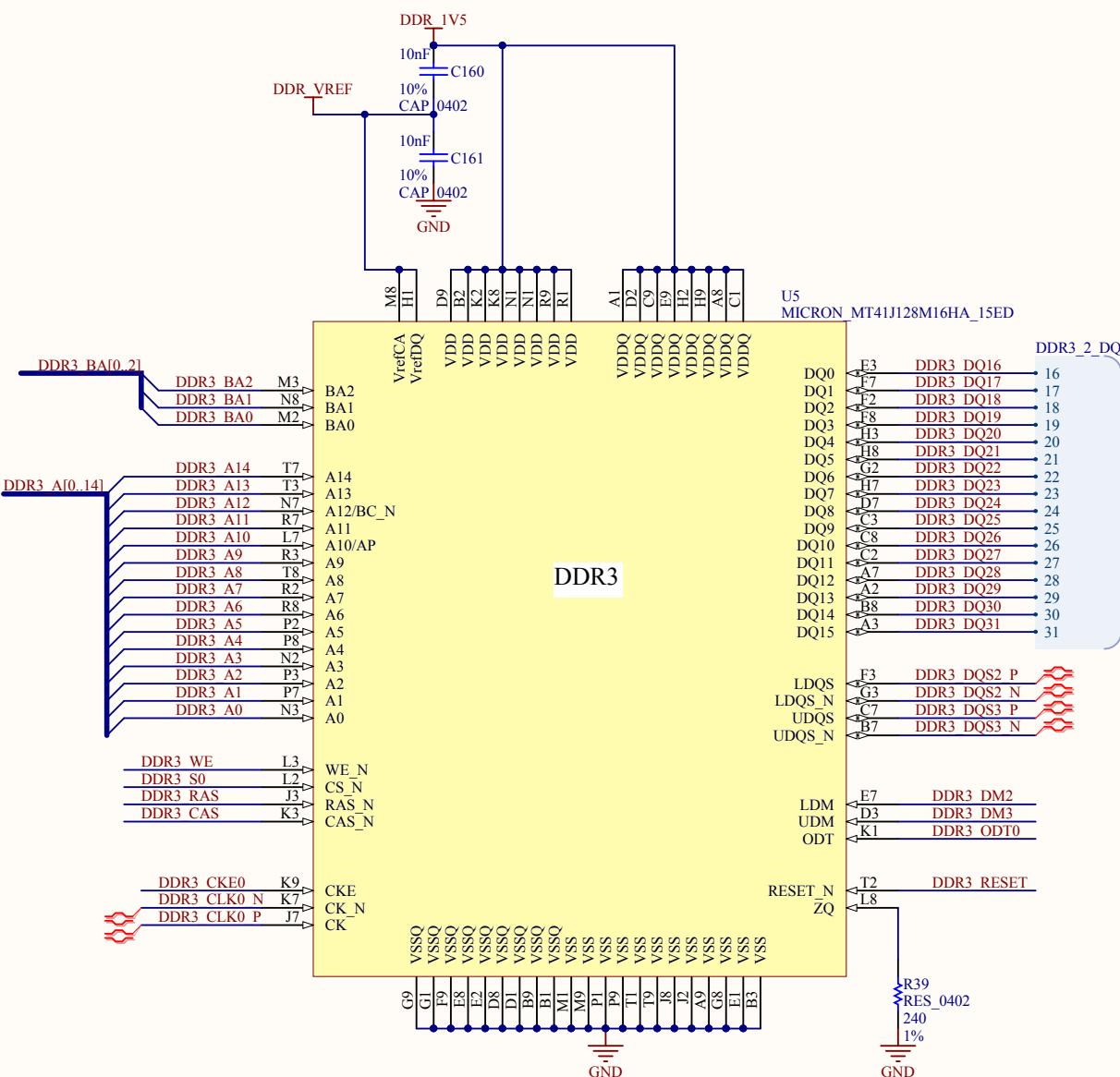
A

B

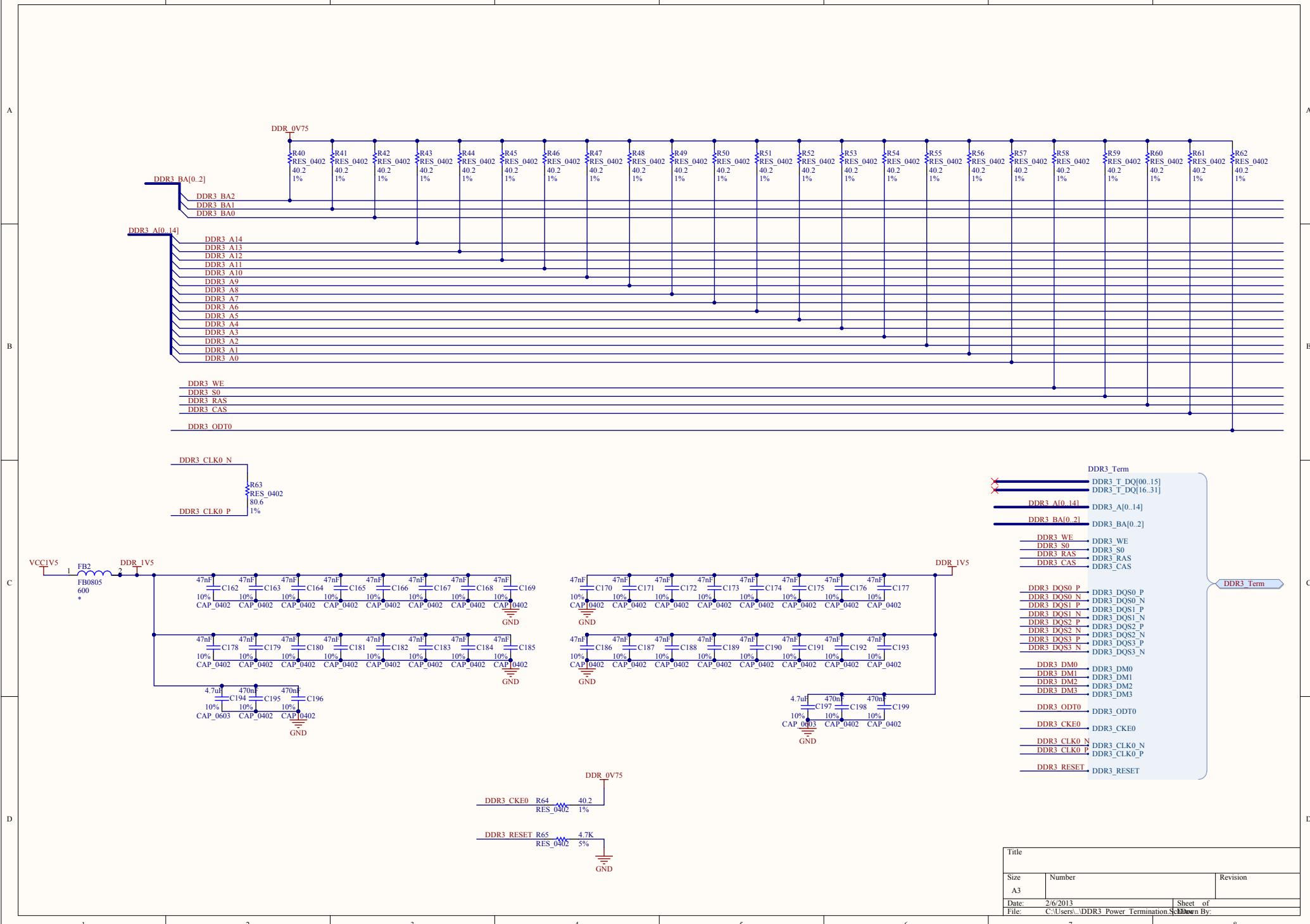
C

D

Title		
Size	Number	Revision
A4		
Date: 2/6/2013	Sheet of	
File: C:\Users\.\DDR3_1.SchDoc	Drawn By:	



Title		
Size A4	Number	Revision
Date:	2/6/2013	Sheet of
File:	C:\Users\...\DDR3_2.SchDoc	Drawn By:



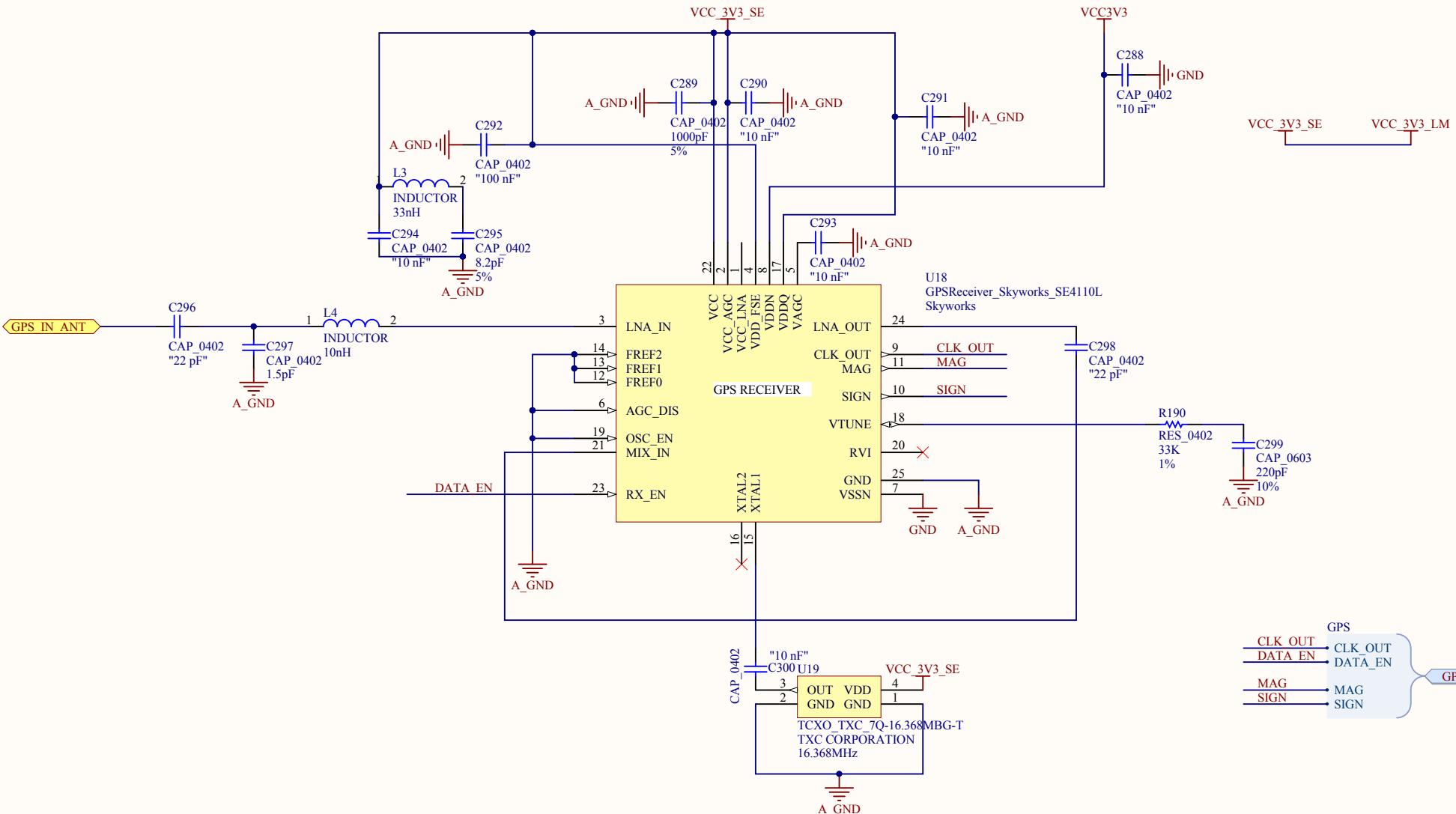
1

2

3

4

A



Title		
Size	Number	Revision
A4		
Date: 2/6/2013	Sheet of	
File: C:\Users...\GPS_Receiver.SchDoc		Drawn By:

1

2

3

4

D

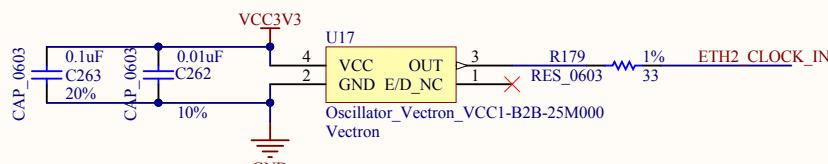
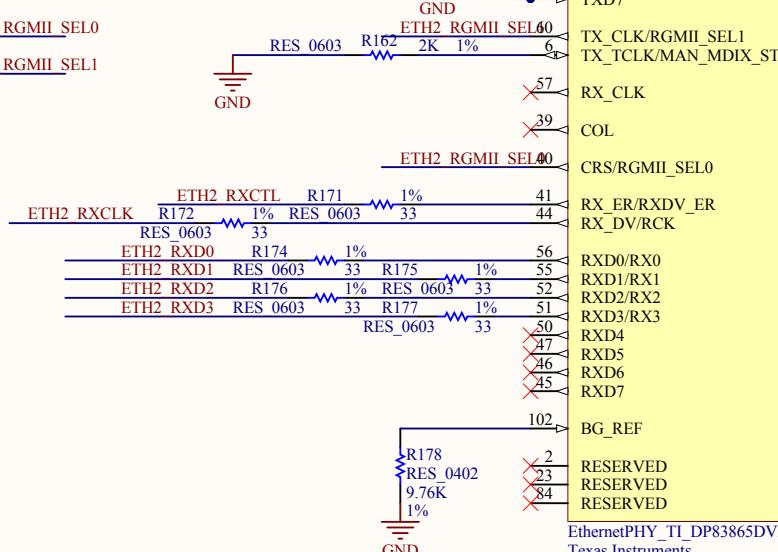
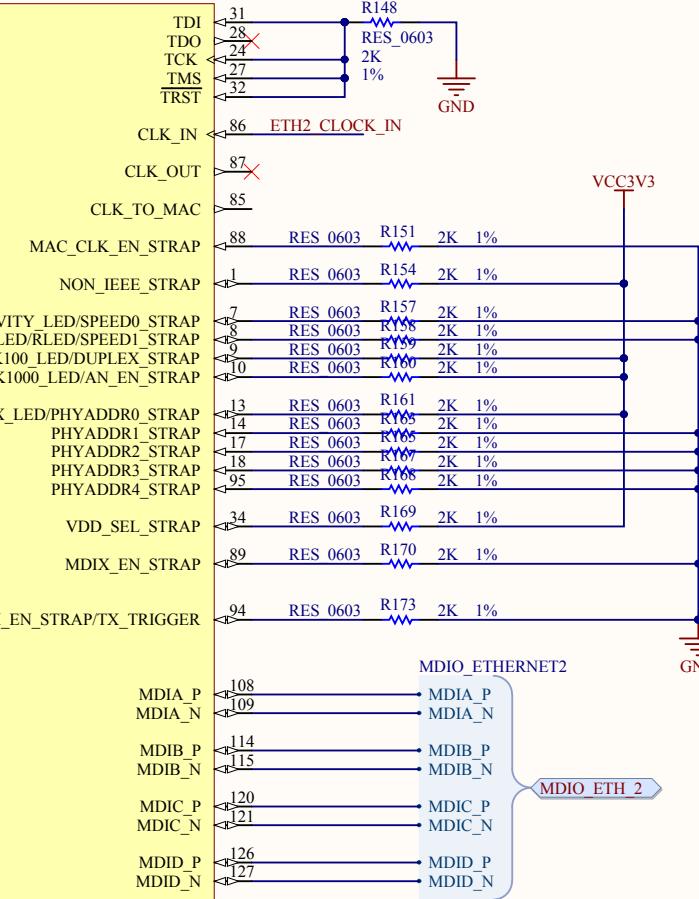
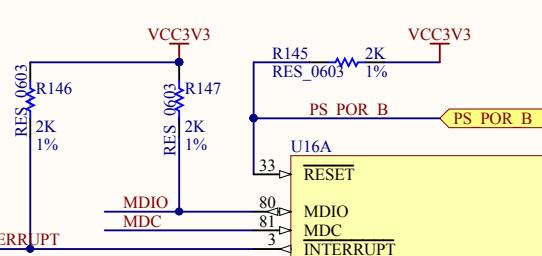
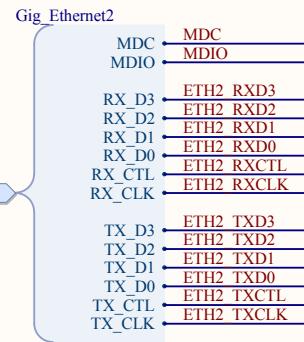
A

B

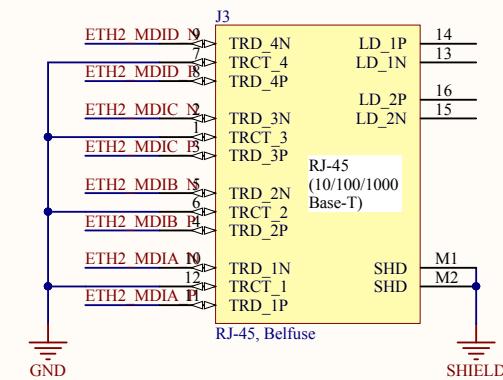
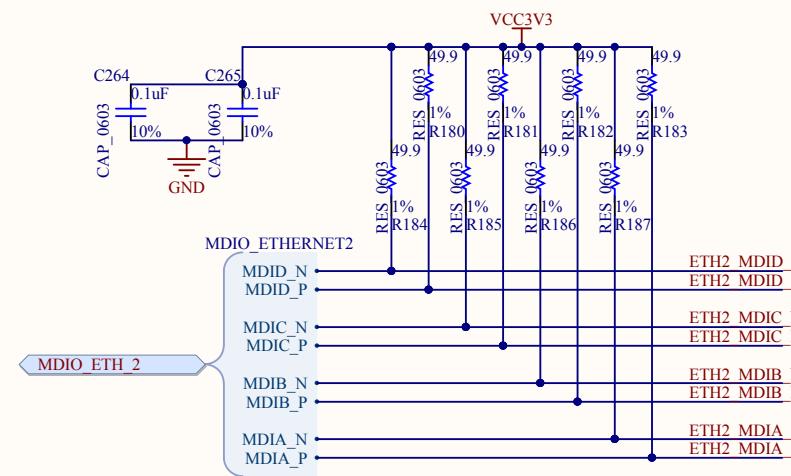
C

D

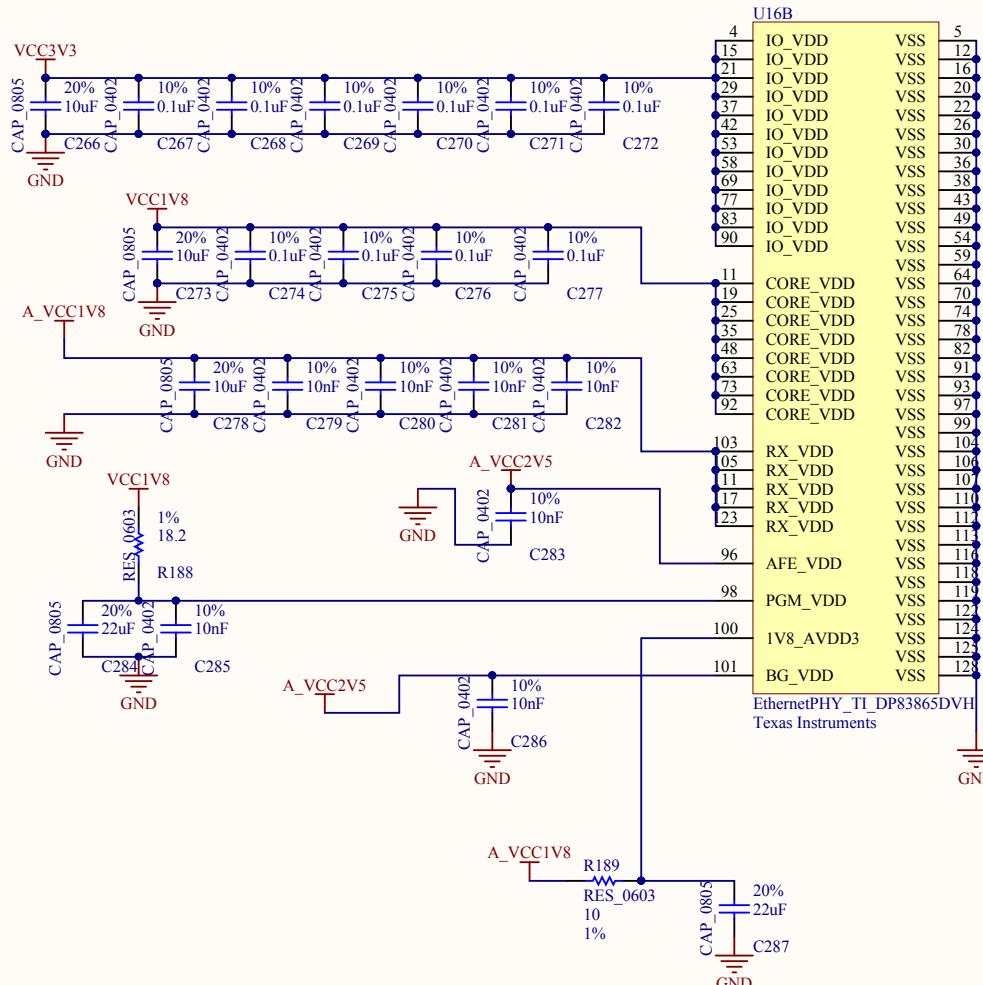
1 2 3 4



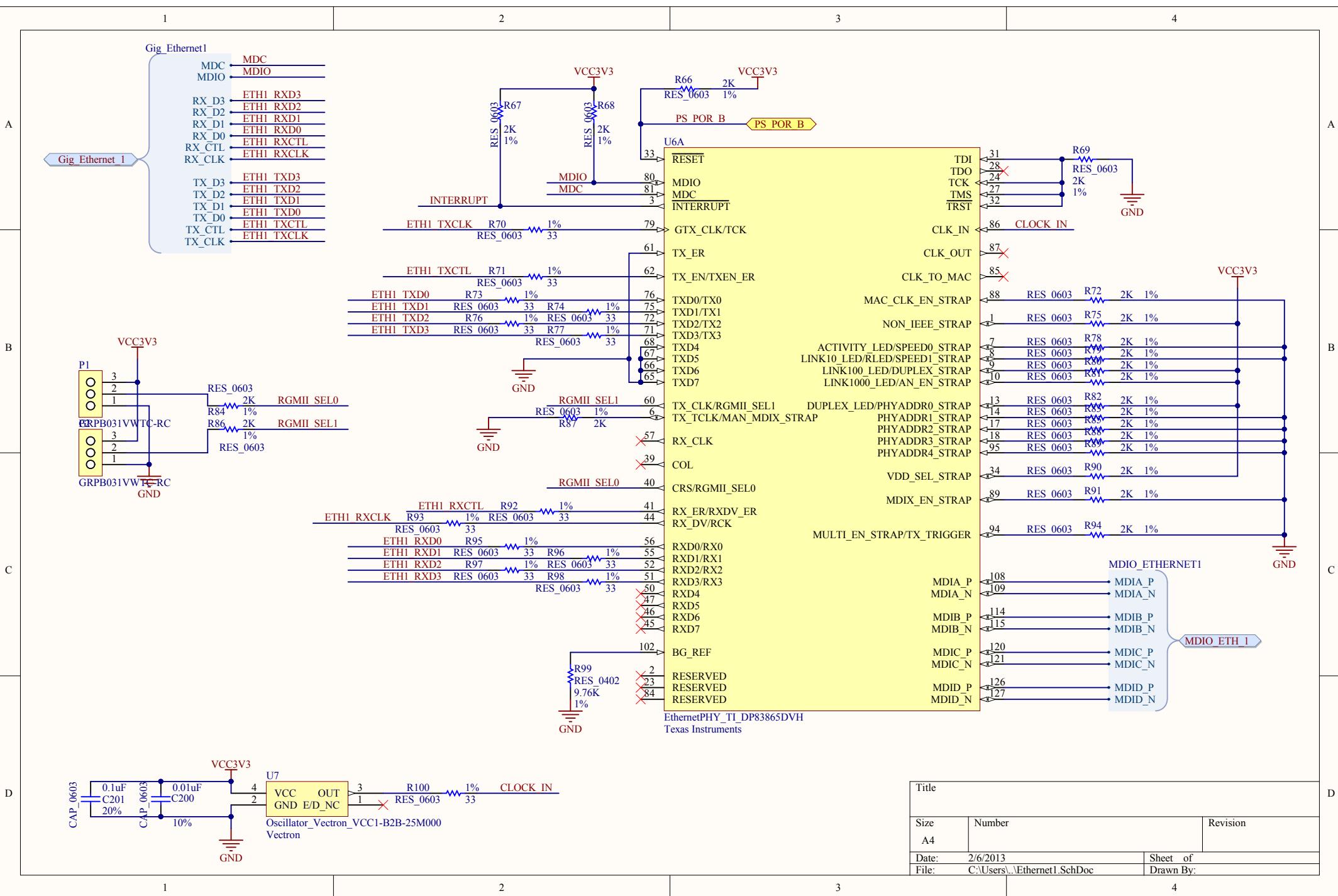
Title		Revision
Size	Number	
A4		
Date:	2/6/2013	Sheet of
File:	C:\Users...\Ethernet2.SchDoc	Drawn By:



Title		
Size A4	Number	Revision
Date: 2/6/2013	Sheet of	
File: C:\Users\...\Ethernet2_MDIO.SchDoc	Drawn By:	



Title		
Size A4	Number	Revision
Date: 2/6/2013	Sheet of	
File: C:\Users\...\Ethernet2_Power.SchDoc		Drawn By:



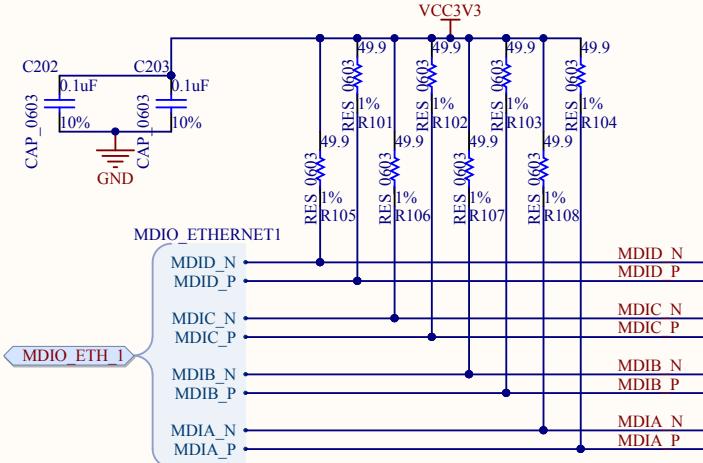
1

2

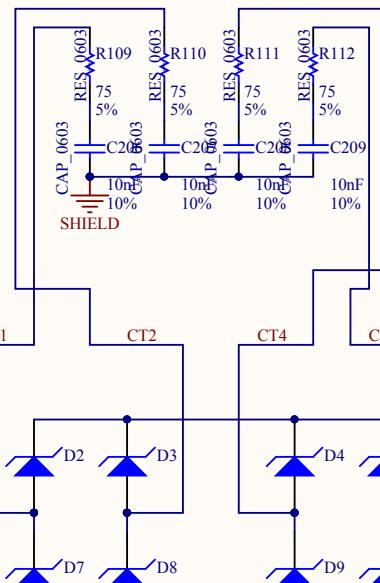
3

4

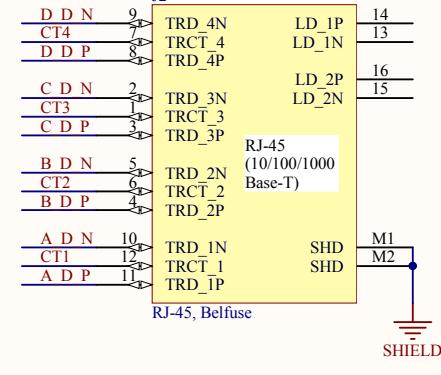
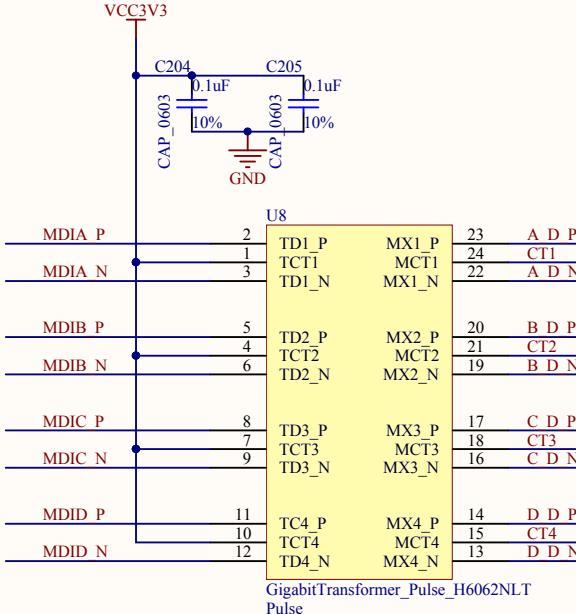
A



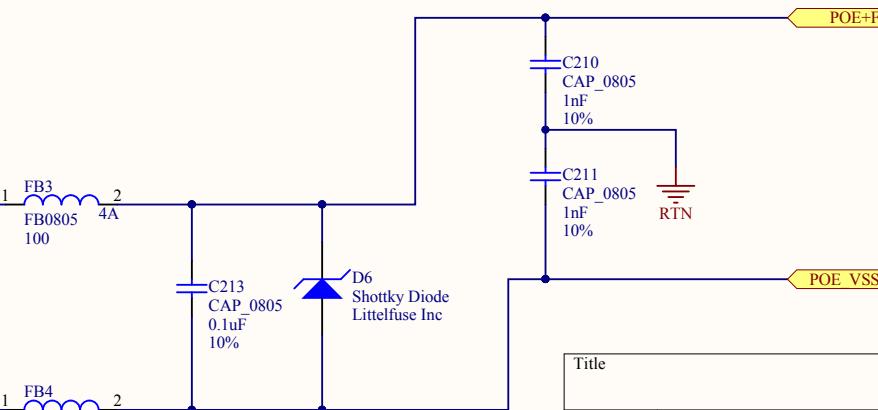
B



C



D



Title		Revision
Size A4	Number	
Date: 2/6/2013	Sheet of	
File: C:\Users\...\Ethernet1_MDIO.SchDoc	Drawn By:	

1

2

3

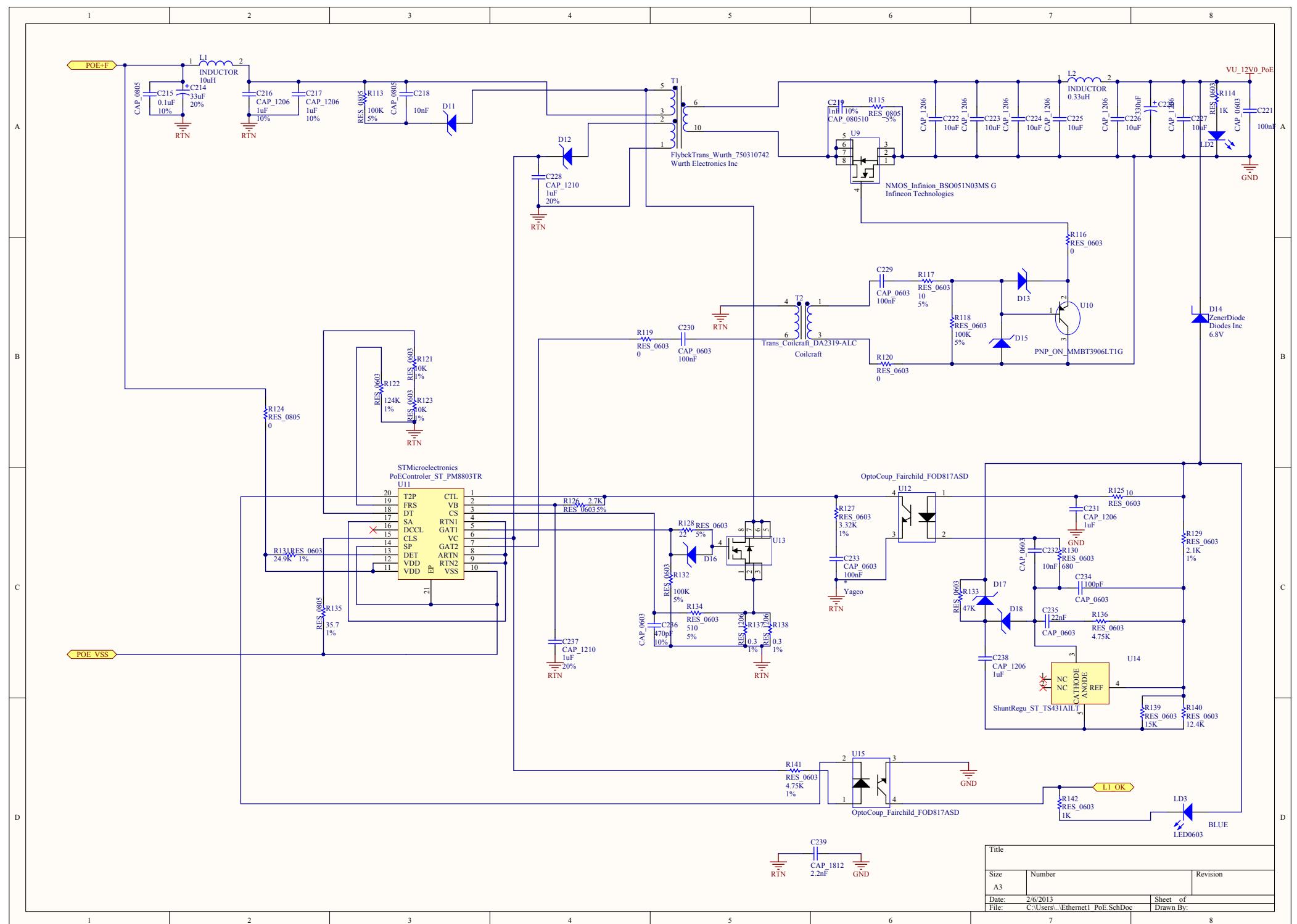
4

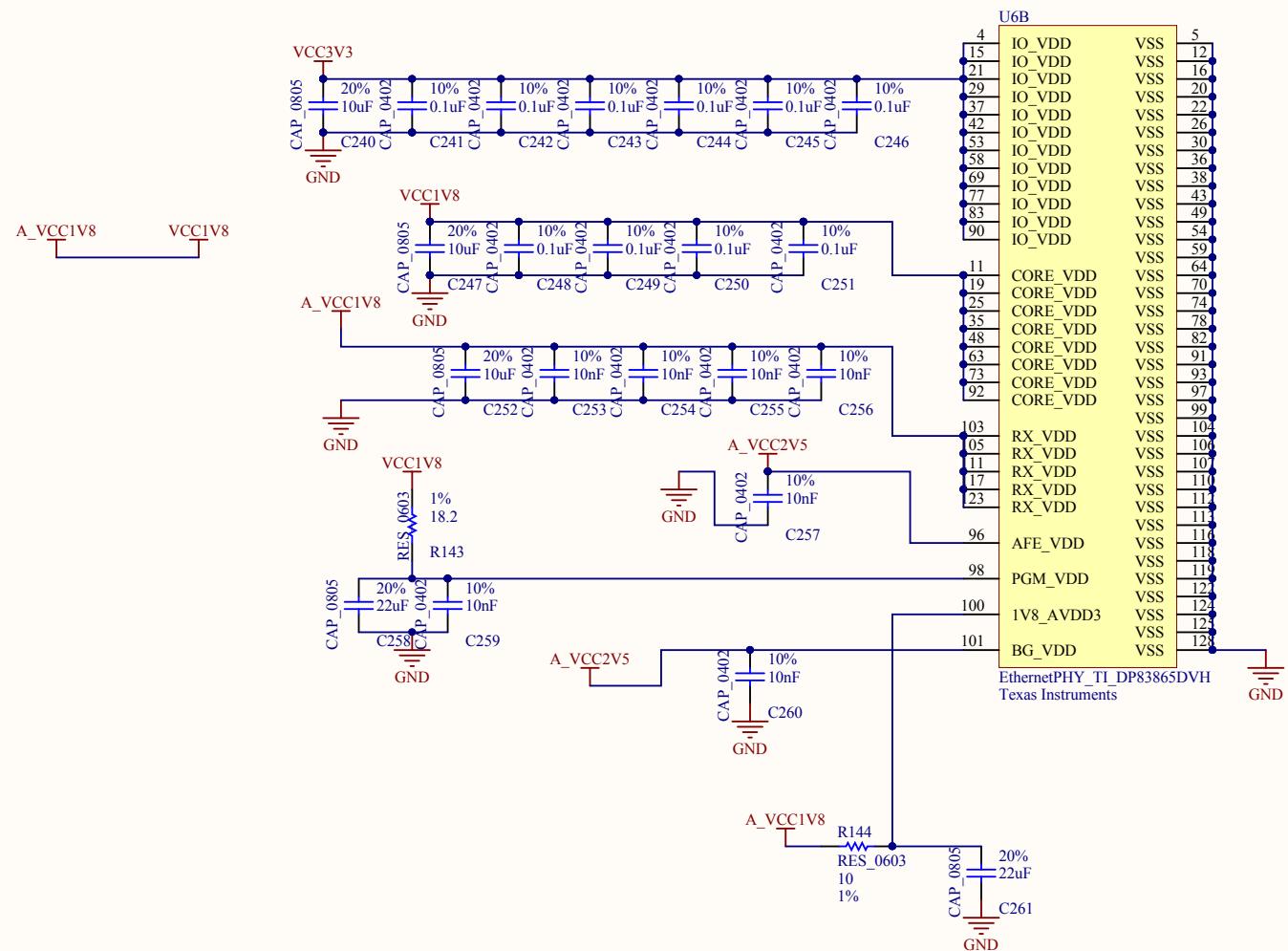
A

B

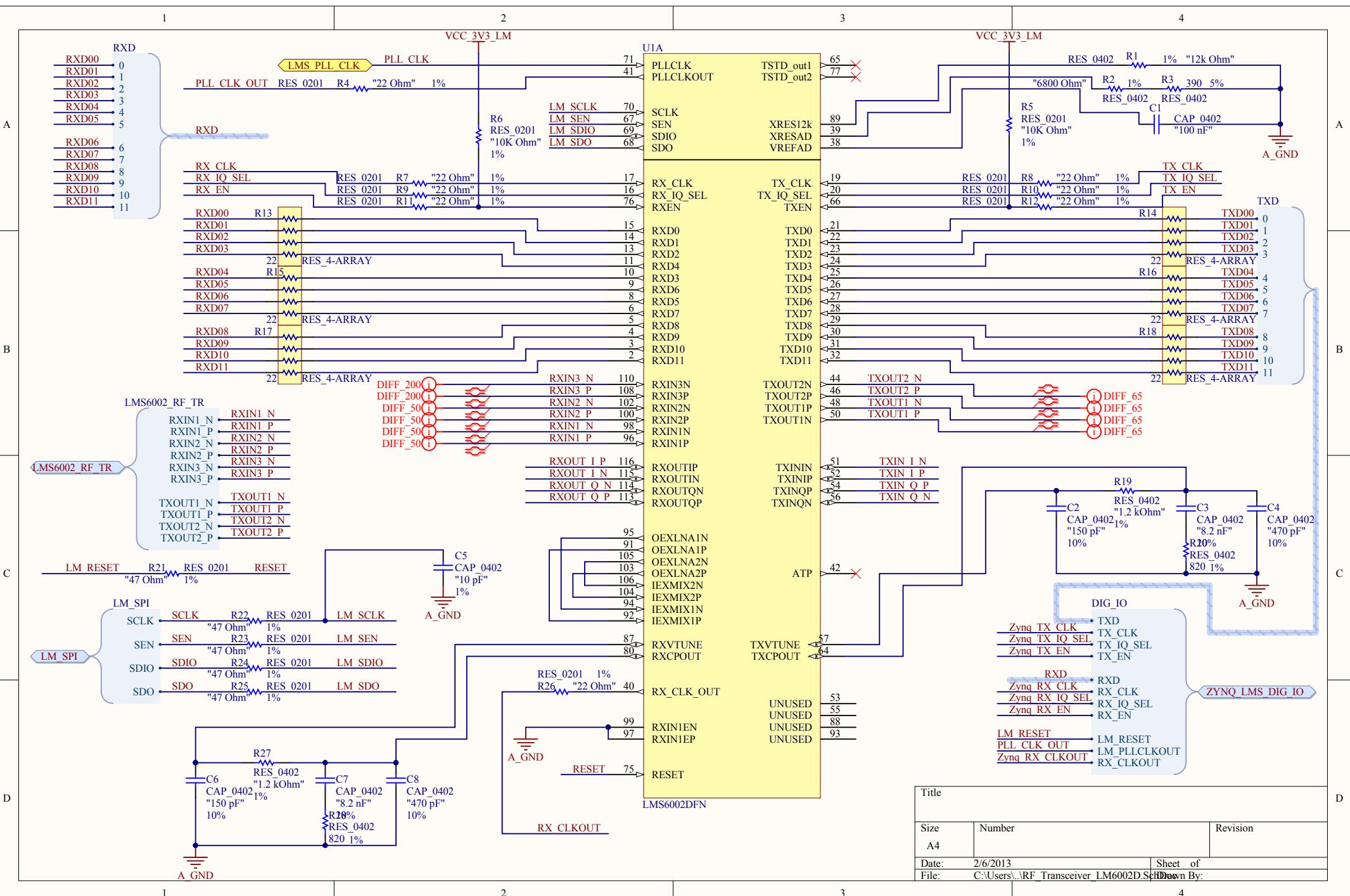
C

D



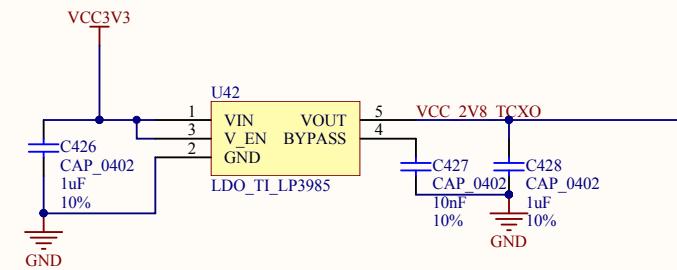


Title		
Size A4	Number	Revision
Date:	2/6/2013	Sheet of
File:	C:\Users\...\Ethernet1 Power.SchDoc	Drawn By:



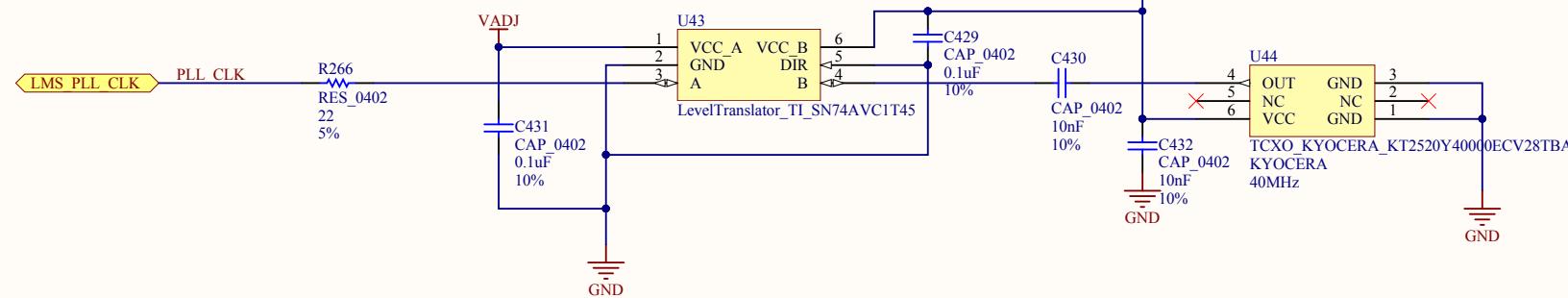
A

A



B

B



C

C

D

D

Title		
Size A4	Number	Revision
Date: 2/6/2013	Sheet of	
File: C:\Users\RF Transeiver LMS6002 Pl.Draft\K\BchDoc		

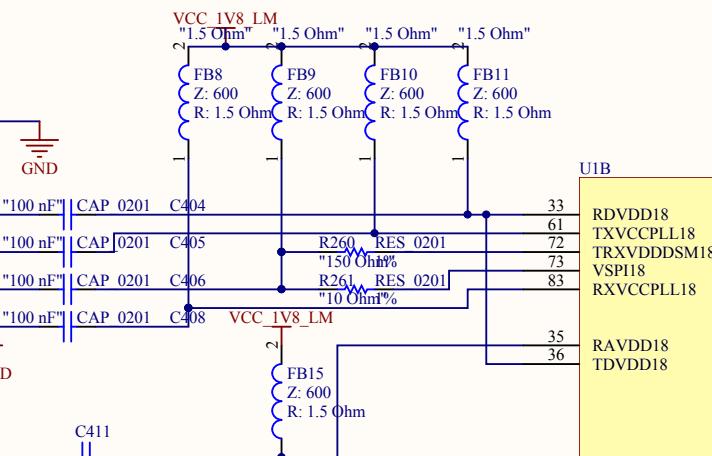
1

2

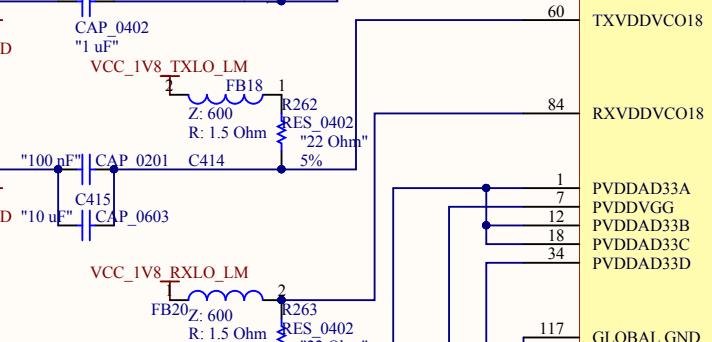
3

4

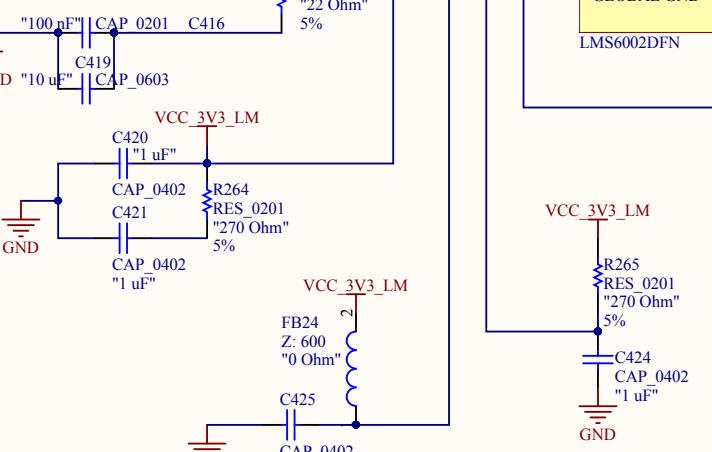
A



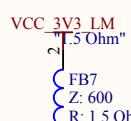
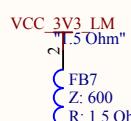
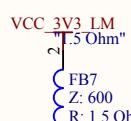
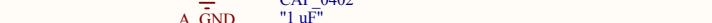
B



C

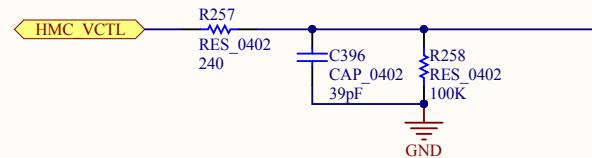


D



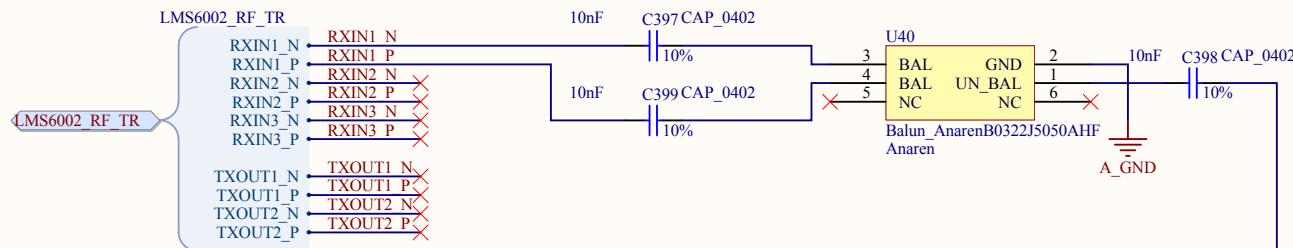
Title		
Size A4	Number	Revision
Date 2/6/2013		Sheet of
File C:\Users\RF Transeiver LM6002D\Documents		

A

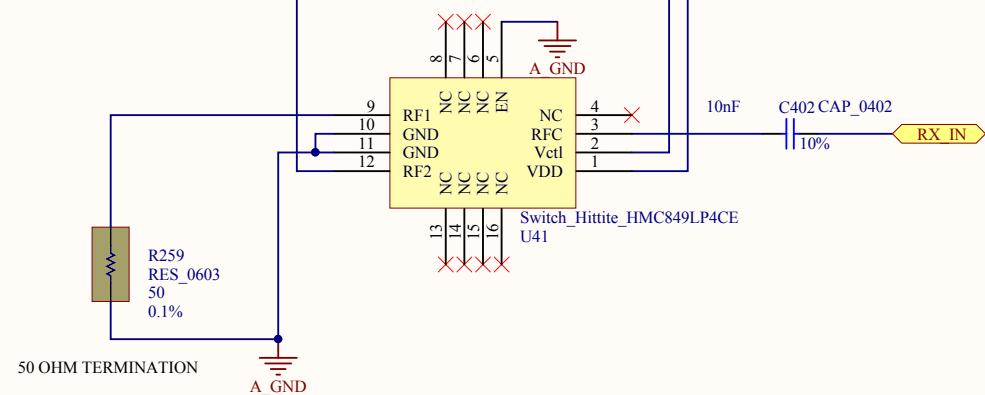


"Vctl"
 LOW - "RX ON"
 HIGH - "RX OFF"
 Default: HIGH - RX OFF

B



C



D

Title		
Size	Number	Revision
A4		
Date: 2/6/2013	Sheet of	
File: C:\Users\...\RF_Receive.SchDoc		Drawn By:

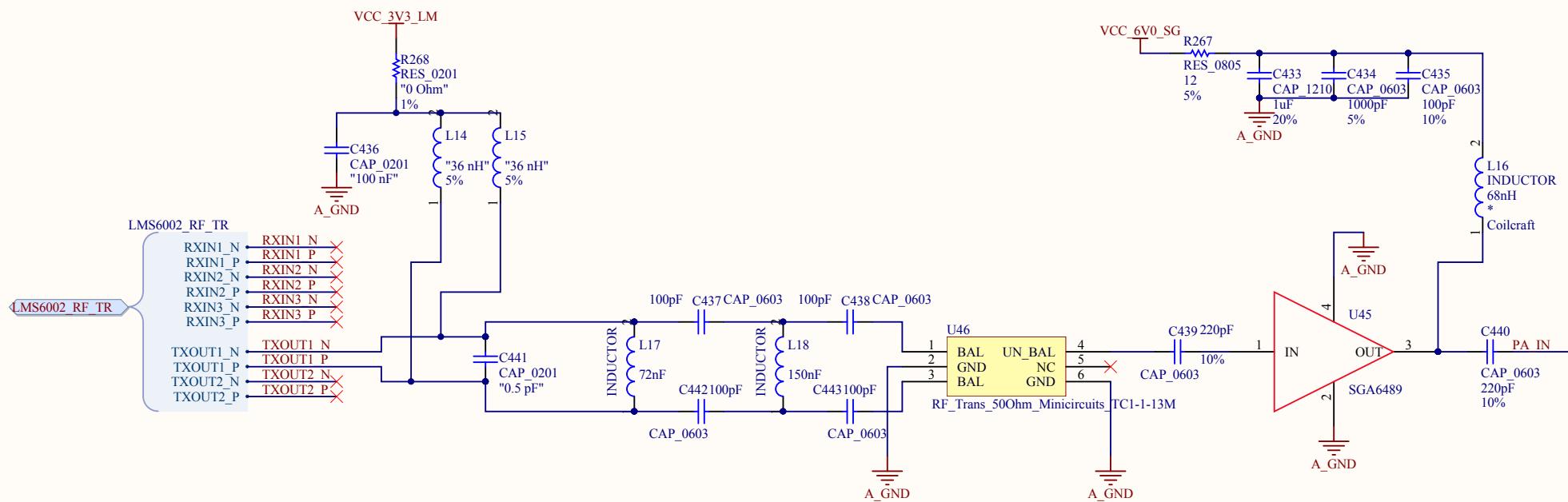
1

2

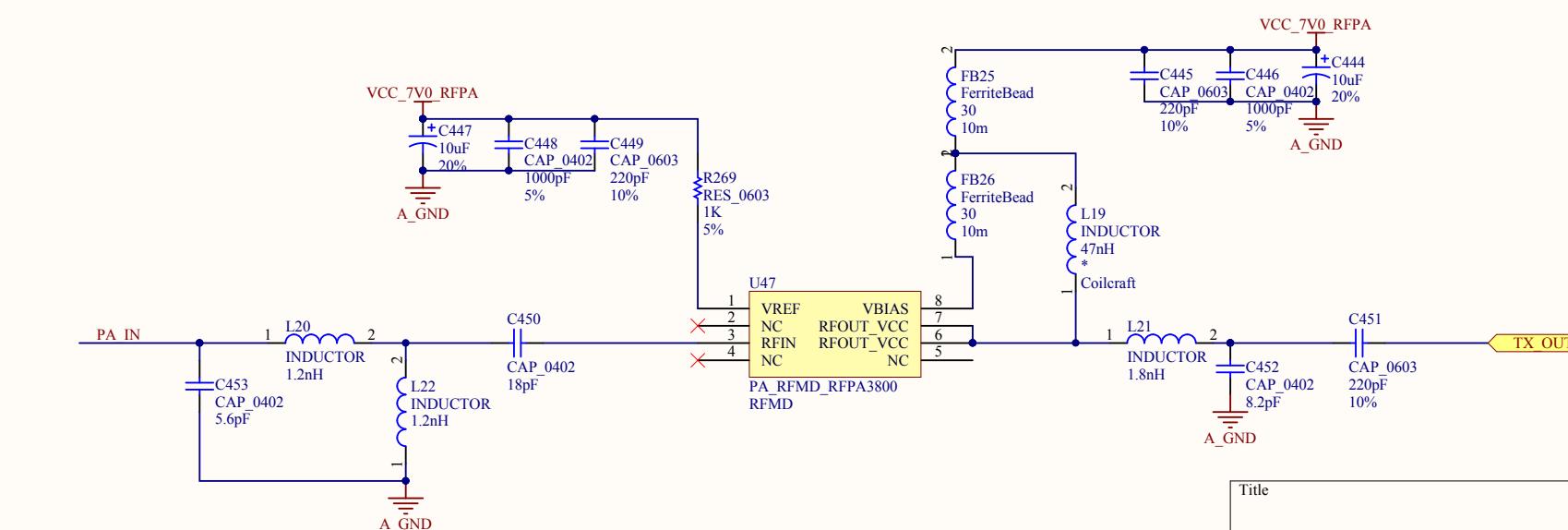
3

4

A



B



C

Title		
Size	Number	Revision
A4		
Date: 2/6/2013	Sheet of	
File: C:\Users\...\RF_Transmit.SchDoc		Drawn By:

1

2

3

4

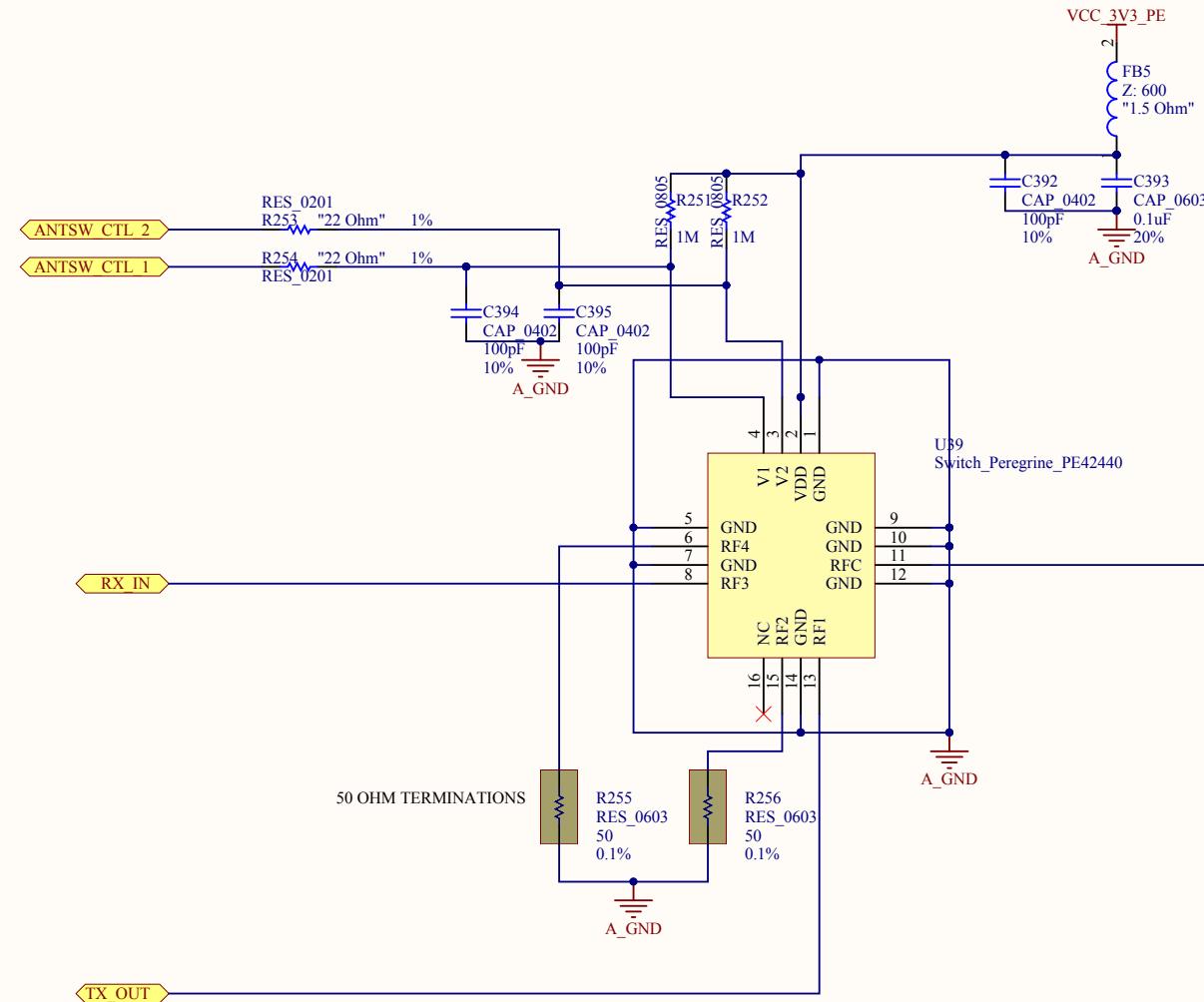
A

B

C

D

A



A

B

C

D

Title		
Size A4	Number	Revision
Date: 2/6/2013	Sheet of	
File: C:\Users\...\RF Antenna Switch.SchDoc	Drawn By:	

1

2

3

4

A

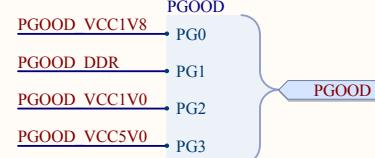
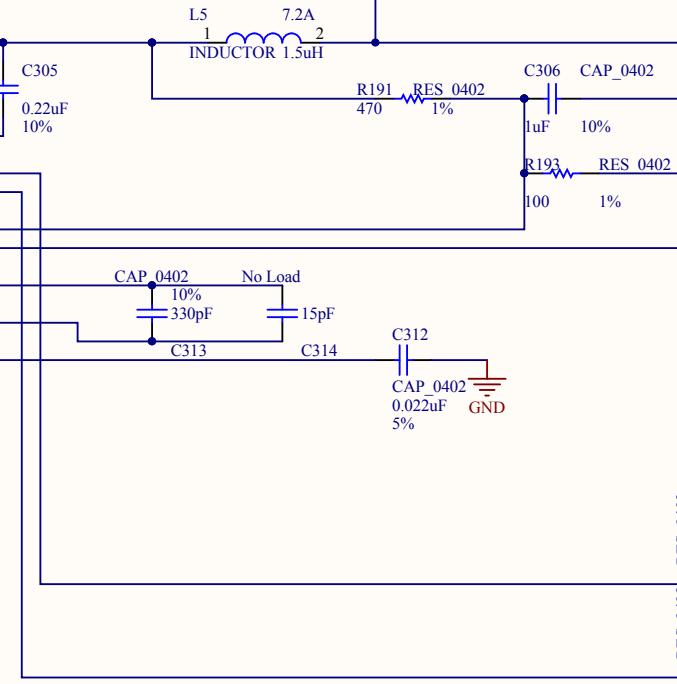
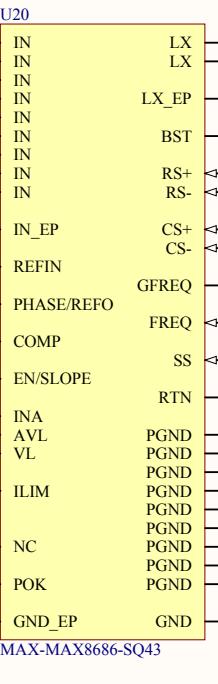
B

C

D

VCC5V0

GND



Title		
Size A4	Number	Revision
Date: 2/6/2013	Sheet of	
File: C:\Users\Power Regulators Digital 1\\$d.htm	By:	

1

2

3

4

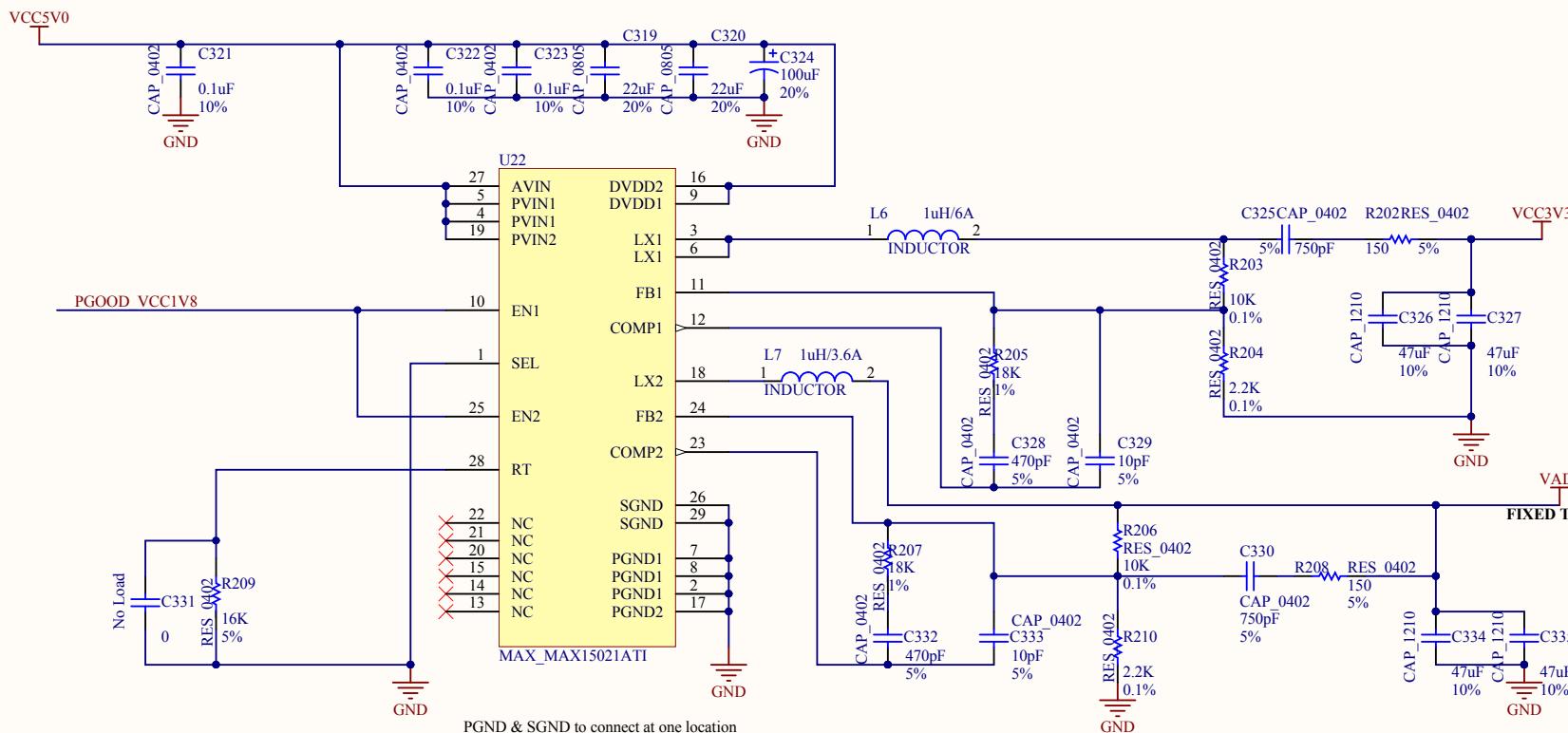
A

B

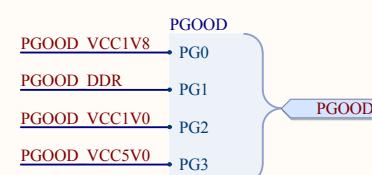
C

D

A



C



Title		
Size A4	Number	Revision
Date: 2/6/2013	Sheet of	
File: C:\Users\Power Regulators Digital 2\Schematic By:		

A

B

C

D

A

B

C

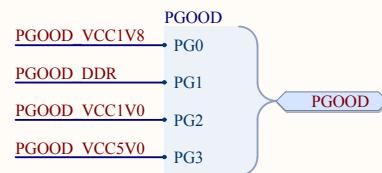
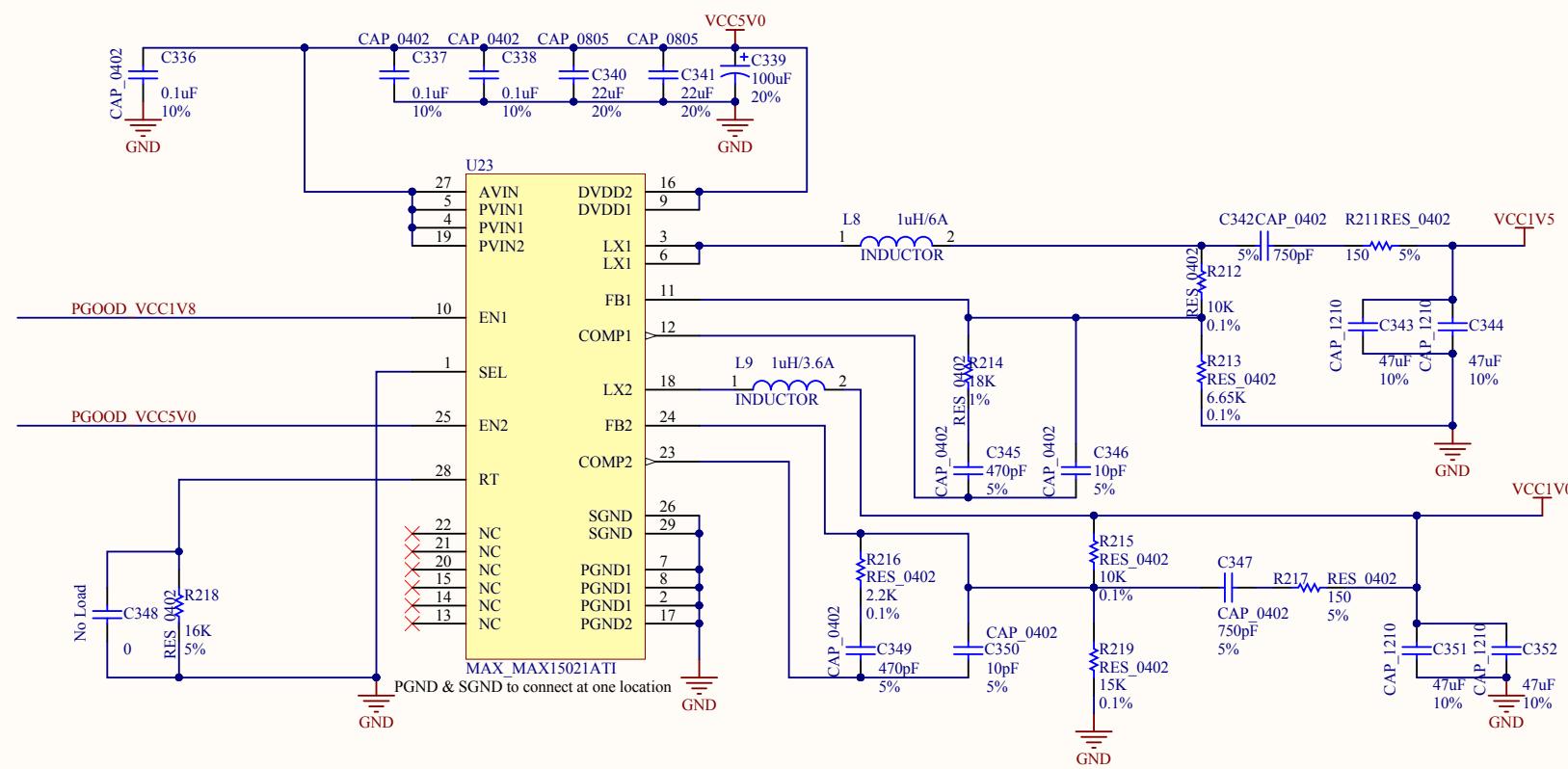
D

A

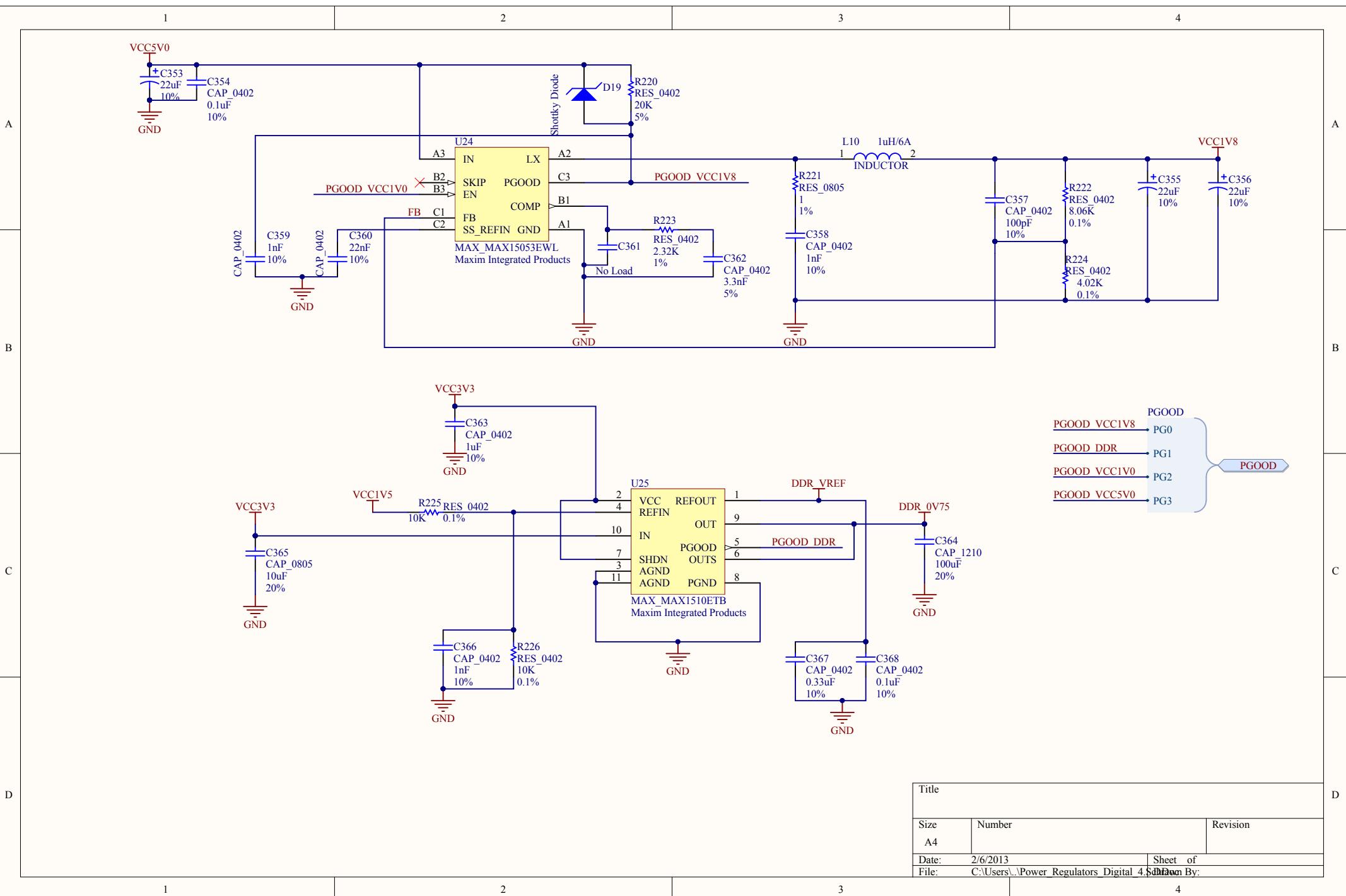
B

C

D



Title		
Size A4	Number	Revision
Date: 2/6/2013	Sheet of	
File: C:\Users\Power Regulators Digital 3	Download By:	



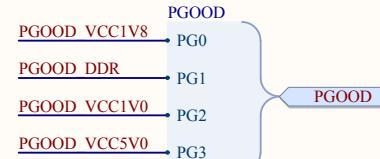
1

2

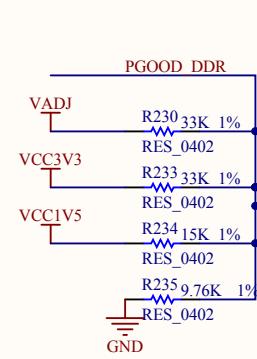
3

4

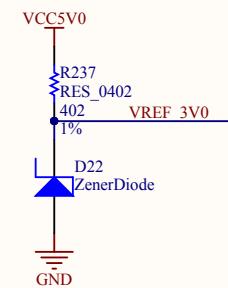
A



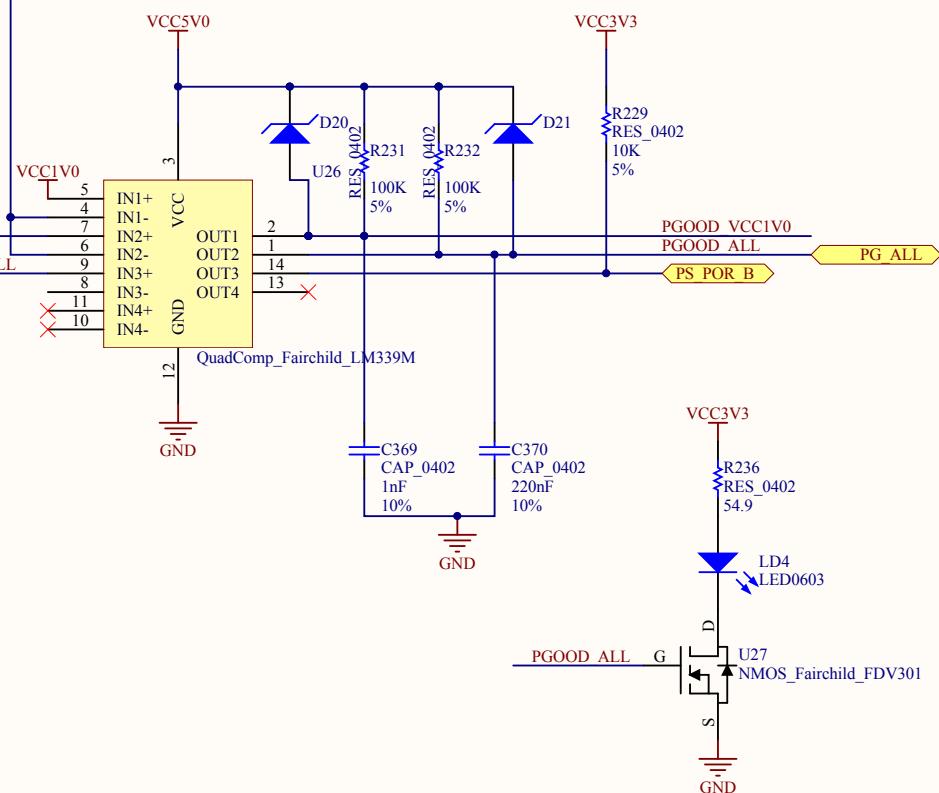
B



C



D



Title		
Size A4	Number	Revision
Date: 2/6/2013	Sheet of	
File: C:\Users\Power Regulators Digital 5\\$d.htm	By:	

1

2

3

4

A

A

B

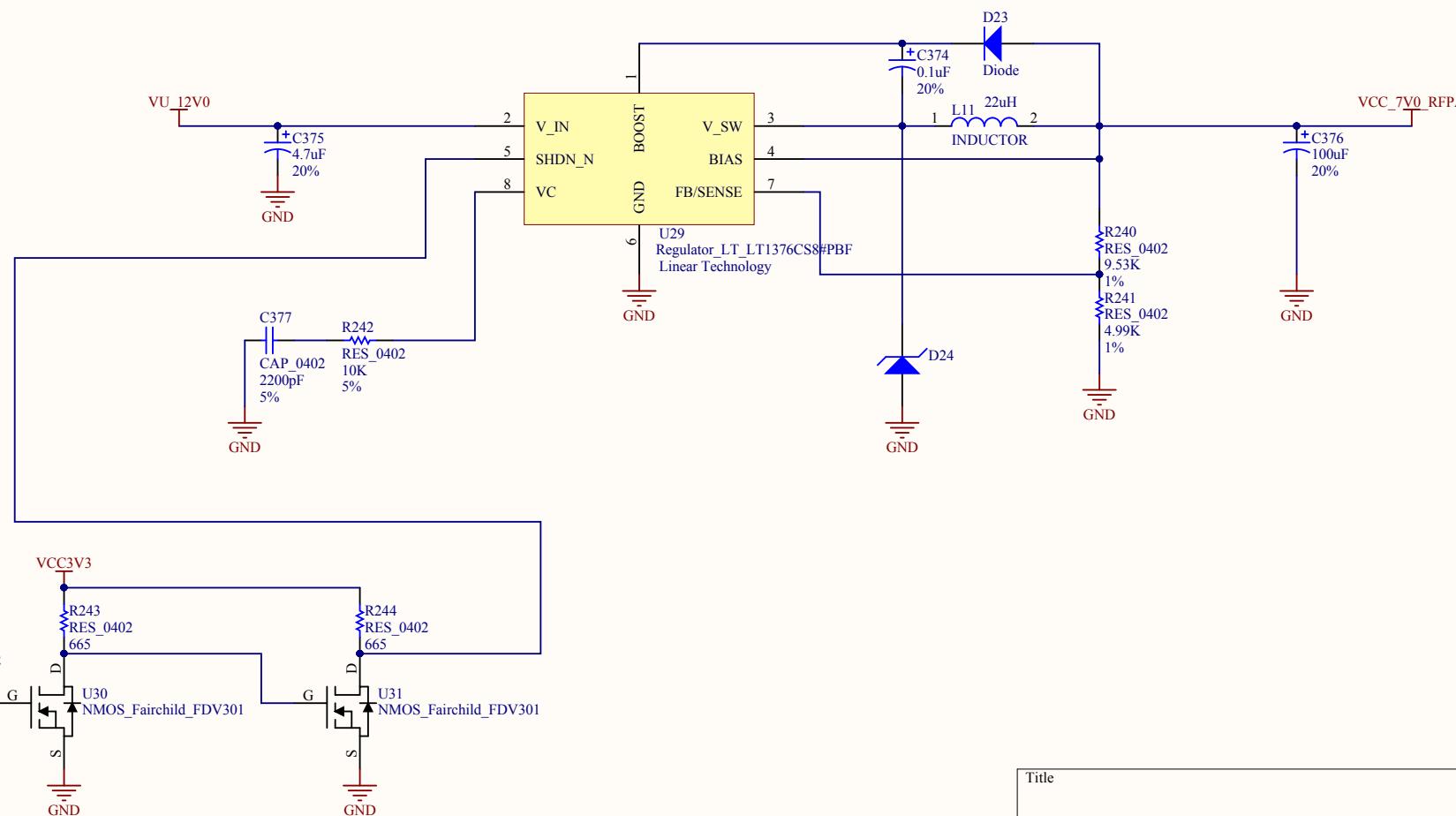
B

C

C

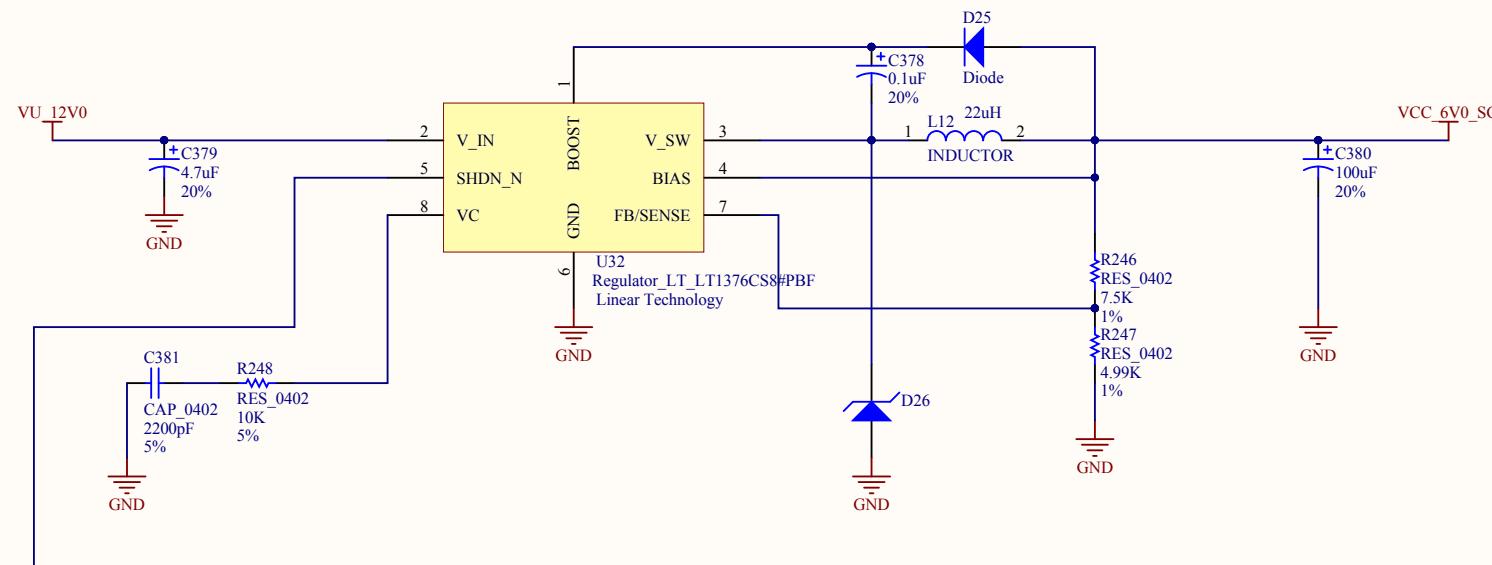
D

D

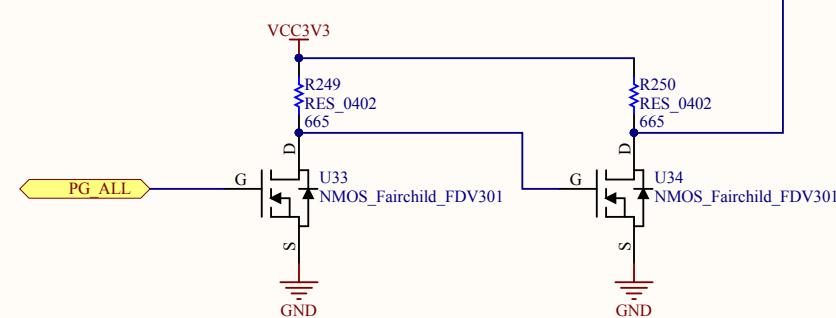


Title		
Size A4	Number	Revision
Date: 2/6/2013	Sheet of	
File: C:\Users\Power Regulators RF 1.Sch	Drawn By:	

A



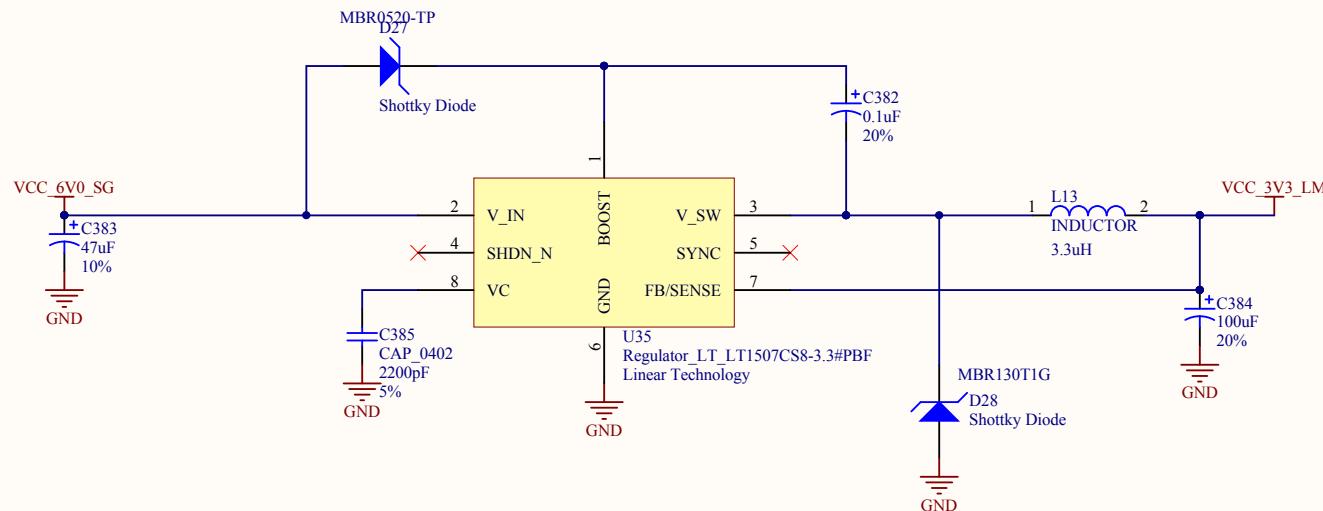
B



C

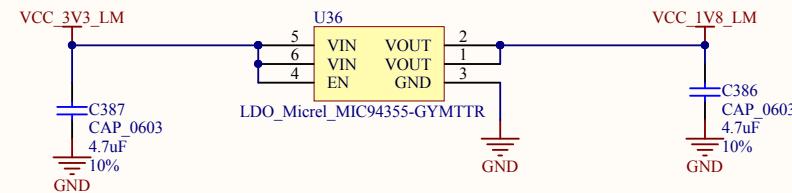
D

Title		
Size A4	Number	Revision
Date: 2/6/2013	Sheet of	
File: C:\Users\...\Power Regulators RF 2.Sch	Drawn By:	

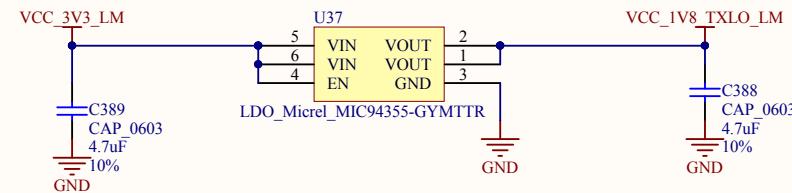


Title		
Size A4	Number	Revision
Date: 2/6/2013	Sheet of	
File: C:\Users\...\Power Regulators RF 3.Sch	Drawn By:	

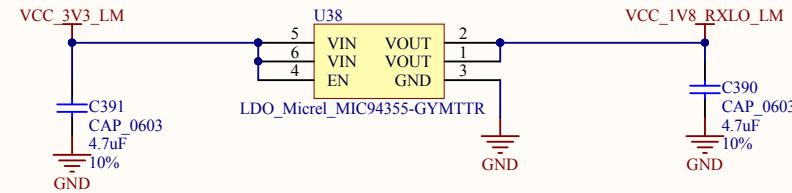
A



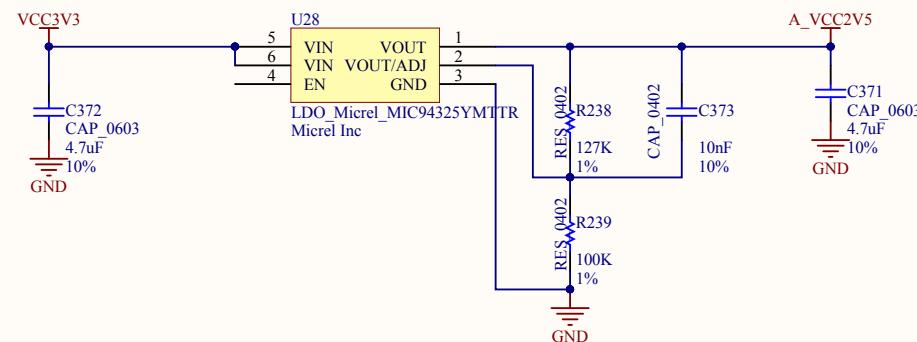
B



C



Title		
Size A4	Number	Revision
Date: 2/6/2013	Sheet of	
File: C:\Users\...\Power Regulators RF 4.SchD	Drawn By:	



Title		
Size	Number	Revision
A4		
Date: 2/6/2013	Sheet of	
File: C:\Users\Power Regulators Ether2V5	Drawn By:	

A

A

B

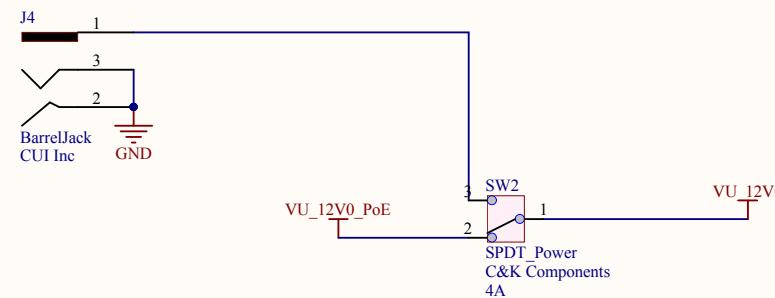
B

C

C

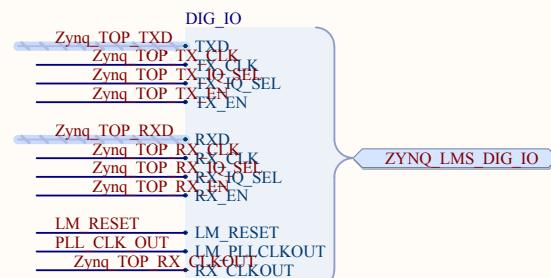
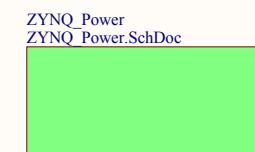
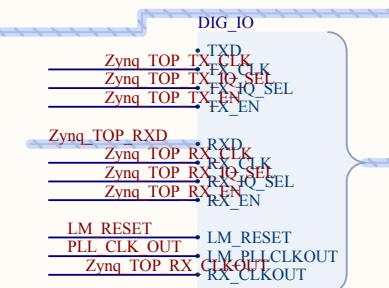
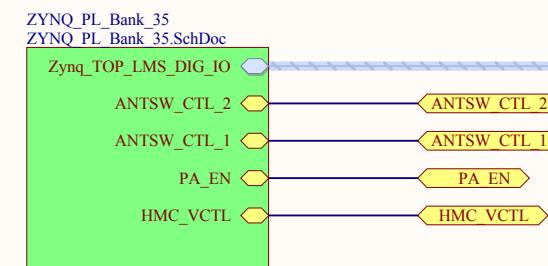
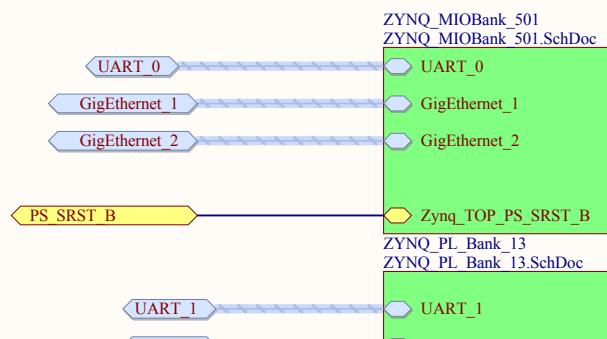
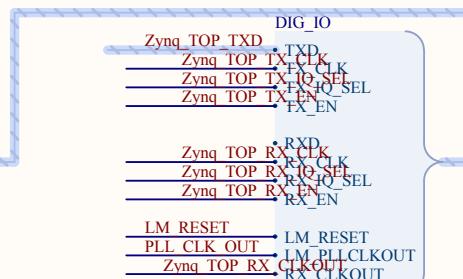
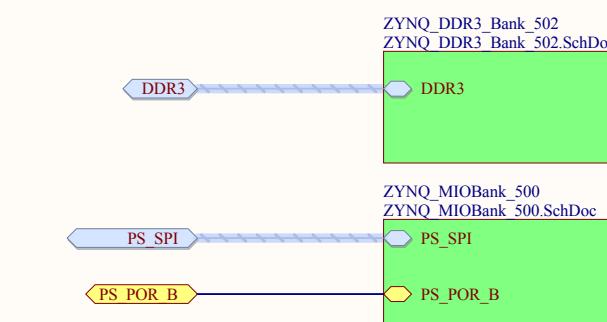
D

D



Title		
Size	Number	Revision
A4		
Date: 2/6/2013	Sheet of	
File: C:\Users\...\PowerSwitch.SchDoc	Drawn By:	

A



Title		
Size	Number	Revision
A4		
Date: 2/6/2013		Sheet of
File: C:\Users\...\ZYNQ_TOP_LEVEL.SchDoc		Drawn By:

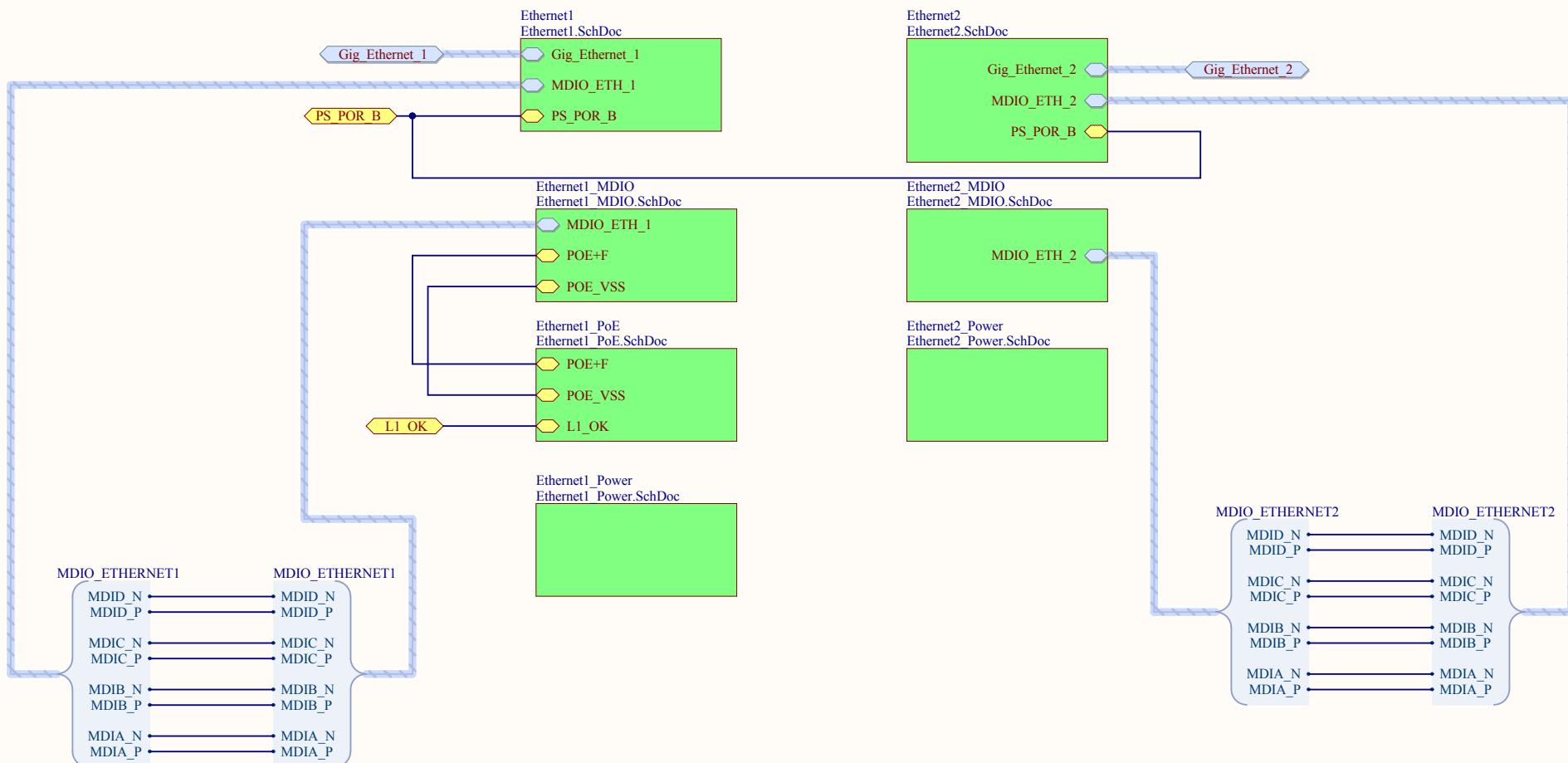
A

B

C

D

A



Title		
Size	Number	Revision
A4		
Date: 2/6/2013	Sheet of	
File: C:\Users\...\ETHERNET_TOP_LEVEL.SchDoc	Down By:	

A

A

B

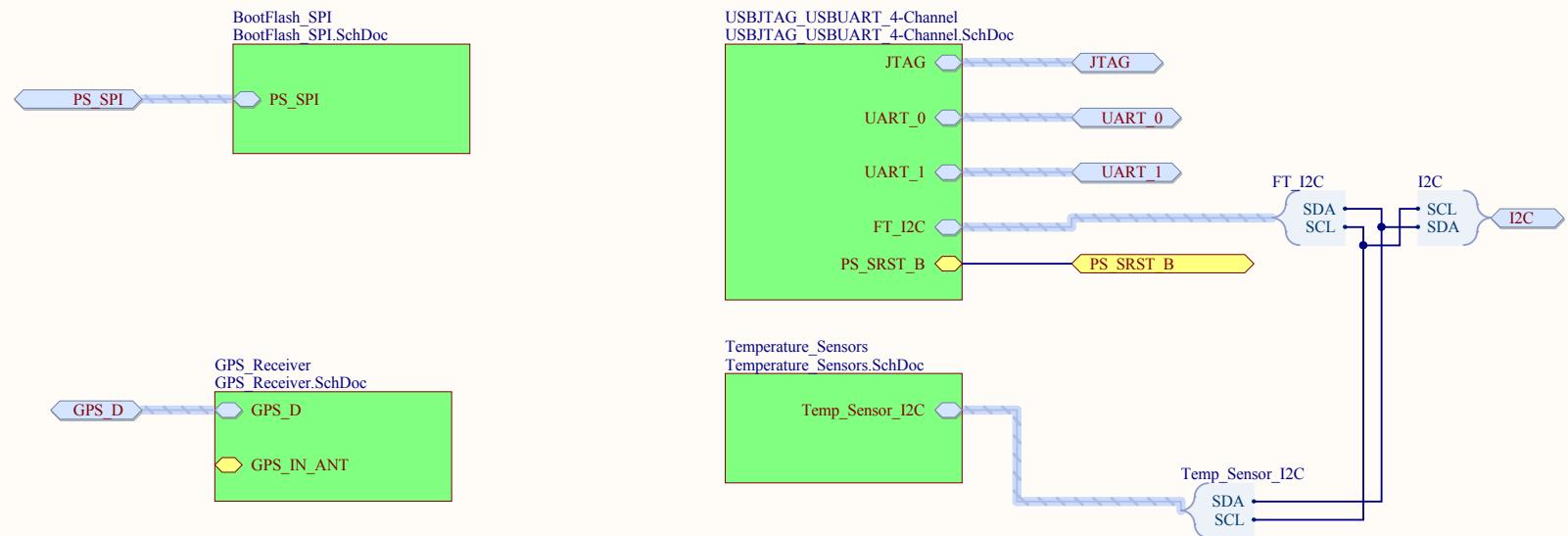
B

C

C

D

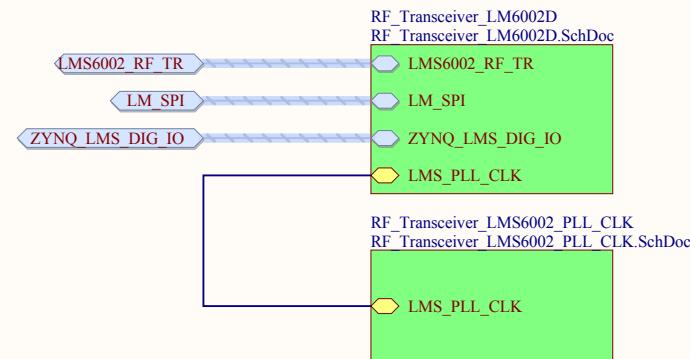
D



Title		
Size A4	Number	Revision
Date: 2/6/2013		Sheet of
File: C:\Users\.\PERIPHERALS.SchDoc		Drawn By:

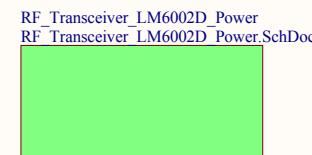
A

A



B

B



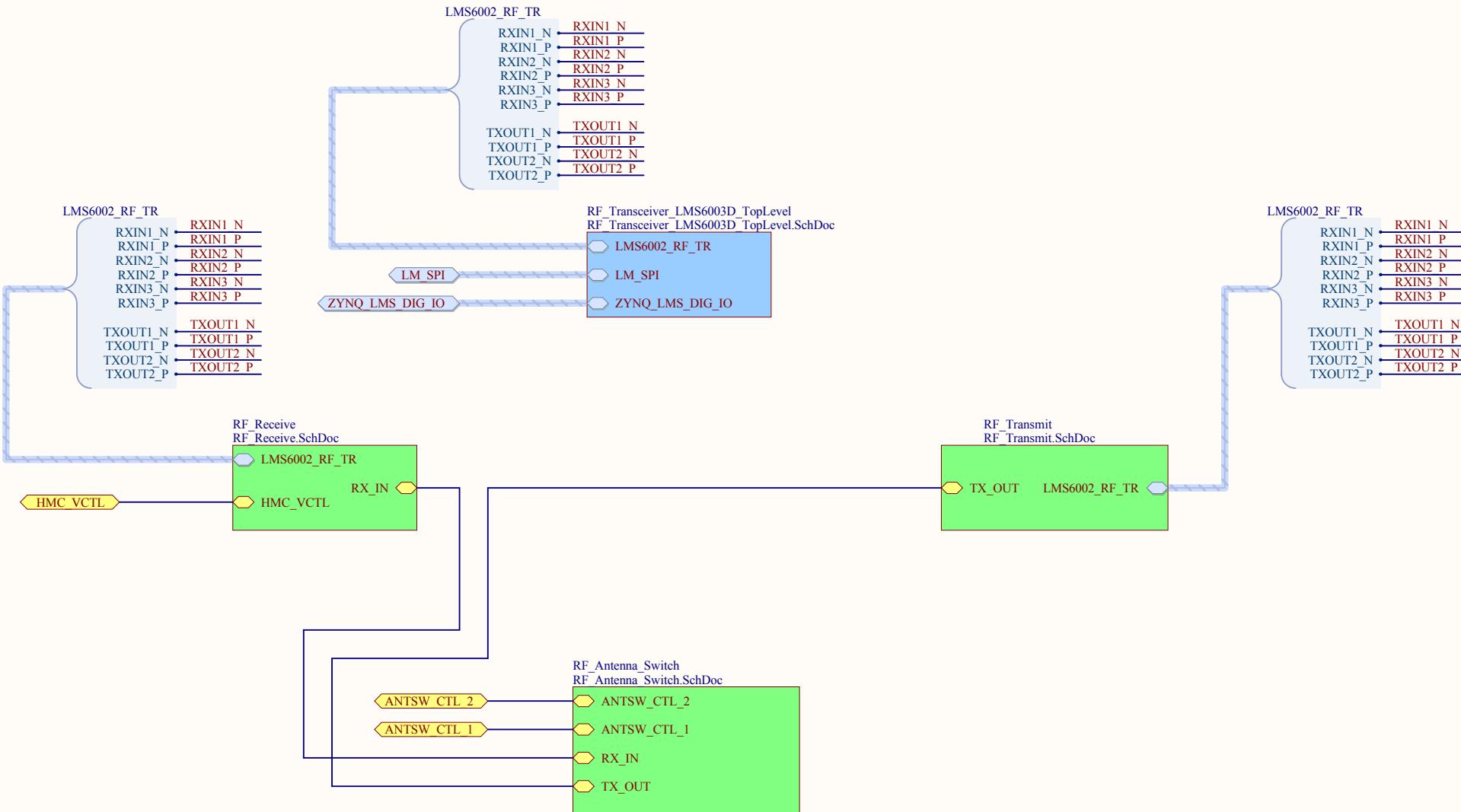
C

C

Title		
Size	Number	Revision
A4		
Date:	2/6/2013	Sheet of
File:	C:\Users\...\RF Transeeiver LM6003D	10 pages SchDoc

D

A



Title		
Size	Number	Revision
A4		
Date: 2/6/2013	Sheet of	
File: C:\Users\...\RF_TOP_LEVEL.SchDoc	Drawn By:	

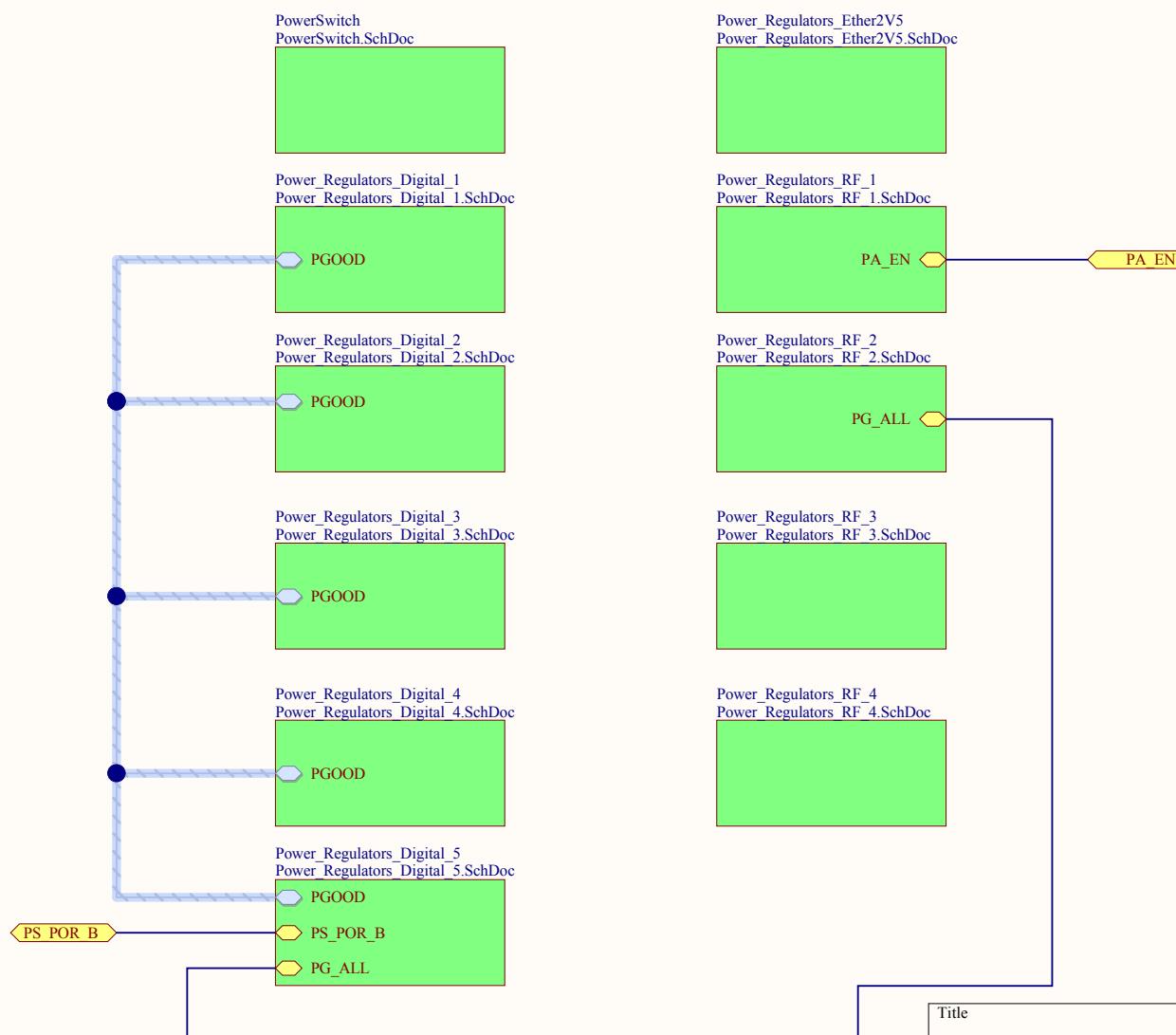
A

B

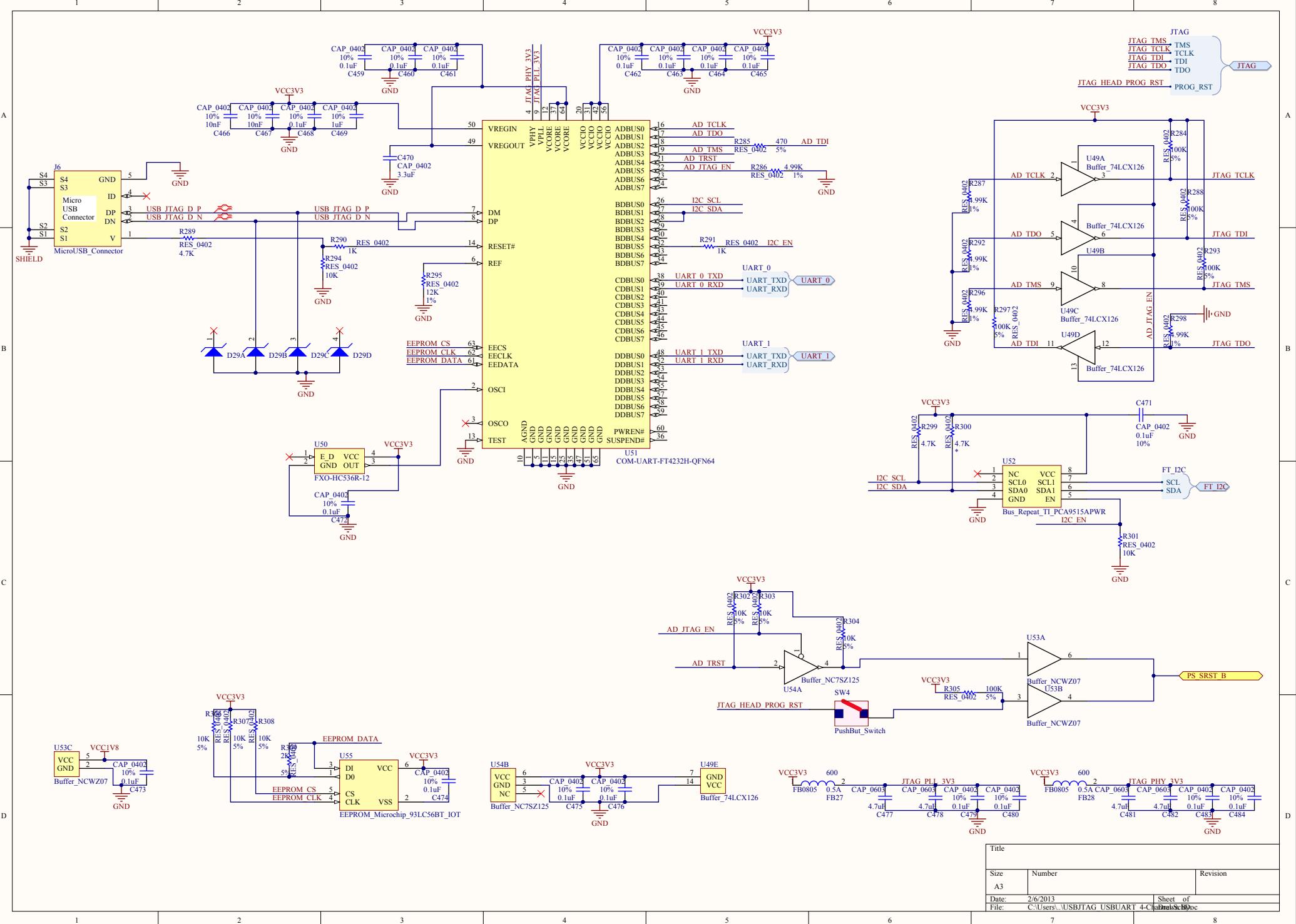
C

D

A

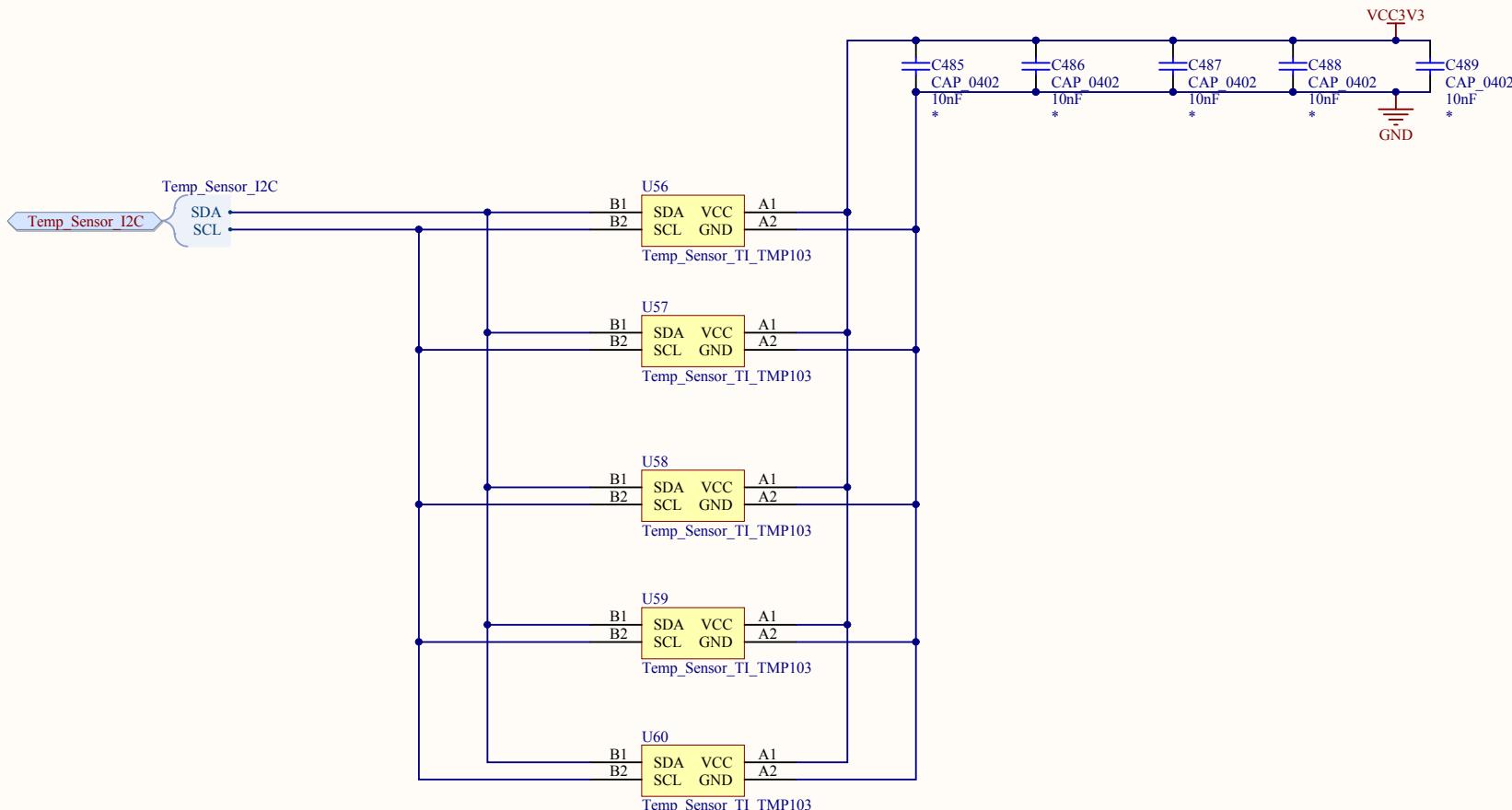


Title		
Size	Number	Revision
A4		
Date: 2/6/2013	Sheet of	
File: C:\Users\...\Power Distribution_TopLevel.SchDocBy:		

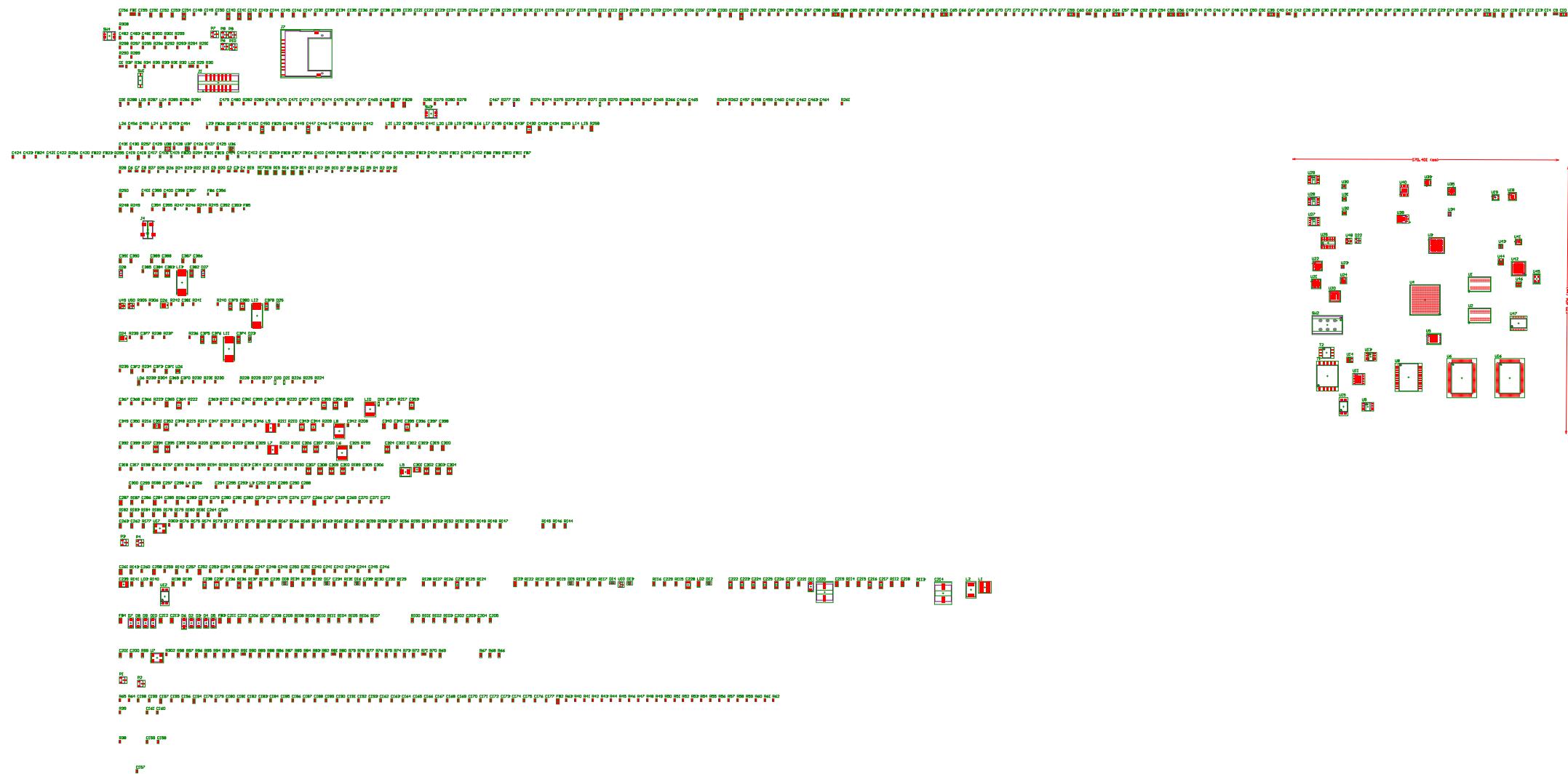


A

A



Title		
Size A4	Number	Revision
Date: 2/6/2013	Sheet of	
File: C:\Users\...\Temperature_Sensors.SchDoc	Drawn By:	



Part Number	Manufacturer	Description	Value	Library Ref	Footprint Ref	Distributor	Manufacturer Datasheet	Quantity
-------------	--------------	-------------	-------	-------------	---------------	-------------	------------------------	----------