

**Table 1. STM32L0x2 memory density**

Memory density	Category 3	Category 5
16 Kbytes	-	-
32 Kbytes	STM32L052x STM32L062x (AES)	-
64 Kbytes	STM32L052x STM32L062x (AES)	STM32L072x
128 Kbytes	-	STM32L072x STM32L082x (AES)
192 Kbytes	-	STM32L072x STM32L082x (AES)

**Table 2. Overview of features per category**

Feature	Category 3	Category 5
MPU	full-featured	full-featured
NVM	full-featured, single bank	full-featured
Cyclic redundancy check calculation unit (CRC)	full-featured	full-featured
Firewall (FW)	full-featured	full-featured
Power control (PWR)	full-featured	full-featured
Reset and clock control (RCC)	full-featured	full-featured
Clock recovery system (CRS)	full-featured	full-featured
GPIOA	full-featured	full-featured
GPIOB	full-featured	full-featured
GPIOC	full-featured	full-featured
GIOD	[2]	full-featured
GPIOE	-	full-featured
GPIOH	[0:1]	[0:1][9:10]
System configuration controller (SYSCFG)	full-featured	full-featured
Direct memory access controller (DMA1)	full-featured	full-featured
Nested vectored interrupt controller (NVIC)	full-featured	full-featured
Extended interrupt and event controller (EXTI)	full-featured	full-featured
Analog-to-digital converter (ADC1)	full-featured	full-featured
Digital-to-analog converter (DAC1)	full-featured	full-featured
Digital-to-analog converter (DAC2)	-	full-featured
Comparator (COMP1)	full-featured	full-featured
Comparator (COMP2)	full-featured	full-featured