

Table 1. STM32L0x1 memory density

Memory density	Category 1	Category 2	Category 3	Category 5
8 Kbytes	STM32L011x STM32L021x (AES)	-	-	-
16 Kbytes	STM32L011x STM32L021x (AES)	STM32L031x STM32L041x (AES)	-	-
32 Kbytes	-	STM32L031x STM32L041x (AES)	STM32L051x	-
64 Kbytes	-	-	STM32L051x	STM32L071x
128 Kbytes	-	-	-	STM32L071x STM32L081x (AES)
192 Kbytes	-	-	-	STM32L071x STM32L081x (AES)

Table 2. Overview of features per category

Feature	Category 1	Category 2	Category 3	Category 5
MPU	-	-	full-featured	full-featured
NVM	full-featured, single bank	full- featured, single bank	full- featured, single bank	full-featured
Cyclic redundancy check calculation unit (CRC)	full-featured	full-featured	full-featured	full-featured
Firewall (FW)	-	-	full-featured	full-featured
Power control (PWR)	full-featured	full-featured	full-featured	full-featured
Reset and clock control (RCC)	HSE supports bypass only, no CSS on HSE	full-featured	full-featured	full-featured
GPIOA	full-featured	full-featured	full-featured	full-featured
GPIOB	[0:9], BOOT0/PB9 sharing the same pin	full-featured	full-featured	full-featured
GPIOC	[14:15]	[0][13:15]	full-featured	full-featured
PIOD	-	-	[2]	full-featured
GPIOE	-	-	-	full-featured
GPIOH	-	[0:1]	[0:1]	[0:1][9:10]
System configuration controller (SYSCFG)	full-featured	full-featured	full-featured	full-featured
Direct memory access controller (DMA1)	full-featured	full-featured	full-featured	full-featured
Nested vectored interrupt controller (NVIC)	full-featured	full-featured	full-featured	full-featured
Extended interrupt and event controller (EXTI)	full-featured	full-featured	full-featured	full-featured
Analog-to-digital converter (ADC1)	full-featured	full-featured	full-featured	full-featured