EE260 -- Introduction to Computer Engineering
Winter 1989-90

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Instructor: Bob Laurie   Office: EERC-734   Phone: 487-2789
Office Hours: Monday, Wednesday, Friday: 3:30-4:30, by arrangement, or when ever my office door is open.

TEXTS:
      I. R.M. Laurie, Introduction to Computer Engineering, 1990
      II. Leventhal, 6809 Assembly Language Programming, 1981
      III. Motorola Inc., MC6809 Microprocessor Programming Manual, 1981

TENTATIVE SCHEDULE:

Date:	*Topics:*	*Reading:*
Nov 27	Binary Numbers and Logic Gates	I: Chap 1 & 2
Nov 29	Boolean Algebra	I: Chap 2
Dec 1	Combinational Circuit Design	I: Chap 2
Dec 4	Integrated Circuits	I: Chap 2
Dec 6	Flip Flops	I: Chap 3
Dec 8	Sequential Circuit Design	I: Chap 3
Dec 11	Number Systems and Binary Addition	I: Chap 4
Dec 13	Hexadecimal Addition and Codes	I: Chap 4
Dec 15	Review	
Dec 18	EXAM 1: *** IN CLASS ***	
Dec 20	Memory	I: Chap 3
Dec 22	Computer Architecture	I: Chap 5 II: Section 1
Jan 8	Computer Architecture Continued	II: pp 3.1-3.5 III: Sec 1
Jan 10	MC6809 Instructions & Addr Modes	II: pp 3.6-3.14 III: Apx A
Jan 12	Indexed Addressing Modes	II: pp 3.14-3.50 III:Sec 2
Jan 15, 17	More Instructions	III: Apx A II: Section 2
Jan 19	Programming	II: Sections 4,5
Jan 22	Programming	II: Sections 6,7
Jan 24	Programming	II: Sections 8,9
Jan 26	Review	
Jan 29	EXAM 2: *** IN CLASS ***	
Jan 31	Subroutines	II: Section 10
Feb 5	Paramenter Passing Techniques	II: Section 11
Feb 7,9	I/O Devices	II: Section 12
Feb 12,14	Interrupts	II: Section 15 III: Sec 3
Feb 16	Final Review	

GRADING:

* A laboratory grade lower than 50% results in failure of course.

QUIZZES:

Unannounced quizzes will be given through out the quarter.   These quizzes
will cover the assigned reading and recently completed problem sets.   Only
students with excused absences from the Registrar's office may make up the
quiz. You must call the registrar before 9:00 AM on the day of the absence.

HOMEWORK:

It is the students responsibility to complete all homework by the due date.
Solutions to problem sets will be posted for ONE week after they are assigned
in the locked cabinet by room 738. It is the students responsibility to check
their solution within one week. Problems similar to the problem sets will
appear on quizzes and exams.

LABORATORY: Begins Monday December 4

Lab Dates	Assignments	Points	Report Due
Dec 4-7	Combinational Logic	10	Dec 11-14
Dec 11-14	Sequential Logic	15	Jan 8-11
Jan 8-11	Introduction to the 6809 Microcomputer	10	Jan 15-18
Jan 15-18	Introduction to the HP64000 Dev. System	15	Jan 22-31
Jan 22-31	Program #1	20	Feb 5
Feb 5-15	Program #2	30	Feb 16

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