CAL STATE LONG BEACH

1. ; JOSEPH OKONOBOH

; 0137755064

; SWITCH BASED-COMBO LOCK

```
MASK
              EQU
                      0x0F
     LED
              EQU
                      P0
                               ;LED port
     switch
              EQU
                      P1
                               ;switches port
                               ;1st input
              EQU
                      0x04
     num1
                               ;2nd input
     num2
              EQU
                      0x05
              EQU
     num3
                      0x07
                               ;3rd input
              EQU
                             ;final input
     num4
                      0x0F
     s0
              EQU
                      0x00 ;state 0
              EQU
                      0x01
                             ;state 1
     s1
                             ;state 2
              EQU
     s2
                      0x02
              EQU
                      0x03
                             ;state 3
     s3
     state
              EQU
                      0x07
             Initialization ----;
             LED, #0x00 ;set Port as output
     VOM
     VOM
              R7, #s0
                            ;R7 contains present state
              LED, #s0
     VOM
; read data from switch into accumulator
READ_DATA:
     VOM
              Α,
                   switch
                   #MASK
     ANL
              Α,
STATEO:
              R7, #s0,
     CJNE
                            STATE1
                                       ;if not in state0 go to state 1
     CJNE
              Α,
                   #num1,
                            READ_DATA
                                       ; if first input is wrong re-read data from switch
     VOM
              R7, #s1
                                       ;go to state 1
              LED, #s1
     VOM
                                       ; signal user that state was successfully changed
              READ_DATA
                                       ;read next input
     SJMP
STATE1:
              R7,
                   #s1,
                            STATE2
                                       ;if not in state1 go to state 2
     CJNE
     CJNE
              Α,
                   #num2,
                            READ_DATA
                                       ; if second input is wrong re-read data from switch
     VOM
              R7, #s2
                                       ;go to state 2
     VOM
              LED. #s2
                                       ; signal user that input two was correct
     SJMP
              READ_DATA
                                       ;read next input
STATE2:
                                       ; if not in state2 go to state 3
     CJNE
              R7, #s2,
                            STATE3
                                       ; if third input is wrong re-read data from switch
     CJNE
              Α,
                   #num3,
                            READ_DATA
     VOM
              R7,
                   #s3
                                       ;go to state 2
              LED, #s3
     VOM
                                       ; signal user that input three was correct
     SJMP
              READ_DATA
                                        ;read next input
```

CECS 285, Spring 2015 Section 1 (4344) Lab #7, Due: 2015, March 25

STATE3:

CJNE R7, #s3, READ_DATA ;if not in state3 or if input is wrong read data f: CJNE A, #num4, READ_DATA

;;;;; Repeatedly display LED for 1s and turn off led for 1s $\,$

BLINK:

MOV LED, #OxFF
ACALL DELAY
MOV LED, #Ox00
ACALL DELAY

BLINK

;;;;; 0.5Hz timing delay (1s ON, 1s OFF)

DELAY:

SJMP

MOV RO, #175

LOOP0:

MOV R1, #250

LOOP1:

MOV R2, #250

LOOP2:

DJNZ R2, LOOP2 DJNZ R1, LOOP1

DJNZ RO, LOOPO

RET

END