CECS 285, Spring 2015 Section 1 (4344) HW #4, Due: 2015, March 16

Activity 1.	TOGGLE:
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VOM	A, #55H	;	Α	<-	01010101B
VOM	PO, A	;	P0	<-	01010101B
VOM	P1, A	;	P1	<-	01010101B
MOV	P2. A	:	P2	<-	01010101B

## ACALL DELAY

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MOV	A, #OAAH	;	Α	<-	10101010B
VOM	PO, A	;	P0	<-	10101010B
VOM	P1, A	;	P1	<-	10101010B
MOV	P2, A	;	P2	<-	10101010B

ACALL DELAY SJMP TOGGLE

## DELAY:

MOV RO, #250 LOOPO: MOV R1, #164 LOOP1: DJNZ R1, LOOP1 DJNZ R0, LOOPO

RET

END

## Activity 2.

MOV	PO,	#0 ;	P0	= OUTPUT
MOV	P2,	#0 ;	P1	= OUTPUT
MOV	A, P	1 ;	Α	<- P1
MOV	PO,	Α ;	PO	<- P1
MOV	P2,	Α ;	P2	<- P1
MOV	RO,	Α ;	RO	<- P1
MOV	R1,	Α ;	R1	<- P1
MOV	R2.	A :	R2	<- P1

END

- Activity 3. 1. Upon reset, all the ports of the 8051 are configured as **input**.
  - 2. To make all the bits of a port an output port we must write  ${\bf 0}$  hex to it.
  - 3. Which ports of the 8051 are bits addressable? **Ans:** P0, P1, P2, and P3.

4.

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MOV P1, #0 ; P1 = OUTPUT

HIGH:

JB P2.4, HIGH MOV P1, #55H

END