#### <sup>i</sup> Front side

Emnekode: DAT320

Emnenavn: Operativsystemer og systemprogrammering

År og semester: 2022 Høst - konte

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Klokkeslett: 09:00-13:00

Tillate hjelpemidler: Kalkulator

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# <sup>1</sup> return-from-trap

Which statement about return-from-trap is true?  Select one alternative:	
Allocates memory	
○ Changes the CPU mode	✓
O Updates the trap table	
o starts a system call	
	Maximum marks: 1

## <sup>2</sup> Process list

Which shell command is used to list the processes?  Select one alternative:	
proclist	
○ Isproc	
O list-processes	
O ps	•
	Maximum marks: 1

# <sup>3</sup> Address translation

	Maximum marks: 1
○ Address Bus	
○ Virtual Frame Translator	
O Page Table Converter	
Translation Look-aside Buffer	
Memory Management Unit	✓
Which device is responsible for address translation?  Select one alternative:	

# 4 exec()

Which statements about the exec() (specifically execvp()) call are true and false?

#### Please match the values:

	True	False
execvp() changes the current process.	0 🗸	0
execvp() loads a program from disk.	0 🗸	0
execvp() is used to create a child process		O 🗸

# <sup>5</sup> Process

Which statements are true or false about the Process Abstraction.

#### Please match the values:

True	False
	0 🗸
O •	0
	0 🗸
O •	
O 🗸	
	True

## <sup>6</sup> Parent child

Select one or more alternatives:	r points all answers must be correct.
☐ Alternative 3	
☐ Alternative 4	
☐ Alternative 2	✓
☐ Alternative 1	
	Maximum marks: 2

## <sup>7</sup> Parent Child 2

Which output(s) of the program is/are possible? To get points Select one or more alternatives:	all answers must be correct.
☐ Alternative 1	✓
Alternative 2	✓
☐ Alternative 4	
☐ Alternative 3	
	Maximum marks: 2

# <sup>8</sup> Memory virtualization

Virtualization of the whole memory is described by which term?  Select one alternative:	
Base and bounds	
○ Address space	~
○ Segmentation	
○ Virtual addressing	
	Maximum marks: 1

# 9 System call

Which statements are true about system calls?

#### Please match the values:

	True	False
Can run restricted operations	O •	0
Can be changed by the user		O •
Executed in user mode		0 🗸
Executed using a trap (software interrupt)	O •	0

## <sup>10</sup> Documentation

Which shell command is used to Select one alternative:	o show documentation about a	system call?
O doc		
○ man		<b>✓</b>
help		
		Maximum marks: 1

# <sup>11</sup> Memory types

This is a memory type is part of a process. True or false?

	False	True
Неар		O 🗸
Random access memory	· •	0
Read only memory	· •	0
Stack		O 🗸

# <sup>12</sup> Memory accesses

	Maximum marks: 1		
How many memory accesses are necessary to load a value from an address	:	(2) .	
Consider paging without caching.			

# <sup>13</sup> PFN size

	Maximum marks: 2
(21) bits	
Given a page size of 2KB and a 32 bit architecture. He	ow many bits are used for the PFN?

# <sup>14</sup> Kopi av TLB

	Maximum marks: 1
a buffer for translated instructions	
A cache for main memory	
A cache for page tables	✓
Memory for page tables	
What is the translation look-aside buffer?  Select one alternative:	

### <sup>15</sup> Allocation

Consider the code in the attached document.

#### Mark the statements as true or false

	False	True
Memory space for r is allocated on the stack	<ul><li>✓</li></ul>	0
Memory space for r is allocated on the heap		·
The function "some_function()" returns a pointer	0	•
Memory space for r is allocated in ROM	○ <b>✓</b>	0
The memory of r cannot be accessed after the function "some_function()" has returned		0

## <sup>16</sup> Address Resolution 1

Given a 16 bit architecture and a page size of 256 bytes.

Page	e Ta	ble

PFN	Р	U/S	R/W	V
a1	1	0	1	1
b2	1	1	0	1
c4	1	0	1	1
		250 items omitted		
01	1	0	1	1
02	1	0	1	0
03	1	0	0	0

What is the physical address for virtual address <b>02fe</b> ? Enter the address in hex:	(c4fe)
What is the physical address for virtual address <b>0101</b> ? Enter the address in hex:	(b201)
What is the physical address for virtual address <b>ff01</b> ? Enter the address in hex:	(0301)
What is the physical address for virtual address <b>feab</b> ? Enter the address in hex:	(02ab)

## <sup>17</sup> Address Resolution 2

Given a 24 bit architecture and a page size of 1024 bytes.

Page	<u>Table</u>

i ago iabio				
PFN	Р	U/S	R/W	V
3ff	1	0	1	1
003	1	1	0	1
2c4	1	0	1	1
		16378 items on	nitted	_
101	1	0	1	1
000	1	0	1	0
001	1	0	0	0

(007fff)	
What is the physical address for virtual address ffffff? Enter the address in hex:	
(000001)	
What is the physical address for virtual address <b>ff8001</b> ? Enter the address in hex:	
(00fffe)	
What is the physical address for virtual address <b>007ffe</b> ? Enter the address in hex:	
(ffffff)	
What is the physical address for virtual address <b>003fff</b> ? Enter the address in hex:	
All addresses should be entered in hexadecimal notation. Each address has 6 cha	aracters.

# <sup>18</sup> Critical section

Max	imum marks: 2
After which line must the mutex be unlocked? Enter the line number here:	8)
After which line must the mutex be locked? Enter the line number here: (16)	

#### 19 Trace

Fill out the trace table for the segmentation fault case. Assume a single CPU.

Thread 1	Thread 2	
Select alternative (not running, c, b, a)	Select alternative (c, not running, b, a)	
Select alternative (a, not running, b, c)	Select alternative (c, a, not running, b)	
Select alternative (b, c, a, not running)	Select alternative (c, a, not running, b)	

#### <sup>20</sup> Deadlock

Which conditions need to be present to cause a deadlock?

#### Please match the values:

	required	not required
limited memory		O •
multiple CPUs		O •
circular wait	O •	0
mutual exclusion	O •	0
hold-and-wait	O 🗸	0
no preemption	O 🗸	0
I/O operations		O •

# <sup>21</sup> Lock primitive

	Maximum marks: 1
O load-or-store	
ocompare-and-swap	<b>~</b>
increment-by-one	
O fetch-and-load	
Which primitive CPU instruction can be used to build a lock?  Select one alternative:	

Maximum marks: 1

# <sup>22</sup> Thread signaling

Which construct should be used to signal between threads  Select one alternative:	
O mutex	
○ flag	
<ul> <li>condition variable</li> </ul>	•

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### 23 Round Robin

Consider these statements about round-robin scheduling. Which are true/false? **Please match the values:** 

	False	True
RR enables low turnaround time	O •	
RR is fair	0	O •
RR enables low response time	0	O •
RR takes into account I/O	O 🗸	

## <sup>24</sup> Multi-level feedback queue

Which rules apply for multi-level feedback queues to avoid causing starvation? **Please match the values:** 

	applies	does not apply
After some time period S, move all the jobs in the system to the topmost queue	<ul><li>✓</li></ul>	0
If priority(A) > priority(B): A runs	< 0	
Once a job uses up its time allotment at a given level its priority is reduced	<b>~</b>	
If priority(A) = Priority(B): A & B run in RR	<ul><li>✓</li></ul>	
If a job gives up CPU before the time slice is up, it stays at the same priority level		0 🗸
When a job enters the system it is placed at the lowest priority		0 🗸

# <sup>25</sup> Process Priority

Which shell command is used to change the priority of a process <b>Select one alternative:</b>	SS
O chgpri	
○ yield	
○ nice	<b>✓</b>
priority	

# <sup>26</sup> Response Time

What is the response time of job C the following schedule?

Enter the response time of job C in seconds: (10)

JOB	Arrival Time	First run time	Completion time
Α	0	0	100
В	10	10	20
С	10	20	30

# Page access cache

Consider a newly-created process that has been allocated a cache size of 5 pages	jes, and then
generates the following page accesses:	

ACEFDBABFFDCCGAGCEFDGEBDGG

How many cache misses are observed for this access stream when using of the FIFO page replacement algorithm?
(13)
What is its hit rate in percent? Hit rate (50) %
How many cache misses are observed for this access stream when using of the FIFO page replacement algorithm and a cache size of 6?  (14)
The cache algorithm is replaced with the least frequently used algorithm. The cache size remains at 6. How many cache misses can be observed?
(8 - 9)  Maximum marks: 12

# <sup>28</sup> Locality

Which properties of the access pattern enable a cache to perform well?

#### Please match the values:

	Does not enable	Does enable
Process locality	O •	0
Remote locality	O •	0
Temporal locality		O •
Address locality	O 🗸	0
Spatial locality		O •

## <sup>29</sup> Disk environment

Which environmental factors increase latency of a harddiselect one alternative:	sk with rotating disks?
○ vibrations	<b>✓</b>
○ Airpressure	
○ Light	
	Maximum marks: 1

## 30 Harddisk performance

Given the following specifications of 2 harddisks, what is the Random I/O Rate in MB/s of the Cheetah: (0.65 - 0.67) vs. the Barracuda (0.31)

	Cheetah 15K	Barracuda
Capacity	300GB	1TB
RPM	15000	7200
Average Seek	4 ms	9 ms
Max Transfer	125 MB/s	105 MB/s
Platters	4	4
Cache	16 MB	16/32MB
Connection	SCSI	SATA

## **Question 15**

Attached





Consider the following code.

```
1 #include <stdio.h>
2 #include <stdlib.h>
3 int *some_function() {
4   int *r = malloc(sizeof(int));
5   *r = 1;
6   return r;
7 };
8
9 int main(int argc, char *argv[])
10 {
11   printf("%d", *some_function());
12 }
```

## **Question 18**

Attached





Consider the following code. The function queue\_enqueue can be called by multiple threads. Identify the critical section in this function.

```
typedef struct __node_t {
 2
      int value;
      struct __node_t *next;
 4 } node_t;
 6
   typedef struct __queue_t {
 7
     node_t *head;
      node_t *tail;
 8
     pthread_mutex_t lock;
10
   } queue_t;
11
12
   void queue_enqueue(queue_t *q, int value) {
13
     node_t *tmp = malloc(sizeof(node_t));
      assert(tmp != NULL);
14
      tmp->value = value;
15
16
      tmp->next = NULL;
17
      q->tail->next = tmp;
18
      q->tail = tmp;
19
   }
20
21
   void queue_init(queue_t *q) {
22
      node_t *tmp = malloc(sizeof(node_t));
23
      tmp->next = NULL;
      q->head = q->tail = tmp;
24
25
      pthread_mutex_init(&q->lock0, NULL);
26 }
```

## **Question 19**

Attached





#### 1 Trace

Consider the following code. This code sometimes fails with a Segmentation fault. Fill out the trace table for this case. Assume a single CPU.

```
#include <pthread.h>
 1
 2
 3
    typedef struct __info {
      int counter = 0;
 5
    } __info_t;
7
    __info_t *info;
8
9
    void *count() {
10
      if (info) { // a
        info->counter = info->counter + 11; // b
11
      }
12
    }
13
14
15
    void *ended() {
      info = NULL; // c
16
17
    }
18
19
    int main(int argc, *char[] argv) {
20
      info = malloc(sizeof(__info_t));
21
      assert(info != NULL);
22
      pthread_t thread_1, thread_2;
23
      phtread_create(&t1, NULL, count);
24
      pthread_create(&t2, NULL, ended);
25 }
```

## **Question 6**

Attached





Which outputs of this program are possible?

```
1 #include <stdio.h>
 2 #include <stdlib.h>
 3 #include <unistd.h>
 4 int main(int argc, char *argv[]) {
     printf("hello world (pid:%d)\n", (int) getpid());
 5
     fflush(stdout);
 6
 7
     int rc = fork();
 8
      if (rc < 0) {
                            // fork failed; exit
        fprintf(stderr, "fork failed\n");
 9
10
        exit(1);
11
      } else if (rc == 0) {
12
        printf("hello, I am child (pid:%d)\n", (int) getpid());
13
      } else {
        int rc_wait = wait(NULL);
14
15
        printf("hello, I am parent of %d (rc_wait:%d) (pid:%d)\n",
16
        rc, rc_wait, (int) getpid());
17
      printf("goodbye world (pid:%d)\n", (int) getpid());
18
19
      return 0;
20 }
  • Alternative 1
    hello world (pid:83485)
    hello, I am parent of 83486 (rc_wait:83486) (pid:83485)
     goodbye world (pid:83485)
    hello, I am child (pid:83486)
     goodbye world (pid:83486)
   • Alternative 2
    hello world (pid:83485)
     hello, I am child (pid:83486)
     goodbye world (pid:83486)
     hello, I am parent of 83486 (rc_wait:83486) (pid:83485)
     goodbye world (pid:83485)
```

• Alternative 3

```
hello world (pid:83486)
hello world (pid:83485)
hello, I am parent of 83486 (rc_wait:83486) (pid:83485)
goodbye world (pid:83485)
hello, I am child (pid:83486)
goodbye world (pid:83486)
```

#### • Alternative 4

```
hello world (pid:83485)
hello, I am parent of 83486 (rc_wait:83486) (pid:83485)
goodbye world (pid:83485)
hello world (pid:83486)
hello, I am child (pid:83486)
```

## **Question 7**

Attached





Consider the program carefully. Which outputs of this program are possible?

```
1 #include <stdio.h>
 2 #include <stdlib.h>
 3 #include <unistd.h>
 4 int main(int argc, char *argv[]) {
     printf("hello world (pid:%d)\n", (int) getpid());
 5
 6
      fflush(stdout);
 7
     int rc = fork();
      if (rc < 0) {
 8
                            // fork failed; exit
 9
        fprintf(stderr, "fork failed\n");
10
       exit(1);
      } else if (rc == 0) {
11
12
        printf("hello, I am child (pid:%d)\n", (int) getpid());
13
      } else {
        printf("hello, I am parent of %d (pid:%d)\n",
14
        rc, (int) getpid());
15
      }
16
17
      return 0;
18 }
   • Alternative 1
    hello world (pid:83655)
    hello, I am child (pid:83656)
    hello, I am parent of 83656 (pid:83655)
   • Alternative 2
    hello world (pid:83655)
    hello, I am parent of 83656 (pid:83655)
    hello, I am child (pid:83656)
   • Alternative 3
    hello world (pid:83655)
    hello world (pid:83655)
    hello, I am child (pid:83656)
    hello, I am parent of 83656 (pid:83655)
```

#### • Alternative 4

hello world (pid:83655) hello, I am child (pid:83656) hello world (pid:83655) hello, I am parent of 83656 (pid:83655)