



BLG 231E - Digital Circuits Assignment 5

Due Date: 14.12.2012, **Friday**, 09:30.

- Write your answers to the **Answer Sheet** provided in “Sınıf Dosyaları” folder at the *ni-nova* system. Any other paper will not be accepted.
- Plagiarized assignments will be given a negative mark.
- **No late submissions** will be accepted.

Submissions: Please submit your solutions to the invigilator before 2nd Midterm Examination hours.

1- Analyze the synchronous sequential circuit given in the figure below. Give functions that derives inputs of the flip-flops, next state and output expressions. Provide the state/output table as well.

