BLG222E - Computer Organization
Assignment 2

Design a synchronous digital circuit such as shown in the figure below that adds the numbers that are stored in a 4×8 (4-bit address, 8-bit data) RAM. The result of the addition should be stored in an 8-bit register.

Implement your design in logisim software, save your design file in .circ format and upload it to ninova before its deadline. You can use the register, adder and RAM that exists in logisim software. However, you have to use your own 4-bit counter that you designed in previous assignment. **Individual work is expected.**