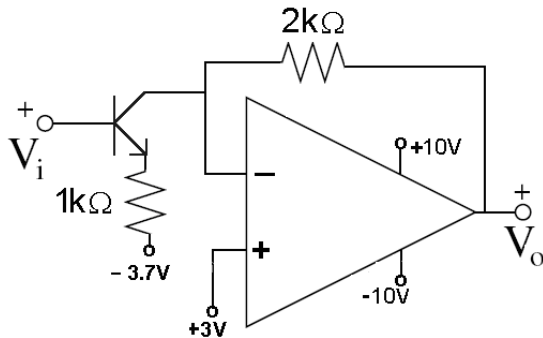


IMPORTANT: Besides your calculator and problem the sheets you are only allowed to have an A4 sized "copy sheet" during this exam. Notes, problems and alike are not permitted. Please submit your "copy sheet" along with your solutions. You may get your "copy sheet" back after your solutions have been graded. **Do not forget to write down units and convert units carefully!** Cell phones are not allowed and should be placed on the front desk before the exam.

ELE222E INTRODUCTION TO ELECTRONICS (21133-21134)

Final Exam  21 May 2012 ⌚ 12.00-14.00

STUDENT ID _____ NAME _____ SIGNATURE _____



PROBLEM-1

Calculate the output voltage V_o as a function for the input voltage V_i for the BJT to operate in the active zone. What are the ranges of V_i and V_o ? You may take $V_{BE} \approx 0,7 \text{ V}$.

For the BJT to be active, (1) $V_C = 3V \geq V_B = V_i \Rightarrow 3V \geq V_i$ and (2) the current on $2k$ resistor should flow from right to left.

Thus, $V_o \geq 3V \Rightarrow 10V \geq V_o \geq 3V$ and $I_C = \frac{V_o - 3V}{2k} = \frac{h_{FE}}{h_{FE} + 1} I_E$. On

the other hand $I_E = \frac{V_i - V_{BE} - (-3,7V)}{1k} = \frac{V_i + 3V}{1k}$. That also means for the BJT to be active, (3) $I_E \geq 0 \Rightarrow V_i \geq -3V$.

So $3V \geq V_i \geq -3V$ and $I_C = \frac{V_o - 3V}{2k} = \frac{h_{FE}}{h_{FE} + 1} \cdot \frac{V_i + 3V}{1k} \Rightarrow V_o = \frac{h_{FE}}{h_{FE} + 1} \cdot 2(V_i + 3V) + 3V \cong 2V_i + 9V$ for $h_{FE} \gg 1$

PROBLEM-2

For the BJT above $\beta_F = h_{fe} = 100$ and $V_{BE} \approx 0,7 \text{ V}$.

What is the gain v_o/v_i at the point of operation for $V_i = 0 \text{ V}$?

$$V_i = 0V \Rightarrow V_E = -0,7V \Rightarrow I_E = \frac{-0,7 - (-3,7)}{1k} = 3mA$$

$$\Rightarrow I_C = \frac{h_{FE}}{h_{FE} + 1} I_E = \frac{100}{101} 3mA = \underline{\underline{2,97mA}}$$

$$\Rightarrow r_e = \frac{V_T}{I_C} = \frac{25mV}{2,97mA} = \underline{\underline{8,42\Omega}}$$

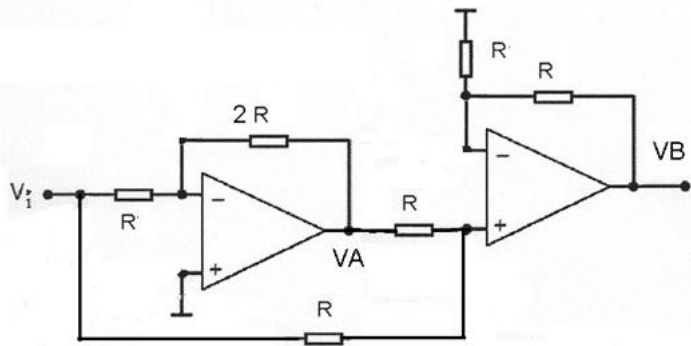
Realize that at AC $v_c = 0V$, that means the collector is grounded for AC signals. The BJT circuit acts like a common-E circuit, Yet because the collector is at ground level, the phase of the output voltage does not change!

$$K_v = \frac{v_o}{v_i} = \frac{2k \parallel R_y}{r_e + R_e} = \frac{2k}{r_e + R_e} = \frac{2k}{8,42\Omega + 1k} = \underline{\underline{1,98}}$$

NOTE 1: You need to solve the problems in the space provided. Each problem is worth 20 points.

NOTE 2: You should solve any 6 of the 8 problems. If you solve more than 6, ONLY the first 6 will be graded!

GOOD LUCK

**PROBLEM-3**

The OPAMPs on the left are ideal with $V_{CC} = 10\text{ V}$ and $V_{EE} = 0\text{ V}$. ($V_{o+} = 10\text{ V}$ and $V_{o-} = 0\text{ V}$; that is, OPAMPs are fed by a single power supply) V_i varies between -5 V and $+5\text{ V}$.

- Find the range of V_A .
- Find the range of V_B and correlate it with V_i (that is, for a sinusoidal input signal show the variation of V_i , and V_B comparatively using the same coordinate system).

$$V_i < 0 \rightarrow V_A = -2V_i$$

$$\text{OPAMP1: } V_i > 0 \rightarrow V_A = V_o^- = 0\text{ V}$$

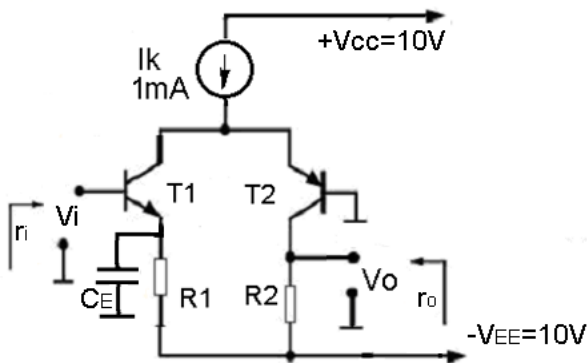
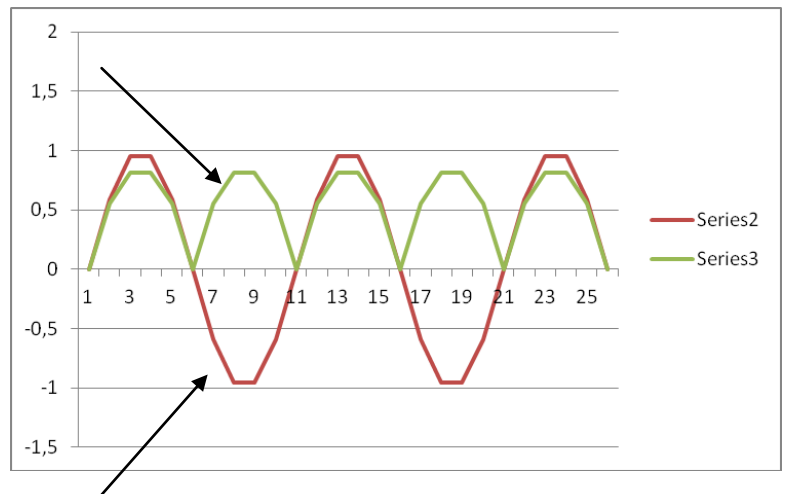
$$\Rightarrow 0 \leq V_A \leq 10\text{ V}$$

$$V_B = V_i + V_A$$

$$\text{OPAMP2: } V_i > 0 \rightarrow V_B = V_i + V_A = V_i + V_o^- = V_i$$

$$V_i < 0 \rightarrow V_B = V_i - 2V_i = -V_i$$

In the chart on the right the red curve gives the input voltage V_i whereas the green one shows the output voltage V_B .

**PROBLEM-4 This is not a differential amplifier!**

For the circuit shown on the left, $\beta_F = h_{fe} = 200$, $|V_{BE}| = 0,6\text{ V}$, $V_T = 25\text{ mV}$ and $V_A = \infty$. Recalling $V_i = 0\text{ V}$ for biasing, find R_1 and R_2 for $r_i = 50\text{ k}$ and the gain $|v_o/v_i| = 10$. Also calculate the output resistance r_o

DC analysis: 2 stages- 1st common-E, 2nd common-B.

$$V_i = 0\text{ V} \Rightarrow V_{E1} = -0,6\text{ V} \Rightarrow I_{E1} = \frac{-0,6\text{ V} - (-10\text{ V})}{R_1} = \frac{9,4\text{ V}}{R_1}$$

$$r_i = h_{fe}(r_{e1} + R_e) = 50\text{ k} = 200(r_{e1} + 0) \Rightarrow r_{e1} = \frac{50\text{ k}}{200} = 250\Omega \Rightarrow I_{C1} = \frac{25\text{ mV}}{r_{e1}} = \frac{25\text{ mV}}{250\Omega} = \underline{\underline{0,1\text{ mA}}}$$

$$\Rightarrow I_{E1} = \frac{h_{FE} + 1}{h_{FE}} I_{C1} \cong \underline{\underline{0,1\text{ mA}}} \Rightarrow R_1 = \frac{9,4\text{ V}}{I_{E1}} = \underline{\underline{93\text{ k}5}}$$

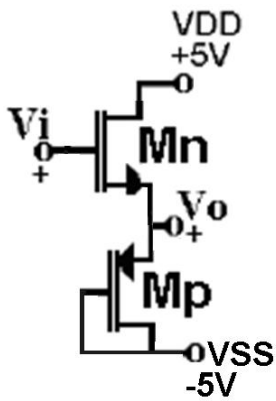
$$\text{With } r_{i2} = R_{e2} \parallel r_{e2} = \underline{\underline{r_{e2}}} \Rightarrow r_{e1} + r_{e2} = \frac{V_T}{I_{C1}} + \frac{V_T}{I_{C2}} = \frac{50\text{ k}}{200} = 250\Omega$$

$$\frac{v_o}{v_i} = K_v = K_{v1} K_{v2} = -\frac{R_{c1} \parallel R_{y1}}{r_{e1} + R_{e1}} \cdot \frac{R_2}{r_{e2}} = -\frac{\infty \parallel r_{i2}}{250\Omega + 0} \cdot \frac{R_2}{r_{e2}} = -\frac{r_{i2}}{250\Omega} \cdot \frac{R_2}{r_{e2}} = -\frac{r_{e2}}{250\Omega} \cdot \frac{R_2}{r_{e2}} = -\frac{R_2}{250\Omega} = -10$$

$$R_2 = 10 \cdot 250\Omega = \underline{\underline{2\text{ k}5}}$$

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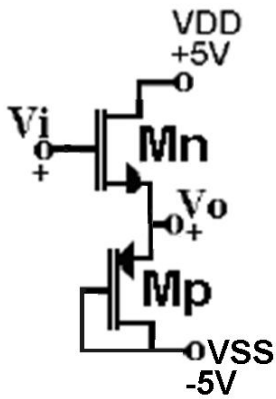
**PROBLEM-5**

The MOS transistors shown are conjugate with $|V_{TH}| = 1 \text{ V}$. At the operation point we require that $V_i = 0 \text{ V}$ and $I_{DN} = I_{DP} = 1 \text{ mA}$. Find β that satisfies these conditions.

$I_{Dn} = I_{Dp} = \beta(V_{GSn} - V_{Tn})^2 = \beta(V_{GSp} - V_{Tp})^2 = 1 \text{ mA}$. Since the two MOS are conjugate, their $|V_{TH}|$ and their β are identical. Thus, their $|V_{GS}|$ should also be the same. Looking at the circuit we realize $V_i - V_o = V_o - (-V_{SS}) \rightarrow 0 - V_o = V_o + 5 \Rightarrow V_o = -2,5 \text{ V}$.

That also means $V_{GSn} = -V_{GSp} = 2,5 \text{ V}$. Inserting this into

$$I_{Dn} = I_{Dp} = \beta(2,5 - 1\text{V})^2 = \beta(-2,5 + 1\text{V})^2 = 1 \text{ mA} \text{ yields } \beta = \frac{1 \text{ mA}}{(2,5 - 1\text{V})^2} = \underline{\underline{444 \mu\text{A}/\text{V}^2}}$$

**PROBLEM-6**

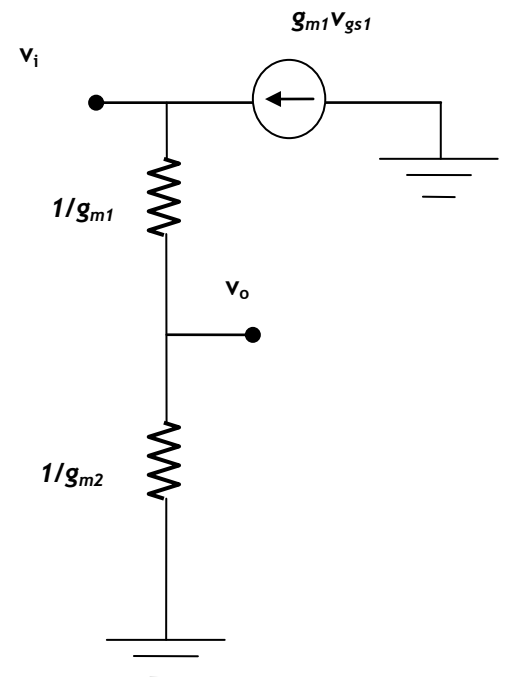
The MOS transistors shown are conjugate with $|V_{TH}| = 1,5 \text{ V}$ and $\beta = 2 \text{ mA/V}^2$. At the operation point $I_{DN} = I_{DP} = 1 \text{ mA}$. Calculate the AC gain v_o/v_i with no load connected to the output.

This might be a little confusing but not as difficult as it seems. I chose to use the equivalent circuit approach. Hereby realize that both resistors should have the same value because the MOS are conjugate and the same current passes through them ($I_{DN} = I_{DP} = 1 \text{ mA}$). Also note that the gate and drain of M_p are grounded at AC.

Is it NOT obvious that $\frac{v_o}{v_i} = \frac{1/g_{m2}}{1/g_{m1} + 1/g_{m2}} = \frac{1/g_m}{1/g_m + 1/g_m} = 1/2$????

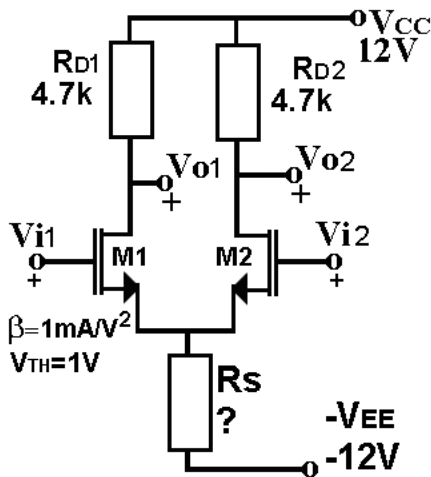
We could have analyzed this circuit otherwise by using gain equations. M_n is in common-D configuration. M_p acts like a resistor because the gate and drain are grounded at AC. Thus:

$$\frac{v_o}{v_i} = \frac{R_s}{1/g_{m1} + R_s} = \frac{1/g_{m2}}{1/g_{m1} + 1/g_{m2}} = \frac{1/g_m}{1/g_m + 1/g_m} = 1/2$$



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**PROBLEM-7**

The MOS transistors shown on the left are identical with $|V_{TH}| = 1\text{ V}$ and $\beta = 1\text{ mA/V}^2$. $I_{D1} = I_{D2} = 1\text{ mA}$ when $V_{i1} = V_{i2} = 0\text{ V}$. Calculate CMRR for single (v_{o2}) and differential output ($v_{o2} - v_{o1}$).

Again using an equation we have seen before and adapting it to MOS we can see:

$$CMRR_{BJT} = 20 \log \left| \frac{2R_E + r_e}{r_e} \right| \Rightarrow CMRR_{MOS} = 20 \log \left| \frac{2R_S + 1/g_m}{1/g_m} \right|$$

$$I_{Dn} = \beta (V_{GSn} - V_{Tn})^2 = 1\text{ mA/V}^2 (V_{GSn} - 1\text{ V})^2 = 1\text{ mA}$$

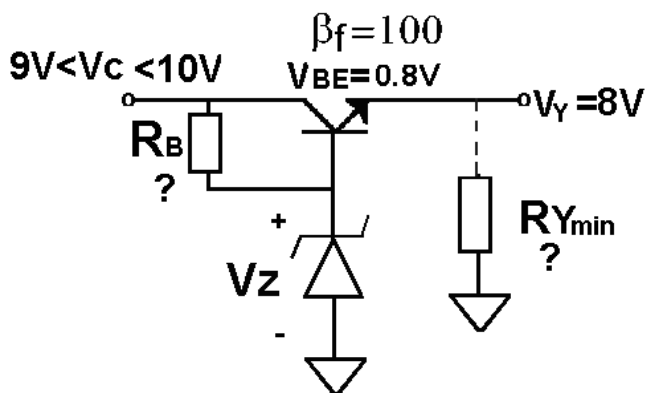
$$\Rightarrow V_{GS} = 1\text{ V} \pm \sqrt{\frac{1\text{ mA}}{1\text{ mA/V}^2}} = \begin{cases} 2\text{ V} > V_{Tn} = 1\text{ V} \\ 0\text{ V} \end{cases}$$

Thus the meaningful solution would be $V_{GS} = 2\text{ V}$. On the other side

$$V_i - (-V_{EE}) = V_{GS} + 2I_D R_S \Leftrightarrow 0 + 12\text{ V} = 2\text{ V} + 2 \cdot 1\text{ mA} \cdot R_S \Rightarrow R_S = \frac{12\text{ V} - 2\text{ V}}{2 \cdot 1\text{ mA}} = 5\text{ k}\Omega$$

$$g_m = 2\sqrt{\beta I_{D1/2}} = 2\text{ mA/V} = (500\Omega)^{-1}$$

$$\Rightarrow CMRR_{MOS} = 20 \cdot \log \left| \frac{2R_S + 1/g_m}{1/g_m} \right| = 20 \cdot \log \left| \frac{2 \cdot 5\text{ k}\Omega + 500\Omega}{500\Omega} \right| = 26,44\text{ dB}$$

**PROBLEM-8**

The circuit will provide a regulated voltage at 8 V from a voltage varying between 9 to 10 V.

For the BJT used $\beta_F = 100$ and $V_{BE} \approx 0,8\text{ V}$, and Zener voltage is $8,8\text{ V}$. The maximum power dissipated by the Zener diode is $P_{Zmax} = 0,1\text{ W}$. Find R_B and the minimum value of load resistor (R_{Ymin}) that can be connected to the output.

Check Problem 1 in Midterm Exam #2 solutions at:

<http://web.itu.edu.tr/cilesiz/courses/ELE222E/ELE222E-Spring-2012-MT2.doc>