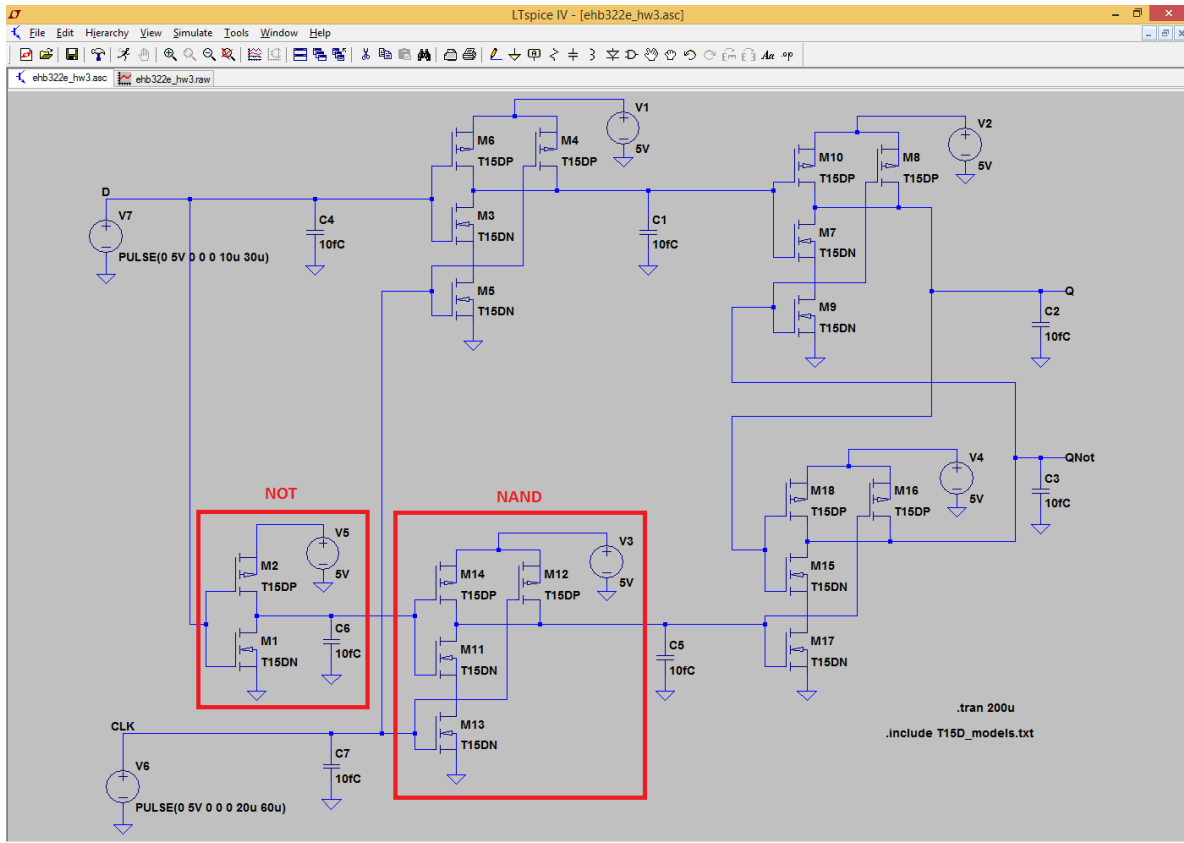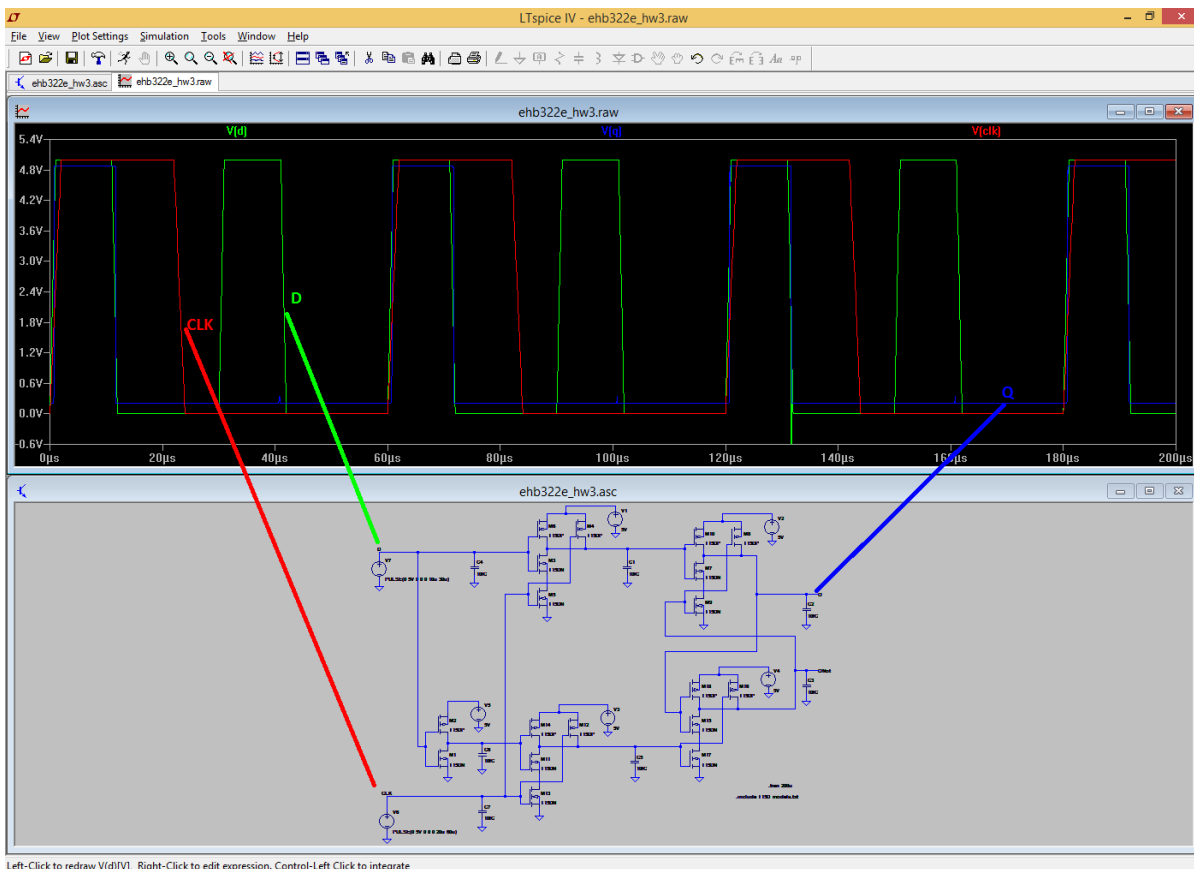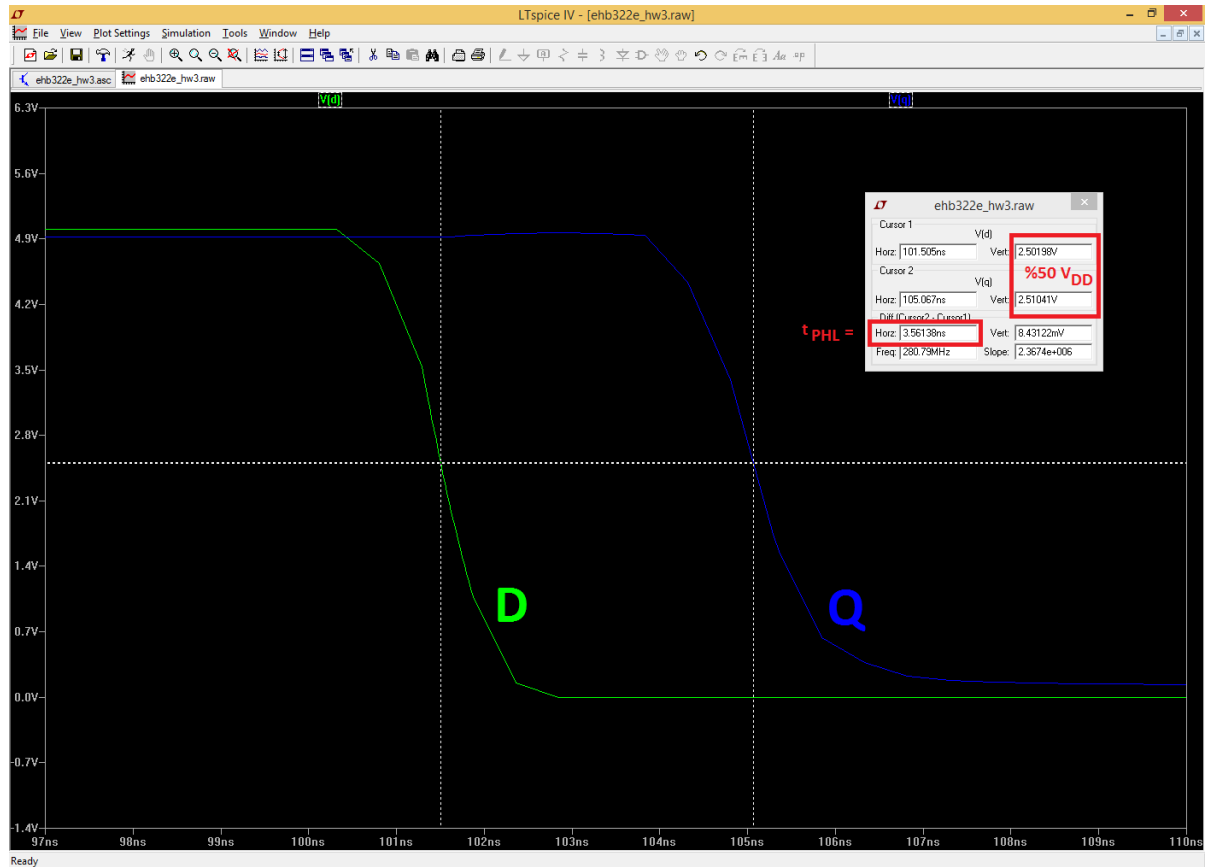# CMOS D-flip-flop

Q output with D and CLK

W.C. PD at Q with D input

W.C. Total t_{PHL} at Q with D input

W.C. Toltal t_{PLH} at Q with D input