ARM The Architecture for the Digital World®

Job Details

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CPU Microarchitect & Design Engineer

Job ID #: 5579

Country: France   **Location:** Sophia Antipolis

Job Category: Hardware Engineering **Employment Type:** Experienced Professionals

Division: CPU   **Department:** CPU - Engineering

Hours of work (per week): 35

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Job Description

Introduction

ARM, multinational company that kept its start-up's soul, leading provider of low-power, high performance processor IP, heavily invests in its French sites. ARM's Sophia Antipolis design center is located on the French Riviera, at the heart of Europe's largest and most dynamic technology park. This exceptional design centre has delivered leading products from ARM's CortexTM processor family, such as Cortex-A9TM and Cortex-A17TM. In this context, we are looking for new talents to work on the next generation processors that will appear in the most desirable products over the next 3 years.

Being part of the ARM community means:
• Creating something that the world is waiting to see!
• Being passionate about digital technology
• Designing components at the heart of most smartphones, tablets and games consoles
• Joining an innovative and collaborative team of experienced engineers and talented graduates, coming from the very best engineering schools

Join our success! We look forward to receiving your application!

Purpose

As CPU microarchitect & RTL Design Engineer, you are part of a small and talented processor RTL design team where you can develop and innovate in various areas. You will contribute to the specification, microarchitecture and RTL design of high performance, energy efficient microprocessors.
The team employs leading-edge modeling, design and verification technologies to design low-power high-performance products. Your technical skills and close collaboration with other Sophia-based engineering teams will create complete IP solutions to address the performance, power and cost requirements for almost all application markets.

Job Requirements

Key Accountabilities/Responsibilities
• CPU microarchitecture specification and design
• Verilog RTL logic design and debug
• Working closely with performance modeling, validation, and implementation teams to meet all functional requirements, performance, power and area goals

Job Requirements
Education
Preferably graduate from a University or Engineering School, in electronic engineering or computer science, although other science graduates would be considered if they have relevant experience

Experience

The candidate should have between 0 and 5 years of experience in the semiconductor industry. A deep knowledge and experience in one or more areas of modern CPU microarchitecture is a plus.

Technical Attributes

Applicants should have:

• An excellent grasp of RTL design (Verilog or VHDL HDL)

• A good understanding of the fundamentals of CPU architecture

• Strong awareness of functionality, performance & physical implementation tradeoffs

Ideal applicants might also have knowledge or experience in the following areas :

• Design for synthesis to achieve specified power, frequency, and area targets

• Programming language (C/C++), and assembly language (preferably ARM)

• Interaction with verification teams, contribution to verification strategy and help in debug fails

General Attributes

It is essential for the successful applicant to:

• Demonstrate enthusiasm, drive and diligence

• Be able to work efficiently alone as well as in a team environment

• Have excellent written and verbal communication skills in French and English

Package

ARM will offer the following benefits for this position:

• Salary - Commensurate with experience

• Shares (Restricted Share Units) at the discretion of ARM Holdings plc

• Bonus Plan

• Private Medical Insurance

• Permanent Health Insurance

• Life Assurance

• Contributory Pension

• 4 weeks paid sabbatical every 4 years

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