BLG242E – Logic Circuits Laboratory

2011-2012 SPRING

	CRN: 20914	CRN: 20915	CRN: 20916
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Week	Topic
1	Lab Registration (No lab)
2	Introduction to Lab
3	Boolean Algebra
4	Combinational Circuits
5	Binary Arithmetic
6	TTL and CMOS circuit characteristics
7	Make-up experiments
8	Buses in Logic Circuit Systems
9	Latches and Flip-flops
10	Sequential Circuit Design
11	Arithmetic Logic Unit
12	Make-up experiments
13	MIDTERM
14	MIDTERM

Grading

- In each lab session(except introduction) you are responsible of the following:
    - Experiment preparation (HANDWRITTEN on the face of an A4 paper) : %10
    - Experimentation : %80
    - Group Report: %10

At the end of term scoring will be:
- Lab session performances %30
- Pop quizzes (can be about ANY item of the lab session grading provided above) %5
- Midterm: Cadet-based: %25
- Final exam: Paper-based: %40
- To enter the final exam you must attend at least 7 experiments.
- To attend the final exam your experiment avarage AND your midterm+quizzes score should be minimum 30.
- At the end of the term, a total average below 40 will fail.

You can follow the course announcements, exam results and your attendance status on the Ninova system (http://ninova.itu.edu.tr/).