Kuanghua Qiao

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The recruitment manager Kepler communications Toronto, ON

Re: Hardware Engineer Intern

The recruitment manager:

I am writing to reply to this Hardware Engineer Intern that I discovered on Indeed.ca. I believe I am well suited for this job for I have obtained most of the required skills listed in the job requirement. In addition, I am will be happy to learn about building CubeSats.

During my year at York University, I have participated in multiple satellite and circuit design project. The first time I was involved in satellite design was in QB50 where I was mostly involved in software development. After that part of the design of the QB50 satellite was inherited by CSDC YorkU branch. There I was responsible for solar panel PCB routing and communicating with the manufacturer. My most recent experience as a research assistant also includes designing a 4-layer PCB as a test platform and programming various boards from Adafruit and Arduino. Up till now, I have brief experience with all stages of PCB prototyping process: designing, manufacturing, and testing. Although, the smallest passive I have soldered was only 0603, but I know how to work on 0402 passives as well. In fact, the soldering work I am most proud of was soldering a DFN6 package, which I believe is the second hardest package to solder other than BGA devices.

Above all, I am a person who enjoys fast-paced building and testing iteration of electronic devices. My passion for electronics and the goals of personal growth would make me a perfect candidate to work at Kepler communications.

Yours sincerely,

Kuanghua Qiao