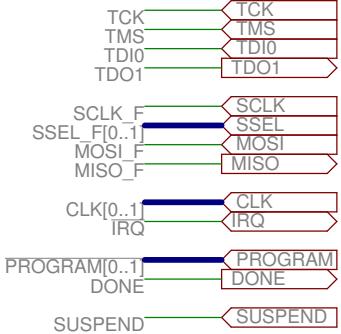
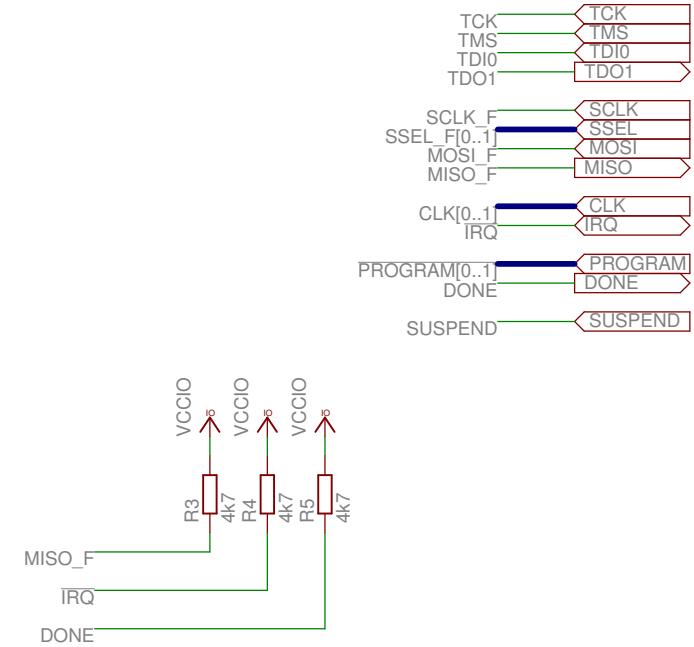
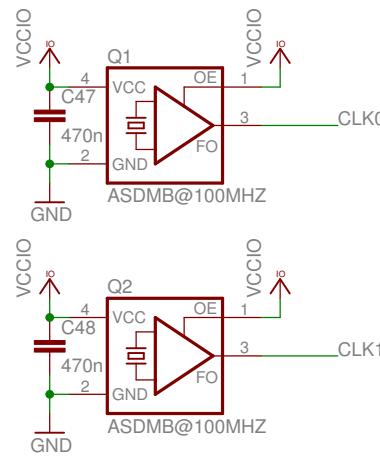


TP1	VCCINT0
TP2	VCCINT1
TP3	VCCIO
TP4	GND
TP5	NC
TP6	TCK
TP7	TMS
TP8	TDI0
TP9	TDO1
TP10	SCLK_F
TP11	SSEL_F0
TP12	SSEL_F1
TP13	MOSI_F
TP14	MISO_F
TP15	IRQ
TP16	PROGRAM0
TP17	PROGRAM1
TP18	DONE
TP19	SUSPEND



## Pullup resistors

TITLE: FPGA

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