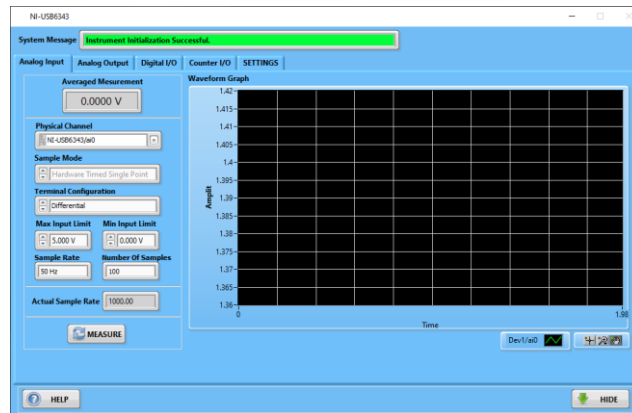


# NI-USB6343 SOFT PANEL

## USER MANUAL

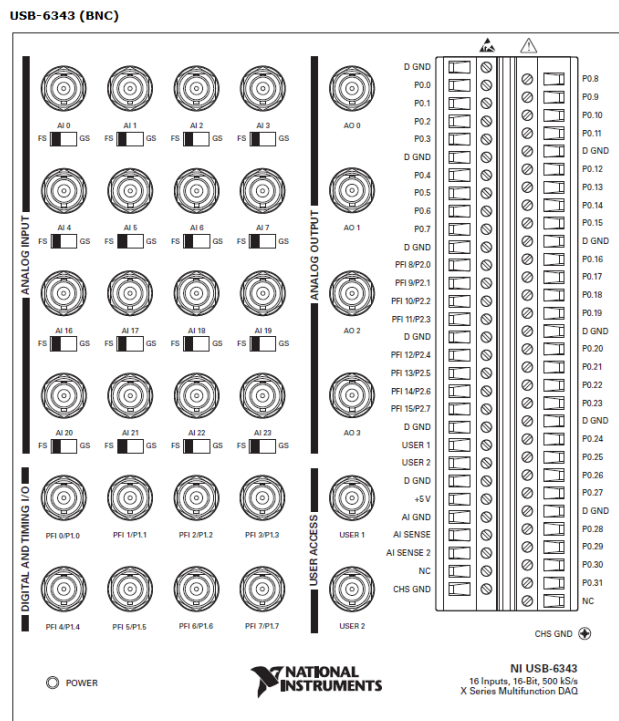
### 1. INTRODUCTION

This document intends to provide the end-user with guidance on the NI-USB6343 soft panel application:



This device does not have user interface. Because of this, the soft panel is the only way to operate this device manually. The NI-USB6343 is the medium class instrument has the 24 16-Bit analog inputs (AI), 4 16-Bit analog outputs (AO), and 48 digital bi-directional lines. The digital lines can be configured to create up to 4 bi-directional counters.

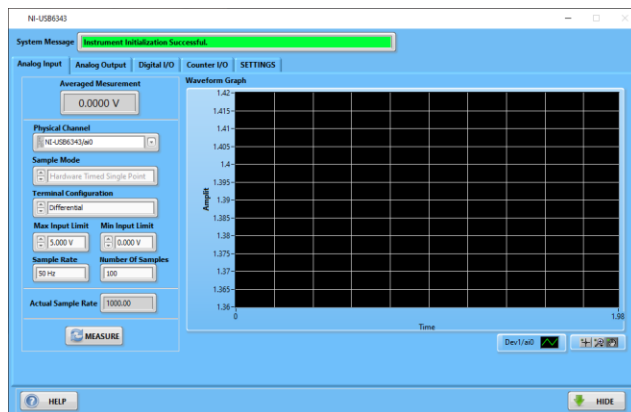
The device pinout presented on the picture below:



The following sections describe the operation of the NI-USB6343 soft panel where each set of functions represented on the dedicated page.

## 2 Analog inputs

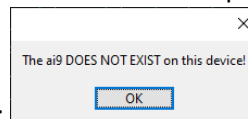
The access to the analog inputs provided on the “Analog Inputs” page:



The following steps describe steps to perform the analog measurements.

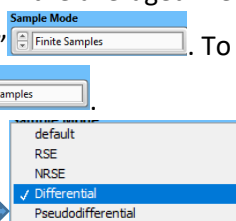
- 2.1 Select the “Physical Channel” . Please note that the device terminals expose not all listed AI channels. Available channels are 0 to 7 and 16 to 23. If incorrect AI physical

channel will be selected, the application will generate the error:



- 2.2 Select the “Sample Mode” . To perform the averaged measurement of the specified number of samples, select “Finite Samples” . To perform continuous measurements, select “Continuous Samples” .

- 2.3 Select “Terminal Configuration” →



Where: default = Differential;

RSE = referenced single ended;

NRSE = non-referenced single ended;



Pseudodifferential = refer to

<https://knowledge.ni.com/KnowledgeArticleDetails?id=kA00Z0000019YuUSAU&l=en-US>

The most commonly used is the “Differential” terminal configuration. The input signals should be connected as in the table below:


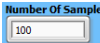
ai0 (AI0+)	ai16 (AI0-)	ai7 (AI7+)	ai23 (AI7-)
ai1 (AI1+)	ai17 (AI1-)		


ai2 (AI2+)	ai18 (AI2-)		
ai3 (AI3+)	ai19 (AI3-)		
ai4 (AI4+)	ai20 (AI4-)		
ai5 (AI5+)	ai21 (AI5-)		
ai6 (AI6+)	ai22 (AI6-)		

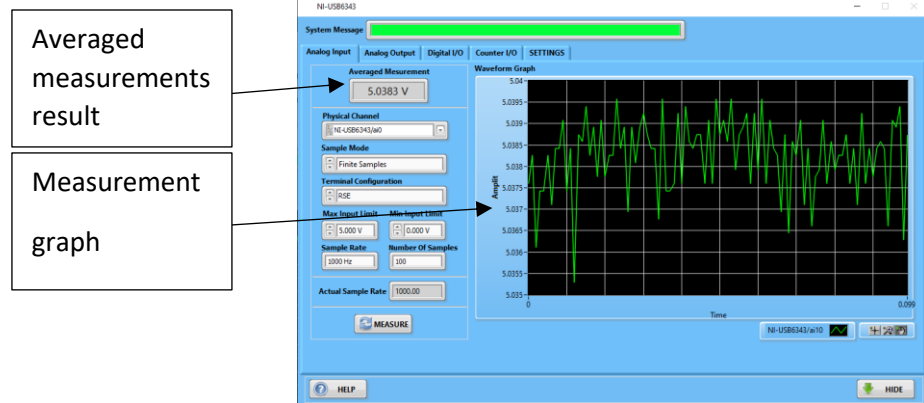
2.4 Set the input signal maximum and minimum values  .

This device has auto scaling features and will automatically amplify the input signal. The input range affects the resolution of the MIO X Series device for an AI channel. Resolution refers to the voltage of one ADC code. For example, a 16-bit ADC converts analog inputs into one of 65,536 (= 2<sup>16</sup>) codes—that is, one of 65,536 possible digital values. These values are spread fairly evenly across the input range. So, for an input range of -10 V to 10 V, the voltage of each code of a 16-bit ADC is:

$$\frac{10\text{V} - (-10\text{V})}{2^{16}} = 305\text{ }\mu\text{V}$$

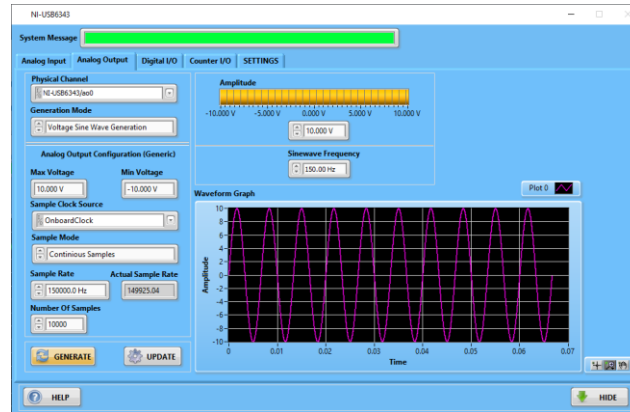
2.5 Set the “Sampling Rate”  and “Number Of Samples”  parameters.

2.6 Press “MEASURE” button . The picture below illustrates the measurements:



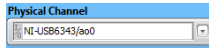
### 3 Analog Outputs

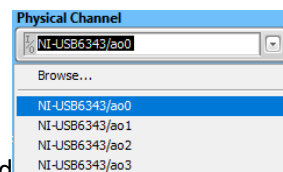
The USB 6343 has four 16-Bits analog output channels. These output channels can operate independently from each other and produce DC voltage, sign waveform, triangle waveform, square waveform, and saw-tooth waveform. The control of the analog output channels located on the “Analog Output” page:



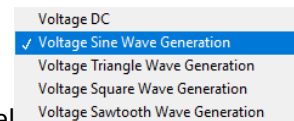
The analog outputs works simultaneously. The application generates the four waveforms with common attributes like the sample clock source, sample mode, sample rate, and number of samples. If there will be need to change one or more of these parameters, while it is running, the user shall select “UPDATE” option.



The following are steps to set up analog outputs.

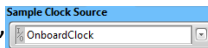
3.1 Select the “Physical Channel”  to be configured

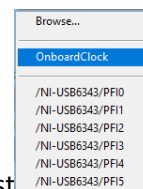


3.2 Select the “Generation Mode”  of the selected channel

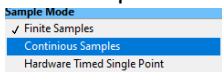


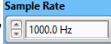
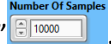
3.3 Select “Max Voltage”  and “Min Voltage”  of the output signals. Please, keep in mind, these parameters will apply to all four analog output channels.

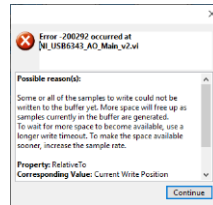
3.4 Select the “Sample Clock Source”  from the list. The simplest selection is the “Onboard Clock”.

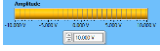



3.5 Select the “Sample Mode”  to generate specified number of samples .

Or, select  for continuous signal generation.

3.6 Select the “Sample Rate”  and the “Number Of Samples” . The correct combination of the sample rate and the number of samples is extremely important for and wrong combination will generate error:



3.7 Select the “Amplitude” of the signal: .

3.8 Select the “Sinewave Frequency” . This parameter applicable to the waveforms, not to DC voltage generation.

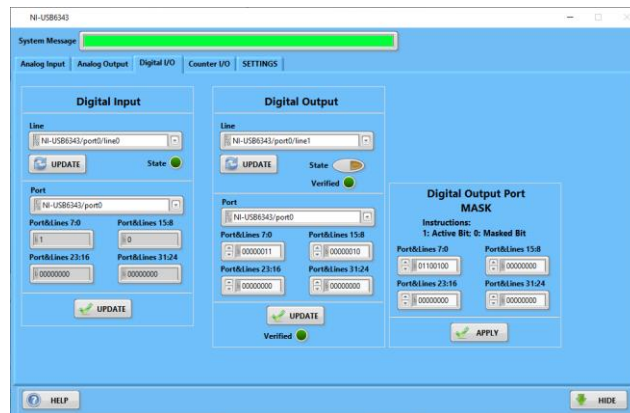
3.9 Press “GENERATE” .

## 4 Digital Input/Outputs

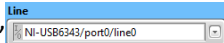


The USB 6343 device has 48 bi-directional digital lines. These lines can be used individually or compounded in ports:

- Port 0 – 32 Bits
- Ports 1 and 2 – 8-Bits.

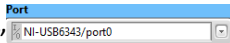
Access to the DIO controls provided on the page “Digital I/O” page:



### 4.1 Digital Input line control

4.1.1 Select digital “Line”  and press “UPDATE”  to read the selected line state: .

### 4.2 Digital Input Port control

4.2.1 Select the “Port” . If the port 0 selected, the four lines will be activated:

Port&Lines 7:0 1	Port&Lines 15:8 0
Port&Lines 23:16 00000000	Port&Lines 31:24 00000000

If the port 1 or port 2 selected, the only one port indicator will be

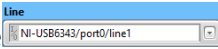
activated:


Port&Lines 7:0 1	Port&Lines 15:8 0
Port&Lines 23:16 00000000	Port&Lines 31:24 00000000

4.2.2 Press “UPDATE”  button. The port indicators will be updated:

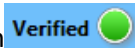
Port&Lines 7:0 00000001	Port&Lines 15:8 00000000
Port&Lines 23:16 00000000	Port&Lines 31:24 00000000

### 4.3 Digital Output Line control

4.3.1 Select the digital output line .

4.3.2 Select the line “State” .

4.3.3 Press the “UPDATE”  button.

4.3.4 The application performs the selected line verification .

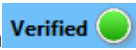
### 4.4 Digital Output Port control

4.4.1 Select digital output “Port” .

Port&Lines 7:0 00000011	Port&Lines 15:8 00000010
Port&Lines 23:16 00000000	Port&Lines 31:24 00000000

4.4.2 Set the port value:

4.4.3 Press the “UPDATE”  button.


4.4.4 The application performs the selected port verification .

4.4.5 There is the digital port masking option:

**Digital Output Port MASK**

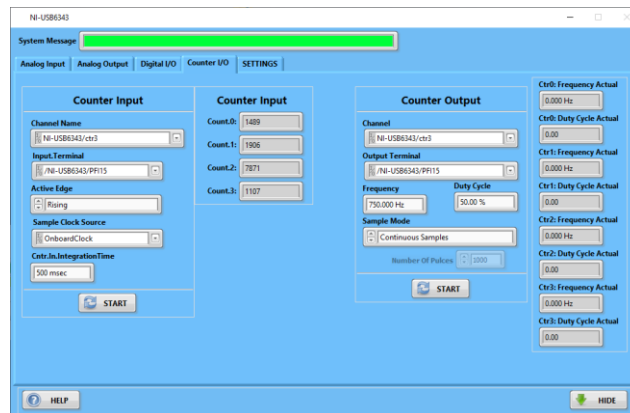
Instructions:  
1: Active Bit; 0: Masked Bit

Port&Lines 7:0 01100100	Port&Lines 15:8 00000000
Port&Lines 23:16 00000000	Port&Lines 31:24 00000000

 APPLY

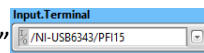
## 5 Counters Control

The device has four by-directional counters. Their control implemented on the “Counter I/O” page:



### 5.1 Counter Input Control

5.1.1 Select the “Channel Name”

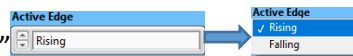


5.1.2 Select the “Input.Terminal” . For correct selection of the input terminals for specific counters, please refer to the picture below:

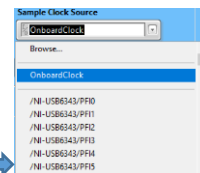
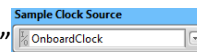
Default NI-DAQmx Counter Terminals

Counter/Timer Signal	Terminal Name
CTR 0 SRC	PFI 8
CTR 0 GATE	PFI 9
CTR 0 AUX	PFI 10
CTR 0 OUT	PFI 12
CTR 0 A	PFI 8
CTR 0 Z	PFI 9
CTR 0 B	PFI 10
CTR 1 SRC	PFI 3
CTR 1 GATE	PFI 4
CTR 1 AUX	PFI 11
CTR 1 OUT	PFI 13
CTR 1 A	PFI 3
CTR 1 Z	PFI 4
CTR 1 B	PFI 11
CTR 2 SRC	PFI 0
CTR 2 GATE	PFI 1
CTR 2 AUX	PFI 2
CTR 2 OUT	PFI 14
CTR 2 A	PFI 0
CTR 2 Z	PFI 1
CTR 2 B	PFI 2
CTR 3 SRC	PFI 5
CTR 3 GATE	PFI 6
CTR 3 AUX	PFI 7
CTR 3 OUT	PFI 15
CTR 3 A	PFI 5
CTR 3 Z	PFI 6
CTR 3 B	PFI 7
FREQ OUT	PFI 14

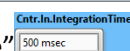
5.1.3 Select the “Active Edge”



5.1.4 Select the “Sample Clock Source”



5.1.5 Select the “Cntr.In.IntegrationTime”



5.1.6 Press the “START” button.



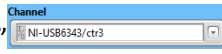
5.1.7 The results of each counter will be displayed on the “Counter Input” displays:

**Counter Input**


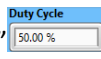
Count.0:	1489
Count.1:	1906
Count.2:	7871
Count.3:	1107

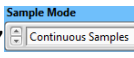
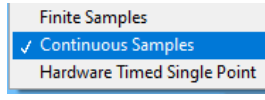
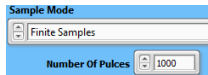
## 5.2 Counter Output Control

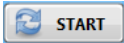
The USB 6343 allows using each of four counters to function independently.

5.2.1 Select the output counter “Channel” .

5.2.2 Select the “Output Terminal” .

5.2.3 Select the “Frequency”  and the “Duty Cycle”  of the output counter.

5.2.4 Select the “Sample Mode” . There are selections available . If the “Finite Samples” selected, the counter will generate the specified “Number Of Pulses” .

5.2.5 Press the “START”  button to generate the pulse train.

5.2.6 The state of the counters is displayed:

**Counter Output State**

Ctrl0: Frequency Actual	0.000 Hz
Ctrl0: Duty Cycle Actual	0.00
Ctrl1: Frequency Actual	0.000 Hz
Ctrl1: Duty Cycle Actual	0.00
Ctrl2: Frequency Actual	0.000 Hz
Ctrl2: Duty Cycle Actual	0.00
Ctrl3: Frequency Actual	0.000 Hz
Ctrl3: Duty Cycle Actual	0.00