

	Result	Time	Cycles	Regs	GPU	SM Frequency	CC	Process
Current	23 - nqfaf (9248, 1, 1)x(64, 1, 1)	655,52 msecond	n/a	38	0 - NVIDIA GeForce GTX 1650 Ti	n/a	7.5	[12588] NQueensFAF.exe

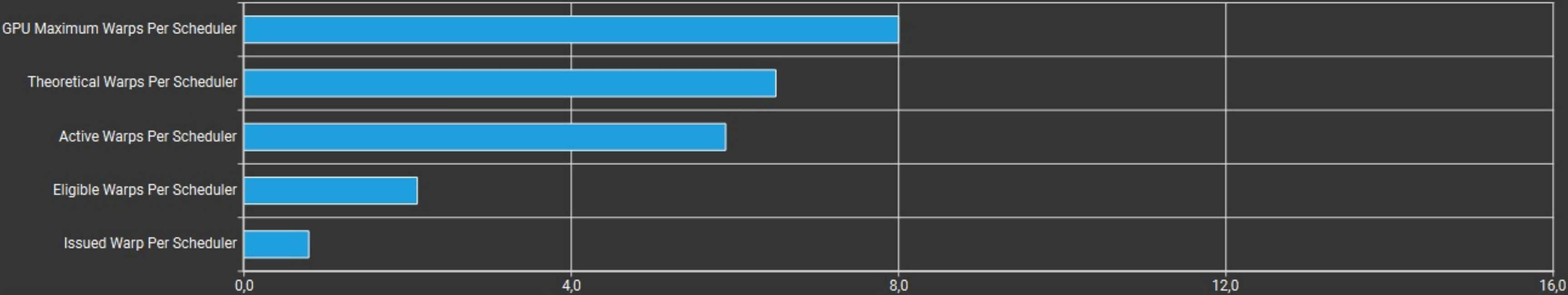


▼ Scheduler Statistics

Summary of the activity of the schedulers issuing instructions. Each scheduler maintains a pool of warps that it can issue instructions for. The upper bound of warps in the pool (Theoretical Warps) is limited by the launch configuration. On every cycle each scheduler checks the state of the allocated warps in the pool (Active Warps). Active warps that are not stalled (Eligible Warps) are ready to issue their next instruction. From the set of eligible warps the scheduler selects a single warp from which to issue one or more instructions (Issued Warp). On cycles with no eligible warps, the issue slot is skipped and no instruction is issued. Having many skipped issue slots indicates poor latency hiding.

Active Warps Per Scheduler [warp]	5,89	No Eligible [%]	20,79
Eligible Warps Per Scheduler [warp]	2,12	One or More Eligible [%]	79,21
Issued Warp Per Scheduler	0,79		

Warps Per Scheduler



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You might also want to consider [adding individual metrics](#).