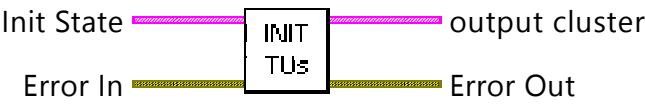


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**InitTiltUnits.vi**



InitTiltUnits.vi

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Error In

statuscode

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source

Init State

FPGA Ref

Hindbrain Settings

Hindbrain Run

FPGA Comms

FPGA Comms Init

MissionClock

Ros Comms

Camera Processes

Camera

Microstrain

NavComm

XBox Controller

Left TU

Right TU

Error Out

statuscode

d0

source

output cluster

FPGA Ref

Hindbrain Settings

Hindbrain Run

FPGA Comms

FPGA Comms Init

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INSTRUMENTS

LabVIEW Home and Student Edition

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