Andrew James Bond

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OBJECTIVE

I'm an electrical engineering student seeking to leverage my skills in digital circuits, Verilog, and C programming to gain experience in hardware and embedded software engineering.

EDUCATION

Texas A&M University; College Station, TX (9/18 – 12/21)

- B.S. in Electrical Engineering – 4.0 GPA
- Minor in Computer Science

Austin Community College; Austin, TX (9/17 – 5/19)

- General Engineering – 4.0 GPA

SKILLS

Theory:	Analog/Digital Circuits, Electromagnetics, Probability Theory, Technical Writing, Computer Architecture, Logic Design, Linux Programming, Multi-Threading, TCP/IP, HTTP, MVC Architecture, ORMs, Systems Programming, Data Structures & Algorithms, Lexers, Parsers, Test-Driven Development
Languages:	Verilog, C, C++, Shell Scripting, SQL, Go, Python, Ruby, JavaScript, Elisp, HTML, CSS
Software:	Multisim, EasyEDA, Eagle, Make, Clang, GCC, LLDB, GDB, Git, Github, Emacs, Vim, Latex, Testing frameworks (Mocha/Chai, Rspec)
Hardware:	Oscilliscopes, Power Supplies, Multimeters, Signal Generators, FPGAs, Soldering

PORTFOLIO

- AggieSat Labs (PCB) – member of the electrical power system team
- Passman (Go) – a faster Unix terminal password manager
- WaveCastr (Ruby) – record podcasts remotely from your browser
- Ling (C++) – a boolean expression parser written to impress a math professor
- Personal Webpage (JavaScript) – resume, portfolio, writing samples

RELEVANT WORK EXPERIENCE

TA/Tutor, Austin Community College (9/17 – 5/18)

Graded C++ assignments and tutored students in C, C++, Python, Java, JavaScript, and PHP.

HONORS & AWARDS

- Texas A&M Dean's List
- Austin Community College President's Honor Roll

OTHER ACTIVITIES

I've won statewide gymnastics competitions, attended a classical music conservatory, toured Europe in a trendy indie band, busked on the streets of New York, and matched wits with KUT's John Aeilli. I can't wait to apply my intellect and unique perspective, energy and intellect to challenging problems, learn as much as possible, and love every bit.