

## **APPENDIX A**

### **Chapter 1 Introduction**

- 1.1 Project Goals and Objectives (Sang)
- 1.2 Problem and Motivation (Samira)
- 1.3 Project Application and Impact (John)
- 1.4 Project Results and Deliverables (Idi)
- 1.5 Market Research (Samira)

### **Chapter 2 Background and Related Work**

- 2.1 Background and Used Technologies (John)
- 2.2 State-of-the-art (Idi, John)
- 2.3 Literature Survey (Idi)

### **Chapter 3 Project Requirements**

- 3.1 Domain and Business Requirements (John)
- 3.2 System Functional Requirements (Samira)
- 3.3 Non-functional Requirements (Samira)
- 3.4 Context and Interface Requirements (Idi)
- 3.5 Technology and Resource Requirements (Idi)

### **Chapter 4 Project Design**

- 4.1 System Design (Samira)
- 4.2 Subsystem Architecture (Samira)
  - 4.2.1 Ethash Processor (John)
  - 4.2.2 KECCAK Processors (Samira) (Sang)
  - 4.2.3 Memory Interface (Idi)
- 4.3 Structure and Logic Design (Sang)
- 4.4 Design Constraints, Problems. Trade-offs, and Solutions (Idi)

### **Chapter 5 Project Implementation**

- 5.1 Implementation Overview (Samira)
- 5.2 Hardware Implementation (Everyone)
- 5.3 Software Implementation (Samira) (John)

### **Chapter 6 Tools and Standards**

- 6.1 Tools Used (Idi)
- 6.2 Standards (Idi)

### **Chapter 7 Testing and Experiment**

- 7.1 Testing and Experiment Scope (Everyone)

7.2	Testing and Experiment Approach	(Everyone)
7.3	Testing and Experiment Results and Analysis	(Everyone)
<b>Chapter 8 Conclusions and Future Work</b>		(John) (Idi)
<b>Appendix A</b>		(Idi)
<b>Appendix B</b>		(All)