Olivia Kuk

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Design Verification Engineer

A fast-learning and detail-oriented engineer with 1 year of experience in semiconductor / Display circuit design and test definition, execution, optimization and reporting of test data. Led multiple team projects successfully that proves having great interpersonal communication skills that help bring the company find the right customers, assist troubleshooting for development and implementation of Mobile applications.

Skills & Abilities

- Programming Language: C, C++, Python, Verilog, SPICE
- Software: Linux, SmartSpice, Gateway, MATLAB, Unity 3D, Zemax Microsoft Office Suite
- English Proficiency: Native
- Hardware: Oscilloscope, Probe Station, SEM, UV-Vis Spectrometer, PECVD, Stepper, EUV

Experience

EMBEDDED SYSTEM PROJECT | MARCH - APRIL 2025

- Developed embedded systems using Texas Instruments MSPM0 MCUs, including firmware and subsystem design in C.
- Integrated analog sensors and communication protocols (UART, I2C) for real-time data acquisition and transmission.
- Solid understanding of SoC architectures, including hardware-software interaction, Linux environments, kernel/driver porting, and automotive SoC applications.

CIRCUIT DESIGN VERIFICATION ENGINEER RESEARCHER | SEMICONDUCTOR DEVICE AND CIRCUIT TEAM, AICT | JULY – DECEMBER 2024

- Designed and integrated Verilog-A-based pulsed signal generators, embedding into SPICE code to improve simulation efficiency, achieving a 96.29% reduction in process time.
- Collaborated closely with software engineers to develop and enhance a AI semiconductor, ReRAM model, improving system functionality.
- Participated in on-site product evaluations and data analysis to address client-specific needs.

INDUSTRIAL SALES ENGINEERING INTERN | WORLD TRADE CENTER DENVER | APRIL – JULY 2023

- Managed educational programs and industry workshops, focusing on technical training for global clients.
- Organized a 1,000+ attendee conference, leveraging bilingual negotiation skills to expand global outreach and support companies entering the global market.

Education

B.S. IN OPTICAL DISPLAY ENGINEERING | FEBRUARY 2025 | KYUNGHEE UNIVERSITY, SEOUL

- Relevant Course of Work: Electric Circuit, Semiconductor Fabrication, Reverse Engineering, ARVR Simulation, Electromagnetic, Thin Film Transistor Engineering, Microcontroller.
- Engineering Projects
  - **Semiconductor Manufacturing**: Grew 30+ CNTs from wafer dicing to etching, applying advanced material analysis techniques.
  - **Reverse Engineering**: Analyzed and deconstructed devices such as the Galaxy Z Flip, focusing on circuit boards and optical responses.
  - **Electrical Engineering**: Investigated LCD circuit boards and evaluated their optical response characteristics.
- Extracurricular Activities: **Field Application Engineering Student Intern(SK Hynix)**, Conference Manager for International Meeting on Information Display(IMID), Student President of the Python Programming Club