



Institute for Program Structures and Data Organization (IPD)



# Gradual Verification and maybe something about implicit dynamic frames

Master's Thesis of

#### Johannes Bader

at the Department of Informatics Institute for Program Structures and Data Organization (IPD)

Reviewer: Prof. Dr.-Ing. Gregor Snelting, Karlsruhe Institute of Technology - Karlsruhe, Germany

Advisors: Assoc. Prof. Jonathan Aldrich, Carnegie Mellon University - Pittsburgh, USA

Assoc. Prof. Éric Tanter, University of Chile - Santiago, Chile

**Duration:** 2016-05-10 - 2016-09-28

I declare that I have developed and written the enclosed thesis completely and have not used sources or means without declaration in the text, and hat the rules of the KIT for upholding good scientific practice.	
Karlsruhe, 2016-09-??	
(Johannes Bader)	

#### Abstract

Formal verification using Hoare logic is a powerful tool to prove properties of imperative computer programs.

However, in practice programmers often face situations ... rigid... not flexible... - incomplete information about parts of the program - laziness, forced to annotate everything - unable to express due to limited syntax - unable to prove something facing undecidability

To counteract these limitations we introduce the notion of gradual formulas with an unknown part "?".

The main contribution of this work is presenting a gradual verification logic that covers the full range between completely unannotated programs and fully annotated programs. We prove the soundness of this logic and ... Siek et al. (2015).

#### Acknowledgments

I wish to thank my advisors Jonathan Aldrich and Éric Tanter for offering me this topic and for their patient assistance throughout the past few months. In any situation and every way, their remarks and thoughts guided me in the right direction.

Also I am very grateful to all my family and friends who encouraged and supported me throughout my life.

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### 1 Introduction

Most modern programming languages use static analysis to some degree, ruling out certain types of runtime failure. Static analysis provides guarantees about the dynamic behavior of a program without actually running the program. Static typing disciplines are among the most common representatives of static analysis, guaranteeing type safety at compile time, obviating the need for dynamic checks.

Another powerful technique is static verification of programs against their specification, i.e. statically proving their "correctness". In practice this is achieved by checking that some annotated invariants or assertions (reflecting the specification) must always hold. Unfortunately, static verification has limitations and drawbacks:

- Syntax
- Decidability
- Difficult and Tedious to annotate programs
- ...

These limitations not only affect programmers trying to statically verify their program. Most general purpose programming languages (C/C++, C#, Java, ...), usually driven by cost-benefit and usability considerations, haven't adopted this level of static analysis in the first place.

The purpose of gradual verification is to weaken if not remove some of these limitations at the cost of turning some static checks into runtime checks, whenever inevitable. We will present a procedure of turning a static verification into a gradual one.

This idea is not new at all and actually common practice in type systems: In C# or Java, explicit type casts are assertions about the actual type of a value. This actual type (usually a subtype of the statically known type) could not be deduced by the static type system due to its limitations. Such an assertion/cast allows subsequent static reasoning about the value assuming its new type at the cost of an additional runtime check, ensuring the validity of the cast. Note that such deviations from a "purely" static type system (one where there is no need for runtime checks) do not affect type safety: It is still guaranteed that execution does not enter an invalid state (one where runtime types are incompatible with statically annotated types) by simply interrupting execution whenever a runtime type check fails. This is usually implemented by throwing an exception.

At the other end of the spectrum are dynamically typed languages. In scenarios where the limitations of a static type system would clutter up the source code, they allow expressing the same logic with less syntactic overhead, but at the cost of less static guarantees and early bug detection.

In terms of program verification, most general purpose languages are on the dynamic end of the spectrum. If they exist as designated syntax, assertions are usually implemented as runtime checks and often even dropped entirely for "release" builds (the Java compiler drops them by default). It is common practice to implement

#### 1 Introduction

A gradual type system is more flexible, as it provides the full continuum between static and dynamic typing, letting the programmer decide ... It can be seen as an extension "unknown" type

This work will also show that gradual verification ... other angle!

- What is the thesis about? Why is it relevant or important? What are the issues or problems? What is the proposed solution or approach? What can one expect in the rest of the thesis?

"Static verification checks that properties are always true, but it can be difficult and tedious to select a goal and to annotate programs for input to a static checker." (http://www.sciencedirect.com/

#### 1.1 Motivation

more practical view? Intro? Background?

## 2 Background

#### 2.1 Categorization of existing stuff

[1] GraVy: metric of progress of the verification process and allows the verification engineer to focus on the remaining statements. Gradual verification is not a new static verification technique. It is an extension that can be applied to any existing static verification techniques to provide additional information to the verification engineer. Thus, issues, such as handling of loops or aliasing are not addressed in this paper. These are problems related to sound verification, but gradual verification is about how to make the use of such verification more traceable and quantifiable

[16] ESC/Java Software development and maintenance are costly endeavors. The cost can be reduced if more software defects are detected earlier in the development cycle. This paper introduces the Extended Static Checker for Java (ESC/Java), an experimental compile-time program checker that finds common programming errors. The checker is powered by verification-condition generation and automatic theorem proving techniques. It provides programmers with a simple annotation language with which programmer design decisions can be expressed formally. ESC/Java examines the annotated software and warns of inconsistencies between the design decisions recorded in the annotations and the actual code, and also warns of potential runtime errors in the code. This paper gives an overview of the checker architecture and annotation language and describes our experience applying the checker to tens of thousands of lines of Java programs.

```
[10] JML => static verification
```

- [4] JML => RAC
- [15] Spec#
- [3] Spec# extension (concurrent OO)
- [14] Design-by-Contract then also: Eiffel by Bertrand Meyer
- [13] Code Contracts! Combines runtime and static checking
- [6] "verified design-by-contract"
- [5] = static verification plus directed dynamic verification In this paper, we present a technique to complement partial verification results by automatic test case generation. In contrast to existing work, our technique supports the common case that the verification results are based on unsound assumptions. We annotate programs to reflect which executions have been verified, and under which assumptions. These annotations are then used to guide dynamic symbolic execution toward unverified program executions. Our main technical contribution is a code instrumentation that causes dynamic symbolic execution to abort tests that lead to verified executions, to prune parts of the search space, and to prioritize tests that cover more properties that are not fully verified.

### 2.2 Hoare Logic

...for static semantics

[9]

#### 2.3 Related Work

#### 2.3.1 Abstracting Gradual Typing

- [18] Gradual Typing for Functional Languages apply their gradual typing approach to other areas
- [19] Refined criteria for gradual typing gradual guarantee: The gradual guarantee says that if a gradually typed program is well typed, then removing type annotations always produces a program that is still well typed. Further, if a gradually typed program evaluates to a value, then removing type annotations always produces a program that evaluates to an equivalent value.
- [7] AGT In this paper, we propose a new formal foundation for gradual typing, drawing on principles from abstract interpretation to give gradual types a semantics in terms of preexisting static types. Abstracting Gradual Typing (AGT for short) yields a formal account of consistency—one of the cornerstones of the gradual typing approach—that subsumes existing notions of consistency, which were developed through intuition and ad hoc reasoning.
- [8] Abstracting Gradual Typing (AGT) is an approach to systematically deriving gradual counterparts to static type disciplines (Garcia et al. 2016). The approach consists of defining the semantics of gradual types by interpreting them as sets of static types, and then defining an optimal abstraction back to gradual types. These operations are used to lift the static discipline to the gradual setting. The runtime semantics of the gradual language then arises as reductions on gradual typing derivations. To demonstrate the flexibility of AGT, we gradualize a prototypical security-typed language with respect to only security labels rather than entire types, yielding a type system that ranges gradually from simply-typed to securely typed. We establish noninterference for our gradual language using Zdancewic's logical relation proof method. Whereas prior work presents gradual security cast languages, which require explicit security casts, this work yields the first gradual security source language, which requires no explicit casts.

prior to AGT [23] the language extends the notion of gradual typing to account for typestate: gradual typestate checking seamlessly combines static and dynamic checking by automatically inserting runtime checks into programs.

- [2] develop a theory of gradual effect checking, which makes it possible to incrementally annotate and statically check effects, while still rejecting statically inconsistent programs. We extend the generic type-and-effect framework of Marino and Millstein with a notion of unknown effects, which turns out to be significantly more subtle than unknown types in traditional gradual typing. We appeal to abstract interpretation to develop and validate the concepts of gradual effect checking.
  - [22] Grad Effects in Scala, benchmarks on runtime impact!

#### 2.3.2 Implicit Dynamic Frames

Race-free Assertion language! => static verification tool able to reason soundly about concurrent programs

- [20] IDF
- [11] Chalice, a verification methodology based on implicit dynamic frames

Chalice's verification methodology centers around permissions and permission transfer. In particular, a memory location may be accessed by a thread only if that thread has permission to do so. Proper use of permissions allows Chalice to deduce upper bounds on the set of locations modifiable by a method and guarantees the absence of data races for

concurrent programs. The lecture notes informally explain how Chalice works through various examples.

also: Viper (Verification Infrastructure for Permission-based Reasoning; is a suite of tools developed at ETH Zurich, providing an architecture on which new verification tools and prototypes can be developed simply and quickly.) has Chalice as front-end

[21] In this paper, we provide both an isorecursive and an equirecursive formal semantics for recursive definitions in the context of Chalice

#### [17] VERY IMPORTANT: chapter 2.2

Finally, we show that we can encode the separation logic fragment of our logic into the implicit dynamic frames fragment, preserving semantics. For the connectives typically supported by tools, this shows that separation logic can be faithfully encoded in a first-order automatic verification tool (Chalice).

Although IDF was partially inspired by separation logic, there are many differences between SL and IDF that make understanding their relationship difficult. SL does not allow expressions that refer to the heap, while IDF does. SL is defined on partial heaps, while IDF is defined using total heaps and permission masks. The semantics of IDF are only defined by its translation to first-order verification conditions, while SL has a direct Kripke semantics for its assertions.

#### **Self-Framing**

# 3 Gradualization of a Statically Verified Language

As illustrated earlier gradual verification can be seen as an extension of both static and dynamic verification. Yet, our approach of "gradualization" formalizes the introduction of the dynamic aspect into a fully static system. Thus, this uses a statically verified language as starting point. Later we will show how a programming language without static verification can be approached.

# 3.1 A Statically Verified Language ( $\mathrm{SVL}$ ) / A Static Verification System?

We now intrude a very simple Java-like statically verified language "SVL" that uses Chalice/Eiffel/Spec# sub-syntax to express method contracts.

#### **3.1.1 Syntax**

```
:= \overline{cls} \ \overline{s}
program \in Program
        cls \in \mathsf{CLASS}
                                  ::= class C \in \overline{field} \ \overline{method} \}
                                  :=T f;
     field \in Field
 method \in Method
                                  := T \ m(T \ x) \ contract \{ \overline{s} \}
contract \in Contract
                                  := requires \phi; ensures \phi;
         T \in \mathsf{Type}
                                  ::= int \mid C
          s \in \mathsf{Stmt}
                                  := T \ x; \ | \ x.f \ := \ y; \ | \ x \ := \ e; \ | \ x \ := \ \text{new} \ C; \ | \ x \ := \ y.m(z);
                                     | return x; | assert \phi; | release \phi; | hold \phi { \overline{s} };
                                  ::= true | (e = e) | (e \neq e) | acc(e.f) | \phi * \phi
         \phi \in FORMULA
          e \in \operatorname{Expr}
                                  := v \mid x \mid e.f
    x, y, z \in VAR
                                  ::= this | result | name
          v \in Val
                                  ::= o \mid n \mid \mathtt{null}
         n \in \mathbb{Z}
  C, f, m
                                  ::= name
```

Figure 3.1. Syntax

We pose false  $\stackrel{\mathsf{def}}{=}$  (null  $\neq$  null).

3 Gradualization of a Statically Verified Language

Further stuff

$$H \qquad \qquad \in \quad (o \rightharpoonup (C, \overline{(f \rightharpoonup v)}))$$

$$\rho \qquad \qquad \in \quad (x \rightharpoonup v)$$

$$\Gamma \qquad \qquad \in \quad (x \rightharpoonup T)$$

$$A_s \qquad \qquad ::= \overline{(e, f)}$$

$$A_d \qquad \qquad ::= \overline{(o, f)}$$

$$S \qquad \qquad ::= (\rho, A_d, \overline{s}) \cdot S \mid nil$$

#### 3.1.2 Formulas

Not all syntactically correct formulas are useful in a static verification system. We will introduce two subsets Satformula and Sfrmformula...

#### 3.1.3 Static Semantics

The static semantics of SVL consist of typing rules and a Hoare calculus making use of those typing rules. All the rules are implicitly parameterized over some program  $p \in PROGRAM$ , necessary for example to extract the type of a field in the following typing rules.

$$\overline{\Gamma \vdash e:T}$$
 
$$\overline{\Gamma \vdash n: \mathtt{int}} \ \mathrm{STValNum}$$
 
$$\overline{\Gamma \vdash \mathtt{null}:C} \ \mathrm{STValNull}$$
 
$$\frac{\Gamma(x) = T}{\Gamma \vdash x:T} \ \mathrm{STVar}$$

$$\frac{\Gamma \vdash e : C \qquad \mathsf{fieldType}_p(C,f) = T}{\Gamma \vdash e \: . \: f : T} \: \mathsf{STFIELD}$$

Figure 3.2. SVL: Static Typing of Expressions

#### 3.1.4 Well-Formedness

With static semantics in place, we can define what makes programs well-formed. Well-formedness is required to ... The following predicates

A program is well-formed if both its classes and main method are. For the main method to be well-formed, it must satisfy our Hoare predicate, given no assumptions.

$$\frac{\overline{cls_i \ \mathsf{OK}} \qquad \vdash \{\mathsf{true}\}\overline{s}\{\mathsf{true}\}}{(\overline{cls_i} \ \overline{s}) \ \mathsf{OK}} \ \mathsf{OKPROGRAM}$$

$$\frac{\text{unique } field\text{-names} \quad \text{unique } method\text{-names} \quad \overline{method_i \ \mathsf{OK} \ \mathsf{in} \ C}}{\left(\mathsf{class} \ C \ \left\{ \ \overline{field_i} \ \overline{method_i} \ \right\}\right) \ \mathsf{OK}} \ \mathsf{OKCLASS}$$

$$FV(\phi_1) \subseteq \{x, \texttt{this}\}$$
 
$$FV(\phi_2) \subseteq \{x, \texttt{this}, \texttt{result}\} \quad x: T_x, \texttt{this}: C, \texttt{result}: T_m \vdash \{\phi_1\} \overline{s} \{\phi_2\}$$
 
$$\frac{\phi_1, \phi_2 \in \mathsf{SFRMFORMULA} \quad \overline{\neg \mathsf{writesTo}(s_i, x)}}{(T_m \ m(T_x \ x) \ \texttt{requires} \ \phi_1; \ \texttt{ensures} \ \phi_2; \ \{\ \overline{s}\ \}) \ \mathsf{OK} \ \mathsf{in} \ C} \ \mathsf{OKMETHOD}$$

- 3.1.5 Dynamic Semantics
- 3.1.6 Soundness
- 3.2 Deriving a Gradually Verified Language
- 3.2.1 Gradual Formulas
- 3.2.2 Abstracting Static Semantics

$$\begin{aligned} \mathsf{wlp}(\text{``x} := \texttt{a.f;''}, \mathsf{acc}(\texttt{b.f})) &= \begin{cases} \mathsf{acc}(\texttt{b.f}) * \mathsf{acc}(\texttt{a.f}) \\ \mathsf{acc}(\texttt{b.f}) * (\texttt{a} = \texttt{b}) \end{cases} \\ \mathcal{H}_s : \mathsf{SFRMFORMULA} &\to \mathsf{SFRMFORMULA} \\ \mathcal{H}_s &= \mathcal{M}_s \circ \mathcal{I}_s \circ \mathcal{F}_s \\ \mathcal{H}_s(\phi^\circ) &= \mathcal{M}_s(\phi^\circ) \quad \text{if } \mathcal{P}(s) \land \phi^\circ \implies \mathcal{J}(s) \\ \mathcal{F}_s : \mathsf{SFRMFORMULA} &\to \mathsf{SFRMFORMULA} \\ \mathcal{F}_s(\phi^\circ) &= \phi^\circ \quad \text{if } \mathcal{P}(s) \end{cases} \\ \mathcal{I}_s : \mathsf{SFRMFORMULA} &\to \mathsf{SFRMFORMULA} \\ \mathcal{I}_s(\phi^\circ) &= \phi^\circ \quad \text{if } \phi^\circ \implies \mathcal{J}(s) \\ \mathcal{M}_s : \mathsf{SFRMFORMULA} &\to \mathsf{SFRMFORMULA} \\ \mathcal{M}_s : \mathsf{SFRMFORMULA} &\to \mathsf{SFRMFORMULA} \end{aligned}$$

#### 3.2.3 Abstracting Dynamic Semantics

#### 3 Gradualization of a Statically Verified Language

Figure 3.3. SVL: Hoare Calculus

 $\frac{\Gamma \vdash \{\phi_p\} s_1 \{\phi_q\} \qquad \Gamma \vdash \{\phi_q\} \overline{s_2} \{\phi_r\}}{\Gamma \vdash \{\phi_n\} s_1 \overline{s_2} \{\phi_r\}} \text{ HSEQ}$ 

$$H, \rho \vdash e \Downarrow v$$

$$\frac{1}{H, \rho \vdash x \Downarrow \rho(x)}$$
 EEVAR

$$\overline{H, \rho \vdash v \Downarrow v}$$
 EEVALUE

$$\frac{H, \rho \vdash e \Downarrow o}{H, \rho \vdash e.f \Downarrow H(o)(f)} \text{ EEAcc}$$

Figure 3.4. SVL: Evaluating Expressions

$$H, \rho, A \vDash \phi$$

$$\overline{H, \rho, A \vDash \mathtt{true}}$$
 EATRUE

$$\frac{H, \rho \vdash e_1 \Downarrow v_1 \qquad H, \rho \vdash e_2 \Downarrow v_2 \qquad v_1 = v_2}{H, \rho, A \vDash (e_1 = e_2)} \text{ EAEQUAL}$$

$$\frac{H, \rho \vdash e_1 \Downarrow v_1 \qquad H, \rho \vdash e_2 \Downarrow v_2 \qquad v_1 \neq v_2}{H, \rho, A \vDash (e_1 \neq e_2)} \text{ EANEQUAL}$$

$$\frac{H, \rho \vdash e \Downarrow o \quad H, \rho \vdash e.f \Downarrow v \quad (o, f) \in A}{H, \rho, A \vDash \mathsf{acc}(e.f)} \; \mathsf{EAAcc}$$

$$\frac{A_1 = A \backslash A_2 \qquad H, \rho, A_1 \vDash \phi_1 \qquad H, \rho, A_2 \vDash \phi_2}{H, \rho, A \vDash \phi_1 * \phi_2} \text{ EASEPOP}$$

Figure 3.5. SVL: Evaluating Expressions

$$(H,S) \to (H,S)$$

$$\frac{H, \rho \vdash x \Downarrow o \quad H, \rho \vdash y \Downarrow v_y \quad (o, f) \in A \quad H' = H[o \mapsto [f \mapsto v_y]]}{(H, (\rho, A, x.f := y; \overline{s}) \cdot S) \to (H', (\rho, A, \overline{s}) \cdot S)} \text{ ESFIELDASSIGN}$$

$$\frac{H, \rho \vdash e \Downarrow v \qquad \rho' = \rho[x \mapsto v]}{(H, (\rho, A, x := e; \overline{s}) \cdot S) \to (H, (\rho', A, \overline{s}) \cdot S)} \text{ ESVARASSIGN}$$

$$\frac{o \not\in \mathsf{dom}(H)}{\rho' = \rho[x \mapsto o]} \quad \begin{array}{c} o \not\in \mathsf{dom}(H) & \mathsf{fields}_p(C) = \overline{T \ f;} \\ A' = A \cup \overline{(o, f_i)} & H' = H[o \mapsto \overline{[f_i \mapsto \mathsf{defaultValue}(T_i)]}] \\ \overline{(H, (\rho, A, x \ := \ \mathsf{new} \ C; \overline{s}) \cdot S)} \to (H', (\rho', A', \overline{s}) \cdot S) \end{array} \\ \to (H', (\rho', A', \overline{s}) \cdot S) \to (H', (\rho', A', \overline{s}) \cdot S)$$

$$\frac{H, \rho \vdash x \Downarrow v_x \qquad \rho' = \rho[\mathtt{result} \mapsto v_x]}{(H, (\rho, A, \mathtt{return} \ x; \overline{s}) \cdot S) \to (H, (\rho', A, \overline{s}) \cdot S)} \text{ ESRETURN}$$

$$\begin{split} H, \rho \vdash y \Downarrow o & H, \rho \vdash z \Downarrow v \\ H(o) = (C, \_) & \text{method}_p(C, m) = T_r \ m(T \ w) \ \text{requires} \ \phi; \ \text{ensures} \ \_; \ \{ \ \overline{r} \ \} \\ \frac{\rho' = [\text{result} \mapsto \text{defaultValue}(T_r), \text{this} \mapsto o, w \mapsto v] \quad H, \rho', A \vDash \phi \quad A' = \lfloor \phi \rfloor_{H, \rho'}}{(H, (\rho, A, x \ := \ y . m(z) \, ; \overline{s}) \cdot S) \rightarrow (H, (\rho', A', \overline{r}) \cdot (\rho, A \backslash A', x \ := \ y . m(z) \, ; \overline{s}) \cdot S)} \ \text{ESCall} \end{split}$$

$$\frac{H, \rho \vdash y \Downarrow o \qquad H(o) = (C, \_)}{H, \rho', A' \vDash \phi \qquad A'' = \lfloor \phi \rfloor_{H, \rho'} \qquad H, \rho' \vdash \texttt{result} \Downarrow v_r}{(H, (\rho', A', \emptyset) \cdot (\rho, A, x := y . m(z) ; \overline{s}) \cdot S) \rightarrow (H, (\rho[x \mapsto v_r], A \cup A'', \overline{s}) \cdot S)} \text{ ESCALLFINISH }$$

$$\frac{H,\rho,A \vDash \phi}{(H,(\rho,A,\mathtt{assert}\ \phi;\overline{s})\cdot S) \to (H,(\rho,A,\overline{s})\cdot S)} \ \mathsf{ESASSERT}$$

$$\frac{H, \rho, A \vDash \phi \qquad A' = A \setminus \lfloor \phi \rfloor_{H, \rho}}{(H, (\rho, A, \mathtt{release} \ \phi; \overline{s}) \cdot S) \to (H, (\rho, A', \overline{s}) \cdot S)} \text{ ESRELEASE}$$

$$\frac{\rho' = \rho[x \mapsto \mathsf{defaultValue}(T)]}{(H, (\rho, A, T \ x; \overline{s}) \cdot S) \to (H, (\rho', A, \overline{s}) \cdot S)} \text{ ESDECLARE}$$

$$\frac{H,\rho,A\vDash\phi \qquad A'=\lfloor\phi\rfloor_{H,\rho}}{(H,(\rho,A,\operatorname{hold}\ \phi\ \{\ \overline{s'}\ \};\overline{s})\cdot S)\to (H,(\rho,A\backslash A',\overline{s'})\cdot (\rho,A',\operatorname{hold}\ \phi\ \{\ \overline{s'}\ \};\overline{s})\cdot S)}\ \operatorname{ESHOLD}$$

# 4 Implementation

...of 3.1

- 4.1 Prepare Hoare Rules
- 4.1.1 Deterministic Functions

$$\mathcal{P}(s) = \{$$

- 4.2 Gradualize Hoare Rules
- 4.2.1 Gradualize Functions
- 4.3 Enhancing an Unverified Language

# 5 Evaluation/Analysis

> E: with gradual typestates the same problem happened: as soon as the potential for unknown annotations was accepted, there was a "baseline cost" just to maintain the necessary infrastructure. With simple gradual types, it's almost nothing. With gradual effects, we've shown that it can boil down to very little (a thread-local variable with little overhead, see OOPSLA'15).

# 6 Conclusion

Recap, remind reader what big picture was. Briefly outline your thesis, motivation, problem, and proposed solution.

### 6.1 Limitations

### 6.2 Future Work

# 7 Appendix

### 8 UNSORTED

#### 8.1 HoareMotivEx

# 8.2 MotivationExamples

#### 8.2.1 As extension to unverified setting

"Make dynamic setting more static" Motivating example:

```
boolean hasLegalDriver(Car c)
{
    return c.driver.age >= 18;
}
```

Motivating example with potential leak:

```
boolean hasLegalDriver(Car c)
{
    allocateSomething();
    boolean result = c.driver.age >= 18;
    releaseSomething();
    return result;
}
```

Motivating example with argument validation:

```
boolean hasLegalDriver(Car c)
{
   if (!(c != null))
        throw new IllegalArgumentException("expected_c_!=_null");
   if (!(c.driver != null))
        throw new IllegalArgumentException("expected_c.driver_!=_null");

// business logic (requires 'c.driver.age' to evaluate)
}
```

Motivating example with declarative approach (JML syntax):

```
//@ requires c != null && c.driver != null;
boolean hasLegalDriver(Car c)
{
    // business logic (requires 'c.driver.age' to evaluate)
}
```

There are two basic ways to turn this annotation into a guarantee:

#### Static Verification (run ESC/Java [12])

In the unlikely event that the verifier can prove the precondition at all call sites, our problem is solved. Otherwise, we have to enhance the call sites in order to convince the verifier. Choices:

- Add parameter validation, effectively duplicating the original runtime check across the program.
- Add further annotations, guiding the verifier towards a proof. This might not always work due to limitations of the verifier or decidability in general.

There are obvious limitations to this approach, static verification tends to be invasive. At least there is a performance benefit: Runtime checks (originally part of every call) are now only necessary in places where verification would not succeed otherwise.

#### Runtime Assertion Checking (RAC, run JML4c, TODO: http://www.cs.utep.edu/cheon/download/jm

This approach basically converts the annotation back into a runtime check equivalent to our manual argument validation. It is therefore less invasive, not requiring further changes to the code, but also lacks the advantages of static verification.

#### 8.2.2 As extension to fully verified setting

"Make static setting more dynamic"

```
int collatzIterations(int iter, int start)
    requires 0 < start;
    ensures 0 < result;
{
    // ...
}</pre>
```

Non-solution:

```
int collatzIterations(int iter, int start)
    requires 0 < start;</pre>
    ensures 0 < result;</pre>
{
    // ...
}
int myRandom(int seed)
    requires 0 < seed</pre>
                          && seed < 10000;
    ensures 0 < result && result < 4;</pre>
{
    int result = collatzIterations(300, seed);
    // we know: result \in \{1, 2, 4\}
    // "cast"
    if (!(result < 5))</pre>
        throw new IllegalStateException("expected_result_5");
    // verifier now knows: 0 < result && result < 5
    if (result == 4) result = 3;
    return result;
```

This solution is not satisfying, - much to write, have to think about what to write (requires you to kind of thing from verifiers perspective) - intuitively the problem is with the method's postcondition being too weak, i.e. we solved the problem at the wrong place!

```
int collatzIterations(int iter, int start)
    requires 0 < start;
    ensures 0 < result && ?;
{
    // ...
}
int myRandom(int seed)
    requires 0 < seed && seed < 10000;
    ensures 0 < result && result < 4;</pre>
```

```
int result = collatzIterations(300, seed);
// we know: result ∈ { 1, 2, 4 }

// verifier allowed to
// assume 0 < result && result < 5
// from 0 < result && ?
// (adding runtime check)

if (result == 4) result = 3;
return result;
}</pre>
```

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