- Update the AHB-Lite salve module to include three Read/Write 32-bit registers (we will call it a peripheral). This slave supports only word (32-bit) read and write to these three regsiters.
- Create a slave module that represents a 8kbytes memory which supports byte, half-word and word read and write (we will call this slave RAM)
- Update the SoC to have three slaves:
    - One instance of peripheral slave [S0, Address = 0x40000000]
    - One instance of the RAM [S1, Address = 0x000000000]
    - One instance of the RAM [S2, Address 0x20000000]
- Update the SoC to add ports that expose S0 three registers to outside (output ports).
- Verify that you can access the three slaves by writing and reading different size data data to/from them.