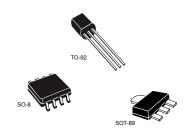


Positive voltage regulators



Features

- Output current up to 100 mA
- Output voltages of 3.3; 5; 6; 8; 9; 10; 12; 15; 18; 24 V thermal overload protection
- Short-circuit protection
- · No external components are required
- Available in either ± 4% (A) or ± 8% (C) selection

Description

The L78L series of three-terminal positive regulators employ internal current limiting and thermal shutdown, making them essentially indestructible. If adequate heat-sink is provided, they can deliver up to 100 mA output current. They are intended as fixed voltage regulators in a wide range of applications including local or oncard regulation for elimination of noise and distribution problems associated with single-point regulation. In addition, they can be used with power pass elements to make high-current voltage regulators. The L78L series used as Zener diode/resistor combination replacement, offers e improvement along with lower quiescent current and lower noise.

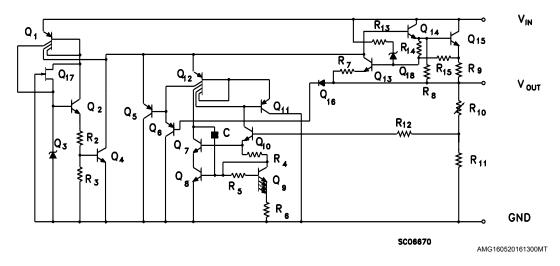
Maturity status link

L78L



1 Diagram

Figure 1. Schematic diagram

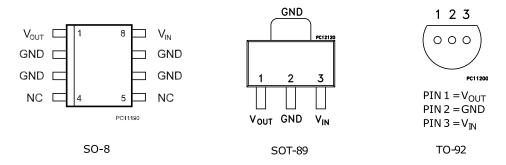


DS0424 - Rev 27 page 2/39



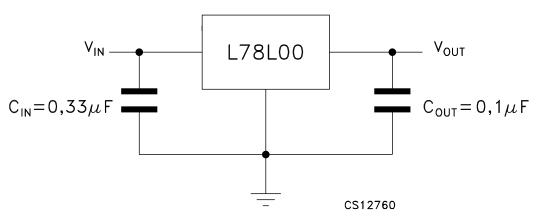
Pin configuration

Figure 2. Pin connection (top view, bottom view for TO-92)



AMG160520161301MT

Figure 3. Test circuits



AMG160520161302MT

DS0424 - Rev 27 page 3/39



3 Maximum ratings

Table 1. Absolute maximum ratings

Symbol	Parameter	Parameter		Unit
		V _O = 3.3 to 9 V	30	V
V_{I}	V _I DC Input voltage	V _O = 12 to 15 V	35	
		V _O = 18 to 24 V	40	
I _O	Output current	Output current		
P _D	Power dissipation		Internally limited (1)	mW
T _{STG}	Storage temperature range	Storage temperature range		°C
T _{OP}	Operating it notion townsoreture range	for L78LxxAC / L78LxxC	0 to 125	°C
1 OP	Operating junction temperature range	for L78LxxAB	-40 to 125	

Our SO-8 package used for voltage regulators is modified internally to have pins 2, 3, 6 and 7 electrically communed to the die attach flag. This particular frame decreases the total thermal resistance of the package and increases its ability to dissipate power when an appropriate area of copper on the printed circuit board is available for heat-sinking. The external dimensions are the same as for the standard SO-8.

Table 2. Thermal data

Symbol	Parameter	SO-8	TO-92	SOT-89	Unit
R _{thJC}	thJC Thermal resistance junction-case (max)			15	°C/W
R _{thJA}	Thermal resistance junction-ambient (max)	55 ⁽¹⁾	200	55 ⁽¹⁾	°C/W

1. Considering 6 cm² of copper Board heat-sink.

DS0424 - Rev 27 page 4/39



4 Electrical characteristics

Table 3. Electrical characteristics of L78L33C - Refer to the test circuits, T_J = 0 to 125 °C, V_I = 8.3 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit	
V _O	Output voltage	T _J = 25 °C	3.036	3.3	3.564	V	
M	Outro to cella a c	I _O = 1 to 40 mA, V _I = 5.3 to 20 V	2.97		3.63	V	
V _O	Output voltage	I _O = 1 to 70 mA, V _I = 8.3 V	2.97		3.63	V	
A\/	Line we will atten	V _I = 5.4 to 20 V, T _J = 25 °C			150		
ΔV_{O}	Line regulation	V _I = 6.3 to 20 V, T _J = 25 °C			100	mV	
A\/	Load regulation	I _O = 1 to 100 mA, T _J = 25 °C			60	mV	
ΔV_{O}		I _O = 1 to 40 mA, T _J = 25 °C			30		
		T _J = 25 °C			6	mA	
I _d	Quiescent current	T _J = 125 °C			5.5	mA	
41		I _O = 1 to 40 mA			0.2		
Δl _d	Quiescent current change	V _I = 6.3 to 20 V			1.5	mA	
eN	Output noise voltage	B = 10 Hz to 100 kHz, T _J = 25 °C		40		μV	
C)/D	Cumply walkens rejection	V _I = 6.3 to 16.3 V, f = 120 Hz	44			4D	
SVR	Supply voltage rejection	I_{O} = 40 mA, T_{J} = 25 °C	41	41	41 49		dB
V _d	Dropout voltage			2		V	

Table 4. Electrical characteristics of L78L05C - Refer to the test circuits, T_J = 0 to 125 °C, V_I = 10 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Vo	Output voltage	T _J = 25 °C	4.6	5	5.4	V
V-	Output valtage	I _O = 1 to 40 mA, V _I = 7 to 20 V	4.5		5.5	V
Vo	Output voltage	I _O = 1 to 70 mA, V _I = 10 V	4.5		5.5	V
A\/	Line ne mulation	V _I = 8.5 to 20 V, T _J = 25 °C			200	
ΔV _O	Line regulation	V_I = 9 to 20 V, T_J = 25 °C			150	mV
AV Load regulation	Lond regulation	I _O = 1 to 100 mA, T _J = 25 °C			60	m)/
ΔV _O	Load regulation	I_O = 1 to 40 mA, T_J = 25 °C			30	mV
		T _J = 25 °C			6	mA
I _d	Quiescent current	T _J = 125 °C			5.5	mA
AI	Outles cont ourset shows	I _O = 1 to 40 mA			0.2	1
ΔI_d	Quiescent current change	V _I = 8 to 20 V			1.5	mA
eN	Output noise voltage	B = 10 Hz to 100 kHz, T _J = 25 °C		40		μV
SVR	Supply voltage rejection	V _I = 9 to 20 V, f = 120 Hz I _O = 40 mA, T _J = 25 °C	40	49		dB
V _d	Dropout voltage			2		V

DS0424 - Rev 27 page 5/39



Table 5. Electrical characteristics of L78L08C - Refer to the test circuits, T_J = 0 to 125 °C, V_I = 14 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Vo	Output voltage	T _J = 25 °C	7.36	8	8.64	V
V	Outrot valta aa	I _O = 1 to 40 mA, V _I = 8.5 to 20 V	7.2		8.8	V
V _O	Output voltage	I _O = 1 to 70 mA, V _I = 12 V	7.2		8.8	V
417	Line mendetien	V _I = 10.5 to 20 V, T _J = 25 °C			200	>/
ΔV_{O}	V _O Line regulation	V _I = 11 to 20 V, T _J = 25 °C			150	mV
41/	l d l-K	I _O = 1 to 100 mA, T _J = 25 °C			80	>/
ΔV_{O}	Load regulation	I _O = 1 to 40 mA, T _J = 25 °C			40	mV
		T _J = 25 °C			6	mA
Ι _d	Quiescent current	T _J = 125 °C			5.5	mA
A.1		I _O = 1 to 40 mA			0.2	
ΔI_d	Quiescent current change	V _I = 8 to 20 V			1.5	mA
eN	Output noise voltage	B = 10 Hz to 100 kHz, T _J = 25 °C		60		μV
CV/D	0 1 11 11	V _I = 9 to 20 V, f = 120 Hz	36	45		dD
SVR	Supply voltage rejection	I_O = 40 mA, T_J = 25 °C		30	30 43	
V _d	Dropout voltage			1.7		V

Table 6. Electrical characteristics of L78L09C - Refer to the test circuits, T_J = 0 to 125 °C, V_I = 15 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Vo	Output voltage	T _J = 25 °C	8.28	9	9.72	V
Vo	Output voltage	I _O = 1 to 40 mA, V _I = 11.5 to 23 V	8.1		9.9	V
VO	Output voltage	I _O = 1 to 70 mA, V _I = 15 V	8.1		9.9	V
41/	l in a manufation	V _I = 11.5 to 23 V, T _J = 25 °C			250	\/
ΔV_{O}	Line regulation	V _I = 12 to 23 V, T _J = 25 °C			200	mV
4)/	Load regulation	I _O = 1 to 100 mA, T _J = 25 °C			80	mV
ΔV_{O}		I _O = 1 to 40 mA, T _J = 25 °C			40	
		T _J = 25 °C			6	mA
I _d	Quiescent current	T _J = 125 °C			5.5	mA
		I _O = 1 to 40 mA			0.2	
$\Delta I_{\sf d}$	Quiescent current change	V _I = 12 to 23 V			1.5	mA
eN	Output noise voltage	B = 10 Hz to 100 kHz, T _J = 25 °C		70		μV
0) (D	Owner have a little was a said at live	V _I = 12 to 23 V, f = 120 Hz	36			
SVR	Supply voltage rejection	I_O = 40 mA, T_J = 25 °C		36 44		dB
V _d	Dropout voltage			1.7		V

DS0424 - Rev 27 page 6/39



Table 7. Electrical characteristics of L78L10C - Refer to the test circuits, T_J = 0 to 125 °C, V_I = 16 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Vo	Output voltage	T _J = 25 °C	9.2	10	10.8	V
W	Outrot will a ma	I _O = 1 to 40 mA, V _I = 12.5 to 23 V	9		11	V
V _O	Output voltage	I _O = 1 to 70 mA, V _I = 16 V	9		11	V
41/	1. 1.	V _I = 12.5 to 23 V, T _J = 25 °C			230	.,
ΔV_{O}	/ _O Line regulation	V _I = 13 to 23 V, T _J = 25 °C			170	mV
	Load regulation	I _O = 1 to 100 mA, T _J = 25 °C			80	mV
ΔV_{O}		I _O = 1 to 40 mA, T _J = 25 °C			40	
		T _J = 25 °C			6	mA
Ι _d	Quiescent current	T _J = 125 °C			5.5	mA
A.I.		I _O = 1 to 40 mA			0.1	
Δl_{d}	Quiescent current change	V _I = 13 to 23 V			1.5	mA
eN	Output noise voltage	B = 10 Hz to 100 kHz, T _J = 25 °C		60		μV
SVR	Supply voltage rejection	V_I = 14 to 23 V, f = 120 Hz I_O = 40 mA, T_J = 25 °C	37	45		dB
V _d	Dropout voltage			1.7		V

Table 8. Electrical characteristics of L78L12C - Refer to the test circuits, T_J = 0 to 125 °C, V_I = 19 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _O	Output voltage	T _J = 25 °C	11.1	12	12.9	V
V	0.44	I _O = 1 to 40 mA, V _I = 14.5 to 27 V	10.8		13.2	
V _O	Output voltage	I _O = 1 to 70 mA, V _I = 19 V	10.8		13.2	V
41/	Line new detien	V _I = 14.5 to 27 V, T _J = 25 °C			250	> /
Δνο	ΔV_{O} Line regulation	V _I = 16 to 27 V, T _J = 25 °C			200	mV
437		I _O = 1 to 100 mA, T _J = 25 °C			100	mV
ΔV_{O}	Load regulation	I _O = 1 to 40 mA, T _J = 25 °C			50	
		T _J = 25 °C			6.5	mA
I _d	Quiescent current	T _J = 125 °C			6	mA
		I _O = 1 to 40 mA			0.2	mA
Δl_d	Quiescent current change	V _I = 16 to 27 V			1.5	
eN	Output noise voltage	B = 10 Hz to 100 kHz, T _J = 25 °C		80		μV
0) / D	Overally walks are as in all and	V _I = 15 to 25 V, f = 120 Hz	36	42	42	-ID
SVR	Supply voltage rejection	I_O = 40 mA, T_J = 25 °C				dB
V _d	Dropout voltage			1.7		V

DS0424 - Rev 27 page 7/39



Table 9. Electrical characteristics of L78L15C - Refer to the test circuits, T_J = 0 to 125 °C, V_I = 23 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Vo	Output voltage	T _J = 25 °C	13.8	15	16.2	V
V	Output valta aa	I _O = 1 to 40 mA, V _I = 17.5 to 30 V	13.5		16.5	V
V _O	Output voltage	I _O = 1 to 70 mA, V _I = 23 V	13.5		16.5	_ v
41/	Line and the second	V _I = 17.5 to 30 V, T _J = 25 °C			300	>/
ΔνΟ	ΔV_{O} Line regulation	V _I = 20 to 30 V, T _J = 25 °C			250	mV
437		I _O = 1 to 100 mA, T _J = 25 °C			150	.,
ΔV_{O}	Load regulation	I _O = 1 to 40 mA, T _J = 25 °C			75	mV
		T _J = 25 °C			6.5	mA
I _d	Quiescent current	T _J = 125 °C			6	mA
A.I.		I _O = 1 to 40 mA			0.2	
$\Delta l_{\sf d}$	Quiescent current change	V _I = 20 to 30 V			1.5	mA
eN	Output noise voltage	B = 10 Hz to 100 kHz, T _J = 25 °C		90		μV
SVR	Supply voltage rejection	V _I = 18.5 to 28.5 V, f = 120 Hz I _O = 40 mA, T _J = 25 °C	33	39		dB
V _d	Dropout voltage			1.7		V

Table 10. Electrical characteristics of L78L18C - Refer to the test circuits, T_J = 0 to 125 °C, V_I = 27 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Vo	Output voltage	T _J = 25 °C	16.6	18	19.4	V
Vo	Output voltage	I _O = 1 to 40 mA, V _I = 22 to 33 V	16.2		19.8	V
٧٥	Output voltage	I _O = 1 to 70 mA, V _I = 27 V	16.2		19.8	V
۸۷/-	Line regulation	V _I = 22 to 33 V, T _J = 25 °C			320	mV
ΔV _O	Line regulation	V _I = 22 to 33 V, T _J = 25 °C			270	IIIV
A\/ -		I _O = 1 to 100 mA, T _J = 25 °C			170	
ΔV _O	Load regulation	I _O = 1 to 40 mA, T _J = 25 °C			85	mV
	Outropy to support	T _J = 25 °C			6.5	mA
I _d	Quiescent current	T _J = 125 °C			6	mA
Al	0	I _O = 1 to 40 mA			0.2	
Δl_d	Quiescent current change	V _I = 23 to 33 V			1.5	mA
eN	Output noise voltage	B = 10 Hz to 100 kHz, T _J = 25 °C		120		μV
SVR	V _I = 23 to 33 V, f = 120 Hz /R Supply voltage rejection 32	32	38		dB	
OVIC	oupply voltage rejection	I_O = 40 mA, T_J = 25 °C	32	30		UD.
V _d	Dropout voltage			1.7		V

DS0424 - Rev 27 page 8/39



Table 11. Electrical characteristics of L78L24C - Refer to the test circuits, T_J = 0 to 125 °C, V_I = 33 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _O	Output voltage	T _J = 25 °C	22.1	24	25.9	V
V	Outrout valta as	I _O = 1 to 40 mA, V _I = 27 to 38 V	21.6		26.4	V
V _O	Output voltage	I _O = 1 to 70 mA, V _I = 33 V	21.6		26.4	V
41/	1	V _I = 27 to 38 V, T _J = 25 °C			350	.,
ΔV_{O}	ΔV _O Line regulation	V _I = 28 to 38 V, T _J = 25 °C			300	mV
	Load regulation	I _O = 1 to 100 mA, T _J = 25 °C			200	.,
ΔV_{O}		I _O = 1 to 40 mA, T _J = 25 °C			100	mV
		T _J = 25 °C			6.5	mA
I _d	Quiescent current	T _J = 125 °C			6	mA
41		I _O = 1 to 40 mA			0.2	
Δl_d	Quiescent current change	V _I = 28 to 38 V			1.5	mA
eN	Output noise voltage	B = 10 Hz to 100 kHz, T _J = 25 °C		200		μV
C) /D	Supply voltage rejection	V _I = 29 to 35 V, f = 120 Hz	30	37		40
SVR		I_O = 40 mA, T_J = 25 °C				dB
V _d	Dropout voltage			1.7		V

Table 12. Electrical characteristics of L78L33AB and L78L33AC - Refer to the test circuits, T_J = 0 to 125 °C (AC) T_J = -40 to 125 °C (AB), V_I = 8.3 V, I_O = 40mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _O	Output voltage	T _J = 25 °C	3.168	3.3	3.432	V
V	Outrot valtage	I _O = 1 to 40 mA, V _I = 5.3 to 20 V	3.135		3.465	
V _O	Output voltage	I _O = 1 to 70 mA, V _I = 8.3 V	3.135		3.465	V
41/	l in a manufation	V _I = 5.4 to 20 V, T _J = 25 °C			150	mV
ΔV_{O}	Line regulation	V _I = 6.3 to 20 V, T _J = 25 °C			100	mv
41/	Load regulation	I _O = 1 to 100 mA, T _J = 25 °C			60	mV
ΔV_{O}		I _O = 1 to 40 mA, T _J = 25 °C			30	
	Out and assessed	T _J = 25 °C			6	mA
I _d	Quiescent current	T _J = 125 °C			5.5	mA
Al		I _O = 1 to 40 mA			0.1	
ΔI_d	Quiescent current change	V _I = 6.3 to 20 V			1.5	mA
eN	Output noise voltage	B = 10 Hz to 100 kHz, T _J = 25 °C		40		μV
CV/D	Supply voltage rejection	V _I = 6.3 to 16.3 V, f = 120 Hz	41	49		
SVR		I_O = 40 mA, T_J = 25 °C				dB
V _d	Dropout voltage			2		V

DS0424 - Rev 27 page 9/39



Table 13. Electrical characteristics of L78L05AB and L78L05AC - Refer to the test circuits, T_J = 0 to 125 °C (AC) T_J = -40 to 125 °C (AB), V_I = 10 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _O	Output voltage	T _J = 25 °C	4.8	5	5.2	V
V-	Output voltage	I _O = 1 to 40 mA, V _I = 7 to 20 V	4.75		5.25	V
Vo	Output voltage	I _O = 1 to 70 mA, V _I = 10 V	4.75		5.25	V
AV/ -	Line we wild the	V _I = 7.3 to 20 V, T _J = 25 °C			150	
ΔV_{O}	Line regulation	V _I = 8 to 20 V, T _J = 25 °C			100	mV
41/	Land vanulation	I _O = 1 to 100 mA, T _J = 25 °C			60	
ΔV_{O}	Load regulation	I _O = 1 to 40 mA, T _J = 25 °C			30	mV
1	Outros and summer	T _J = 25 °C			6	mA
I _d	Quiescent current	T _J = 125 °C			5.5	mA
Al	Outros and support all and	I _O = 1 to 40 mA			0.1	^
$\Delta l_{\sf d}$	Quiescent current change	V _I = 8 to 20 V			1.5	mA
eN	Output noise voltage	B = 10 Hz to 100 kHz, T _J = 25 °C		40		μV
SVR	Supply voltage rejection	V _I = 8 to 18 V, f = 120 Hz I _O = 40 mA, T _J = 25 °C	41	49		dB
V _d	Dropout voltage	.0 .0 , 1, 20 0		2		V

Table 14. Electrical characteristics of L78L06AB and L78L06AC - Refer to the test circuits, T_J = 0 to 125 °C (AC) T_J = -40 to 125 °C (AB), V_I = 12 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _O	Output voltage	T _J = 25 °C	5.76	6	6.24	V
Vo	Output voltage	I_{O} = 1 to 40 mA, V_{I} = 8.5 to 20 V	5.7		6.3	V
٧٥	Output voltage	I _O = 1 to 70 mA, V _I = 12 V	5.7		6.3	V
۸۷/ -	Line regulation	V _I = 8.5 to 20 V, T _J = 25 °C			150	mV
ΔV_{O}	Line regulation	V _I = 9 to 20 V, T _J = 25 °C			100	mv
۸۷/ -	L and unavidation	I _O = 1 to 100 mA, T _J = 25 °C			60	
ΔV_{O}	Load regulation	I _O = 1 to 40 mA, T _J = 25 °C			30	mV
	Quiescent current	T _J = 25 °C			6	mA
I _d		T _J = 125 °C			5.5	mA
AI	Outless and support all and	I _O = 1 to 40 mA			0.1	
ΔI_d	Quiescent current change	V _I = 9 to 20 V			1.5	mA
eN	Output noise voltage	B = 10 Hz to 100 kHz, T _J = 25 °C		50		μV
CVD	Complete and animation	V _I = 9 to 20 V, f = 120 Hz	20	40		40
SVR	Supply voltage rejection	I_O = 40 mA, T_J = 25 °C	39	46		dB
V _d	Dropout voltage			1.7		V

DS0424 - Rev 27 page 10/39



Table 15. Electrical characteristics of L78L08AB and L78L08AC - Refer to the test circuits, T_J = 0 to 125 °C (AC) T_J = -40 to 125 °C (AB), V_I = 14 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Vo	Output voltage	T _J = 25 °C	7.68	8	8.32	V
	Outrot walta as	I _O = 1 to 40 mA, V _I = 10.5 to 23 V	7.6		8.4	V
Vo	Output voltage	I _O = 1 to 70 mA, V _I = 14 V	7.6		8.4	V
A \ /	Line new detter	V _I = 10.5 to 23 V, T _J = 25 °C			175	>/
ΔV_{O}	Line regulation	V _I = 11 to 23 V, T _J = 25 °C			125	mV
A) /	ΔV_{O} Load regulation	I _O = 1 to 100 mA, T _J = 25 °C			80	
ΔνΟ		I _O = 1 to 40 mA, T _J = 25 °C			40	mV
		T _J = 25 °C			6	mA
I _d	Quiescent current	T _J = 125 °C			5.5	mA
A.I.		I _O = 1 to 40 mA			0.1	
Δl_{d}	Quiescent current change	V _I = 11 to 23 V			1.5	mA
eN	Output noise voltage	B = 10 Hz to 100 kHz, T _J = 25 °C		60		μV
SVR	Supply voltage rejection	V_I = 12 to 23 V, f = 120 Hz I_O = 40 mA, T_J = 25 °C	37	45		dB
V _d	Dropout voltage			1.7		V

Table 16. Electrical characteristics of L78L09AB and L78L09AC - Refer to the test circuits, T_J = 0 to 125 °C (AC) T_J = -40 to 125 °C (AB), V_I =15 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Vo	Output voltage	T _J = 25 °C	8.64	9	9.36	V
V-	Output voltage	I _O = 1 to 40 mA, V _I = 11.5 to 23 V	8.55		9.45	V
V _O	Output voltage	I _O = 1 to 70 mA, V _I = 15 V	8.55		9.45	V
A\/ -	Line regulation	V _I = 11.5 to 23 V, T _J = 25 °C			225	mV
ΔV _O	Line regulation	V _I = 12 to 23 V, T _J = 25 °C			150	IIIV
A\/ -	ΔV_{O} Load regulation	I _O = 1 to 100 mA, T _J = 25 °C			80	
ΔνΟ		I _O = 1 to 40 mA, T _J = 25 °C			40	mV
	Outre and summed	T _J = 25 °C			6	mA
I _d	Quiescent current	T _J = 125 °C			5.5	mA
A.I.	0	I _O = 1 to 40 mA			0.1	
Δl _d	Quiescent current change	V _I = 12 to 23 V			1.5	mA
eN	Output noise voltage	B = 10 Hz to 100 kHz, T _J = 25 °C		70		μV
SVR	Supply voltage rejection	V _I = 12 to 23 V, f = 120 Hz	37	44		dB
SVK	Supply voltage rejection	I_O = 40 mA, T_J = 25 °C	31	44		UD
V _d	Dropout voltage			1.7		V

DS0424 - Rev 27 page 11/39



Table 17. Electrical characteristics of L78L10AC - Refer to the test circuits, T_J = 0 to 125 °C (AC) T_J = -40 to 125 °C (AB), V_I = 16 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Vo	Output voltage	T _J = 25 °C	9.6	10	10.4	V
W	Output valta aa	I _O = 1 to 40 mA, V _I = 12.5 to 23 V	9.5		10.5	V
V _O	Output voltage	I _O = 1 to 70 mA, V _I = 16 V	9.5		10.5	V
41/	L'an annudation	V _I = 12.5 to 23 V, T _J = 25 °C			230	>/
ΔV_{O}	Line regulation	V _I = 13 to 23 V, T _J = 25 °C			170	mV
437	ΔV _O Load regulation	I _O = 1 to 100 mA, T _J = 25 °C			80	.,
ΔVO		I _O = 1 to 40 mA, T _J = 25 °C			40	mV
1		T _J = 25 °C			6	mA
Ι _d	Quiescent current	T _J = 125 °C			5.5	mA
A.I.		I _O = 1 to 40 mA			0.1	
Δl_{d}	Quiescent current change	V _I = 13 to 23 V			1.5	mA
eN	Output noise voltage	B = 10 Hz to 100 kHz, T _J = 25 °C		60		μV
SVR	Supply voltage rejection	V_I = 14 to 23 V, f = 120 Hz I_O = 40 mA, T_J = 25 °C	37	45		dB
V _d	Dropout voltage			1.7		V

Table 18. Electrical characteristics of L78L12AB and L78L12AC - Refer to the test circuits, T_J = 0 to 125 °C (AC) T_J = -40 to 125 °C (AB), V_I =19 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _O	Output voltage	T _J = 25 °C	11.5	12	12.5	V
V _O	Output voltage	I _O = 1 to 40 mA, V _I = 14.5 to 27 V	11.4		12.6	V
٧٥	Output voltage	I _O = 1 to 70 mA, V _I = 19 V	11.4		12.6	V
۸۷/ -	Line regulation	V _I = 14.5 to 27 V, T _J = 25 °C			250	mV
ΔV_{O}	Line regulation	V _I = 16 to 27 V, T _J = 25 °C			200	IIIV
A\/ -	L and unavidation	I _O = 1 to 100 mA, T _J = 25 °C			100	
ΔV_{O}	Load regulation	I _O = 1 to 40 mA, T _J = 25 °C			50	mV
	Quiescent current	T _J = 25 °C			6.5	mA
I _d		T _J = 125 °C			6	mA
Al	Outlease at summer to the same	I _O = 1 to 40 mA			0.1	0
Δl_d	Quiescent current change	V _I = 16 to 27 V			1.5	mA
eN	Output noise voltage	B = 10 Hz to 100 kHz, T _J = 25 °C		80		μV
SVR	Supply voltage rejection	V_{I} = 15 to 25 V, f = 120 Hz I_{O} = 40 mA, T_{J} = 25 °C	37	42		dB
V _d	Dropout voltage			1.7		V

DS0424 - Rev 27 page 12/39



Table 19. Electrical characteristics of L78L15AB and L78L15AC - Refer to the test circuits, T_J = 0 to 125 °C (AC) T_J = -40 to 125 °C (AB), V_I = 23 V_I 0= 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Vo	Output voltage	T _J = 25 °C	14.4	15	15.6	V
W	Outrot walta as	I _O = 1 to 40 mA, V _I = 17.5 to 30 V	14.25		15.75	V
Vo	Output voltage	I _O = 1 to 70 mA, V _I = 23 V	14.25		15.75	V
41/	Line new detter	V _I = 17.5 to 30 V, T _J = 25 °C			300	>/
ΔV_{O}	Line regulation	V _I = 20 to 30 V, T _J = 25 °C			250	mV
437		I _O = 1 to 100 mA, T _J = 25 °C			150	>/
ΔV_{O}	Load regulation	I _O = 1 to 40 mA, T _J = 25 °C			75	mV
		T _J = 25 °C			6.5	mA
I _d	Quiescent current	T _J = 125 °C			6	mA
A.I.		I _O = 1 to 40 mA			0.1	
Δl_d	Quiescent current change	V _I = 20 to 30 V			1.5	mA
eN	Output noise voltage	B = 10 Hz to 100 kHz, T _J = 25 °C		90		μV
SVR	Supply voltage rejection	V_I = 18.5 to 28.5 V, f = 120 Hz I_O = 40 mA, T_J = 25 °C	34	39		dB
V _d	Dropout voltage			1.7		V

Table 20. Electrical characteristics of L78L18AC - Refer to the test circuits, T_J = 0 to 125 °C (AC) T_J = -40 to 125 °C (AB), V_I = 27 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _O	Output voltage	T _J = 25 °C	17.3	18	18.7	V
V	Outrut valtage	I _O = 1 to 40 mA, V _I = 22 to 33 V	17.1		18.9	V
V _O	Output voltage	I _O = 1 to 70 mA, V _I = 27 V	17.1		18.9	V
437	Line we wide the	V _I = 22 to 33 V, T _J = 25 °C			320	>/
ΔV_{O}	Line regulation	V _I = 22 to 33 V, T _J = 25 °C			270	mV
AV/	ΔV _O Load regulation	I _O = 1 to 100 mA, T _J = 25 °C			170	
ΔνΟ		I _O = 1 to 40 mA, T _J = 25 °C			85	mV
		T _J = 25 °C			6.5	mA
I _d	Quiescent current	T _J = 125 °C			6	mA
A.I.		I _O = 1 to 40 mA			0.1	
ΔI_d	Quiescent current change	V _I = 23 to 33 V			1.5	mA
eN	Output noise voltage	B = 10 Hz to 100 kHz, T _J = 25 °C		120		μV
CVD	Complete and an institute	V _I = 23 to 33 V, f = 120 Hz	22	20		-10
SVR	Supply voltage rejection	voltage rejection $I_O = 40 \text{ mA}, T_J = 25 \text{ °C}$	33	38		dB
V _d	Dropout voltage			1.7		V

DS0424 - Rev 27 page 13/39



Table 21. Electrical characteristics of L78L24AB and L78L24AC - Refer to the test circuits, T_J = 0 to 125 °C (AC) T_J = -40 to 125 °C (AB), V_I = 33 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _O	Output voltage	T _J = 25 °C	23	24	25	V
V	Outrut valtaria	I _O = 1 to 40 mA, V _I = 27 to 38 V	22.8		25.2	V
V _O	Output voltage	I _O = 1 to 70 mA, V _I = 33 V	22.8		25.2	V
41/	Line we wide the	V _I = 27 to 38 V, T _J = 25 °C			350	>/
ΔV _O	Line regulation	V _I = 28 to 38 V, T _J = 25 °C			300	mV
ΔV_{O} Load regulation		I _O = 1 to 100 mA, T _J = 25 °C			200	> (
	Load regulation	I _O = 1 to 40 mA, T _J = 25 °C			100	mV
		T _J = 25 °C			6.5	mA
I _d	Quiescent current	T _J = 125 °C			6	mA
A.I.		I _O = 1 to 40 mA			0.1	
ΔI_d	Quiescent current change	V _I = 28 to 38 V			1.5	mA
eN	Output noise voltage	B = 10 Hz to 100 kHz, T _J = 25 °C		200		µV5y
SVR	Supply voltage rejection	V_I = 29 to 33 V, f = 120 Hz I_O = 40 mA, T_J = 25 °C	31	37		dB
V _d	Dropout voltage			1.7		V

DS0424 - Rev 27 page 14/39



5 Typical performance

Figure 4. L78L05/12 output voltage vs. ambient temperature

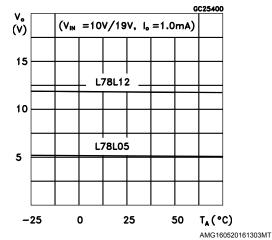


Figure 5. L78L05/12/24 load characteristics

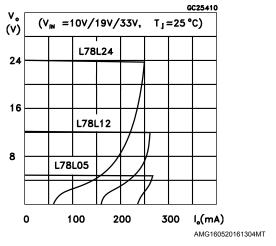


Figure 6. L78L05/12/24 thermal shutdown

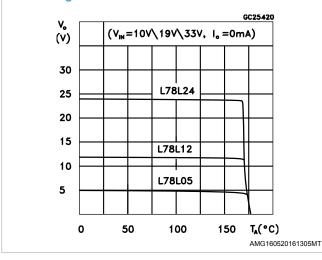
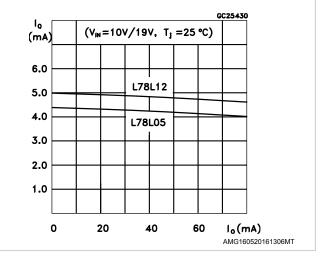


Figure 7. L78L05/12 quiescent current vs. output current



DS0424 - Rev 27 page 15/39



Figure 8. L78L05 quiescent current vs. input voltage

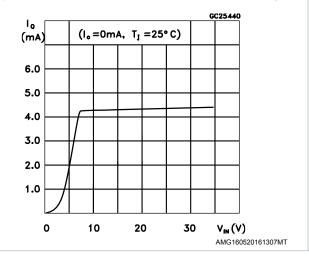


Figure 9. L78L05/12/24 output characteristics

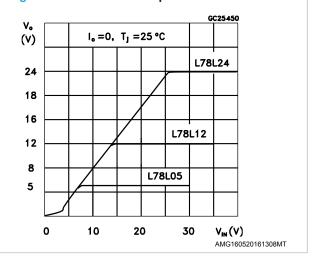


Figure 10. L78L05/12/24 ripple rejection

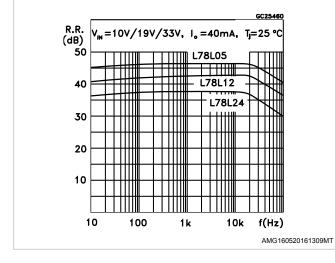


Figure 11. L78L05 dropout characteristics

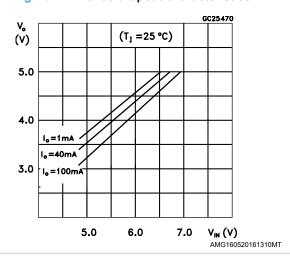
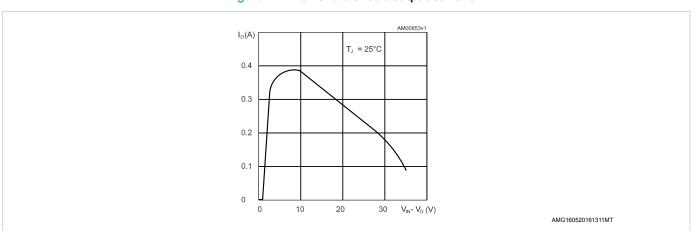


Figure 12. L78L short-circuit output current



DS0424 - Rev 27 page 16/39



6 Typical application

Figure 13. High output current short-circuit protected

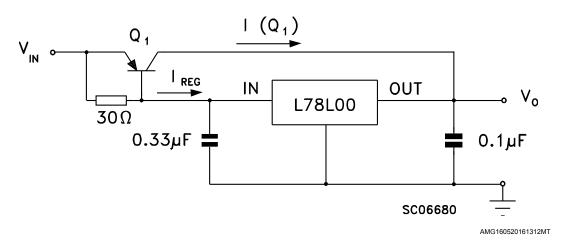
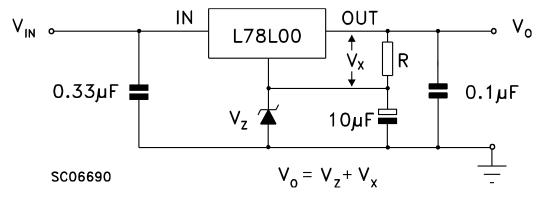
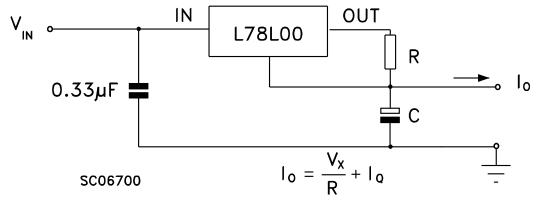


Figure 14. Outuput boost circuit



AMG160520161313MT

Figure 15. Current regulator

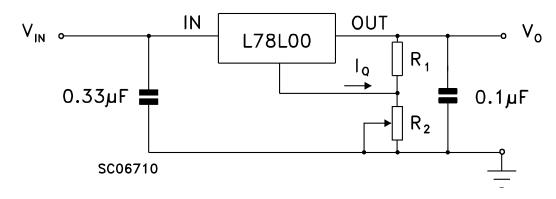


AMG160520161314MT

DS0424 - Rev 27 page 17/39



Figure 16. Adjustable output regulator



AMG160520161315MT

DS0424 - Rev 27 page 18/39

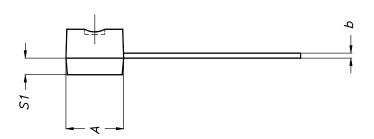


7 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

7.1 TO-92 package information

Figure 17. TO-92 package outline



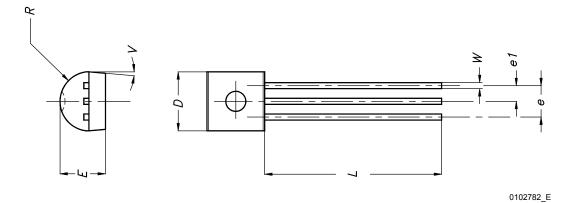


Table 22. TO-92 mechanical data

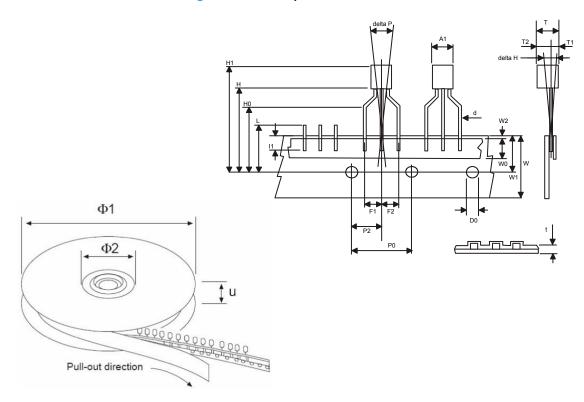
Dim.		mm	
Dilli.	Min.	Тур.	Max.
А	4.32		4.95
b	0.36		0.51
D	4.45		4.95
E	3.30		3.94
е	2.41		2.67
e1	1.14		1.40
L	12.70		15.49
R	2.16		2.41
S1	0.92		1.52
W	0.41		0.56
V		5°	

DS0424 - Rev 27 page 19/39



7.2 TO-92 packing information

Figure 18. TO-92 tape and reel outline



DS0424 - Rev 27 page 20/39



Table 23. TO-92 tape and reel mechanical data

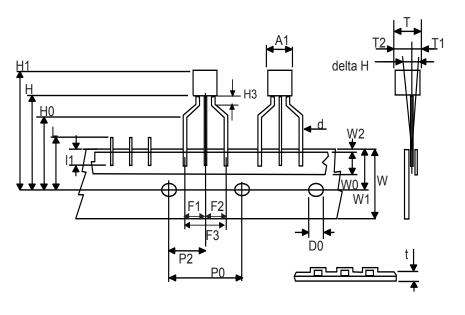
Div		mm	
Dim.	Min.	Тур.	Max.
A1			4.80
Т			3.80
T1			1.60
T2			2.30
d	0.45	0.47	0.48
P0	12.50	12.70	12.90
P2	5.65	6.35	7.05
F1, F2	2.40	2.50	2.94
F3	4.98	5.08	5.48
delta H	-2.00		2.00
W	17.50	18.00	19.00
W0	5.5	6.00	6.5
W1	8.50	9.00	9.25
W2			0.50
Н		18.50	21
H3	0.5	1	2
H0	15.50	16.00	18.8
H1		25.0	27.0
D0	3.80	4.00	4.20
t			0.90
L			11.00
I1	3.00		
delta P	-1.00		1.00
Ø1	352	355	358
Ø2	28	30	32
u	44	47	50

DS0424 - Rev 27 page 21/39



7.3 TO-92 Ammopak packing information

Figure 19. TO-92 Ammopak tape and reel outline



0050910S_Rev_U

Table 24. TO-92 Ammopak tape and reel mechanical data

Dim.		mm	
Dilli.	Min.	Тур.	Max.
A1			4.80
Т			3.80
T1			1.60
T2			2.30
d	0.45	0.47	0.48
P0	12.50	12.70	12.90
P2	5.65	6.35	7.05
F1, F2	2.40	2.50	2.94
F3	4.98	5.08	5.48
delta H	-2.00		2.00
W	17.50	18.00	19.00
W0	5.5	6.00	6.5
W1	8.50	9.00	9.25
W2			0.50
Н		18.50	21
H3	0.5	1	2
H0	15.50	16.00	18.8
H1		25.0	27.0
D0	3.80	4.00	4.20

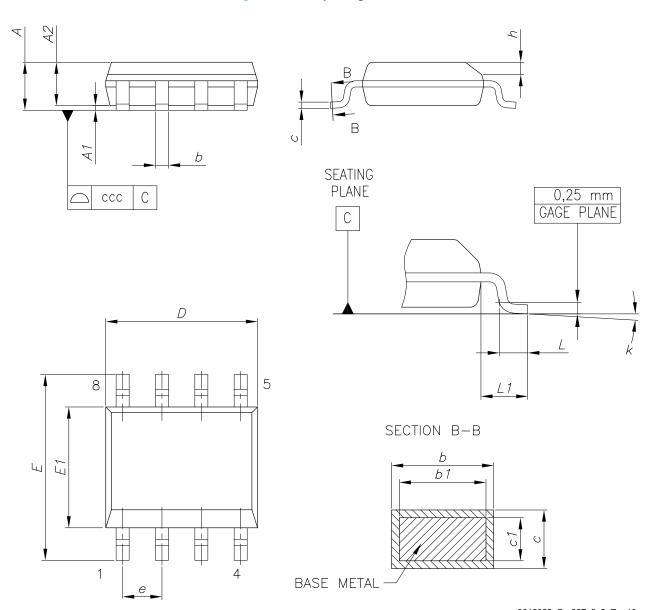
DS0424 - Rev 27 page 22/39



Dim.	mm		
	Min.	Typ.	Max.
t			0.90
L			11.00
I1	3.00		
delta P	-1.00		1.00

7.4 SO-8 package information

Figure 20. SO-8 package outline



0016023_So-807_fig2_Rev10

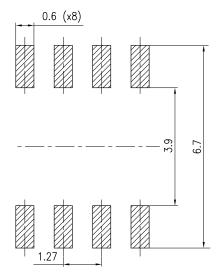
DS0424 - Rev 27 page 23/39



Table 25. SO-8 mechanical data

Dim		mm	
	Min.	Тур.	Max.
А			1.75
A1	0.10		0.25
A2	1.25		
b	0.31		0.51
b1	0.28		0.48
С	0.10		0.25
c1	0.10		0.23
D	4.80	4.90	5.00
E	5.80	6.00	6.20
E1	3.80	3.90	4.00
е		1.27	
h	0.25		0.50
L	0.40		1.27
L1		1.04	
L2		0.25	
k	0°		8°
ccc			0.10

Figure 21. SO-8 recommended footprint (dimensions are in mm)



0016023_So-807_footprint_Rev10

DS0424 - Rev 27 page 24/39



7.5 SO-8 packing information

Figure 22. SO-8 tape and reel dimensions

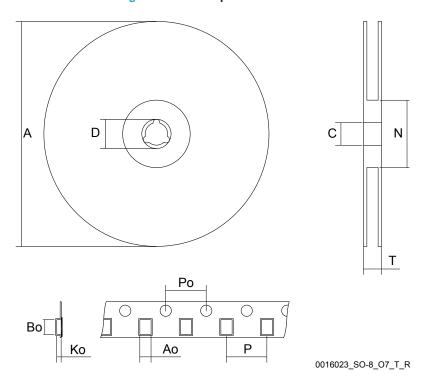
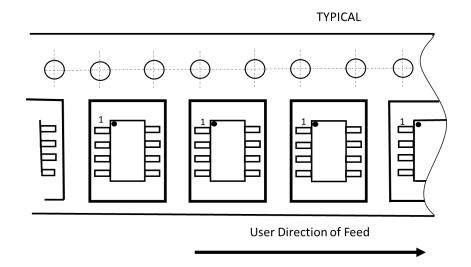


Figure 23. Tape orientation



DS0424 - Rev 27 page 25/39



Table 26. SO-8 tape and reel mechanical data

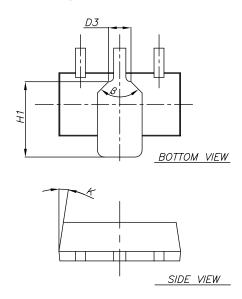
Dim.		mm	
	Min.	Тур.	Max.
Α			330
С	12.8		13.2
D	20.2		
N	60		
Т			22.4
Ao	6.5	-	6.7
Во	5.4		5.6
Ko	2.0		2.2
Po	3.9		4.1
Р	7.9		8.1

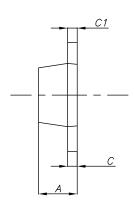
DS0424 - Rev 27 page 26/39

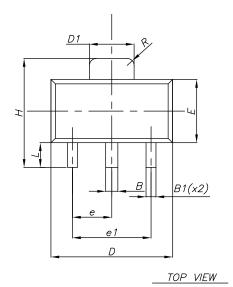


7.6 SOT-89 package information

Figure 24. SOT-89 package outline







7098166_REV_F

DS0424 - Rev 27 page 27/39



Table 27. SOT-89 mechanical data

Dim		mm	
	Min.	Тур.	Max.
A	1.40		1.60
В	0.44		0.56
B1	0.36		0.48
С	0.35		0.44
C1	0.35		0.44
D	4.40		4.60
D1	1.62		1.83
D3		0.90	
E	2.29		2.60
е	1.42		1.57
e1	2.92		3.07
Н	3.94		4.25
H1	2.70		3.10
K	1°		8°
L	0.89		120
R		0.25	
β		90°	

DS0424 - Rev 27 page 28/39

Footprint

page 29/39



DS0424 - Rev 27

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Figure 25. SOT-89 recommended footprint



7.7 SOT-89 packing information

Figure 26. SOT-89 carrier tape outline

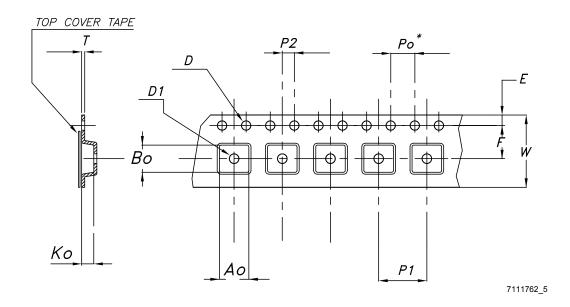
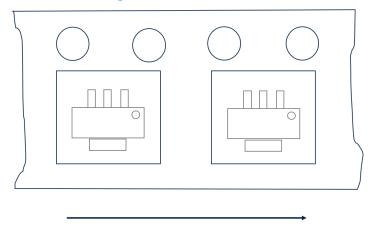


Figure 27. SOT-89 device orientation



User direction of feed

DS0424 - Rev 27 page 30/39



Table 28. SOT-89 carrier tape mechanical data

Dim.		mm
Dilli.	Value	Tolerance
Ao	4.91	± 0.10
Во	4.52	± 0.10
Ко	1.90	± 0.10
F	5.50	± 0.10
E	1.75	± 0.10
W	12	± 0.30
P2	2	± 0.10
Po	4	± 0.10
P1	8	± 0.10
Т	0.30	± 0.10
D	Ø 1.55	± 0.05
D1	Ø 1.60	± 0.10

DS0424 - Rev 27 page 31/39



8 Ordering information

Table 29. Order codes

		Part numbers			Output
SO-8	TO-92 (Bag) ⁽¹⁾	TO-92 (ammopack)	TO-92 (tape and reel)	SOT-89	voltages (V)
L78L33ABD-TR		L78L33ABZ-AP		L78L33ABUTR	3.3
L78L33ACD13TR	L78L33ACZ	L78L33ACZ-AP	L78L33ACZTR	L78L33ACUTR	3.3
L78L33CD-TR					3.3
L78L05ABD13TR	L78L05ABZ	L78L05ABZ-AP	L78L05ABZ-TR	L78L05ABUTR	5
L78L05ACD13TR	L78L05ACZ	L78L05ACZ-AP	L78L05ACZTR	L78L05ACUTR	5
L78L05CD13TR	L78L05CZ				5
	L78L06ABZ			L78L06ABUTR	6
L78L06ACD13TR				L78L06ACUTR	6
L78L08ABD13TR		L78L08ABZ-AP	L78L08ABZTR	L78L08ABUTR	8
L78L08ACD13TR	L78L08ACZ	L78L08ACZ-AP	L78L08ACZTR	L78L08ACUTR	8
L78L08CD13TR					8
L78L09ABD13TR	L78L09ABZ			L78L09ABUTR	9
L78L09ACD13TR		L78L09ACZ-AP	L78L09ACZ-TR	L78L09ACUTR	9
L78L09CD13TR					9
				L78L10ACUTR	10
L78L12ABD-TR	L78L12ABZ	L78L12ABZ-AP		L78L12ABUTR	12
L78L12ACD13TR	L78L12ACZ	L78L12ACZ-AP	L78L12ACZ-TR	L78L12ACUTR	12
L78L12CD13TR					12
		L78L15ABZ-AP		L78L15ABUTR	15
L78L15ACD13TR	L78L15ACZ			L78L15ACUTR	15
L78L15CD-TR					15
				L78L18ACUTR	18
L78L18CD13TR					18
	L78L24ABZ-TR				24
			L78L24ACZ-AP	L78L24ACUTR	24
L78L24CD-TR					24

^{1.} Available in Ammopak with the suffix "-AP" or in tape and reel with the suffix "TR". Please note that in these cases pins are shaped according to tape and reel specifications.

DS0424 - Rev 27 page 32/39



Table 30. Marking information

Part numbers	Marking	Packages	Output voltages
L78L05ABD13TR	78L05B	SO-8	5 V
L78L05ABUTR	8C	SOT-89	5 V
L78L05ABZ	L78L05A	TO-92	5 V
L78L05ABZ-AP	L78L05A	TO-92	5 V
L78L05ABZ-TR	L78L05A	TO-92	5 V
L78L05ACD13TR	78L05A	SO-8	5 V
L78L05ACUTR	8C	SOT-89	5 V
L78L05ACZ	L78L05A	TO-92	5 V
L78L05ACZ-AP	L78L05A	TO-92	5 V
L78L05ACZTR	L78L05A	TO-92	5 V
L78L05CD13TR	78L05	SO-8	5 V
L78L05CZ	L78L05A	TO-92	5 V
L78L06ABUTR	8E	SOT-89	6 V
L78L06ABZ	L78L06A	TO-92	6 V
L78L06ACD13TR	L78L06A	SO-8	6 V
L78L06ACUTR	8E	SOT-89	6 V
L78L08ABD13TR	78L08B	SO-8	8 V
L78L08ABUTR	8G	SOT-89	8 V
L78L08ABZ-AP	L78L08A	TO-92	8 V
L78L08ABZTR	L78L08A	TO-92	8 V
L78L08ACD13TR	78L08A	SO-8	8 V
L78L08ACUTR	8G	SOT-89	8 V
L78L08ACZ	L78L08A	TO-92	8 V
L78L08ACZ-AP	L78L08A	TO-92	8 V
L78L08ACZTR	L78L08A	TO-92	8 V
L78L08CD13TR	78L08	SO-8	8 V
L78L09ABD13TR	78L09B	SO-8	8 V
L78L09ABZ	L78L09A	TO-92	9 V
L78L09ABUTR	8H	SOT-89	9 V
L78L09ACD13TR	78L09A	SO8	9 V
L78L09ACUTR	8H	SOT-89	9 V
L78L09ACZ-AP	L78L09A	TO-92	9 V
L78L09ACZ-TR	L78L09A	TO-92	9 V
L78L09CD13TR	L78L09A	SO-8	9 V
L78L10ACUTR	81	SOT-89	10 V
L78L12ABD-TR	78L12B	SO-8	12 V
L78L12ABUTR	8K	SOT-89	12 V
L78L12ABZ	L78L12A	TO-92	12 V
L78L12ABZ-AP	L78L12A	TO 92	12 V
L78L12ACD13TR	78L12A	SO-8	12 V

DS0424 - Rev 27 page 33/39



Part numbers	Marking	Packages	Output voltages
L78L12ACUTR	8K	SOT-89	12 V
L78L12ACZ	L78L12A	TO-92	12 V
L78L12ACZ-AP	L78L12A	TO-92	12 V
L78L12ACZ-TR	L78L12A	TO-92	12 V
L78L12CD13TR	78L12	SO-8	12 V
L78L15ABUTR	8L	SOT-89	15 V
L78L15ABZ-AP	L78L15A	TO-92	15 V
L78L15ACD13TR	78L15A	SO-8	15 V
L78L15ACUTR	8L	SOT-89	15 V
L78L15ACZ	L78L15A	TO-92	15 V
L78L15CD-TR	78L15	SO-8	15 V
L78L18ACUTR	8B	SOT-89	18 V
L78L18CD13TR	L78L18	SO-8	18 V
L78L24ABZ-TR	L78L24A	TO-92	24 V
L78L24ACUTR	8P	SOT-89	24 V
L78L24ACZ-AP	L78L24A	TO-92	24 V
L78L24CD-TR	78L24	SO-8	24 V
L78L33ABD-TR	78L33B	SO-8	3.3 V
L78L33ABUTR	8A	SOT-89	3.3 V
L78L33ABZ-AP	L78L33A	TO-92	3.3 V
L78L33ACD13TR	78L33A	SO-8	3.3 V
L78L33ACUTR	8A	SOT-89	3.3 V
L78L33ACZ	L78L33A	TO-92	3.3 V
L78L33ACZ-AP	L78L33A	TO-92	3.3 V
L78L33ACZTR	L78L33A	TO-92	3.3 V

DS0424 - Rev 27 page 34/39



Revision history

Table 31. Document revision history

Date	Revision	Changes
14-Mar-2005	9	Add tape and reel for TO-92.
15-Mar-2005	10	Add note on Table 3.
23-Dec-2005	11	Mistake on ordering Table in header.
12-Sep-2006	12	Order codes updated.
07-Jun-2007	13	Order codes updated.
18-Sep-2007	14	Added Table 1 in cover page.
15-Jul-2008	15	Modified: Table 1 and Table 28: Order codes.
18-Aug-2008	16	Modified Figure 12 on page 26.
03-Apr-2009	17	Added: RthJA value for SOT-89 Table 2 on page 5.
08-Feb-2011	18	Added note Table 26 on page 39
21-Feb-2012	19	Modified: SOT-89 Figure 2 on page 4.
14 Aug 2012	20	Updated TOP value for L78L00AC in Table 1 on page 5.
14-Aug-2012	20	Minor text changes.
07-Sep-2012	21	Added: Table 29: Marking information.
		Part numbers L78LxxAB, L78LxxAC, L78LxxC changed to L78L.
		Removed Table1: Device summary.
14-Apr-2014	22	Updated features and description in cover page, Table 28: Order codes, Table 29: Marking information and Section 7: Package information.
		Added Section: Packing mechanical data.
		Minor text changes.
06-Oct-2014	23	Updated Table 28: Order codes and Table 29: Marking information.
		Minor text changes.
10-Feb-2015	24	Updated Table 29: Marking information.
		Minor text changes.
10-Feb-2016	25	Updated Table 29: Marking information and Section 7.7: SOT-89 packing information.
		Minor text changes.
21-Jun-2016	26	Changed test condition values for the parameter "Line regulation"and the typical value for the parameter "Dropout voltage" in : Table 3: "Electrical characteristics of L78L33C", Table 4: "Electrical characteristics of L78L05C", Table 12: "Electrical characteristics of L78L33AB and L78L33AC" and Table 13: "Electrical characteristics of L78L05AB and L78L05AC".
		Minor text changes.
30-Nov-2020	27	Added Figure 27. SOT-89 device orientation.

DS0424 - Rev 27 page 35/39



Contents

1	Diag	gram	
2	Pin	configuration	3
3	Max	kimum ratings	4
4	Elec	ctrical characteristics	5
5	Тур	ical performance	15
6	Тур	ical application	17
7	Pac	kage information	19
	7.1	TO-92 package information	19
	7.2	TO-92 packing information	20
	7.3	TO-92 Ammopak packing information	
	7.4	SO-8 package information	23
	7.5	SO-8 packing information	25
	7.6	SOT-89 package information	27
	7.7	SOT-89 packing information	30
8	Ord	ering information	32
Rev	ision	history	35
Con	tents	S	36
List	of ta	bles	37
List	of fig	gures	38



List of tables

Table 1.	Absolute maximum ratings
Table 2.	Thermal data4
Table 3.	Electrical characteristics of L78L33C - Refer to the test circuits, T_J = 0 to 125 °C, V_I = 8.3 V, I_O = 40 mA, C_I = 0.33
	μ F, C _O = 0.1 μ F unless otherwise specified
Table 4.	Electrical characteristics of L78L05C - Refer to the test circuits, T_J = 0 to 125 °C, V_I = 10 V, I_O = 40 mA, C_I = 0.33
	μ F, C _O = 0.1 μ F unless otherwise specified
Table 5.	Electrical characteristics of L78L08C - Refer to the test circuits, T_J = 0 to 125 °C, V_I = 14 V, I_O = 40 mA, C_I = 0.33 μ F,
	C _O = 0.1 μF unless otherwise specified
Table 6.	Electrical characteristics of L78L09C - Refer to the test circuits, T _L = 0 to 125 °C, V _L = 15 V, I _O = 40 mA, C _L = 0.33
	μ F, C _O = 0.1 μ F unless otherwise specified
Table 7.	Electrical characteristics of L78L10C - Refer to the test circuits, $T_{LI} = 0$ to 125 °C, $V_{LI} = 16$ V, $I_{CI} = 40$ mA, $C_{LI} = 0.33$
	μ F, C_0 = 0.1 μ F unless otherwise specified
Table 8.	Electrical characteristics of L78L12C - Refer to the test circuits, $T_{.l}$ = 0 to 125 °C, V_{l} = 19 V, I_{O} = 40 mA, C_{l} = 0.33
Table 6.	μ F, C _O = 0.1 μ F unless otherwise specified
Table 9.	Electrical characteristics of L78L15C - Refer to the test circuits, $T_{LI} = 0$ to 125 °C, $V_{LI} = 23$ V, $I_{CI} = 40$ mA, $C_{LI} = 0.33$
Table 3.	μ F, C _O = 0.1 μ F unless otherwise specified
Table 10.	Electrical characteristics of L78L18C - Refer to the test circuits, $T_J = 0$ to 125 °C, $V_I = 27$ V, $I_O = 40$ mA, $C_I = 0.33$
Table 10.	μ F, $C_0 = 0.1 \mu$ F unless otherwise specified
Table 44	Electrical characteristics of L78L24C - Refer to the test circuits, $T_1 = 0$ to 125 °C, $V_1 = 33$ V, $I_0 = 40$ mA, $C_1 = 0.33$
Table 11.	
T. I. I. 40	μ F, $C_0 = 0.1 \mu$ F unless otherwise specified
Table 12.	Electrical characteristics of L78L33AB and L78L33AC - Refer to the test circuits, $T_J = 0$ to 125 °C (AC) $T_J = -40$ to
	125 °C (AB), $V_1 = 8.3 \text{ V}$, $I_0 = 40 \text{mA}$, $C_1 = 0.33 \mu\text{F}$, $C_0 = 0.1 \mu\text{F}$ unless otherwise specified
Table 13.	Electrical characteristics of L78L05AB and L78L05AC - Refer to the test circuits, $T_J = 0$ to 125 °C (AC) $T_J = -40$ to
	125 °C (AB), V_I = 10 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified
Table 14.	Electrical characteristics of L78L06AB and L78L06AC - Refer to the test circuits, $T_J = 0$ to 125 °C (AC) $T_J = -40$ to
	125 °C (AB), V_I = 12 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified
Table 15.	Electrical characteristics of L78L08AB and L78L08AC - Refer to the test circuits, $T_J = 0$ to 125 °C (AC) $T_J = -40$ to
	125 °C (AB), V_I = 14 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified
Table 16.	Electrical characteristics of L78L09AB and L78L09AC - Refer to the test circuits, $T_J = 0$ to 125 °C (AC) $T_J = -40$ to
	125 °C (AB), V_I =15 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified
Table 17.	Electrical characteristics of L78L10AC - Refer to the test circuits, $T_J = 0$ to 125 °C (AC) $T_J = -40$ to 125 °C (AB), $V_I = -40$ to 125
	16 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified
Table 18.	Electrical characteristics of L78L12AB and L78L12AC - Refer to the test circuits, $T_J = 0$ to 125 °C (AC) $T_J = -40$ to
	125 °C (AB), V_I =19 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified
Table 19.	Electrical characteristics of L78L15AB and L78L15AC - Refer to the test circuits, $T_J = 0$ to 125 °C (AC) $T_J = -40$ to
	125 °C (AB), V_I = 23 V_I 0= 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified
Table 20.	Electrical characteristics of L78L18AC - Refer to the test circuits, T_J = 0 to 125 °C (AC) T_J = -40 to 125 °C (AB), V_I =
	27 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified
Table 21.	Electrical characteristics of L78L24AB and L78L24AC - Refer to the test circuits, T_J = 0 to 125 °C (AC) T_J = -40 to
	125 °C (AB), V_I = 33 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F unless otherwise specified
Table 22.	TO-92 mechanical data
Table 23.	TO-92 tape and reel mechanical data
Table 24.	TO-92 Ammopak tape and reel mechanical data
Table 25.	SO-8 mechanical data
Table 26.	SO-8 tape and reel mechanical data
Table 27.	SOT-89 mechanical data
Table 28.	SOT-89 carrier tape mechanical data
Table 29.	Order codes
Table 30.	Marking information
Table 31.	Document revision history

DS0424 - Rev 27 page 37/39



List of figures

rigure i.	Scrienalic dayram	. 4
Figure 2.	Pin connection (top view, bottom view for TO-92)	. 3
Figure 3.	Test circuits	. 3
Figure 4.	L78L05/12 output voltage vs. ambient temperature	15
Figure 5.	L78L05/12/24 load characteristics	15
Figure 6.	L78L05/12/24 thermal shutdown	15
Figure 7.	L78L05/12 quiescent current vs. output current	15
Figure 8.	L78L05 quiescent current vs. input voltage	16
Figure 9.	L78L05/12/24 output characteristics	16
Figure 10.	L78L05/12/24 ripple rejection	16
Figure 11.	L78L05 dropout characteristics	16
Figure 12.	L78L short-circuit output current	16
Figure 13.	High output current short-circuit protected	17
Figure 14.	Outuput boost circuit	17
Figure 15.	Current regulator	17
Figure 16.	Adjustable output regulator	18
Figure 17.	TO-92 package outline	19
Figure 18.	TO-92 tape and reel outline	20
Figure 19.	TO-92 Ammopak tape and reel outline	22
Figure 20.	SO-8 package outline	23
Figure 21.	SO-8 recommended footprint (dimensions are in mm)	24
Figure 22.	SO-8 tape and reel dimensions	25
Figure 23.	Tape orientation	25
Figure 24.	SOT-89 package outline	27
Figure 25.	SOT-89 recommended footprint	29
Figure 26.	SOT-89 carrier tape outline	30
Figure 27.	SOT-89 device orientation	30



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DS0424 - Rev 27 page 39/39

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L78L08ABD13TR L78L09ABD13TR L78L06ABUTR L78L24ABZ-TR L78L05ABZ-TR L78L33ABZ-AP L78L15ABZ-AP L78L12ABZ-AP L78L05ABZ-AP L78L15ACD13TR L78L12ACD13TR L78L33ACD13TR L78L05ACD13TR
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