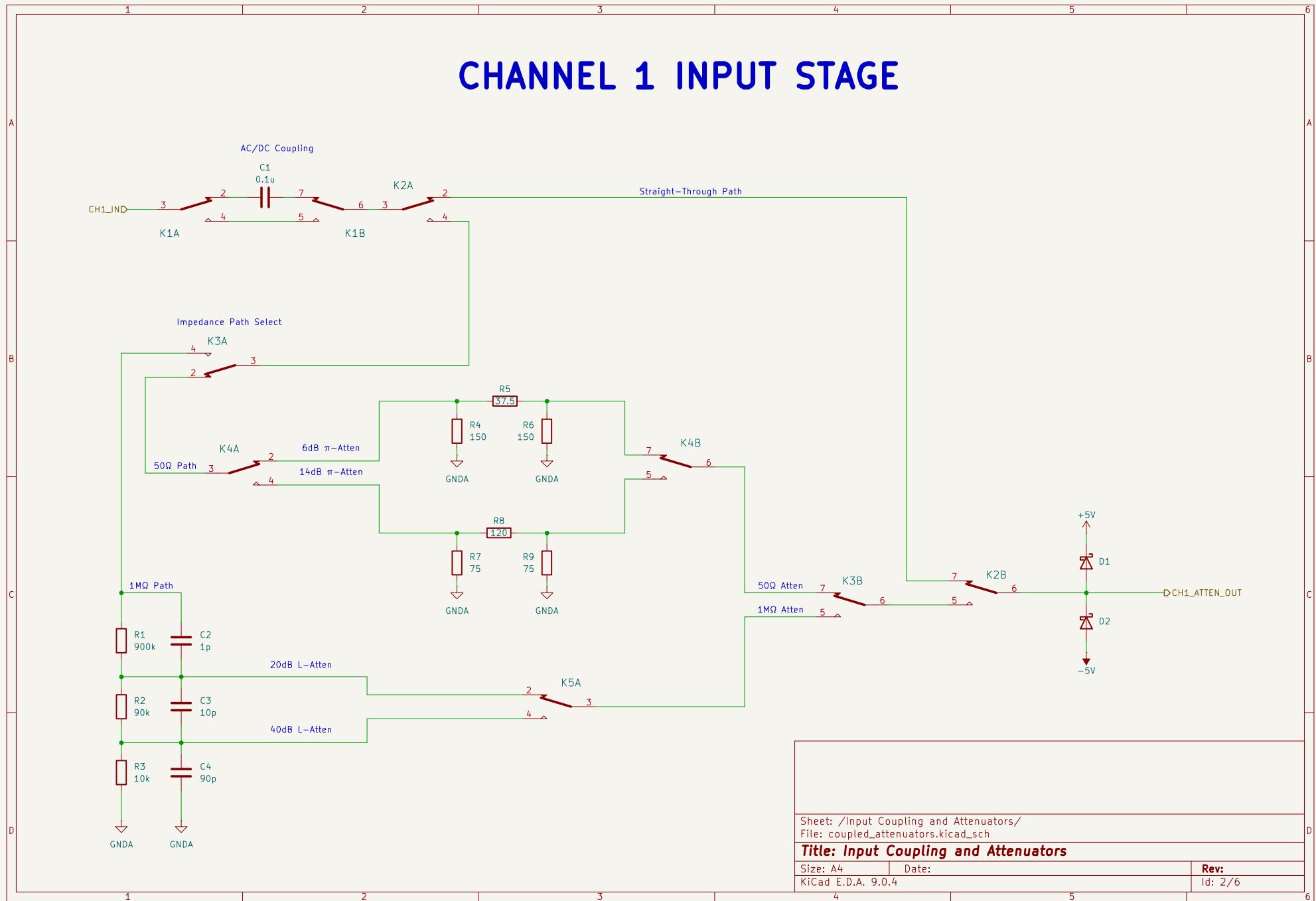
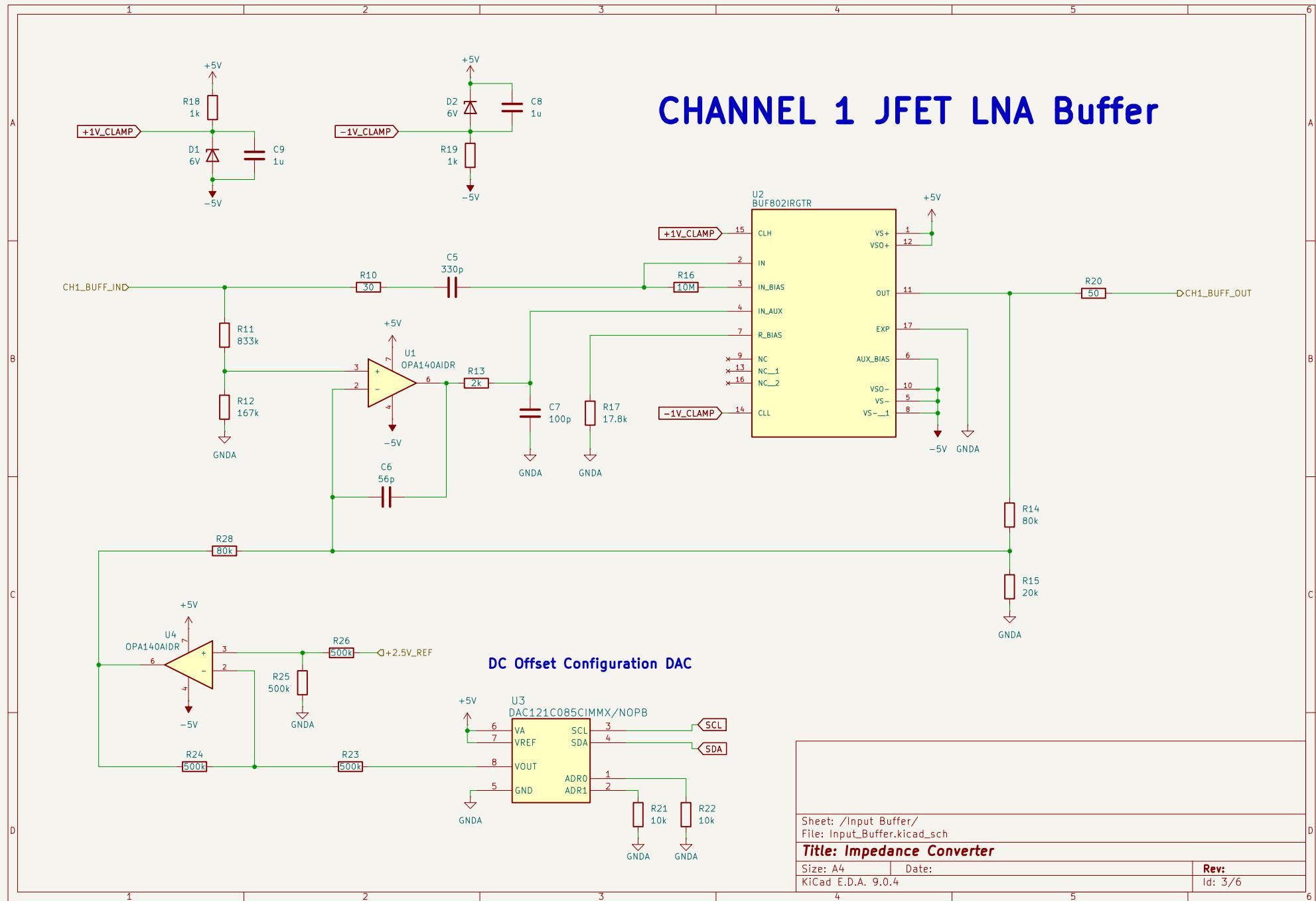


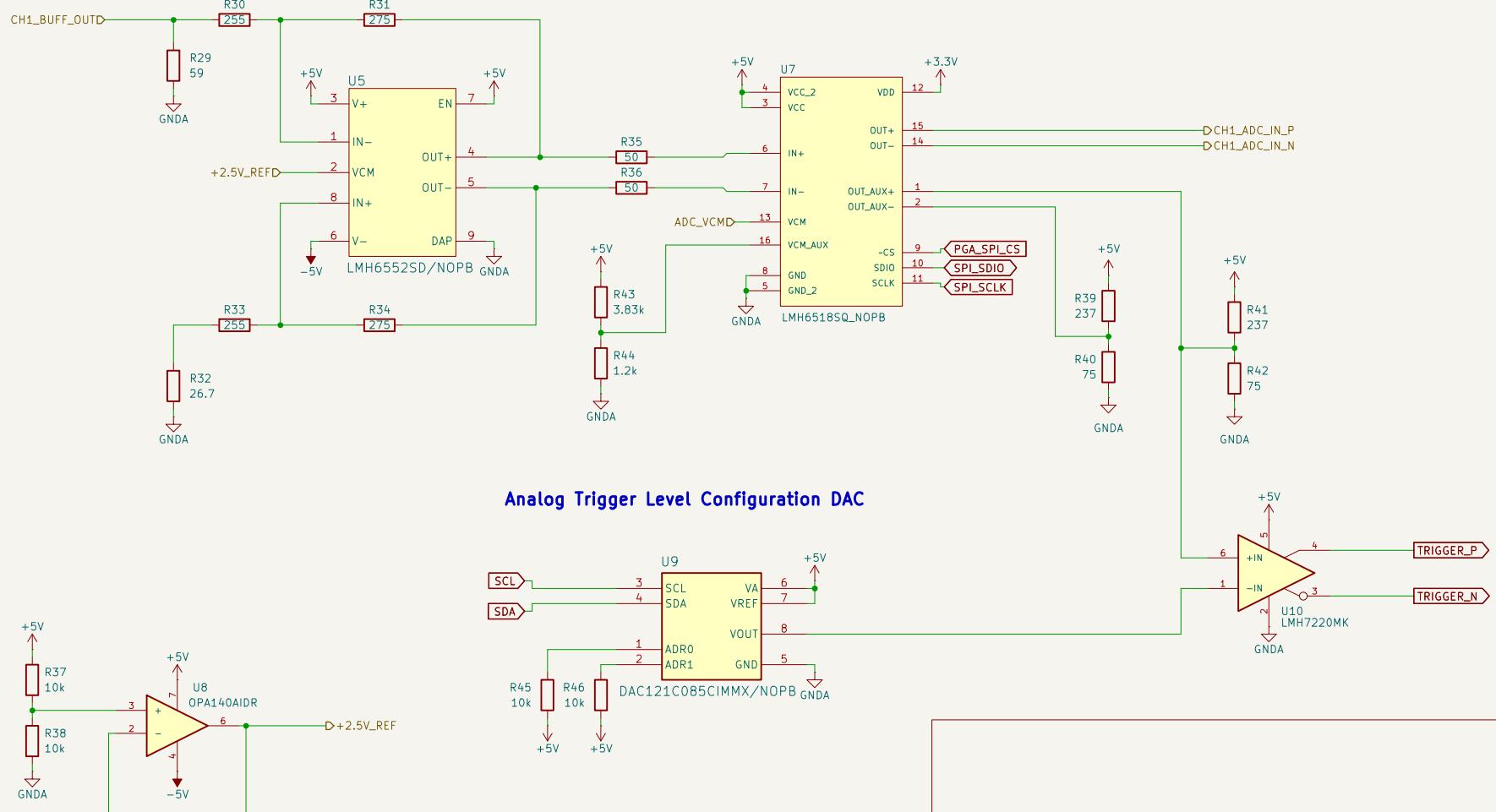
CHANNEL 1 INPUT STAGE



CHANNEL 1 JFET LNA Buffer



Level Shifter and PGA



Analog Trigger Level Configuration DAC

Sheet: /Programmable Gain Amplifier/
File: PGA.kicad_sch

Title:

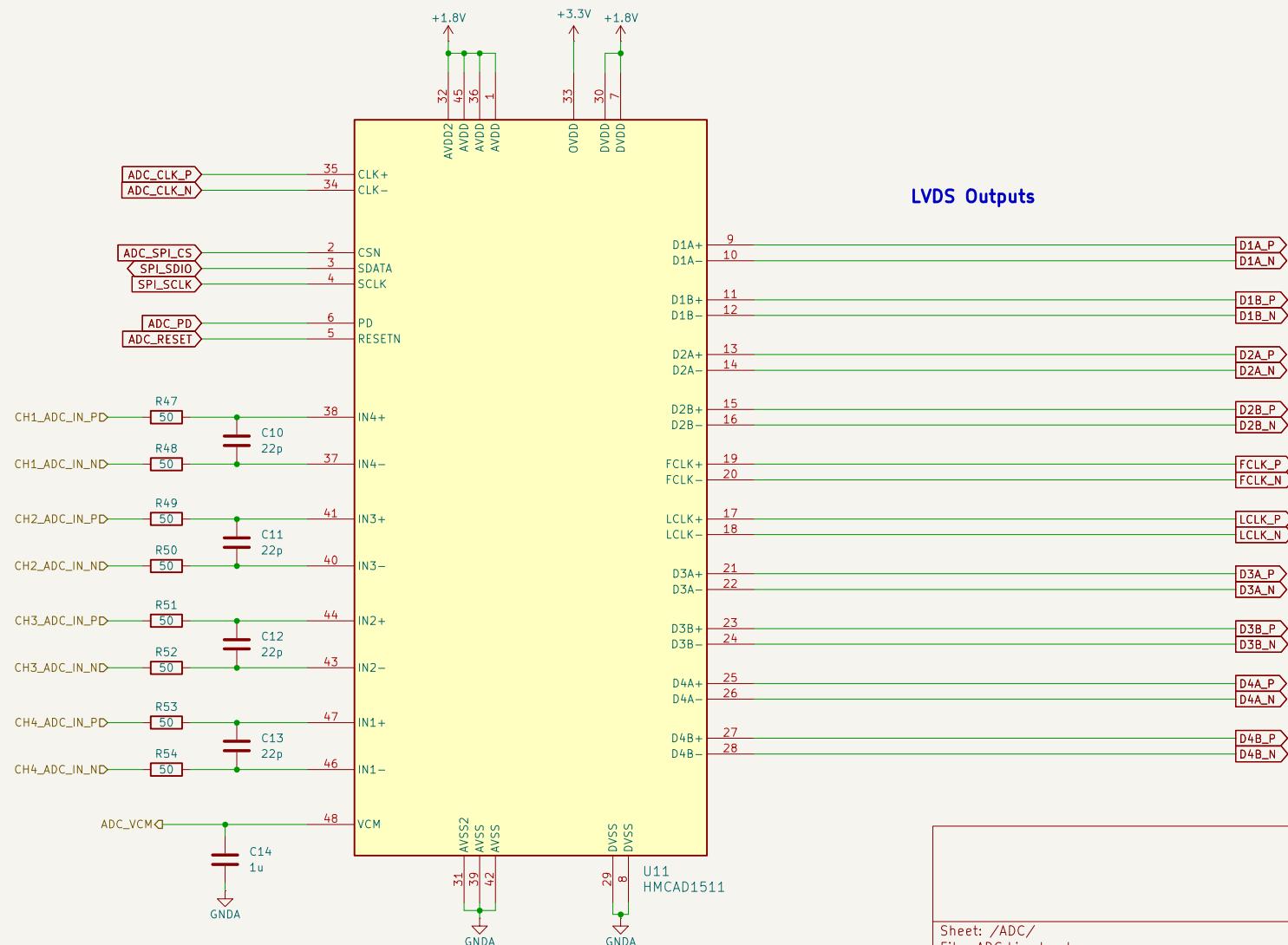
Size: A4 Date:

KiCad E.D.A. 9.0.4

Rev:

Id: 4/6

ADC



LVDS Outputs

Sheet: /ADC/
File: ADC.kicad_sch

Title:

Size: A4 Date:
KiCad E.D.A. 9.0.4

Rev:
Id: 5/6

