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Title: Intel 8086 Family User's Manual October 1979 Author: INTEL Keywords: Intel 8086 8088 8089 microprocessor Created Date: 5/8/2009 5:36:54 PM

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The Intel 8086 high performance 16-bit CPU is available in three clock rates: 5, 8 and 10 MHz. The CPU is implemented in N-Channel, depletion load, silicon gate technology (HMOS-III), and packaged in a 40-pin CERDIP or plastic package. The 8086 operates in both single processor and multiple processor configurations to achieve high performance levels.

Intel 8086, 8086-1, 8086-2 User Manual

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The 8086 (also called iAPX 86) is a 16-bit microprocessor chip designed by Intel between early 1976 and June 8, 1978, when it was released. The Intel 8088, released July 1, 1979, is a slightly modified chip with an external 8-bit data bus (allowing the use of cheaper and fewer supporting ICs), and is notable as the processor used in the original IBM PC design, including the widespread version ...

Intel 8086 - Wikipedia

Intel® 64 and IA-32 Architectures Software Developer's Manual Volume 2 (2A, 2B, 2C & 2D): Instruction Set Reference, A-Z NOTE: The Intel 64 and IA-32 Architectures Software Developer's Manual consists of three volumes:

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about this manual 1.1. overview of the intel architecture software developer's manual, volume 2: instruction set reference 1-1 1.2. overview of the intel architecture software developer's manual, volume 1: basic architecture 1-2 1.3. overview of the intel architecture software developer's manual, volume 3: system programming guide 1-3 1.4.

Volume 2: Instruction Set Reference

x86 integer instructions. This is the full 8086/8088 instruction set of Intel. Most if not all of these instructions are available in 32-bit mode; they just operate on 32-bit registers (eax, ebx, etc.) and values instead of their 16-bit (ax, bx, etc.) counterparts. See also x86 assembly language for a quick tutorial for this processor family. The updated instruction set is also grouped ...

x86 instruction listings - Wikipedia

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REG, immediate memory, REG REG, SREG zSome examples contain macros, so it is advisable to use Shift + F8 hot key to Step Over (to make macro code execute at maximum speed set step delay to zero), otherwise emulator will step through each instruction of a macro. Here is an example that uses PRINTN macro:

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