APPLE II SCHEMATICS
TABLE OF CONTENTS

Source: The Apple II Circuit Description by W. Gayler
Replication by Omega9380 - July 2022
https://github.com/omega9380/Retro-Schematics
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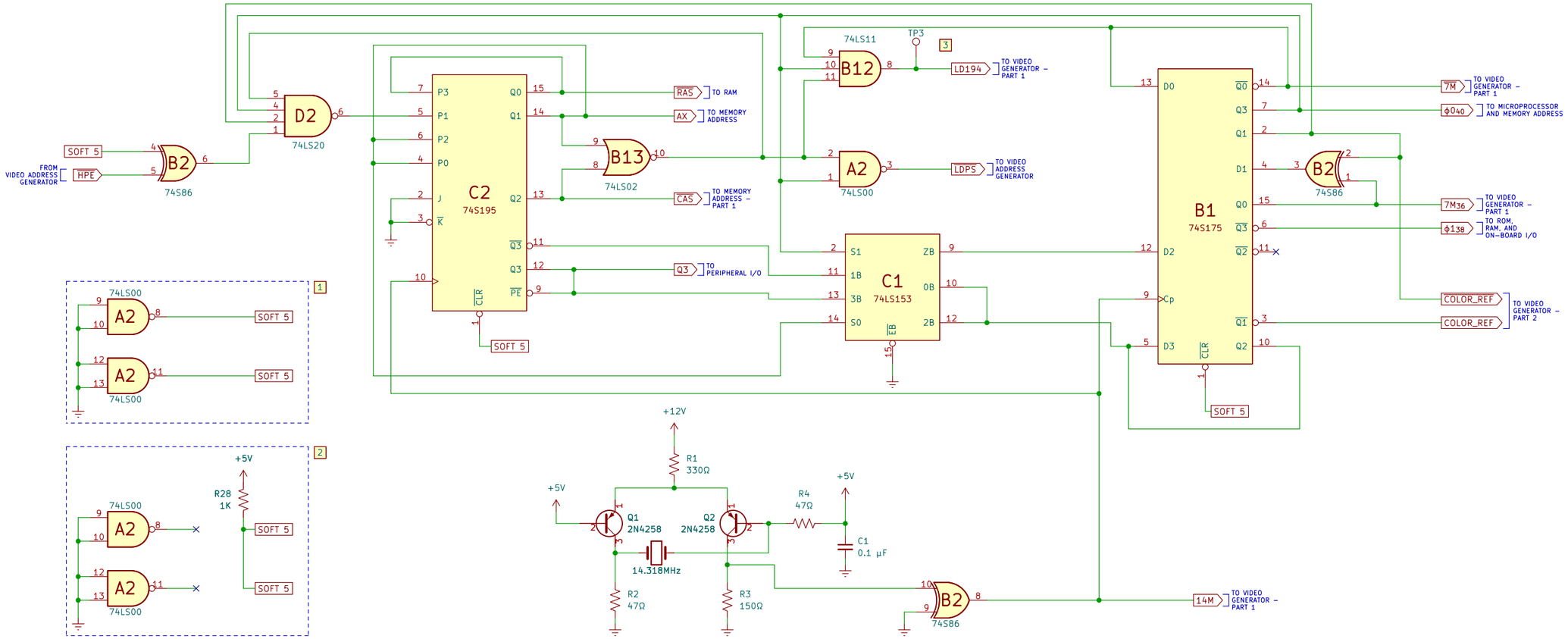
Sheet: /
File: Apple II.kicad_sch

Title: Apple II Schematic Cover Sheet

Size: A4	Date: 2022-07-11	**Rev:**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 1/23



1 CIRCUIT FOR REV. 0,1,7

2 CIRCUIT FOR REV. RFI

3 REV. 7 AND RFI ONLY

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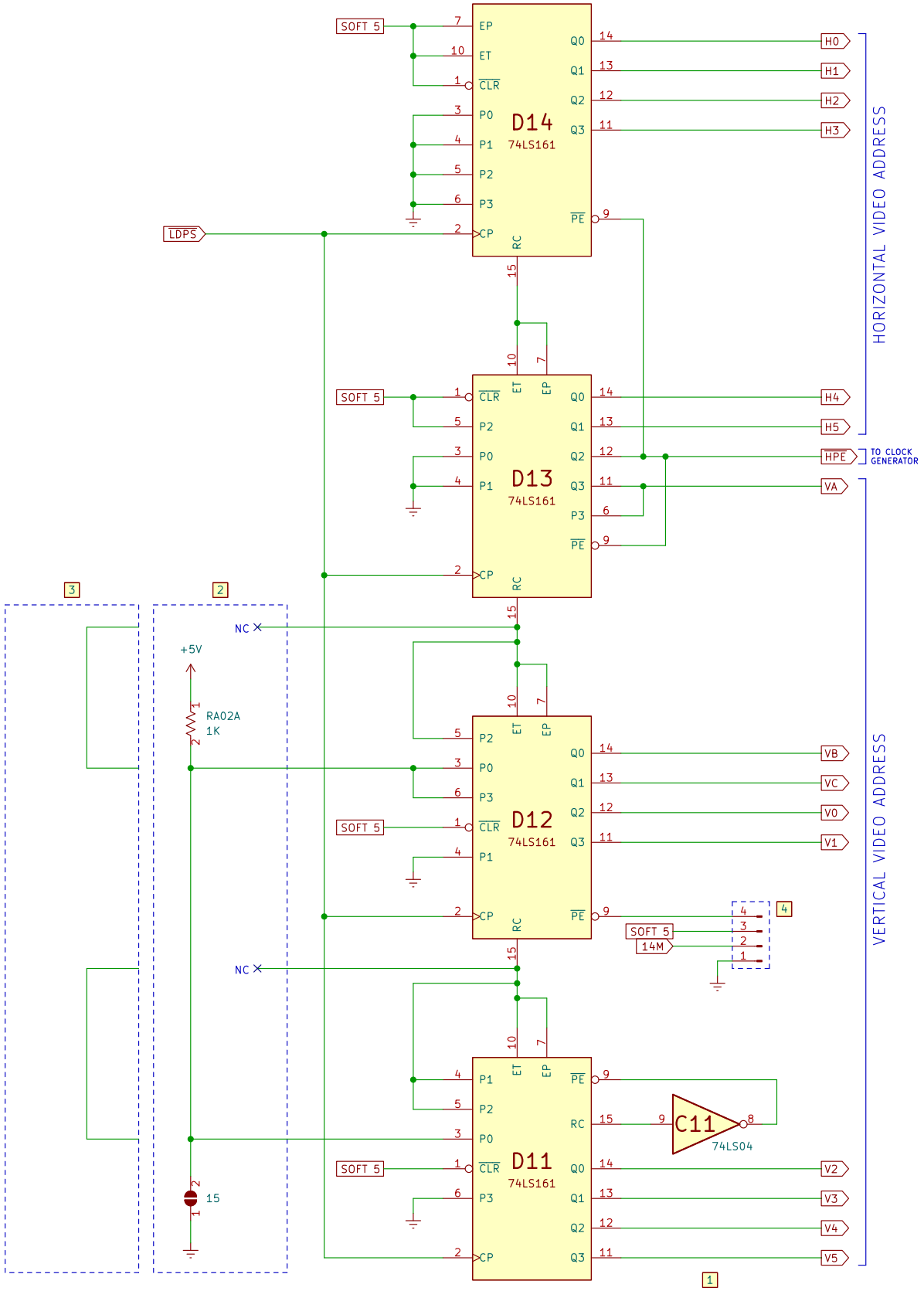
Sheet: /Clock Generator (All Revisions)/
File: Apple II – Clock Generator (all revisions).kicad_sch

Title: Apple II – Clock Generator

Size: A3	Date: 2022-07-11	**Rev: All**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 2/23



1. D11-11 NOT CONNECTED ON REV. 0
2. CIRCUIT FOR REV. 1,7,RFI
3. CIRCUIT FOR REV. 0
4. TEST CONNECTOR NEAR B2 ON SOME RFI BOARDS. A SIMILAR CONNECTOR WITH DIFFERENT CONNECTIONS MAY APPEAR ON REV. 7 BOARDS.

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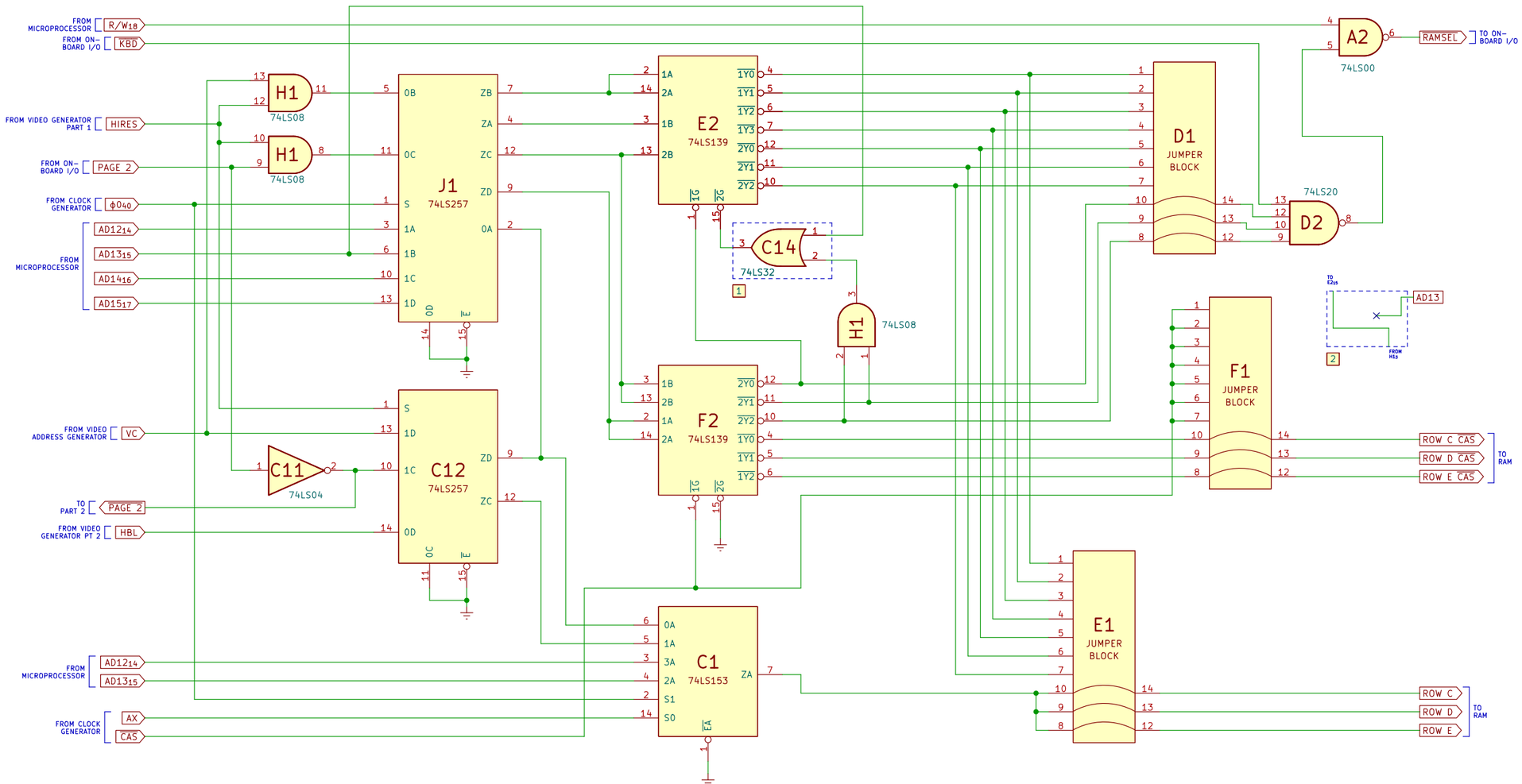
Sheet: /Video Address Generator (All Revisions)/
File: Apple II – Video Address Generator (all revisions).kicad_sch

Title: Apple II – Video Address Generator

Size: A4	Date: 2022-07-11	**Rev: All**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 3/23



1 CIRCUIT FOR REV. 1

2 CIRCUIT FOR REV. 0

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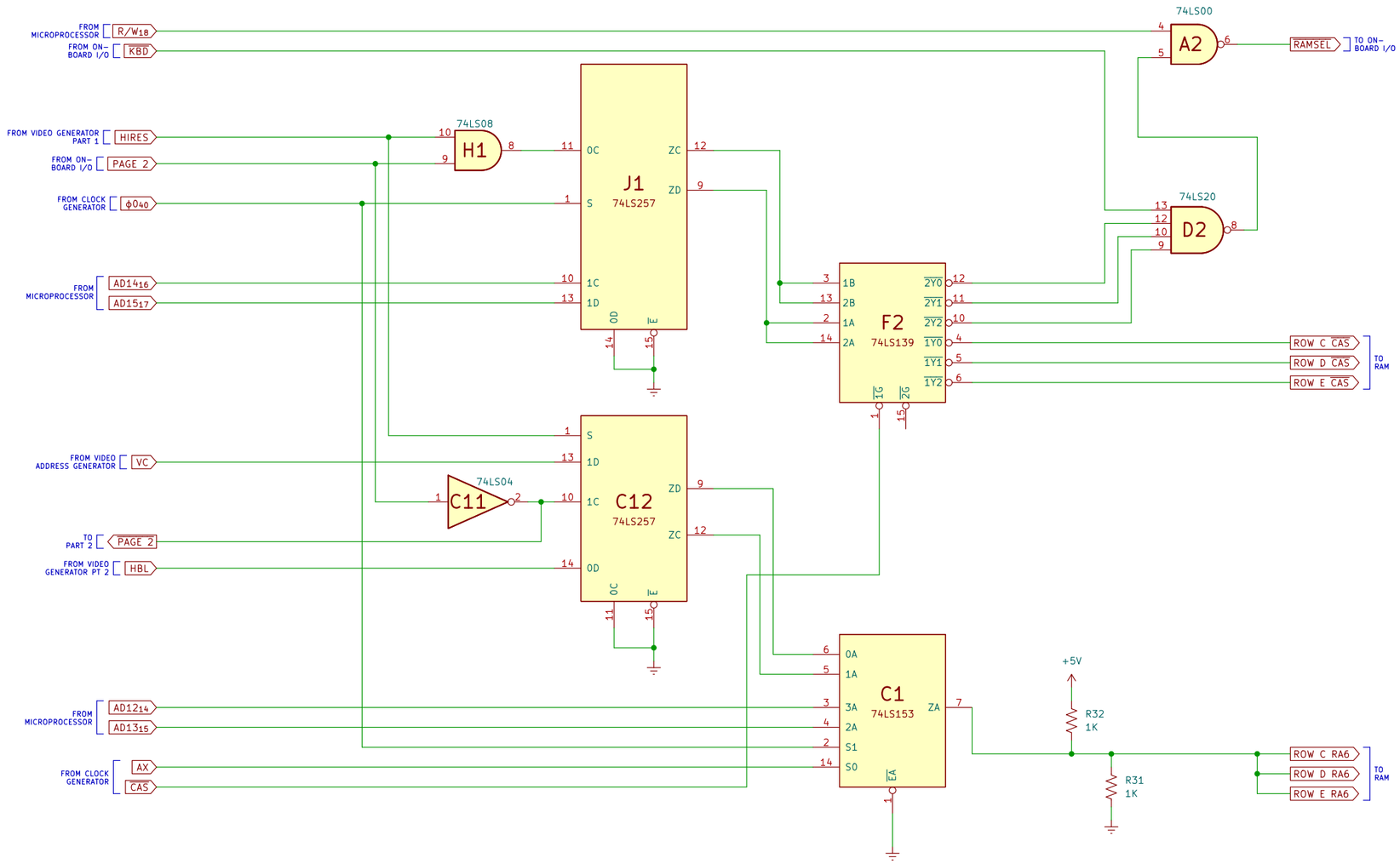
Sheet: /Memory Address Pt 1 of 2 (Rev 0,1)/
File: Apple II – Memory Address Pt 1 of 2 (Rev 0,1).kicad_sch

Title: Apple II – Memory Address Part 1

Size: A3	Date: 2022-07-11	**Rev: 0,1**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 4/23



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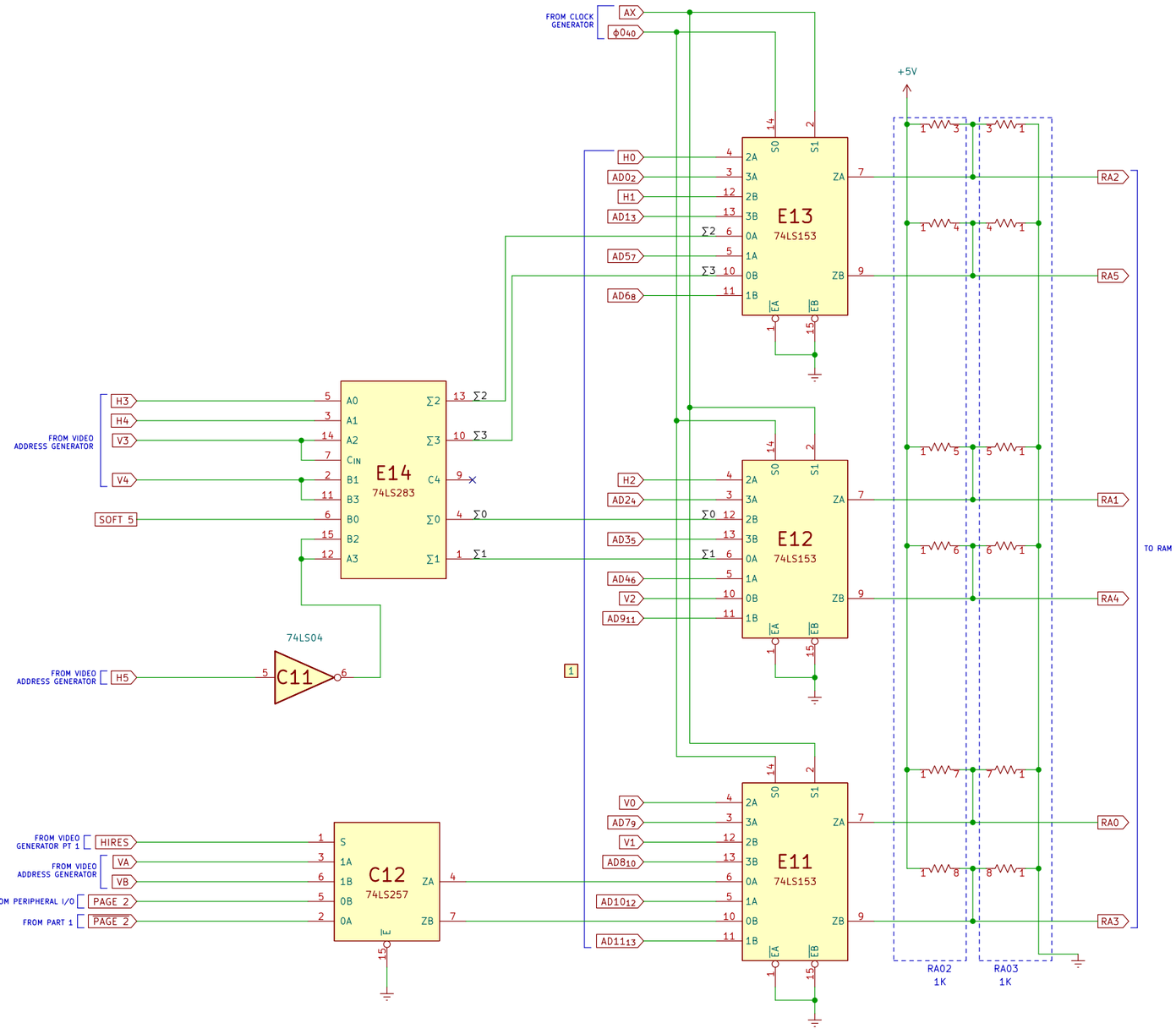
Sheet: /Memory Address Pt 1 of 2 (Rev 7,RFI)/
File: Apple II – Memory Address Pt 1 of 2 (Rev 7,RFI).kicad_sch

Title: Apple II – Memory Address Part 1

Size: A3	Date: 2022-07-11	**Rev: 7, RFI**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 5/23



1 AD SIGNALS ARE FROM MICROPROCESSOR. H AND V SIGNALS ARE FROM VIDEO ADDRESS GENERATOR.

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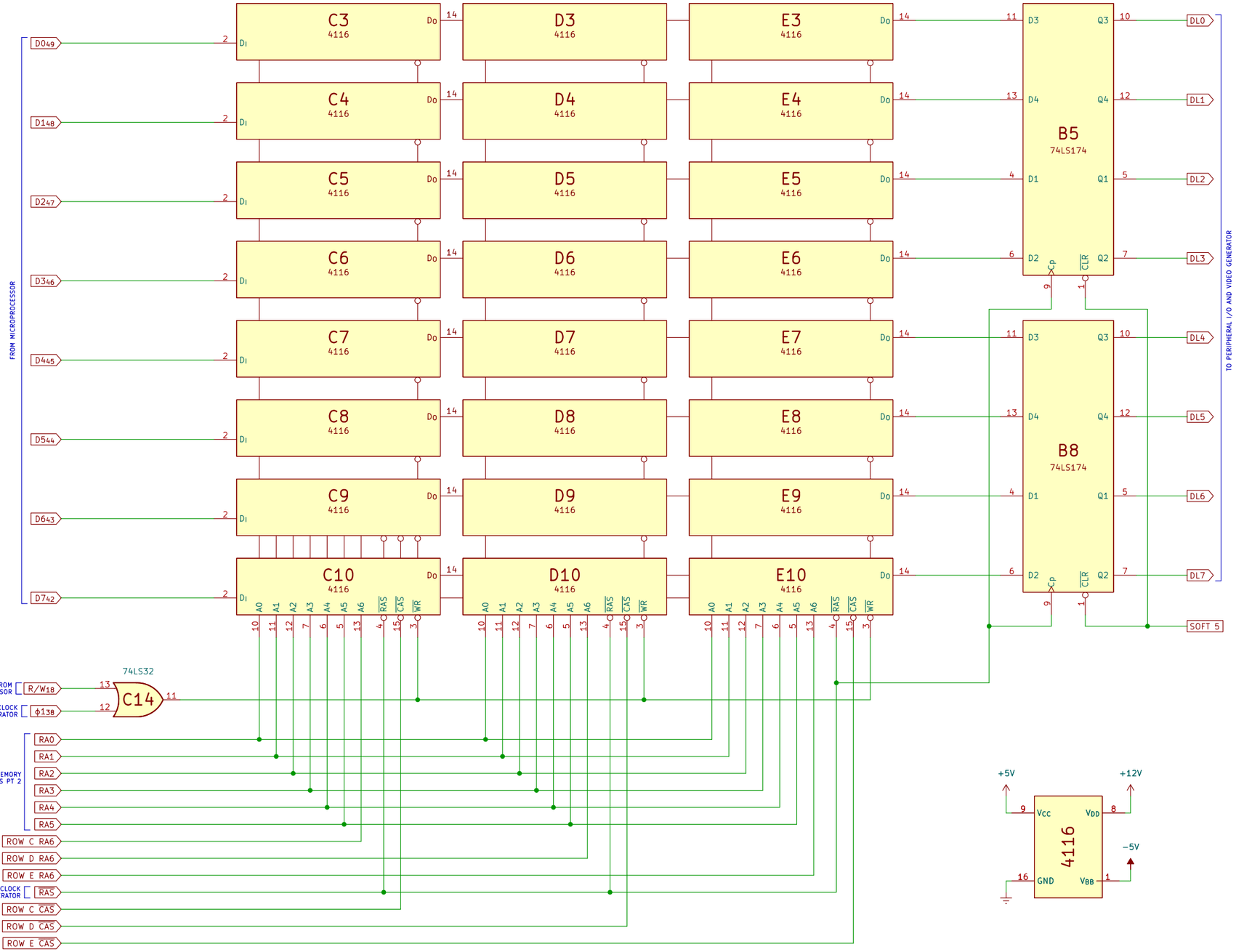
Sheet: /Memory Address Pt 2 of 2 (All Revisions)/
File: Apple II – Memory Address Pt 2 of 2 (All Revisions).kicad_sch

Title: Apple II – Memory Address Part 2

Size: A3	Date: 2022-07-11	**Rev: ALL**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 6/23



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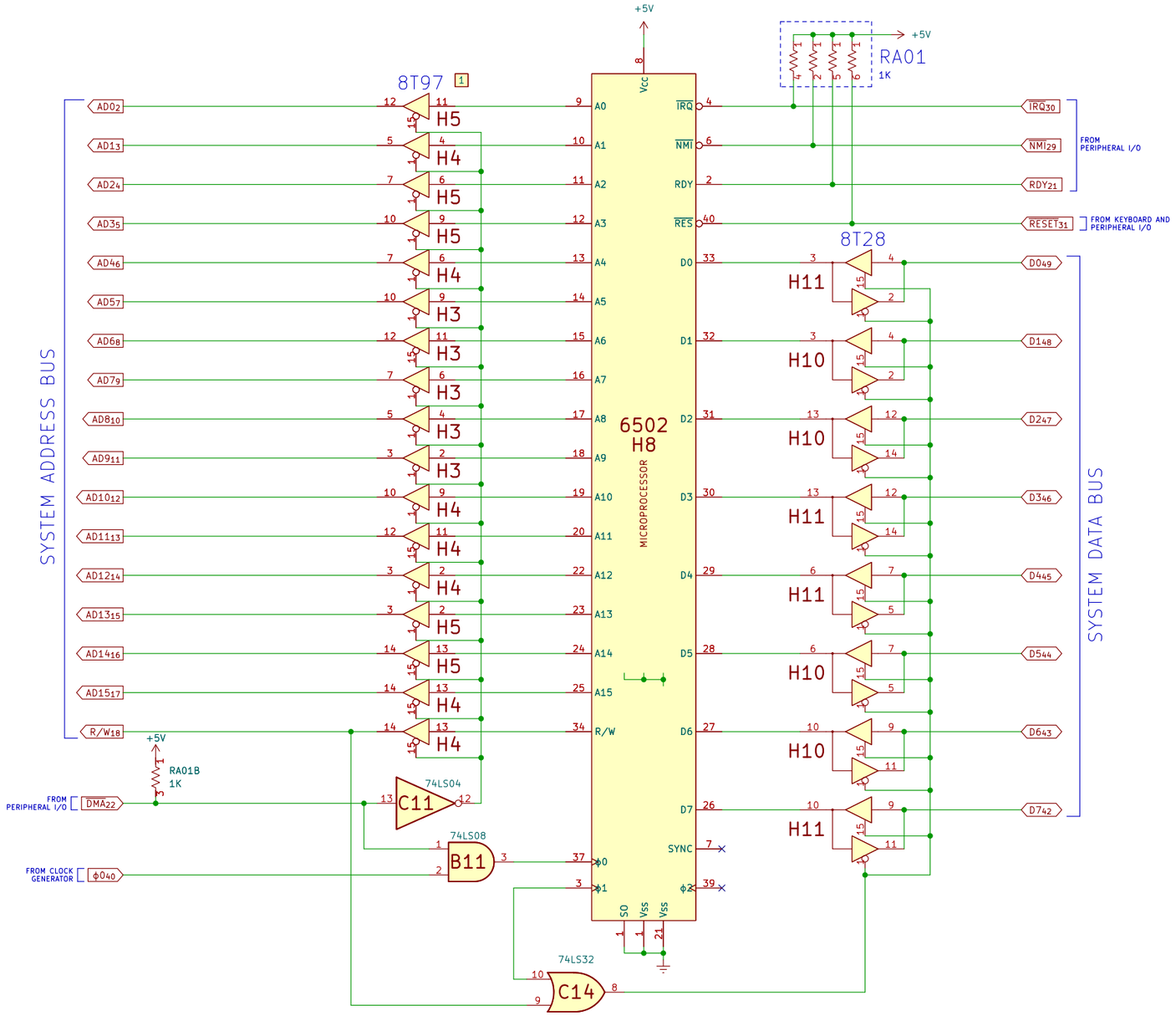
Sheet: /RAM (All Revisions)/
File: Apple II – RAM (All Revisions).kicad_sch

Title: Apple II – RAM

Size: A3	Date: 2022-07-11	**Rev: ALL**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 7/23



1 MAY BE SUBSTITUTED WITH 74LS367 ON SOME BOARDS

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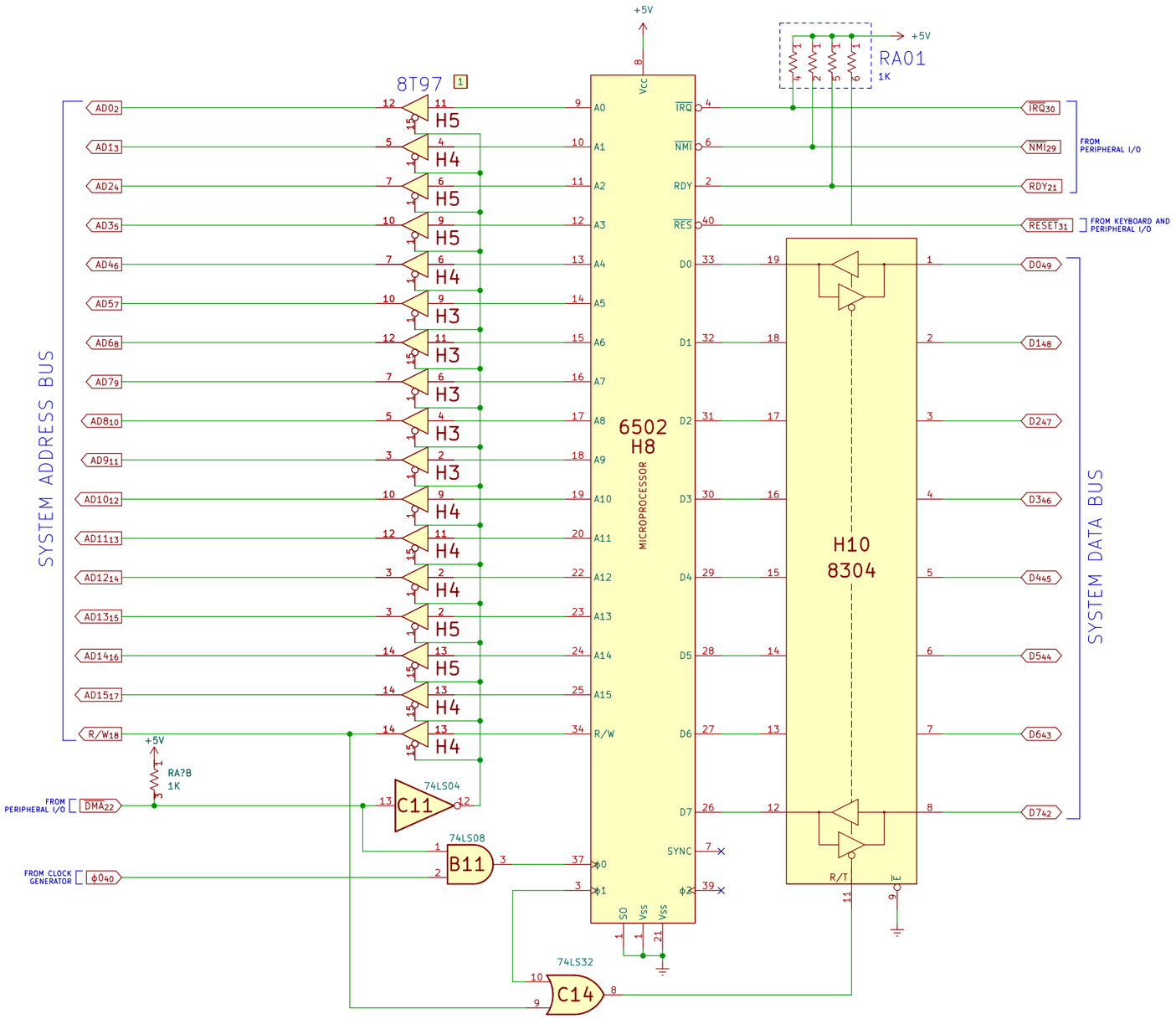
Sheet: /Microprocessor (Rev 0,1,7)/
File: Apple II – Microprocessor (Rev 0,1,7).kicad_sch

Title: Apple II – Microprocessor

Size: A3	Date: 2022-07-11	**Rev: 0,1,7**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 8/23



1 MAY BE SUBSTITUTED WITH 74LS367 ON SOME BOARDS

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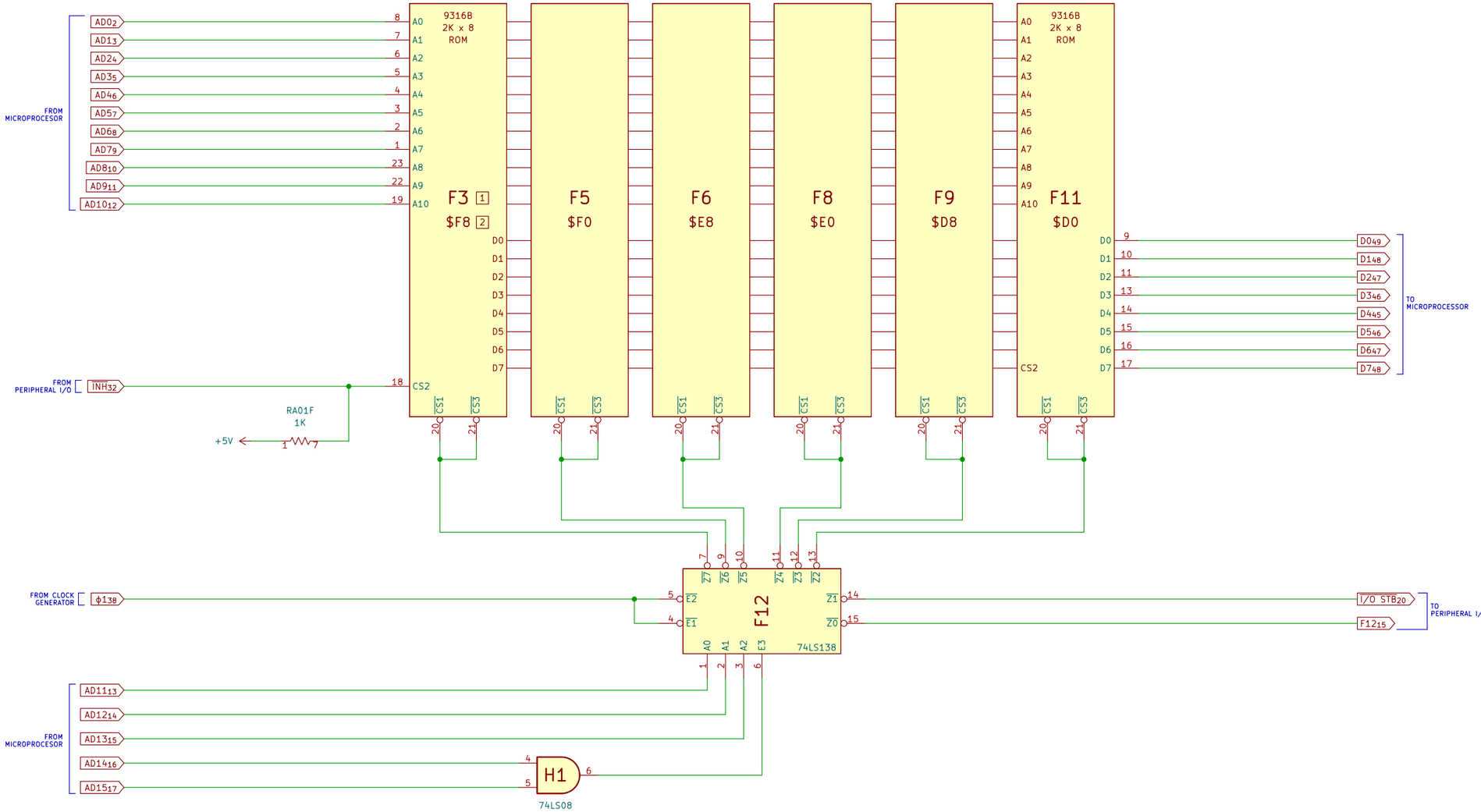
Sheet: /Microprocessor (Rev RFI)/
File: Apple II – Microprocessor (Rev RFI).kicad_sch

Title: Apple II – Microprocessor

Size: A3	Date: 2022-07-11	**Rev: RFI**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 9/23



1 MOTHERBOARD LOCATION

2 ADDRESS

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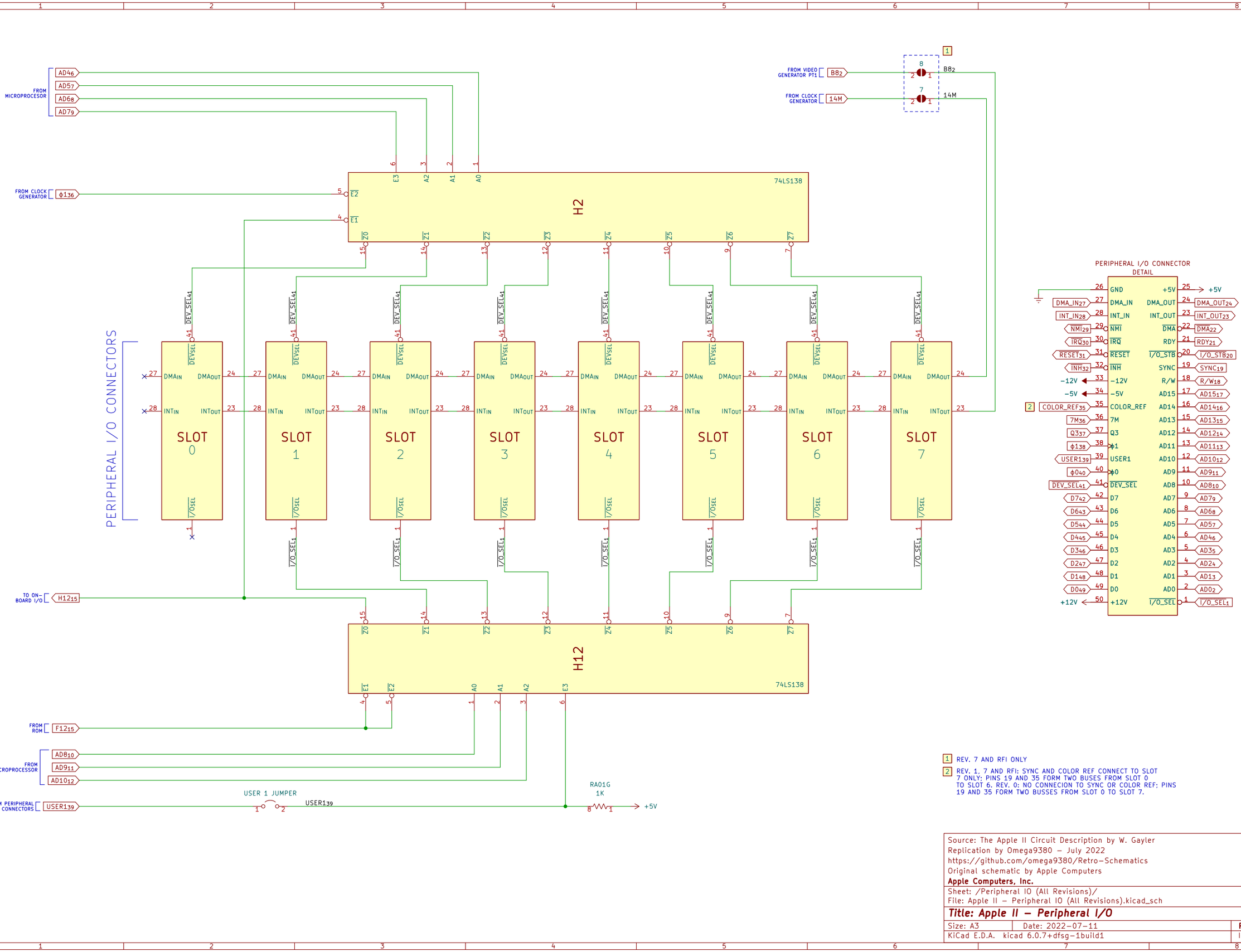
Sheet: /ROM (All Revisions)/
File: Apple II – ROM (All Revisions).kicad_sch

Title: Apple II – ROM

Size: A3	Date: 2022-07-11	**Rev: ALL**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 10/23



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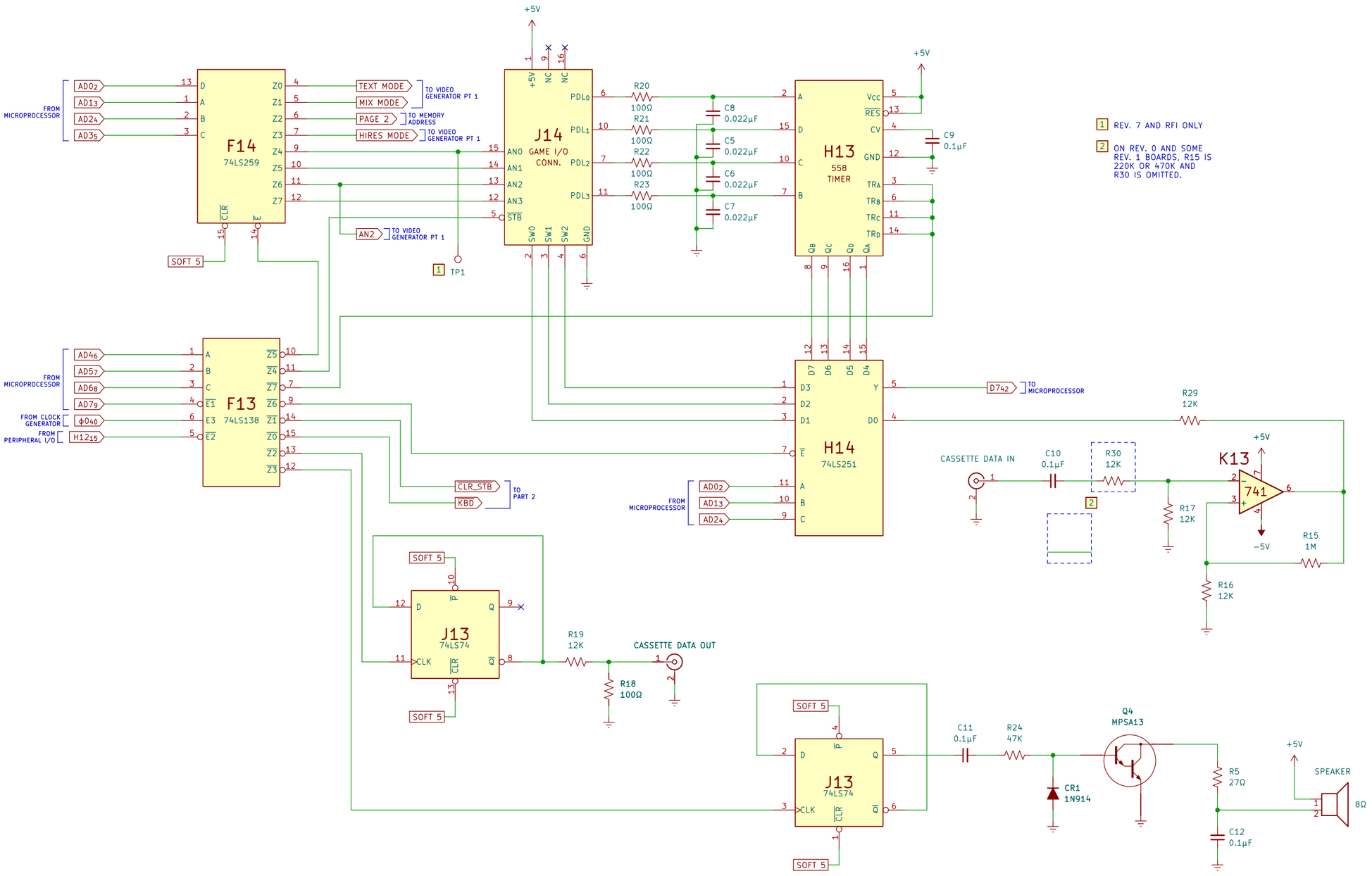
Sheet: /Peripheral IO (All Revisions)/
File: Apple II – Peripheral IO (All Revisions).kicad_sch

Title: Apple II – Peripheral I/O

Size: A3	Date: 2022-07-11	**Rev: ALL**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 11/23



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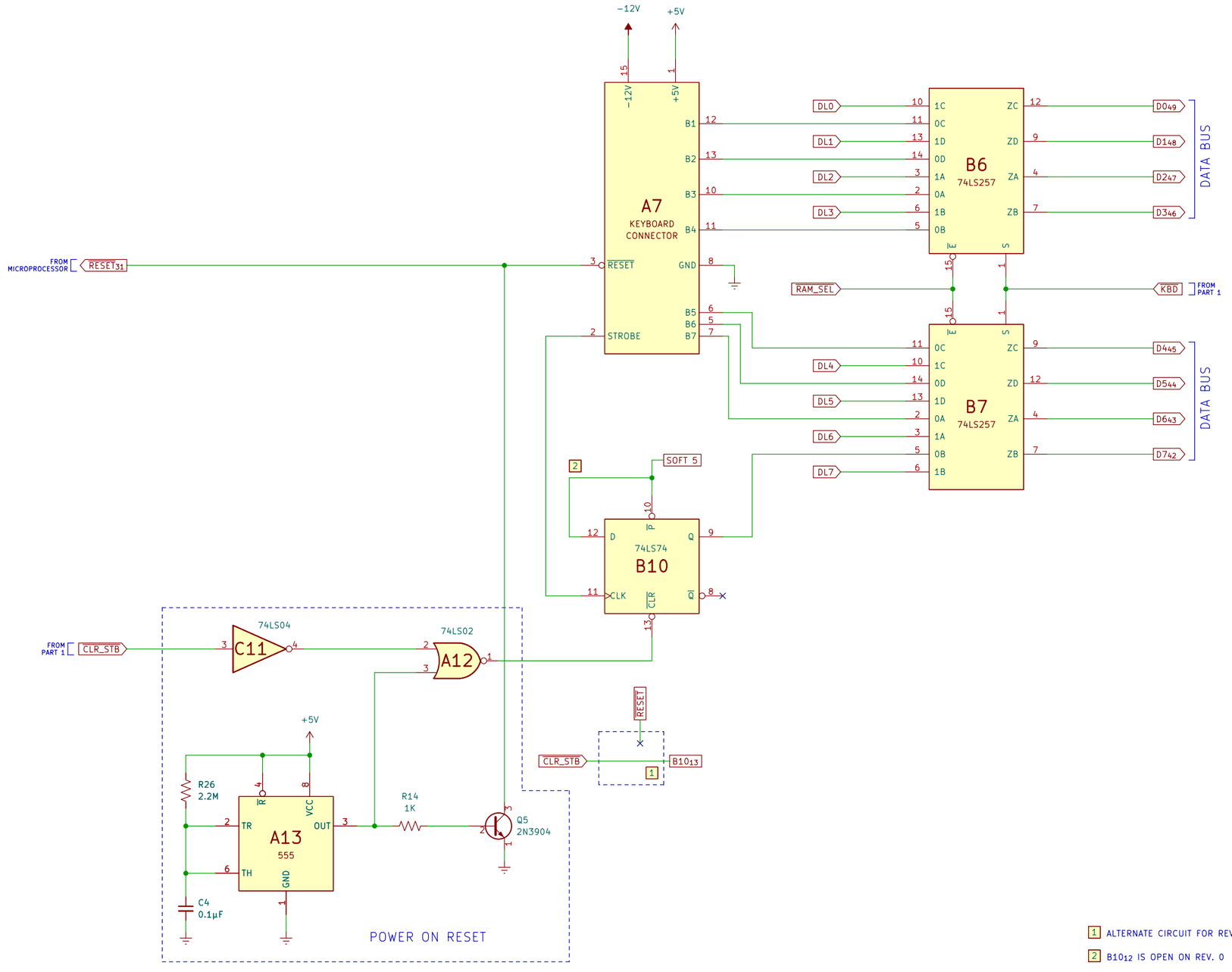
Sheet: /On-board IO Pt 1 of 2 (All Revisions)/
File: Apple II - On-board IO Pt 1 of 2 (All Revisions).kicad_sch

Title: Apple II – On-Board I/O Part 1

Size: A3	Date: 2022-07-11	**Rev: ALL**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 12/23



1 ALTERNATE CIRCUIT FOR REV. 0

2 $B10_{12}$ IS OPEN ON REV. 0

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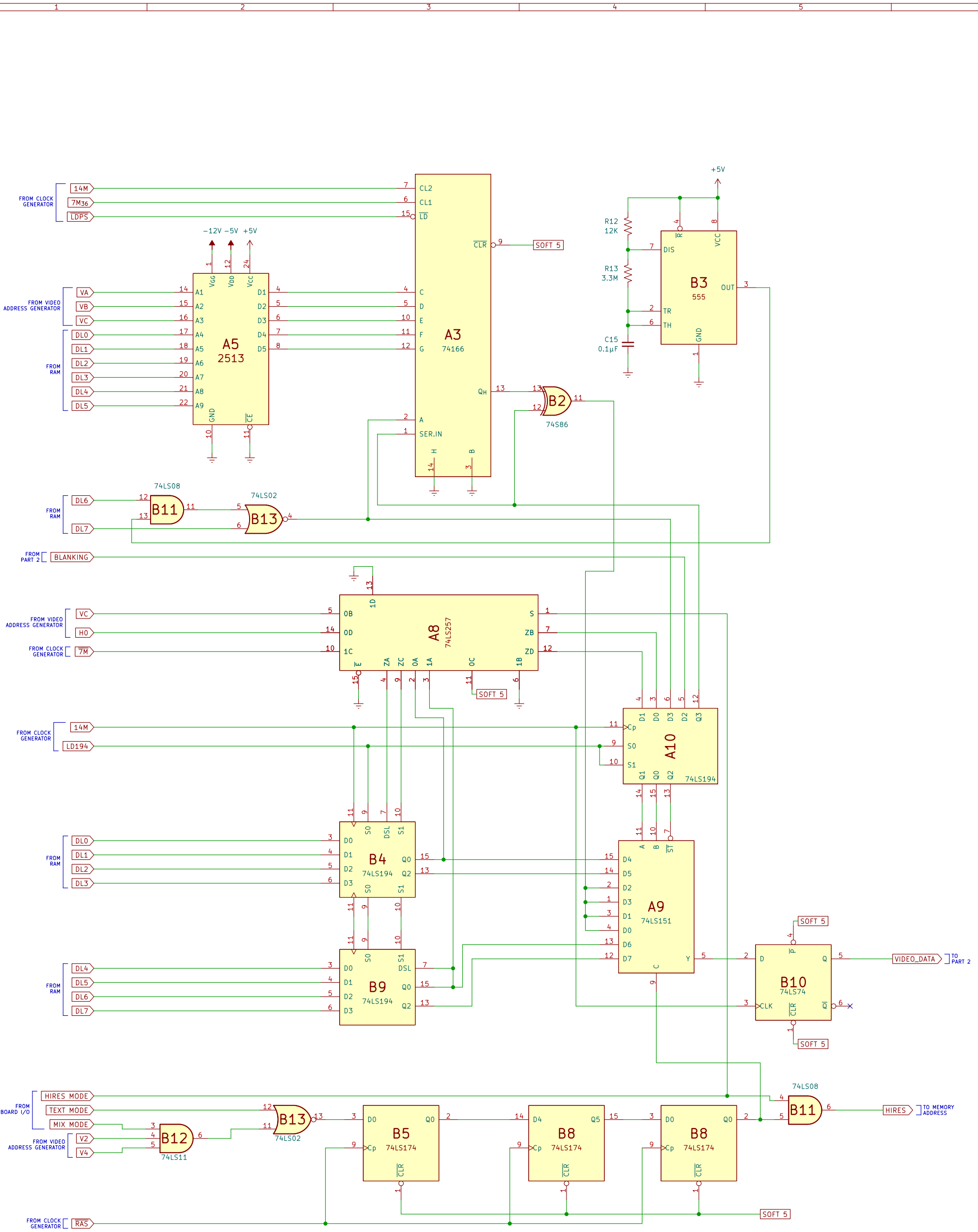
Sheet: /On-board IO Pt 2 of 2 (All Revisions)/
File: Apple II – On-board IO Pt 2 of 2 (All Revisions).kicad_sch

Title: Apple II – On-Board I/O Part 2

Size: A3	Date: 2022-07-11	**Rev: ALL**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 13/23



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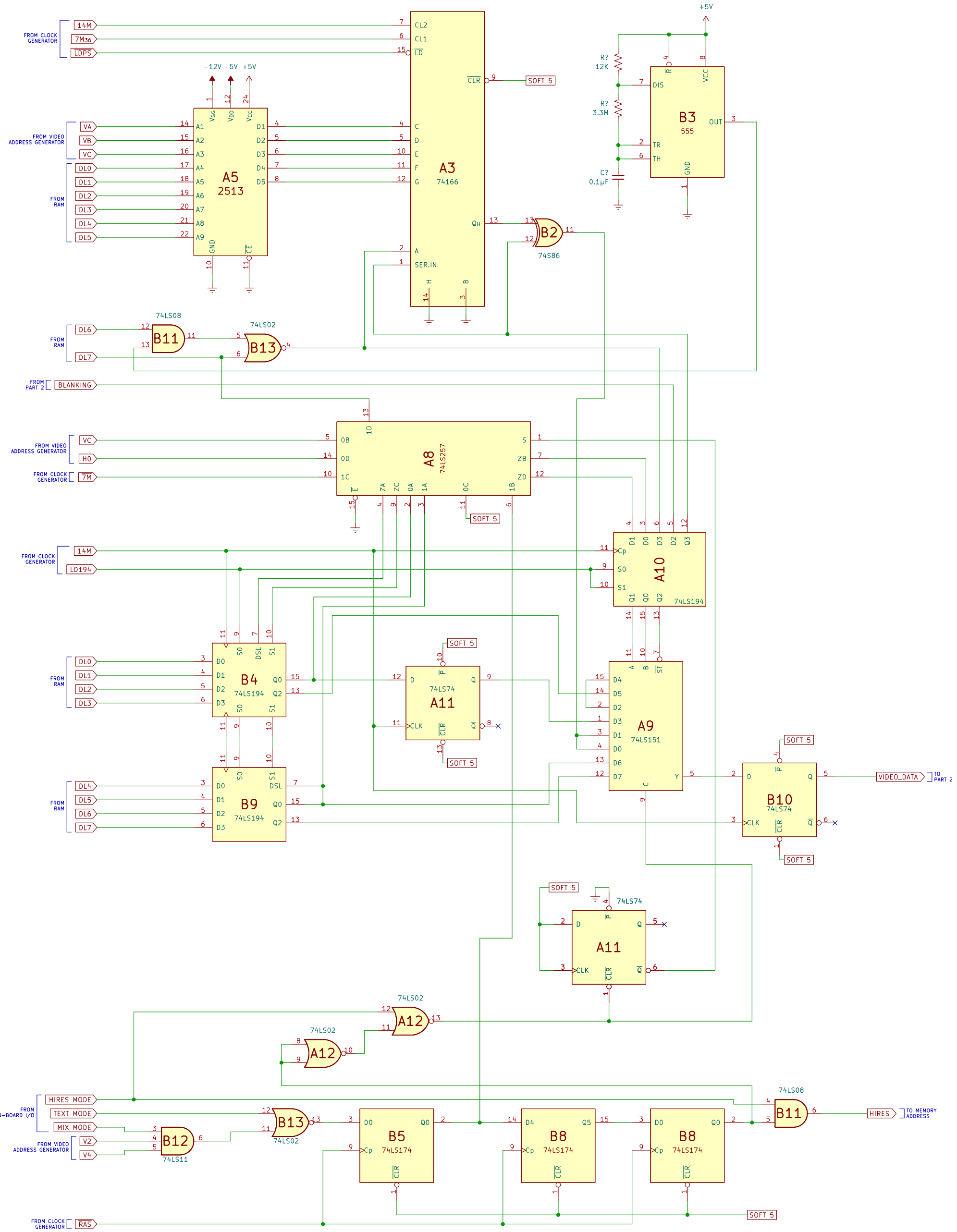
Sheet: /Video Generator Pt 1 of 2 (Rev 0)/
File: Apple II – Video Generator Pt 1 of 2 (Rev 0).kicad_sch

Title: Apple II – Video Generator Part 1

Size: A3	Date: 2022-07-11	**Rev: 0**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 14/23



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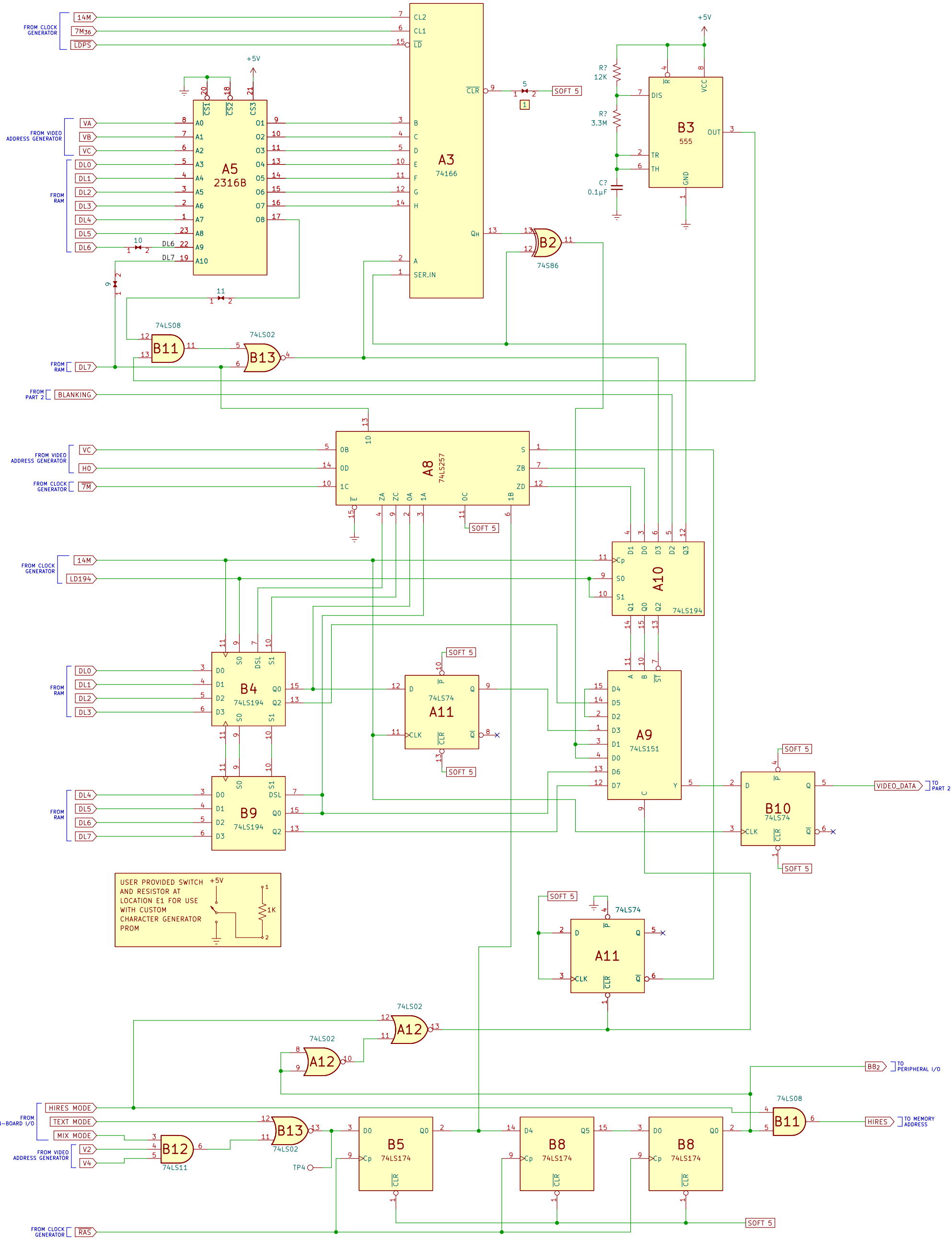
Sheet: /Video Generator Pt 1 of 2 (Rev 1)/
File: Apple II – Video Generator Pt 1 of 2 (Rev 1).kicad_sch

Title: Apple II – Video Generator Part 1

Size: A3	Date: 2022-07-11	**Rev: 1**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 15/23



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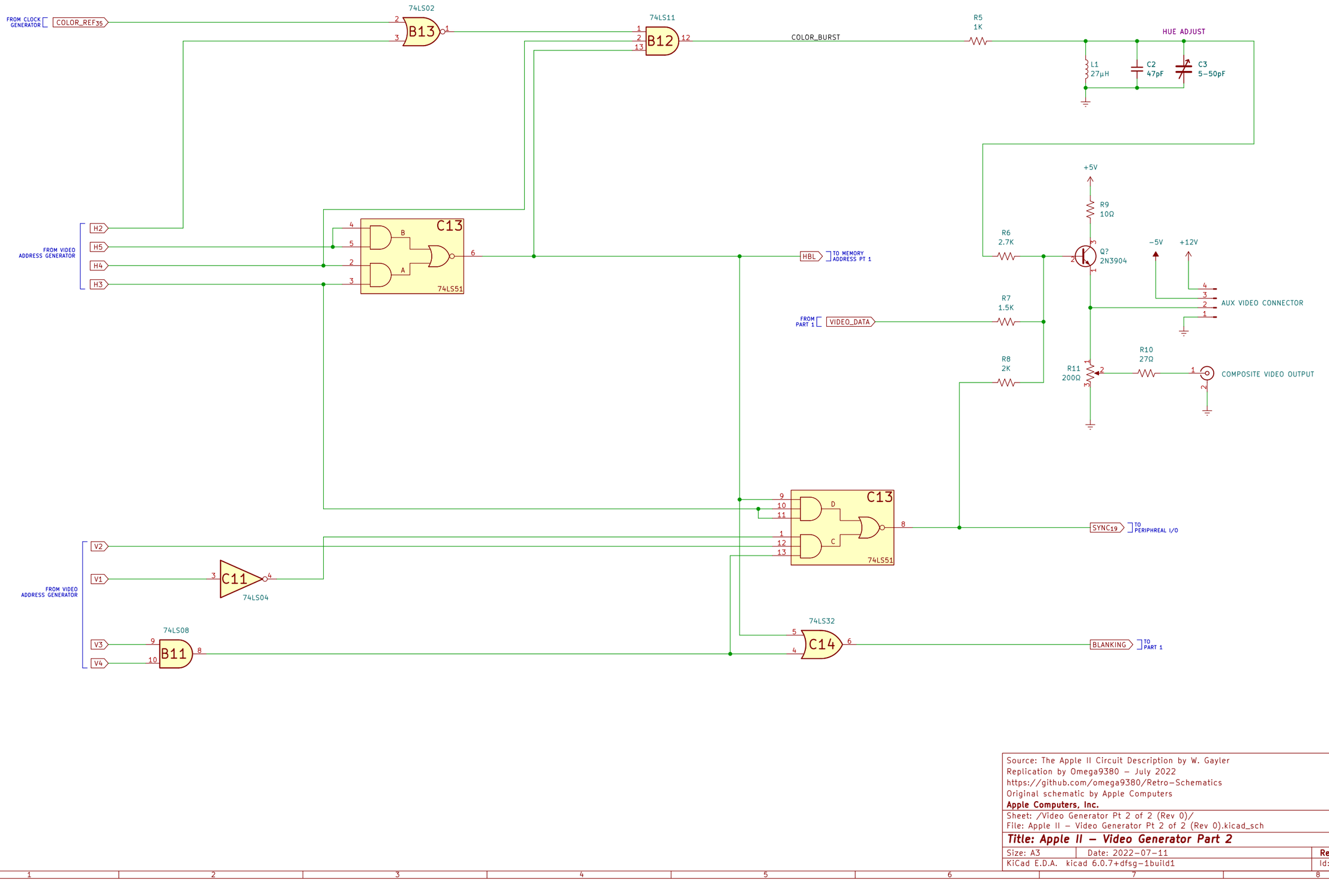
Sheet: /Video Generator Pt 1 of 2 (Rev 7,RFI)/
File: Apple II – Video Generator Pt 1 of 2 (Rev 7,RFI).kicad_sch

Title: Apple II – Video Generator Part 1

Size: A3	Date: 2022-07-11	**Rev: 7, RFI**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 16/23



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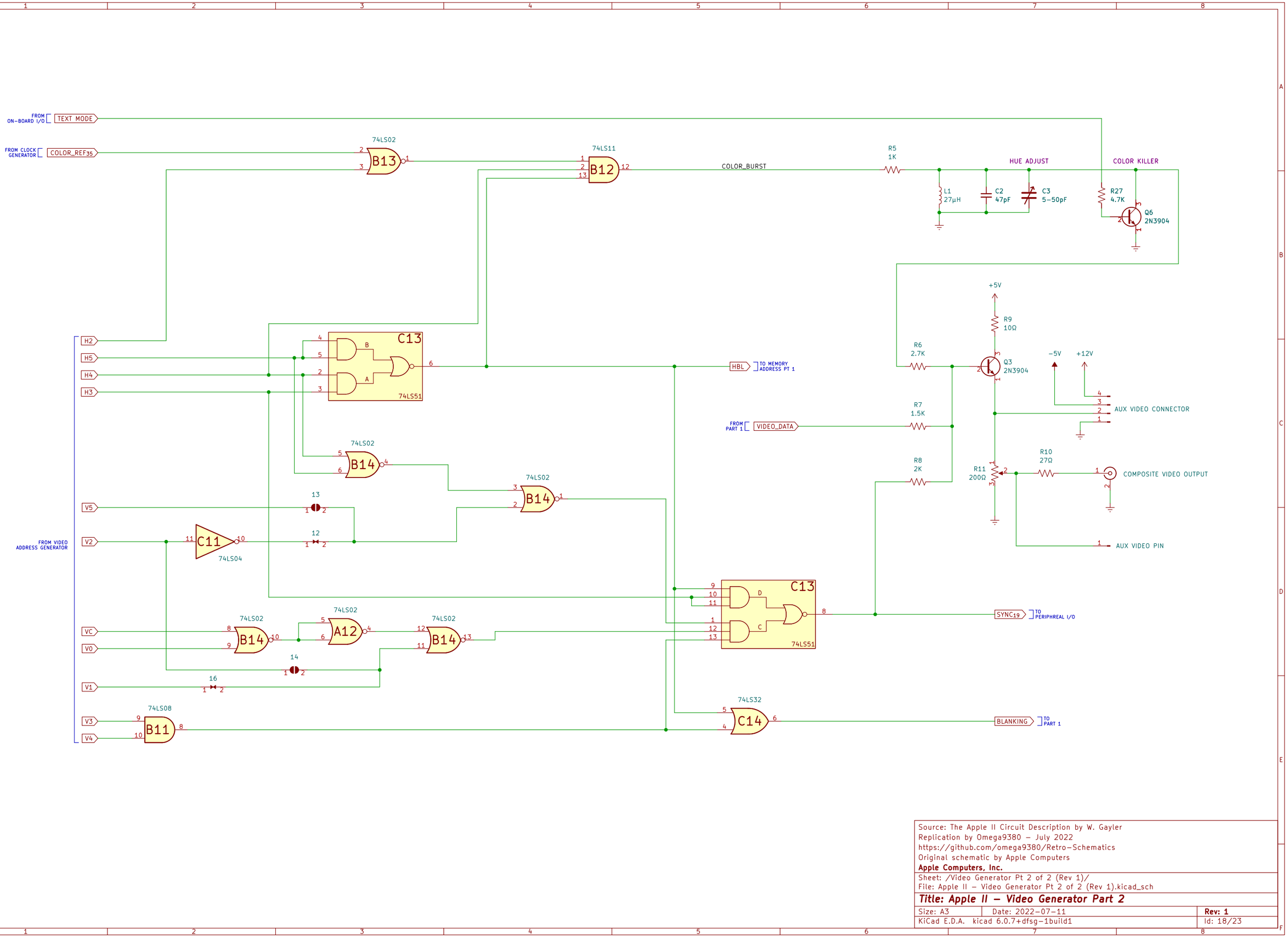
Sheet: /Video Generator Pt 2 of 2 (Rev 0)/
File: Apple II – Video Generator Pt 2 of 2 (Rev 0).kicad_sch

Title: Apple II – Video Generator Part 2

Size: A3	Date: 2022-07-11	**Rev: 0**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 17/23



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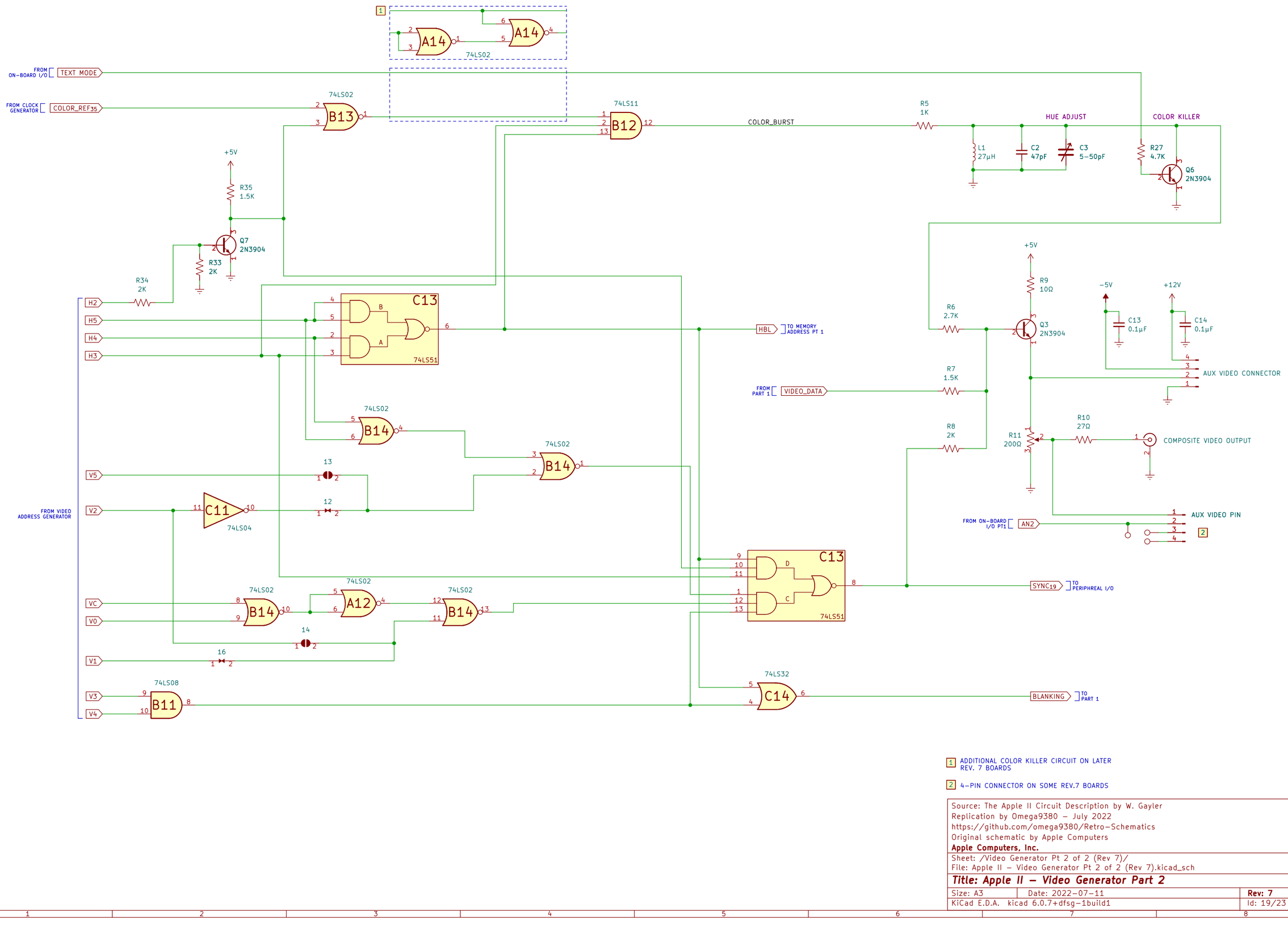
Sheet: /Video Generator Pt 2 of 2 (Rev 1)/
File: Apple II – Video Generator Pt 2 of 2 (Rev 1).kicad_sch

Title: Apple II – Video Generator Part 2

Size: A3	Date: 2022-07-11	**Rev: 1**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 18/23



1 ADDITIONAL COLOR KILLER CIRCUIT ON LATER REV. 7 BOARDS

2 4-PIN CONNECTOR ON SOME REV.7 BOARDS

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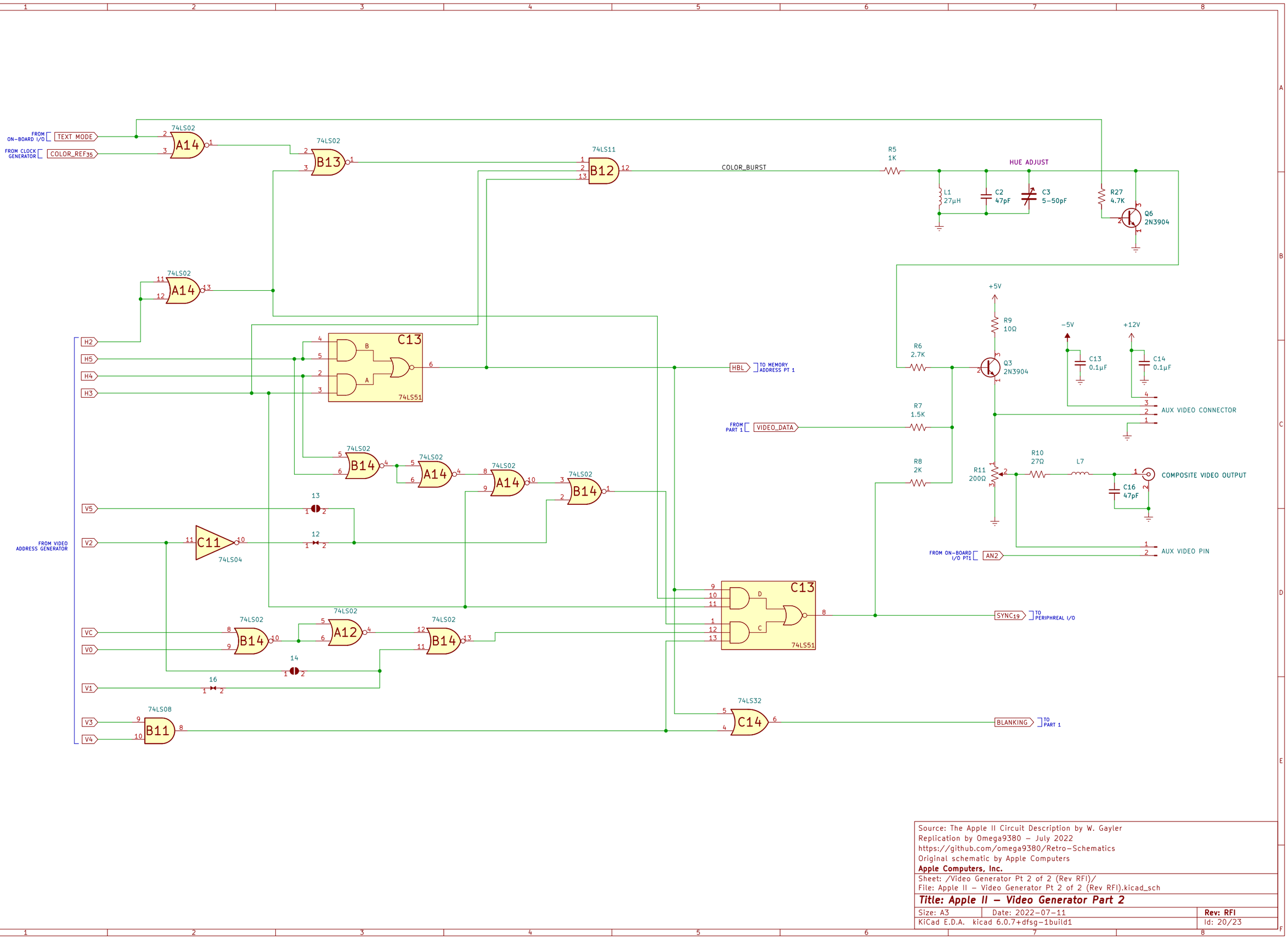
Sheet: /Video Generator Pt 2 of 2 (Rev 7)/
File: Apple II – Video Generator Pt 2 of 2 (Rev 7).kicad_sch

Title: Apple II – Video Generator Part 2

Size: A3	Date: 2022-07-11	**Rev: 7**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 19/23



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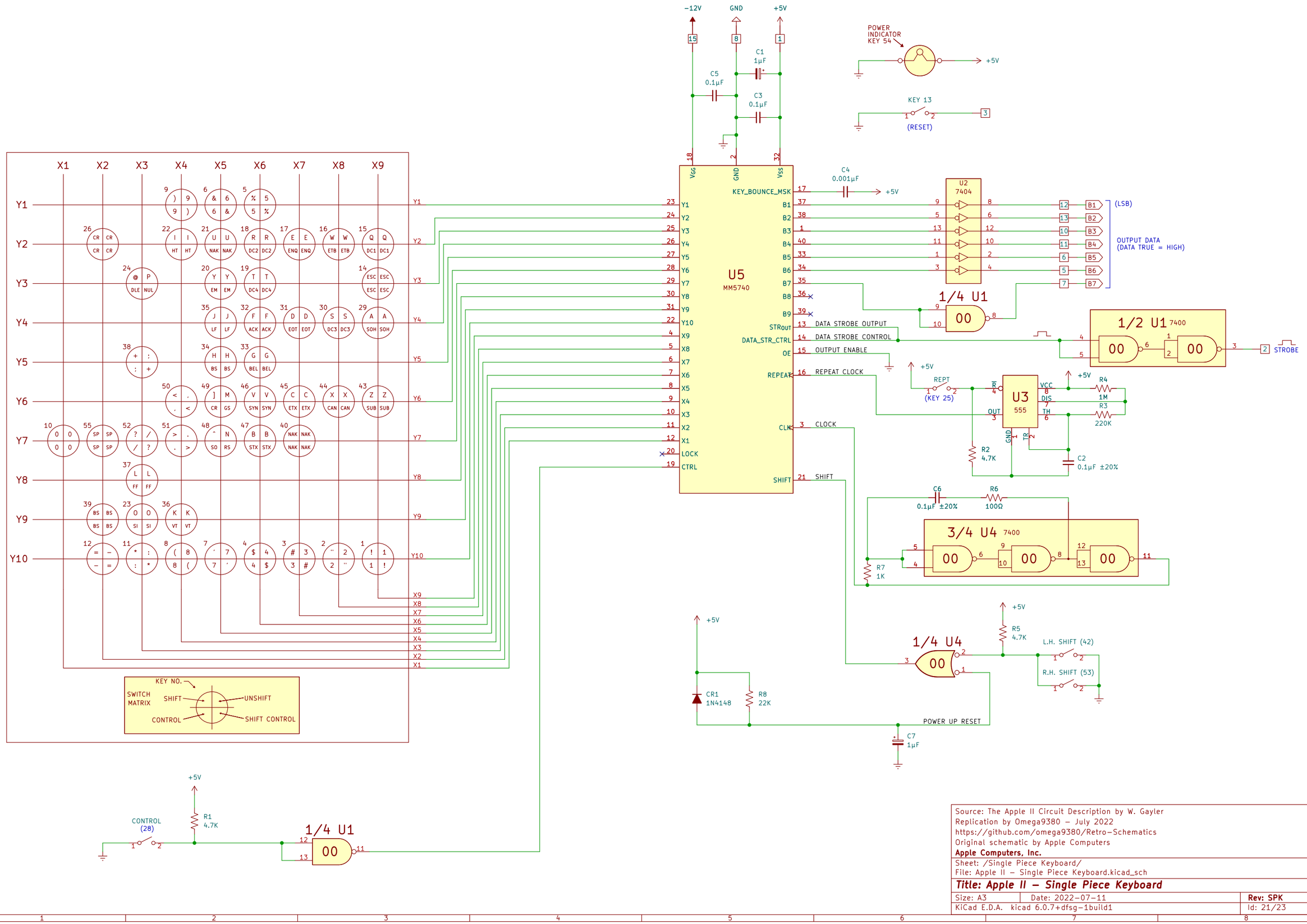
Sheet: /Video Generator Pt 2 of 2 (Rev RFI)/
File: Apple II – Video Generator Pt 2 of 2 (Rev RFI).kicad_sch

Title: Apple II – Video Generator Part 2

Size: A3	Date: 2022-07-11	**Rev: RFI**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 20/23



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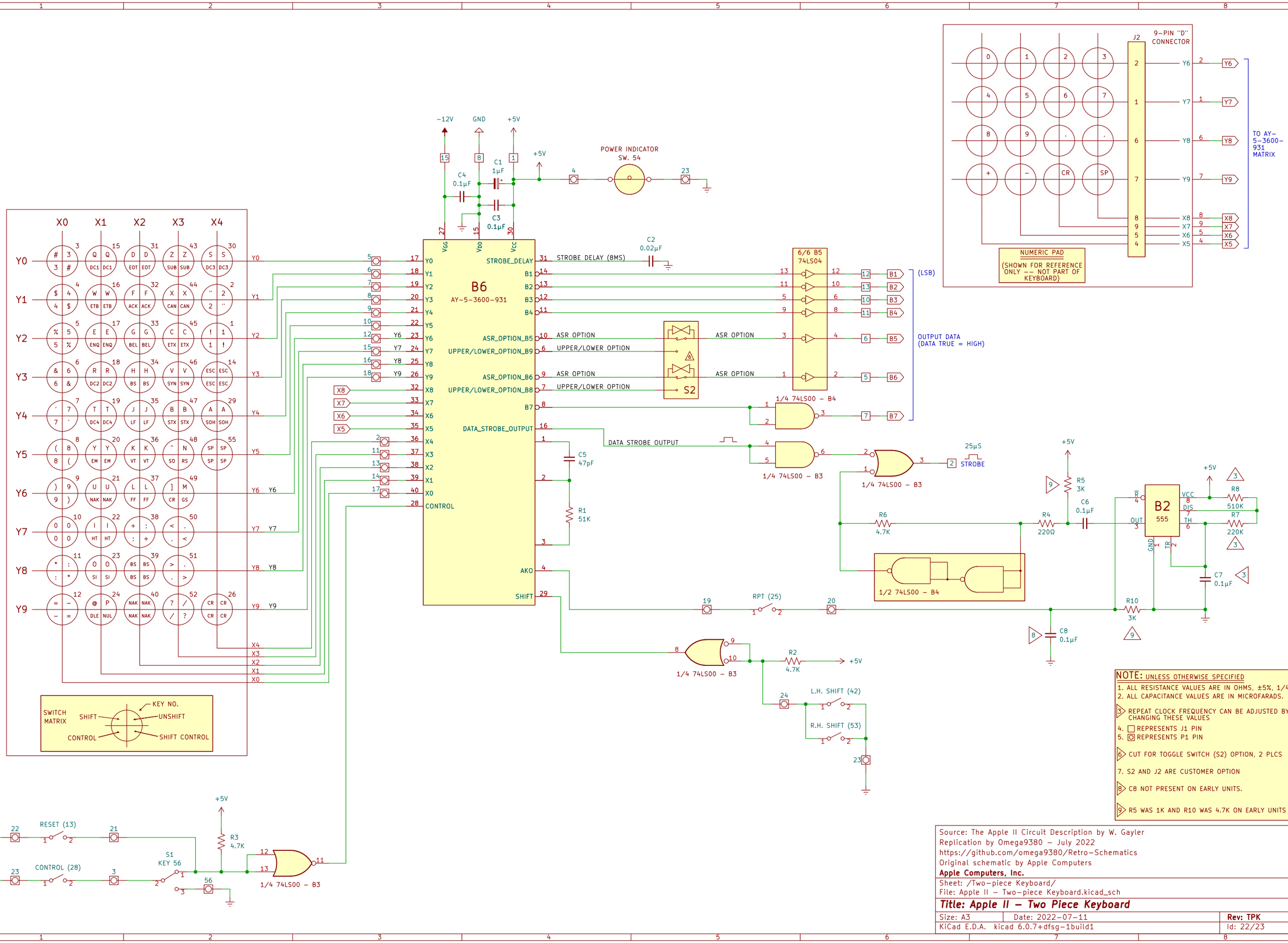
Sheet: /Single Piece Keyboard/
File: Apple II – Single Piece Keyboard.kicad_sch

Title: Apple II – Single Piece Keyboard

Size: A3	Date: 2022-07-11	**Rev: SPK**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 21/23



NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTANCE VALUES ARE IN OHMS, ±5%, 1/4W
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. REPEAT CLOCK FREQUENCY CAN BE ADJUSTED BY CHANGING THESE VALUES
4. □ REPRESENTS J1 PIN
5. ◎ REPRESENTS P1 PIN
6. CUT FOR TOGGLE SWITCH (S2) OPTION, 2 PLCS
7. S2 AND J2 ARE CUSTOMER OPTION
8. C8 NOT PRESENT ON EARLY UNITS.
9. R5 WAS 1K AND R10 WAS 4.7K ON EARLY UNITS

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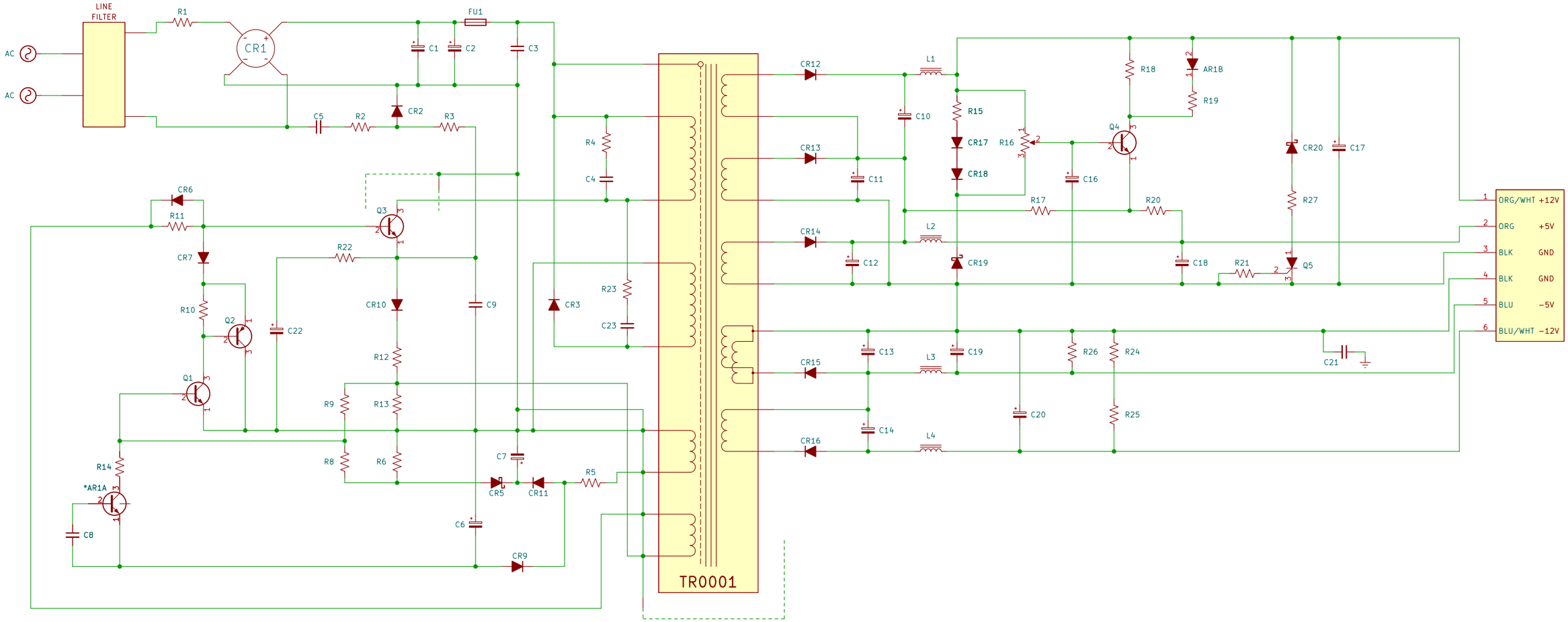
Sheet: /Two−piece Keyboard/
File: Apple II − Two−piece Keyboard.kicad_sch

Title: Apple II − Two Piece Keyboard

Size: A3	Date: 2022−07−11	**Rev: TPK**
KiCad E.D.A. kicad 6.0.7+dfsg−1build1		Id: 22/23



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Sheet: /Power Supply/
File: Apple II – Power Supply.kicad_sch

Title: Apple II – Power Supply

Size: A3	Date: 2022-07-11	**Rev: ALL**
KiCad E.D.A. kicad 6.0.7+dfsg-1build1		Id: 23/23