

Tuesday, January 11, 2016 – A56, Department of Informatics and Telecommunications.

9:30-10:00	Angelos Charalambidis <i>Extensional Higher-Order Logic Programming</i>
10:00-10:30	Ioanna Symeonidou <i>Equivalence of two Fixed-Point Semantics for Definitional Higher-Order Logic Programs.</i>
10:30-11:00	Angelos Charalambidis <i>Minimum Model Semantics for Extensional Higher-order Logic Programming with Negation</i>
11:00-11:30	Antonis Troumpoukis <i>Expressing preferences in logic programming using an infinite-valued logic</i>
11:30-12:00	Panos Rondogiannis <i>Preferences, Hopes, Compromises and Wishes</i>
12:00-12:30	Orestis Melkonian <i>Towards a Dataflow Approach for Robot Programming</i>
12:30-14:00	Discussion