
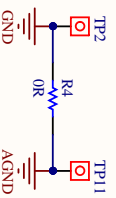
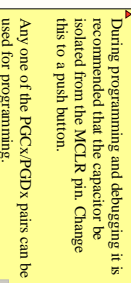


Title		<i>[PD1-3] - Top</i>		*	
Size: A4		Number: 1		*	
		Revision: *		*	
Date: 01-Oct-18		Time: 9:51:20 PM		*	
		Sheet 1 of 5		*	
File: C:\Users\lamm\Dropbox\University\Capstone\Hardware\PCB\PD1-3 - Power Breakout\PD1-3 - Top\SchDoc					





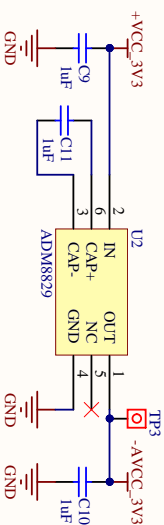
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2

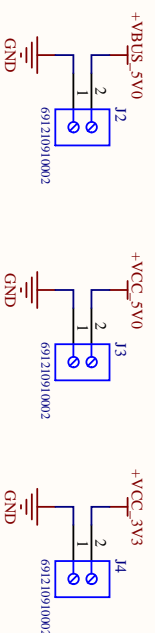
3

4

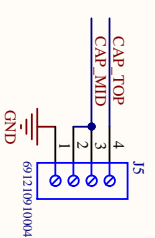
VOLTAGE INVERTER



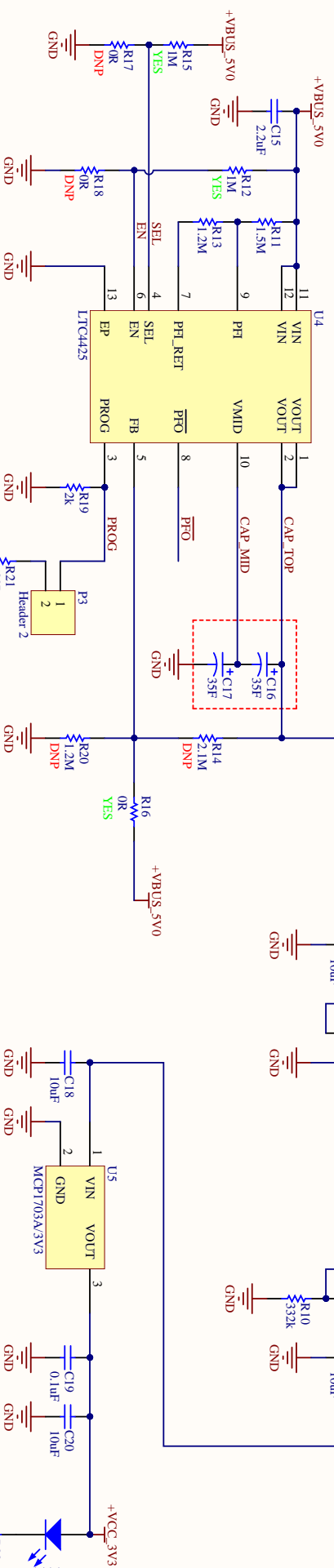
POWER CONNECTORS



ULTRA CAPACITOR HEADER

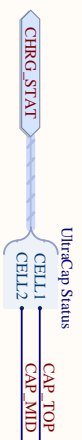
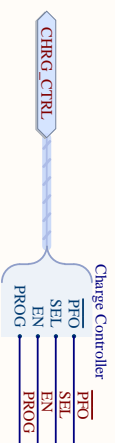


Ensures loop stability when ultra capacitors are disconnected.



L7C4425 is in charge current profile mode when the FB is shorted to the input voltage. LDO mode can be configured by connecting the FB pin to the resistor divider network.

Connect jumper to change L7C4425 output (charging) current to 2A. Ensure that USB is disconnected and supply voltage is coming from an external source.



Title **[PDI-3] - Power**

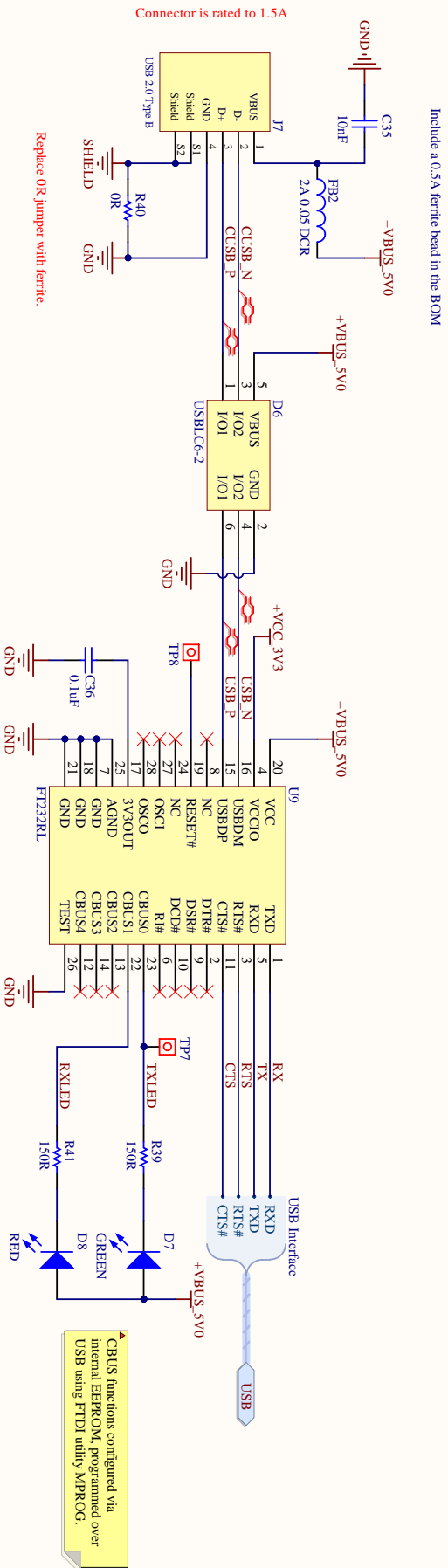
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Date: 01-Oct-18 Time: 9:51:21 PM Sheet 3 of 5

File: C:\Users\Jiamin\Dropbox\University\Capstone\Hardware\PCB\ [PDI-3] Power Breakout\ [PDI-3] - Power_SchDoc

Altium

Decide if the TVS diode is required, and if the CBUS0 clock output is required.
Need to configure one of the CBUS pins as PWREN# and use it to keep the current below 100mA on plug-in and 2.5mA on USB suspend.



May need to reduce 10uF cap to 4.7uF.

Title		[PD1-3] - USB	
Size:	A4	Number:	4
Date:	01-Oct-18	Time:	9:51:21 PM
File:	C:\Users\lauri\Dropbox\University\Capstone\Hardware\PCB [PD1-3] Power Breakout\ [PD1-3] - USB.SchDoc	Sheet:	4 of 5



