	U1						
PGEC1 1	PGEC1/AN21/SDA1/RI	B7 AN2/PG	A1P3/PGA2P2/CMP1	C/CMP2A/RA2	23	RTS	
2	ANIALT/RC4	71112/1 0	AN3/PGA2P3/CMP1		24	RXD	
3	The state of the s				25	EN	
4	ANOALT/RC5		AN4/CMP2C/CMF		26	SEL	- 6
5	AN17/RC6		AN5/CMP2D/CMP3		27	PROG	
× 6	RC3		AN1	1/PGA1N3/RC9	28	TROO	
7	VSS		AN10/PGA1P4/E	EXTREF2/RC10			$\rightarrow$
	VCAP			VDD	29		62 - 04
× 8	TMS/PWM3H/RB11			VSS	30		
9	TCK/PWM3L/RB12		ANS/PGA2	P4/CMP4C/RC1	31		V
PWMH 10	PWM2H/RB13	OSCI/CI	KI/AN6/CMP3C/CMF		32		
PWML 11					33		
12	PWM2L/RB14		/AN7/PGA1N2/CMP3		34	PFO	
13	PWM1H/RA4		PGED2/AN18/DACC		35	LED	
14	PWM1L/RA3			ADTRG31/RB4	36	LLD	6 8
15	FLT12/RC0		AN9/CMP4D/	EXTREF1/RC2	37		$\rightarrow$
*	FLT11/RC13			ASDA1/RC7			$\times$
16	AVSS			ASCL1/RC8	38		$\rightarrow$
17	AVDD			VSS	39		
MCLR 18	MCLR			VDD	40		
	AN12/ISRC1/RC11		DCED2/CD	A2/FLT31/RB8	41	SW	
20					42		1
CTS 21	AN14/PGA2N3/RC12			EC3/SCL2/RB15	43		
TXD 22	AN0/PGA1P1/CMP1A/			9/PGA2N2/RB5	44	PGED1	$\rightarrow$
	AN1/PGA1P2/PGA2P1	/CMP1B/RA1	PGED1/TDI/A	N20/SCL1/RB6		TOLDI	
	dsPIC33EP64GS504				l.		
	usr1C55EF04U5504						