

Logic Circuits & Design

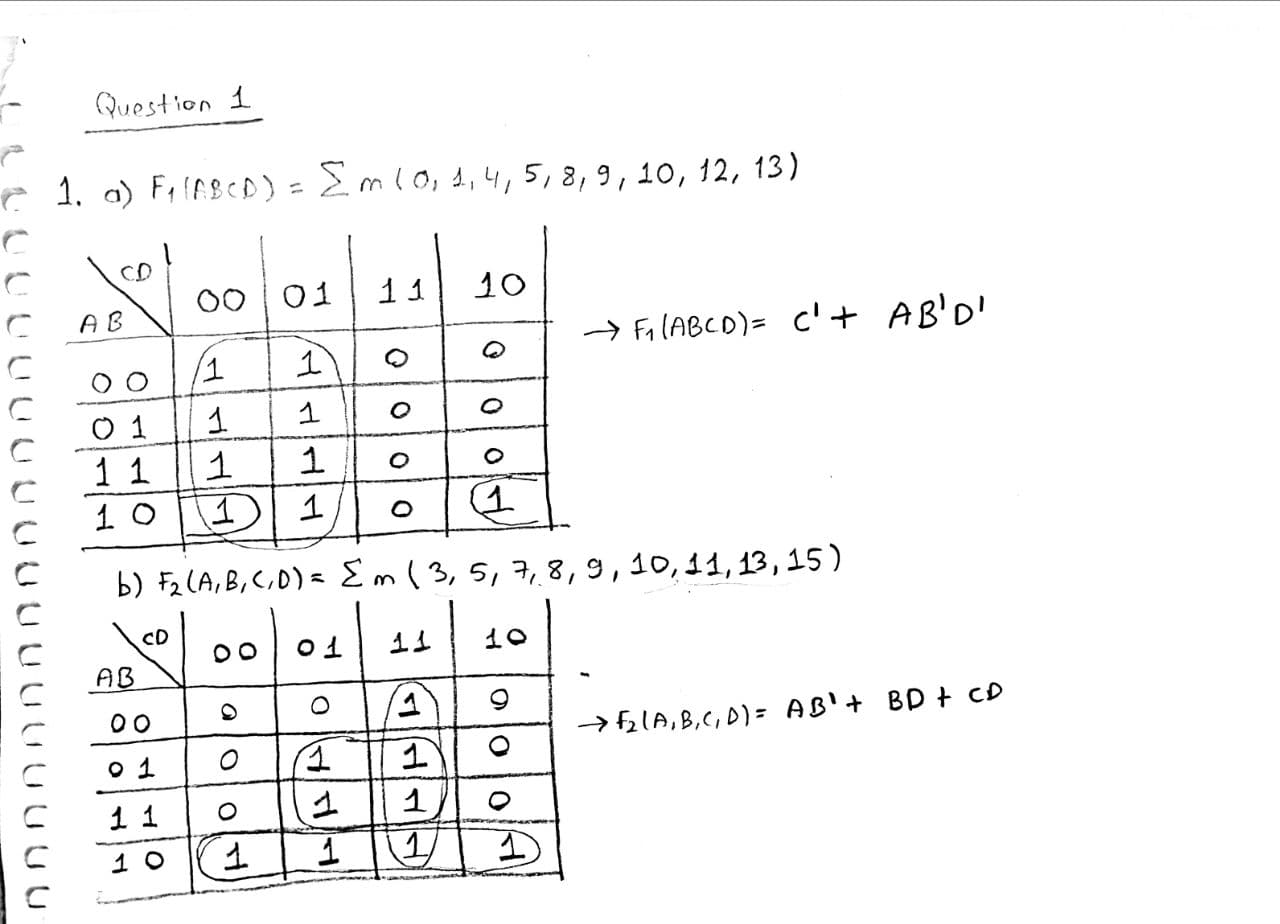
Homework - 2



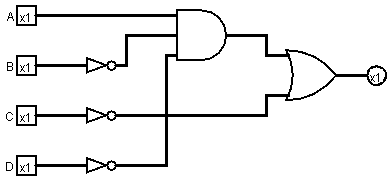
ÖMER FARUK BİTİKÇİOĞLU

161044010

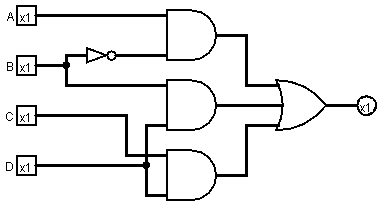
**Question 1**



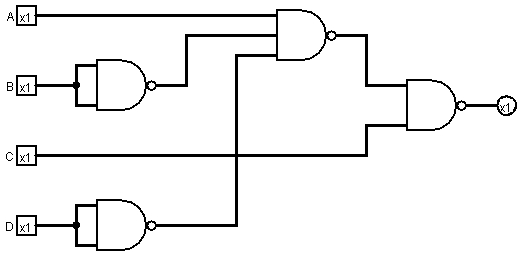
1. **F1:**

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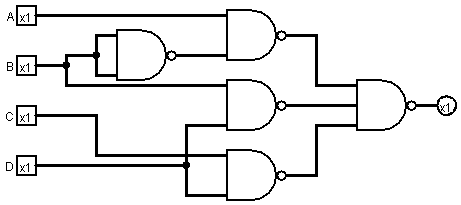
**F2:**



1. **F1 by NAND gates:**

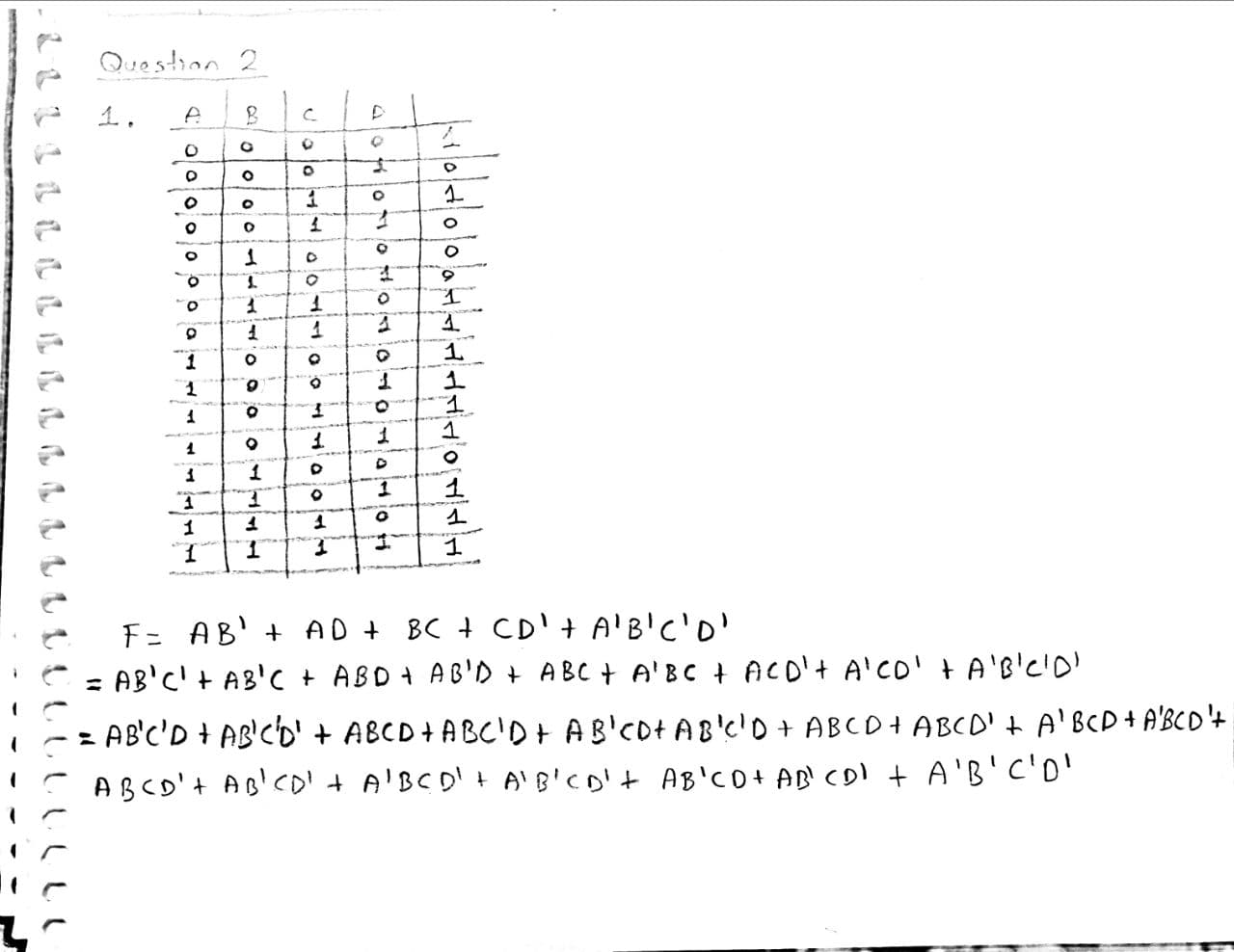
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**F2 by NAND gates:**

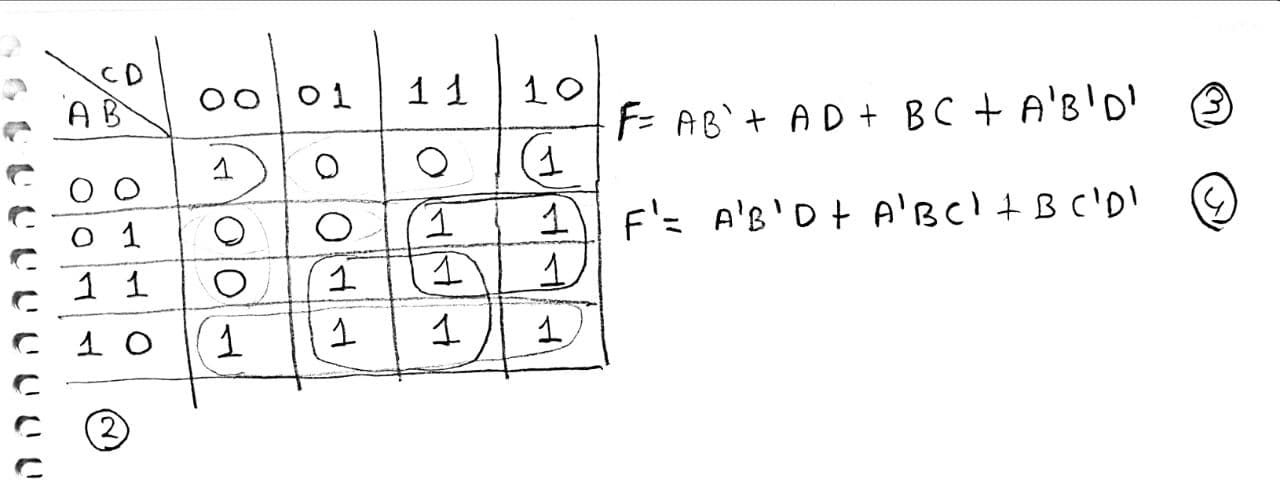
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**Question 2**

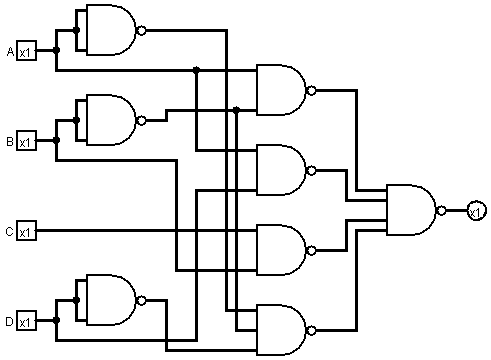
**1 -)**

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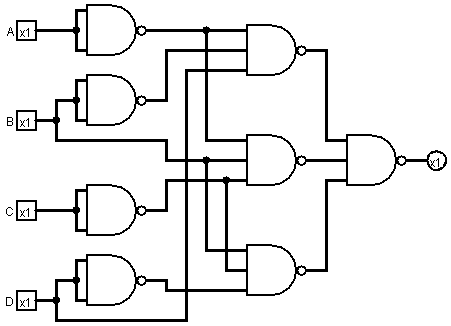
**2, 3, 4 -)**

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**5 -) F by NAND gates:**



**F’ by NAND gates:**

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