Voltage-Controlled Current Source (VCCS) Design – Simulation Results
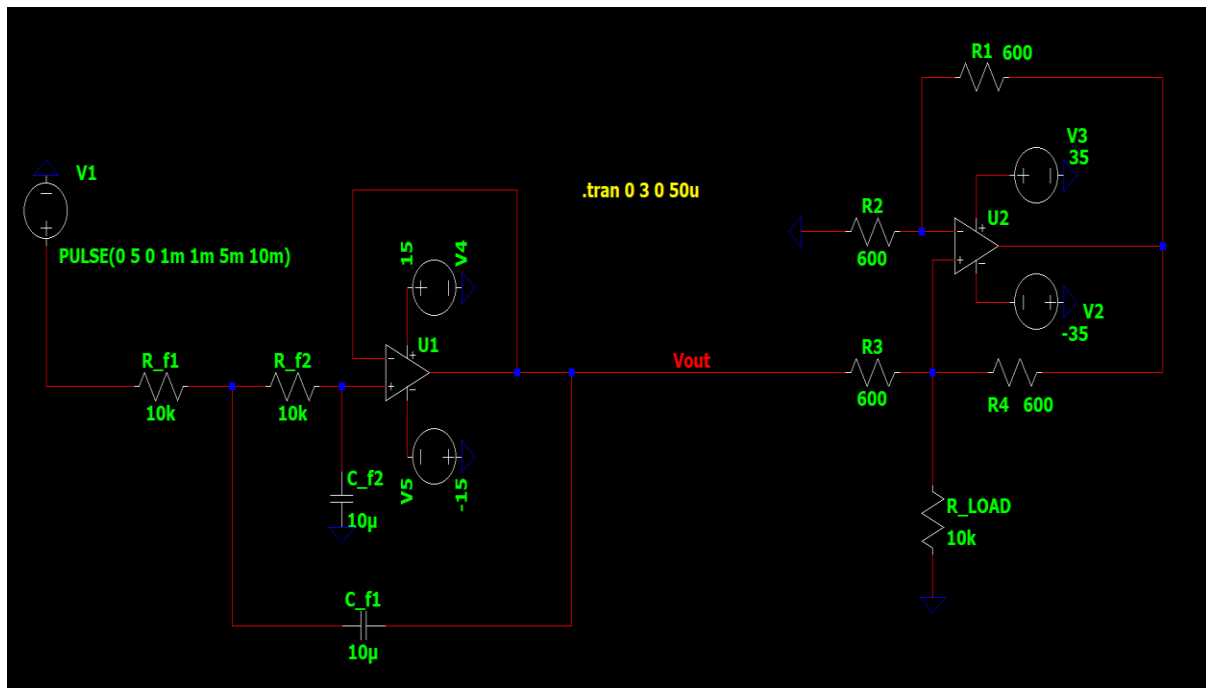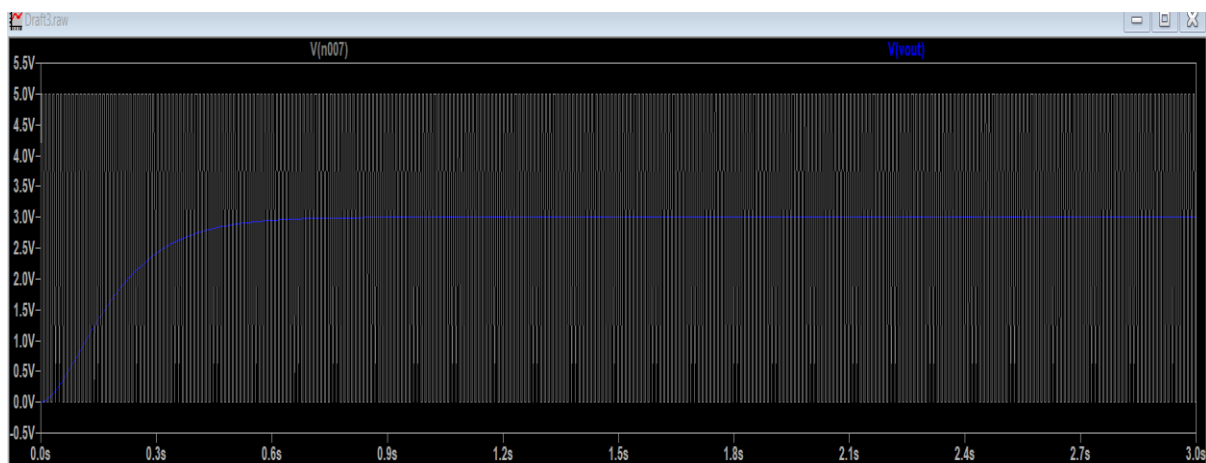
CIRCUIT DIAGRAM :-
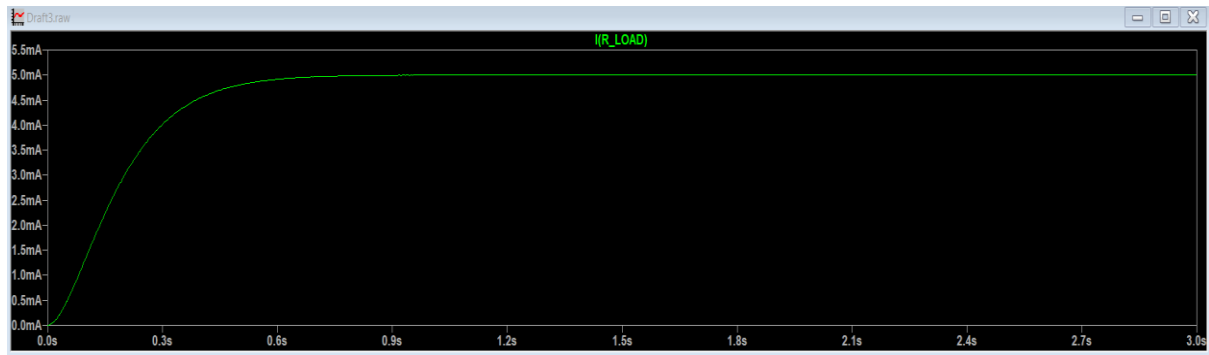

Filtering Of PWM :-

- **60% Duty Cycle at 100Hz**
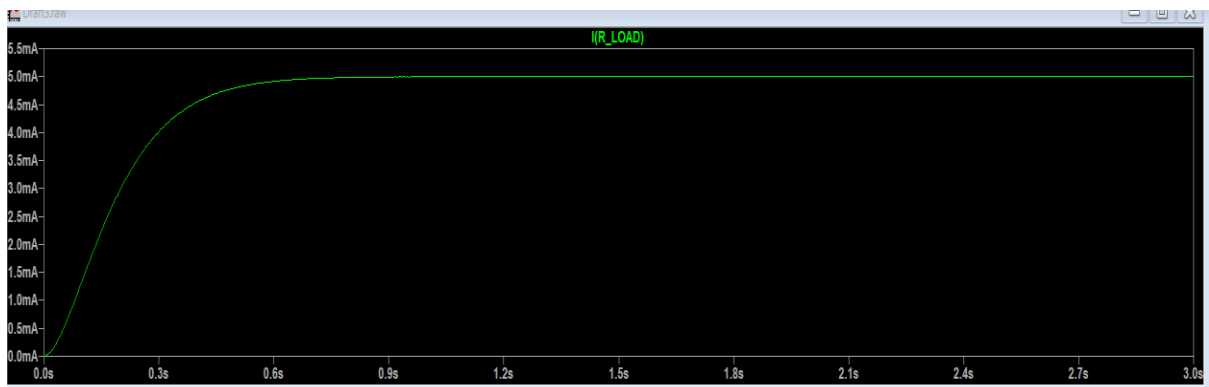- **0-5V PWM**

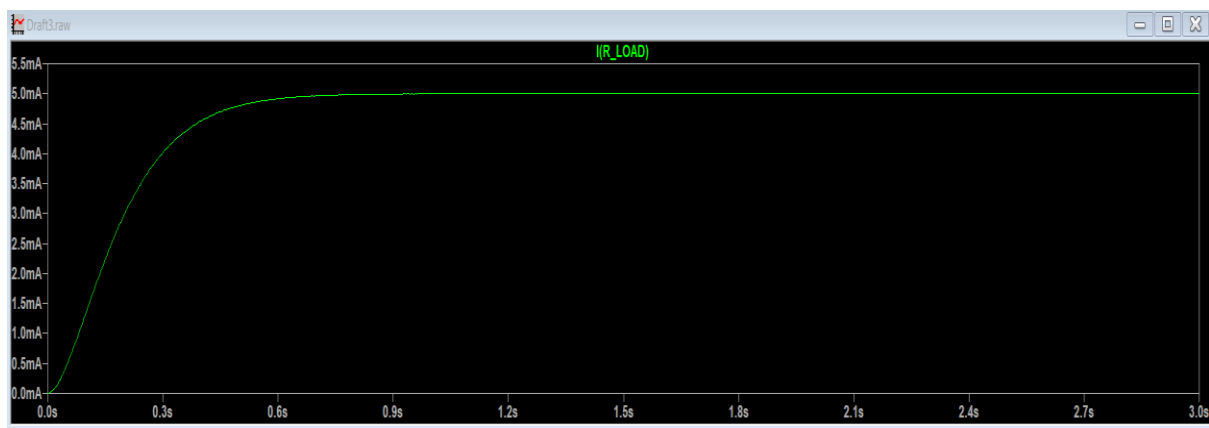## Output Current for R_load = 1kΩ :-

- **0-5V PWM and 100Hz**

Output Current for R_load = 5kΩ :-
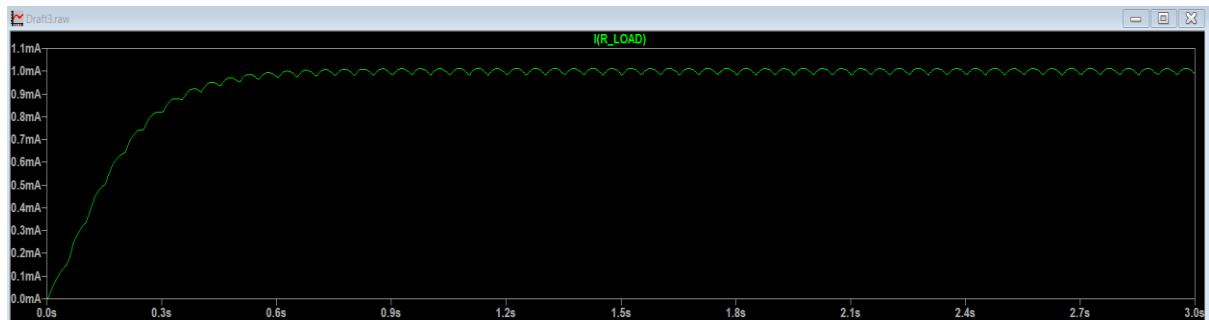
- **0-5V PWM and 100Hz**

Output Current for R_load = 10kΩ :-
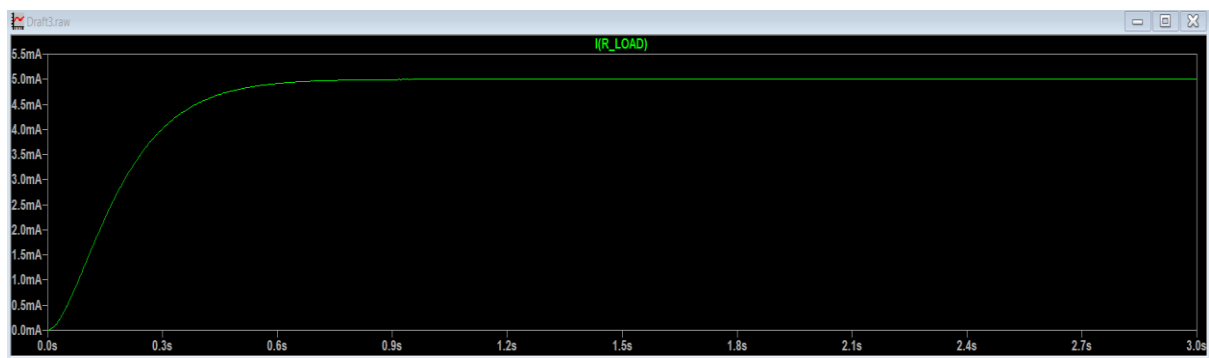
- **0-5V PWM and 100Hz**

Output Current for PWM Frequency = 20Hz :-

- **0-5V PWM**

Output Current for PWM Frequency = 100Hz :-

- **0-5V PWM**

Output Current for PWM Frequency = 200Hz :-

- **0-5V PWM**