

STP0144B-128128 Series TFT LCD PANEL USER MANUAL

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Reference Controller Datasheet

TFT LCD Panel Selection Guide

ST7735S

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1.General Description 基本描述

MODEL NO 产品型号	STP0144B-128128 YT144S025
Display Mode 显示模式	Transmissive 全透
Display Format 显示格式	Graphic 128RGB*128 Dot-matrix 128xRGBx128 图形点阵
Input Data 显示屏接口类型	SPI-4 line interface SPI-4 线接口
Viewing Direction 视角方向	12 o'clock 12 点钟
Drive 显示屏驱动芯片	ST7735S (台湾矽创)

2. Mechanical Specification 机械规格

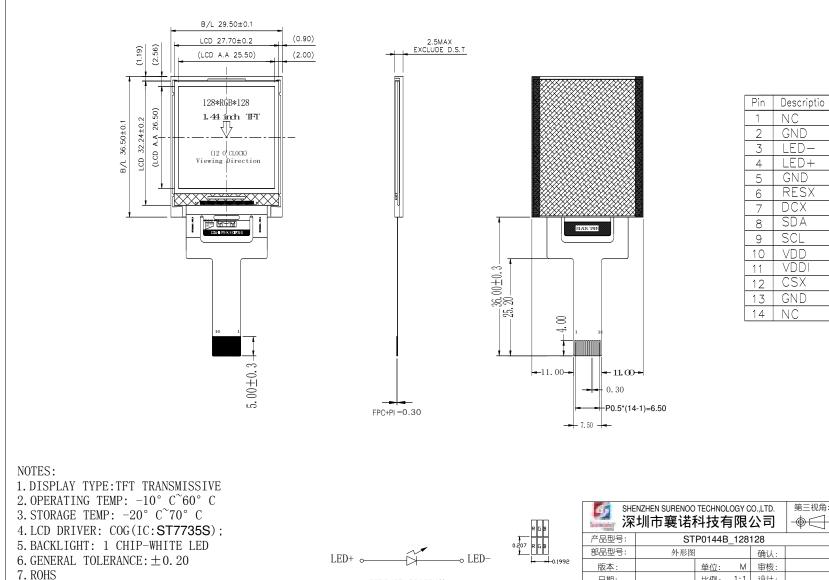
Item	Specifications	Unit
Dimensional outline	29.50(W)*36.50(H)*2.50(T)(不带触摸)	
显示屏外围尺寸	(FPC not include)	mm
Resolution	128RGB*128	dots
分辨率		dots
LCD Active area	25.50(W)*26.50 (H)	mm
显示尺寸		mm
Pixel size	0.199(W)*0.207(H)	mm
像素尺寸		mm

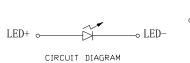
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3.Mechanical Dimension 机械尺寸图





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	产品型号:	STP0144B_128128						
	部品型号:	外形图		确认:				
2	版本:		単位:	М	审核:			
	⊟期:		比例:	1:1	设计:			
	UNMRKE	TOLERANCE:	±0.20	mm	页码:	1	OF	1





4. Electrical Maximum Ratings 电气极限

Item	Symbol	Min	Max	Unit	Note
项目	符号	最小值	最大值	单位	备注
Supply voltage (VDDI) 工作电压(VDDI)	V	1.8	3.3	V	-
Supply voltage(VDD) 工作电压(VDD)	V	2.8	3.3	V	-
Operating temperature 工作温度范围	T_{OPR}	-20	70	°C	-
Storage temperature 存储温度范围	T_{STR}	-30	80	°C	-

※NOTE: VDDI 和 VDD 可以直接连一起,共用一组(2.8V~3.3V)电压供电。

5. Brightness characteristic&Power dissipation 亮度特性&功耗

Item 项目	Symbol 符号	Min 最小值	Typical 典型值	Max 最大值	Unit
LED module Forward voltage LED 背光源正向电压	V _{LED}	2. 9	3.1	3.3	V
LED module current LED 背光源电流	I_{LED}	-	18	-	mA
LCD Surface Luminance 显示屏表面亮度	Ls	160	180	-	Cd/m²
LCD Surface brightness uniform LED 背光源均匀度	L_{D}	80	-	-	%
LCD power dissipation 显示屏总功耗	P _{LCD}	-	0.060	-	W

 $\textcolor{red}{\raisebox{0.5ex}{\times}} NOTE \text{:} P_{LCD=VDD} * (I_{LED} \text{-} I_{LCD})$

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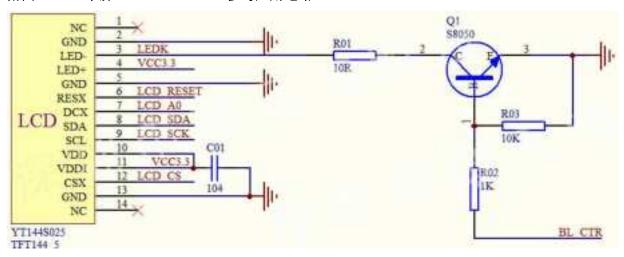
6. Module Function Description 显示屏脚位定义

PIN No. 引脚序号	Symbol 引脚名称	Description 作用描述	Notes 备注
1	NC	No connection (空脚)	-
2	GND	Ground (接地脚)	-
3	LED-	Cathode of Backlight (背光负极供电脚)	-
4	LED+	Anode of Backlight (2.9V-3.3V Typical:3.1V) (背光正极供电脚,电压范围:2.9-3.3V,典型值:3.1V)	-
5	GND	Ground (接地脚)	-
6	RESX	-This signal will reset the device and it must be applied to properly initialize the chipSignal is active low. (显示屏复位脚,低电平有效)	-
7	DCX	-Display data/command selection pin in 4-line SPI interface. (4线 SPI 接口显示数据或显示指令选择脚) DCX='1': display data or parameter. (DCX=1:选择显示数据或参数寄存器) DCX='0': command data (DCX=0:选择指令寄存器)	-
8	SDA	-Serial input/output signal in 4-line SPI. (4线 SPI 串口数据输入/输出脚)	-
9	SCL	-4-line SPI serial interface clock. (4线 SPI 串口时钟脚)	-
10	VDD	Power Supply for Analog, Digital System and Booster Circuit. (显示屏主电源供电脚 2.8-3.3V)	-
11	VDDI	Power Supply for I/O System. (显示屏 I/O 口电源供电脚 1.8-3.3V)	-
12	CSX	-Chip selection pin Low enable. High disable. (显示屏驱动芯片选脚,低电平使能)	-
13	GND	Ground (接地脚)	-
14	NC	No connection (空脚)	-

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附图 6-1: 显示屏 STP0144B-128128 参考应用电路

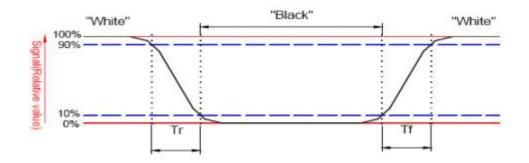


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7.Response time&Contrast ratio 响应时间与对比度

Itom	Symbol	Condition		Remark		Unit
Item 项目	Symbol 符号	条件	Min. 最小值	Typ. 典型值	Max. 最大值	单位
Response time 响应时间	Tr+Tf	θ =0°	-	30	60	ms
Contrast ratio 对比度	CR	θ =0°	200	300	-	-



响应时间图示

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Contrast ratio (CR)=

Brightness on the "white" state

Brightness on the "black" state

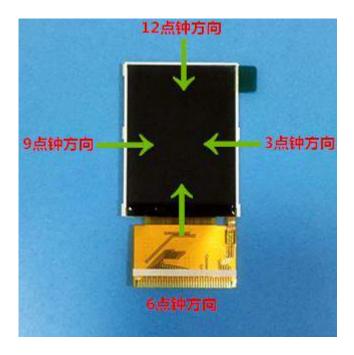
对比度计算公式

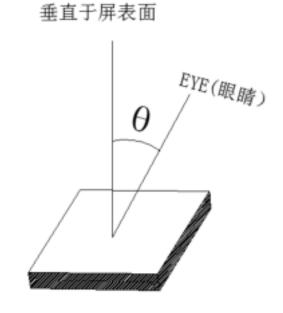
8.Viewing Angle 视角宽度

Item	Cymbol	ymbol Condition 符号 条件		Unit		
项目			Min. 最小值	Typ. 典型值	Max. 最大值	単位
	Top 12 点钟方向	CR≥10 对比度大于等于 10	30	40	-	
Viewing	Bottom 6 点方向	CR≥10 对比度大于等于 10	40	50	-	Deg.
angle 视角宽度	Left 9 点钟方向	CR≥10 对比度大于等于 10	40	50	-	度
	Right 3 点钟方向	CR≥10 对比度大于等于 10	40	50	-	

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NOTE:3 点,6 点,9 点,12 点方向视角的大小指的是垂直于屏表面的线眼睛视线之间的夹角(0)。

9. Reliability Trial 可靠性实验

NO.	ITEM	CONDITION	CRITERION
序号	实验项目	实验环境	实验规范
1	High Temperature Non-Operating Test	80°C*120Hrs	No Defect Of
1	高温存储实验	00 C 120111S	Operational
2	Low Temperature Non-Operating Test	-30°C*120Hrs	Function In Room
2	低温存储实验	-50 C · 120HIS	Temperature Are
3	High Temperature/Humidity Non Operating Test	60°C*90%RH*120Hrs	Allowable
3	高温高湿实验	00 C '90%KH '120HIS	室温运行功能无缺陷
4	High Temperature Operating Test	70°C*72Hrs	
4	高温工作实验	70 C * 721118	
5	Low Temperature Operating Test	-20°C *72Hrs	
3	低温工作实验	-20 C · /2HIS	
6	Thermal Shock Test	-20 °C (30Min) ↔70 °C	
	热冲实验	(30Min) *10CYCLES	

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10.Inspection standards 检验标准

10.1Glass defect

NO	Defect item	Criteria	Remark
1	Dimension Unconformity (Major defect)	By Engineering Drawing	
2	Cracks (Major defect)	 Linear cracks panel Reject Nonlinear crack contrast by limited sample 	
3	Glass extrude the conductive area (minor defect)	a: disregards and no influence assemblage. 1) b≤1/3Pin width(non bonding area) 【Accept】 2)bonding area≤0. 5mm 【Accept】	A: Length, b: Width
4	Pin-side ,conductive area damaged (minor defect)	(a c: disregards) b≤1/3of effective length for bonding electrode 【Accept】	a: length, b: Width, c: Thickness
5	Pin-side,non-conductive area damaged (minor defect)	1)Damage area don't touch the ITO (Inclueling contraposition mark, except scribing mark) [Accept] 2)C <t 3)c="T" 3of="" 4)a="" [accept]="" b="" b≤bm1="" disregards<="" glue="" not="" seal="" td="" the="" touch="" width=""><td>a: Length, b: Width c: Thickness</td></t>	a: Length, b: Width c: Thickness
6	Non-pin-side damage (minor defect)	c <t 1="" 1)b="" 3bm="" [reject]="" [reject]<="" b="" c="T" exceeds="" glue="" not="" seal="" td="" the="" touch=""><td>c: Thickness b: width of</td></t>	c: Thickness b: width of

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10.2LCD appearance defect(View area)

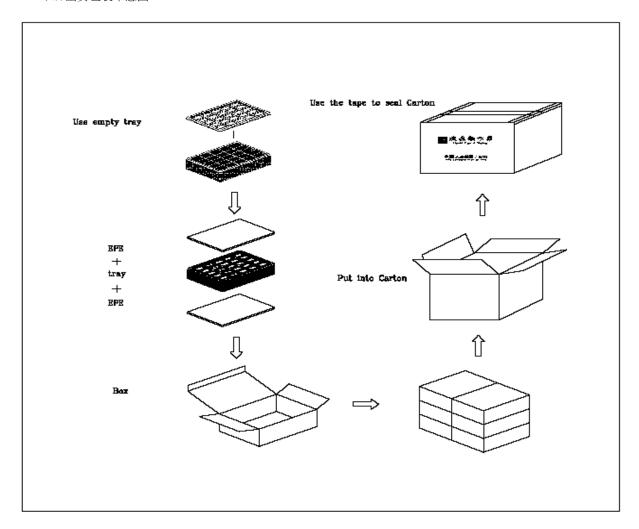
NO	Defect item	Criteria		Remark
		Specification	Allowable	note1:L: Length, W: Width
	T:'1 1	W ≤ 0.03mm	disregard	note2: disregard if out of AA
	Fiber glass cratch polarizer	$0.03 \text{mm} < W \le 0.05 \text{mm};$	2	← т →
1	scratch/folded	L≦3.0mm	L ≤ 3.0mm	
	(minor defect)	0.05 mm \leq W \leq 0.1mm;	1	V 3\\
	(illilloi delect)	L≦3.0mm	1	W
		W>0.1mm;L>3.0mm	0	W
	Polarizer bubble	φ ≤ 0.2mm	disregard	note1: $\Phi = (L+W)/2$, L:Length,
2	concave and convex (minor defect)	0.2 mm $< \phi \le 0.3$ mm	2	W :Width
		0.3 mm $< \phi \le 0.5$ mm	1	note2:disregard if out of AA
	(minor defect)	0.5mm< φ	0	
		$\Phi \leq 0.15$ mm	disregard	note2:disregard if out of AA
	Black dots, dirty dots,	0.15 mm $< \phi \le 0.25$ mm	2	
3	impurities, eye winker	0.25 mm< $\phi \leq 0.3$ mm	1	<u>, φ</u>
	(minor defect)	0.3mm< φ	0	φ
		φ ≦0.1mm	disregard	note1: $\Phi = (L+W)/2$, L=Length,
4	Polarizer prick	0.1 mm< $\phi \leq 0.25$ mm	3	W=Width
	(minor defect)	Ф>0.25mm	0	note2:the distance between two dots>5mm

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11.Package Method 包装方法

显示屏出货包装示意图:



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