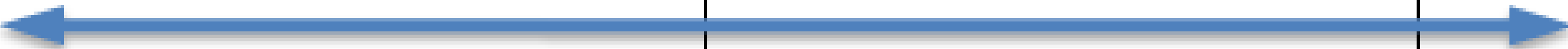









	Year 1	Year 2	Year 3
Task 1. Design Methodology			
1.1. Device modeling			
1.2. Memory design flow			
1.3. Design space exploration			
Task 2. Circuit Techniques			
2.1. Reliability			
2.2. Yield			
2.3. Density			
Student #	NYU:2, PSU: 1	NYU:1, PSU:1	NYU:1, PSU:1