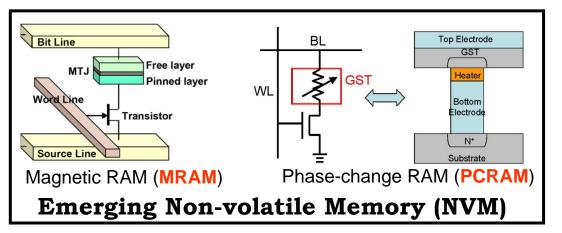


What is the impact of emerging NVM technologies on computer memory hierarchies?

25000~2000000



~100

Latency:

Cycles)

~10

Proposed MAAEMO Project:

Task 1: Modeling

>5000000

Task 2: Architecture

Task 3: Application