Omnik Maurya

Technology motivated student looking for internship oppurtunities to utilize my engineering knowledge for growth of an organization as well as to tailor my own skills.

- Bilt, Bhigwan, Pune, Maharastra 413105
- 8287711562
- https://omnitrix123.github.io
- 212221008@nitdelhi.ac.in
- 20 September 1997
- Omnik Maurya
- omnitrix123

SKILLS

ASIC DESIGN FLOW USING OPENROAD
Beginner

COMPUTER ARCHITECTURE
Intermediate

CADENCE VIRTUOSO
Beginner

VERILOG HDL
Intermediate

LT SPICE
Intermediate

C
Intermediate

SYSTEM VERILOG
Beginner

Linux
Intermediate

LANGUAGES

ENGLISH
Proficient

HINDI
Native

MARATHI
Limited

HOBBIES

PLAYING BASKETBALL

WATCHING TRAVEL AND SCIENCE DOCUMENTARIES

LISTENING TO MUSIC

CERTIFICATIONS

FUNDAMENTALS OF DEEP LEARNING AND COMPUTER VISION
NVIDIAs Deep Learning Institute
June 2019

FUNDAMENTALS OF ACCELERATED COMPUTING USING CUDA C
NVIDIAs Deep Learning Institute
August 2019

WORK EXPERIENCE

CHEGG INDIA PRIVATE LIMITED
Subject Matter Expert
(February 2021 - September 2021)

FREELANCING AND GAME DEVELOPMENT
Developer
(March 2020 - July 2020)

I was working on developing websites and portfolios. Also I learnt a lot about 2D game development using Lua language and love2D game engine.

COGNIFRONT SOFTWARE LABS
Intern
(June 2018 - June 2018)

This was internship targeting Machine learning and Computer Vision. We build a web based interactive application that was use to classify the Human (Boy or Girl).

UL AUTOMATION PRIVATE LIMITED
Intern
(June 2017 - June 2017)

Here at UL automation apart from technical knowledge about Soldering, PCB manufacturing, Testing, Quality Control and Wave Soldering, I understood the work culture of an Electronics Product manufacturing company.

EDUCATION

NATIONAL INSTITUTE OF TECHNOLOGY DELHI
Master's Degree Mtech VLSI Design
(August 2021 - July 2023)
7.28 (current)

VISHWAKARMA INSTITUTE OF INFORMATION TECHNOLOGY PUNE
Bachelor's Degree Electronics & Telecommunications Engineering
(June 2015 - July 2019)
8.81

GURU NANAK KHALSA COLLEGE OF ARTS, SCIENCE AND COMMERECE, MUMBAI
High School Bifocal Science
(July 2013 - May 2015)
79.8%

VIDYA PRATISHTHAN'S ENGLISH MEDIUM SCHOOL, BARAMATI
Primary and Secondary Schooling CBSE
(June 2000 - June 2013)
9.8

PROJECTS

IMPLEMENTATION OF NEURAL NETWORKS ON FPGAs FOR INFERENCING.

This is ongoing Mtech Final year project.

SINGLE CYCLE MIPS BASED MICROPROCESSOR DESIGN USING VERILOG HDL

I have implemented the entire base MIPS ISA.

DEVELOPMENT OF ELECTRONIC VISUAL ASSISTANCE

Final Year BE Project.

AUTONOMOUS VEHICLE

Third year mini project.

LOW COST MOBILE BASED SPECTROPHOTOMETER

AWARDS

WITRIFIED First Position
College Of Engineering Pune

VOLTUS: MICROAPPS Third Position
College of Engineering Pune

IMPEDANCE Second Position
Army Institute of Technology, Pune

CIRCUIT EYE Second Position
Vishwakarma Institute of Technology Pune

CIRCUIT FIXER 2 Third Position
College of Engineering Pune

SCITECH 2016 First Position
Vishwakarma Institute of Information Technology Pune