



1. Description

1.1. Project

Project Name	Elevator
Board Name	custom
Generated with:	STM32CubeMX 6.15.0
Date	08/26/2025

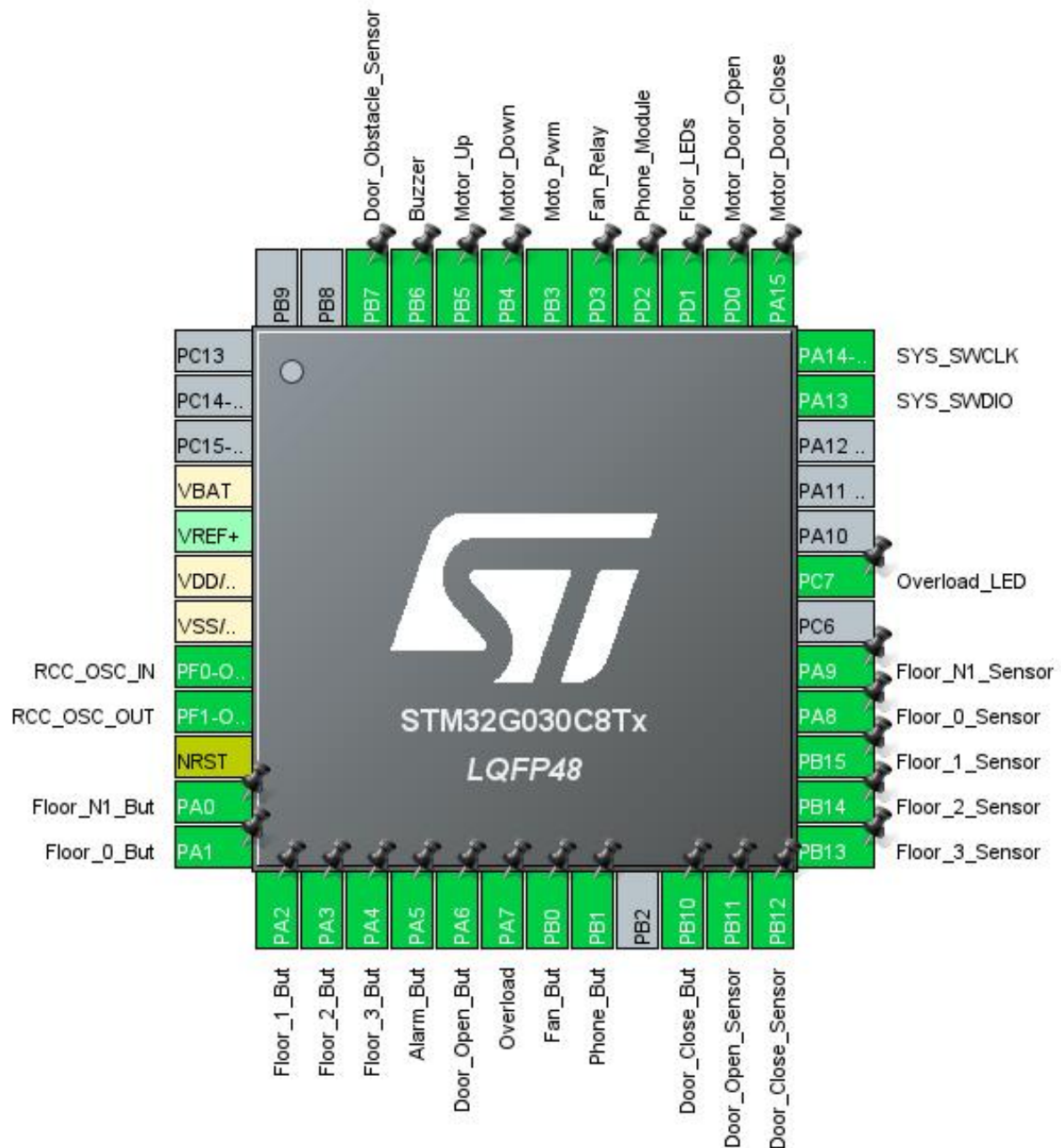
1.2. MCU

MCU Series	STM32G0
MCU Line	STM32G0x0 Value line
MCU name	STM32G030C8Tx
MCU Package	LQFP48
MCU Pin number	48

1.3. Core(s) information

Core(s)	ARM Cortex-M0+
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2. Pinout Configuration



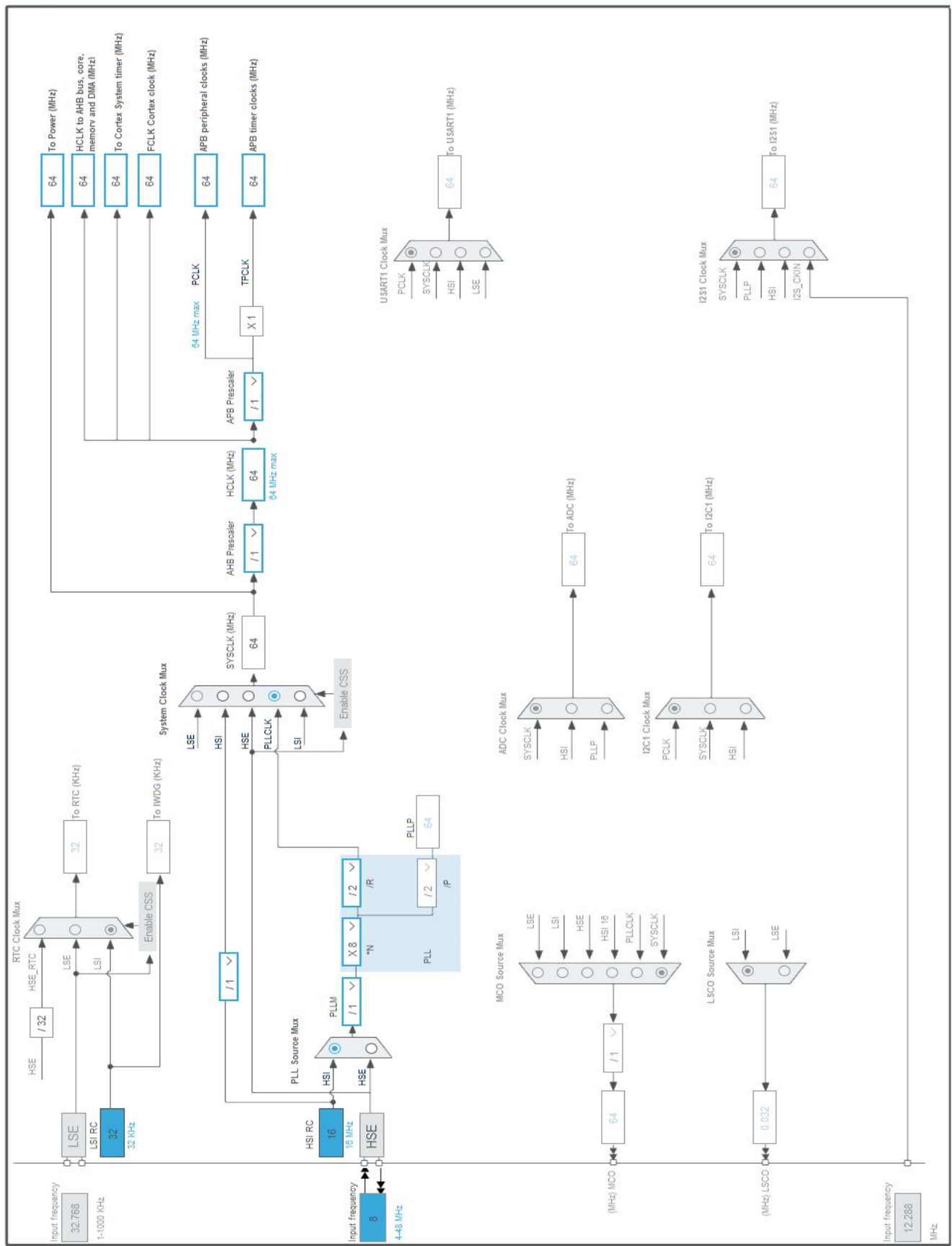
3. Pins Configuration

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
4	VBAT	Power		
6	VDD/VDDA	Power		
7	VSS/VSSA	Power		
8	PF0-OSC_IN (PF0)	I/O	RCC_OSC_IN	
9	PF1-OSC_OUT (PF1)	I/O	RCC_OSC_OUT	
10	NRST	Reset		
11	PA0	I/O	GPIO_EXTI0	Floor_N1_But
12	PA1	I/O	GPIO_EXTI1	Floor_0_But
13	PA2	I/O	GPIO_EXTI2	Floor_1_But
14	PA3	I/O	GPIO_EXTI3	Floor_2_But
15	PA4	I/O	GPIO_EXTI4	Floor_3_But
16	PA5	I/O	GPIO_EXTI5	Alarm_But
17	PA6	I/O	GPIO_EXTI6	Door_Open_But
18	PA7	I/O	ADC1_IN7	Overload
19	PB0 *	I/O	GPIO_Input	Fan_But
20	PB1 *	I/O	GPIO_Input	Phone_But
22	PB10	I/O	GPIO_EXTI10	Door_Close_But
23	PB11	I/O	GPIO_EXTI11	Door_Open_Sensor
24	PB12	I/O	GPIO_EXTI12	Door_Close_Sensor
25	PB13	I/O	GPIO_EXTI13	Floor_3_Sensor
26	PB14	I/O	GPIO_EXTI14	Floor_2_Sensor
27	PB15	I/O	GPIO_EXTI15	Floor_1_Sensor
28	PA8	I/O	GPIO_EXTI8	Floor_0_Sensor
29	PA9	I/O	GPIO_EXTI9	Floor_N1_Sensor
31	PC7 *	I/O	GPIO_Output	Overload_LED
35	PA13	I/O	SYS_SWDIO	
36	PA14-BOOT0	I/O	SYS_SWCLK	
37	PA15 *	I/O	GPIO_Output	Motor_Door_Close
38	PD0 *	I/O	GPIO_Output	Motor_Door_Open
39	PD1 *	I/O	GPIO_Output	Floor_LEDs
40	PD2 *	I/O	GPIO_Output	Phone_Module
41	PD3 *	I/O	GPIO_Output	Fan_Relay
42	PB3	I/O	TIM1_CH2	Moto_Pwm
43	PB4 *	I/O	GPIO_Output	Motor_Down
44	PB5 *	I/O	GPIO_Output	Motor_Up
45	PB6 *	I/O	GPIO_Output	Buzzer

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
46	PB7	I/O	GPIO_EXTI7	Door_Obstacle_Sensor

* The pin is affected with an I/O function

4. Clock Tree Configuration



1. Power Consumption Calculator report

1.1. Microcontroller Selection

Series	STM32G0
Line	STM32G0x0 Value line
MCU	STM32G030C8Tx
Datasheet	DS12991_Rev1

1.2. Parameter Selection

Temperature	25
Vdd	3.0

1.3. Battery Selection

Battery	Li-SOCL2(AAA700)
Capacity	700.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	10.0 mA
Max Pulse Current	30.0 mA
Cells in series	1
Cells in parallel	1

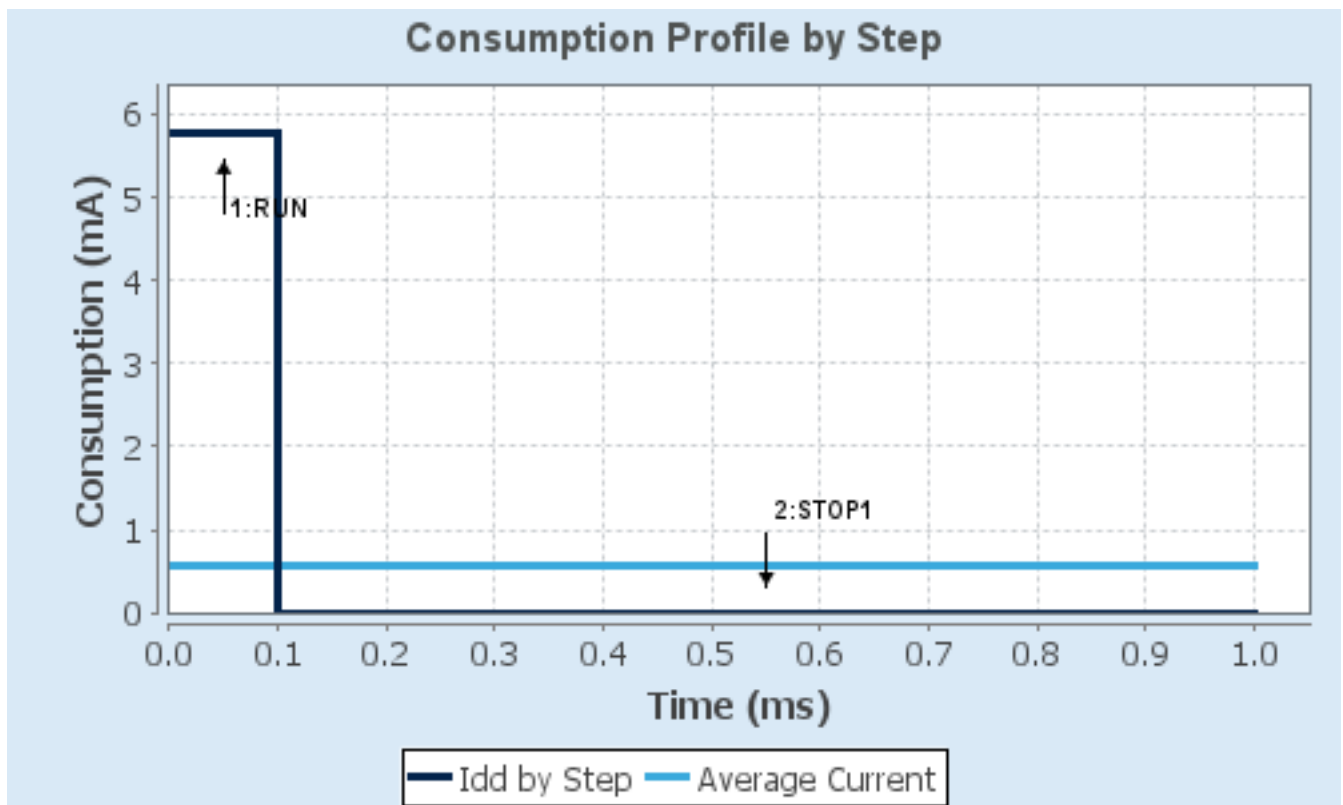
1.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP1
Vdd	3.0	3.0
Voltage Source	Battery	Battery
Range	Range1-High	NoRange
Fetch Type	FLASH	Flash-PowerDown
CPU Frequency	64 MHz	16 MHz
Clock Configuration	HSI PLL	HSI
Clock Source Frequency	16 MHz	16 MHz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	5.77 mA	3.7 μ A
Duration	0.1 ms	0.9 ms
DMIPS	80.0	0.0
Ta Max	128.7	130
Category	In DS Table	In DS Table

1.5. Results

Sequence Time	1 ms	Average Current	580.33 μ A
Battery Life	1 month, 19 days, 18 hours	Average DMIPS	80.0 DMIPS

1.6. Chart



2. Software Project

2.1. Project Settings

Name	Value
Project Name	Elevator
Project Folder	C:\Users\Umrn\stm32_works\G030C8T6\Elevator
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_G0 V1.6.2
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

2.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	No

2.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_TIM1_Init	TIM1
4	MX_ADC1_Init	ADC1

3. Peripherals and Middlewares Configuration

3.1. ADC1

mode: IN7

3.1.1. Parameter Settings:

ADC_Settings:

Clock Prescaler	Synchronous clock mode divided by 2
Resolution	ADC 12-bit resolution
Data Alignment	Right alignment
Sequencer	Sequencer set to fully configurable
Scan Conversion Mode	Disabled
Continuous Conversion Mode	Disabled
Discontinuous Conversion Mode	Disabled
DMA Continuous Requests	Disabled
End Of Conversion Selection	End of single conversion
Overrun behaviour	Overrun data preserved
Low Power Auto Wait	Disabled
Auto Off	Disabled
Oversampling Mode	Disabled

ADC_Regular_ConversionMode:

SamplingTime Common 1	7.5 Cycles *
SamplingTime Common 2	7.5 Cycles *
Number Of Conversion	1
External Trigger Conversion Source	Regular Conversion launched by software
External Trigger Conversion Edge	None
Trigger Frequency	High frequency
Rank	1
Channel	Channel 7
Sampling Time	Sampling time common 1

Analog Watchdog 1:

Enable Analog WatchDog1 Mode	false
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Analog Watchdog 2:

Enable Analog WatchDog2 Mode	false
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Analog Watchdog 3:

Enable Analog WatchDog3 Mode	false
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3.2. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

3.2.1. Parameter Settings:

System Parameters:

VDD voltage (V)	3.3
Instruction Cache	Enabled
Prefetch Buffer	Enabled
Data Cache	Enabled
Flash Latency(WS)	2 WS (3 CPU cycle)

RCC Parameters:

HSI Calibration Value	64
HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000

Power Parameters:

Power Regulator Voltage Scale	Power Regulator Voltage Scale 1
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Peripherals Clock Configuration:

Generate the peripherals clock configuration	TRUE
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3.3. SYS

mode: Debug

Timebase Source: SysTick

3.4. TIM1

Clock Source : Internal Clock

Channel2: Output Compare CH2

3.4.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value)	319 *
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value)	999 *
Internal Clock Division (CKD)	No Division
Repetition Counter (RCR - 16 bits value)	0
auto-reload preload	Disable

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)	Disable (Trigger input effect not delayed)
Trigger Event Selection TRGO	Reset (UG bit from TIMx_EGR)
Trigger Event Selection TRGO2	Reset (UG bit from TIMx_EGR)

Break And Dead Time management - BRK Configuration:

BRK State	Disable
BRK Polarity	High
BRK Filter (4 bits value)	0
BRK Sources Configuration	
- Digital Input	Disable

Break And Dead Time management - BRK2 Configuration:

BRK2 State	Disable
BRK2 Polarity	High
BRK2 Filter (4 bits value)	0
BRK2 Sources Configuration	
- Digital Input	Disable

Break And Dead Time management - Output Configuration:

Automatic Output State	Disable
Off State Selection for Run Mode (OSSR)	Disable
Off State Selection for Idle Mode (OSSI)	Disable
Lock Configuration	Off

Clear Input:

Clear Input Source	Disable
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Output Compare Channel 2:

Mode	Frozen (used for Timing base)
Pulse (16 bits value)	0
Output compare preload	Disable
CH Polarity	High
CH Idle State	Reset

* User modified value

4. System Configuration

4.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
ADC1	PA7	ADC1_IN7	Analog mode	No pull-up and no pull-down	n/a	Overload
RCC	PF0-OSC_IN (PF0)	RCC_OSC_IN	n/a	n/a	n/a	
	PF1-OSC_OUT (PF1)	RCC_OSC_OUT	n/a	n/a	n/a	
SYS	PA13	SYS_SWDIO	n/a	n/a	n/a	
	PA14-BOOT0	SYS_SWCLK	n/a	n/a	n/a	
TIM1	PB3	TIM1_CH2	Alternate Function Push Pull	No pull-up and no pull-down	Low	Moto_Pwm
GPIO	PA0	GPIO_EXTI0	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	Floor_N1_But
	PA1	GPIO_EXTI1	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	Floor_0_But
	PA2	GPIO_EXTI2	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	Floor_1_But
	PA3	GPIO_EXTI3	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	Floor_2_But
	PA4	GPIO_EXTI4	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	Floor_3_But
	PA5	GPIO_EXTI5	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	Alarm_But
	PA6	GPIO_EXTI6	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	Door_Open_But
	PB0	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	Fan_But
	PB1	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	Phone_But
	PB10	GPIO_EXTI10	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	Door_Close_But
	PB11	GPIO_EXTI11	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	Door_Open_Sensor
	PB12	GPIO_EXTI12	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	Door_Close_Sensor
	PB13	GPIO_EXTI13	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	Floor_3_Sensor
	PB14	GPIO_EXTI14	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	Floor_2_Sensor
	PB15	GPIO_EXTI15	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	Floor_1_Sensor
	PA8	GPIO_EXTI8	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	Floor_0_Sensor

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
	PA9	GPIO_EXTI9	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	Floor_N1_Sensor
	PC7	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	Overload_LED
	PA15	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	Motor_Door_Close
	PD0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	Motor_Door_Open
	PD1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	Floor_LEDs
	PD2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	Phone_Module
	PD3	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	Fan_Relay
	PB4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	Motor_Down
	PB5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	Motor_Up
	PB6	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	Buzzer
	PB7	GPIO_EXTI7	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	Door_Obstacle_Sensor

4.2. DMA configuration

nothing configured in DMA service

4.3. NVIC configuration

4.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
System service call via SWI instruction	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	3	0
EXTI line 0 and line 1 interrupts	true	0	0
EXTI line 2 and line 3 interrupts	true	0	0
EXTI line 4 to 15 interrupts	true	0	0
Flash global interrupt	unused		
RCC global interrupt	unused		
ADC1 interrupt	unused		
TIM1 break, update, trigger and commutation interrupts	unused		
TIM1 capture compare interrupt	unused		

4.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
System service call via SWI instruction	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true
EXTI line 0 and line 1 interrupts	false	true	true
EXTI line 2 and line 3 interrupts	false	true	true
EXTI line 4 to 15 interrupts	false	true	true

* User modified value

5. System Views

5.1. Category view

5.1.1. Current

Middleware

System Core

Analog

Timers

Connectivity

Multimedia

Computing

DMA

ADC1



TIM1



GPIO



IIVIC



RCC



SYS



6. Docs & Resources

Type	Link
IBIS models	https://www.st.com/resource/en/ibis_model/stm32g0_ibis.zip
System View Description	https://www.st.com/resource/en/svd/stm32g0-svd.zip
Presentations	https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf
Presentations	https://www.st.com/resource/en/product_presentation/stm32_eval-tools_portfolio.pdf
Presentations	https://www.st.com/resource/en/product_presentation/stm32_stm8_functional-safety-packages.pdf
Presentations	https://www.st.com/resource/en/product_presentation/stm32g0_marketing_pres.pdf
Presentations	https://www.st.com/resource/en/product_presentation/stm32-usb-c-pd-solutions-presentation.pdf
Presentations	https://www.st.com/resource/en/product_presentation/stm32-stm8_software_development_tools.pdf
Presentations	https://www.st.com/resource/en/product_presentation/microcontrollers-stm32-family-overview.pdf
Presentations	https://www.st.com/resource/en/product_presentation/microcontrollers-stm32-entry-level-graphics.pdf
Brochures	https://www.st.com/resource/en/brochure/products-and-solutions-for-plcs-and-smart-i-os.pdf
Flyers	https://www.st.com/resource/en/flyer/flstm32g0.pdf
Flyers	https://www.st.com/resource/en/flyer/flstm32nucleo.pdf
Flyers	https://www.st.com/resource/en/flyer/flstm32trust.pdf
Flyers	https://www.st.com/resource/en/flyer/flstpfc11120.pdf
Security Bulletin	https://www.st.com/resource/en/technical_note/tn1489-security-bulletin-tn1489stpsirt-physical-attacks-on-stm32-and-stm32cube-firmware-stmicroelectronics.pdf
Application Notes	https://www.st.com/resource/en/application_note/an1709-emc-design-

guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf

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Application Notes https://www.st.com/resource/en/application_note/an4221-i2c-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf

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Application Notes https://www.st.com/resource/en/application_note/an4803-highspeed-si-simulations-using-ibis-and-boardlevel-simulations-using-hyperlynx-si-on-stm32-mcus-and-mpus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4989-stm32-microcontroller-debug-toolbox-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an5027-interfacing-pdm-digital-microphones-using-stm32-mcus-and-mpus-stmicroelectronics.pdf

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Application Notes https://www.st.com/resource/en/application_note/an5145-migration-of-

applications-from-stm32f0-series-to-stm32g0-series--
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Application Notes https://www.st.com/resource/en/application_note/an4991-how-to-wake-up-an-stm32-microcontroller-from-lowpower-mode-with-the-usart-or-the-lpuart-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4838-introduction-to-memory-protection-unit-management-on-stm32-mcus-stmicroelectronics.pdf

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[dma-controller-for-stm32-mcus-stmicroelectronics.pdf](#)

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for related Tools
& Software

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& Software

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