Assignment 3

Link to this Assignment
Link to my of repository

Preparation

Source link

Multiplexer

Listing of architecture

```
-- architecture Behavioral of mux_2x1_4bit is

begin
process (s_i, b_i, a_i) -- sensitivity list
  begin
    if s_i = '0' then -- "0"
      y_o <= a_i;
    elsif s_i = '1' then
      y_o <= b_i;
    else
      y_o <= (others => '0');
    -- if s_i = '0' then
    -- y_o <= a_i;
    -- else
    -- y_o <= b_i;
    -- end if;
    -- y_o <= (others => '0');
    -- case s_i is
    -- when '0' => y_o <= a_i;
    -- when '1' => y_o <= b_i;
    -- when '-' => y_o <= (others => '0');
    -- end case;
    -- y_o <= (others => '0');
    -- with s_i select
    -- y_o <= a_i when '0',
    -- b_i when '1',
    -- (others => '0') when '-';
    -- y_o <= (others => '0');
    -- y_o <= a_i when s_i = '0' else
    -- b_i when s_i = '1' else
    -- (others => '0');
    -- y_o <= (others => '0');
    -- y_o <= a_i when s_i = '0' else b_i when s_i = '1' else
```