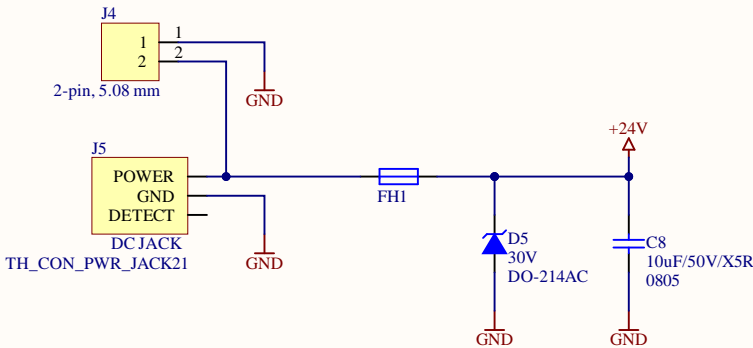
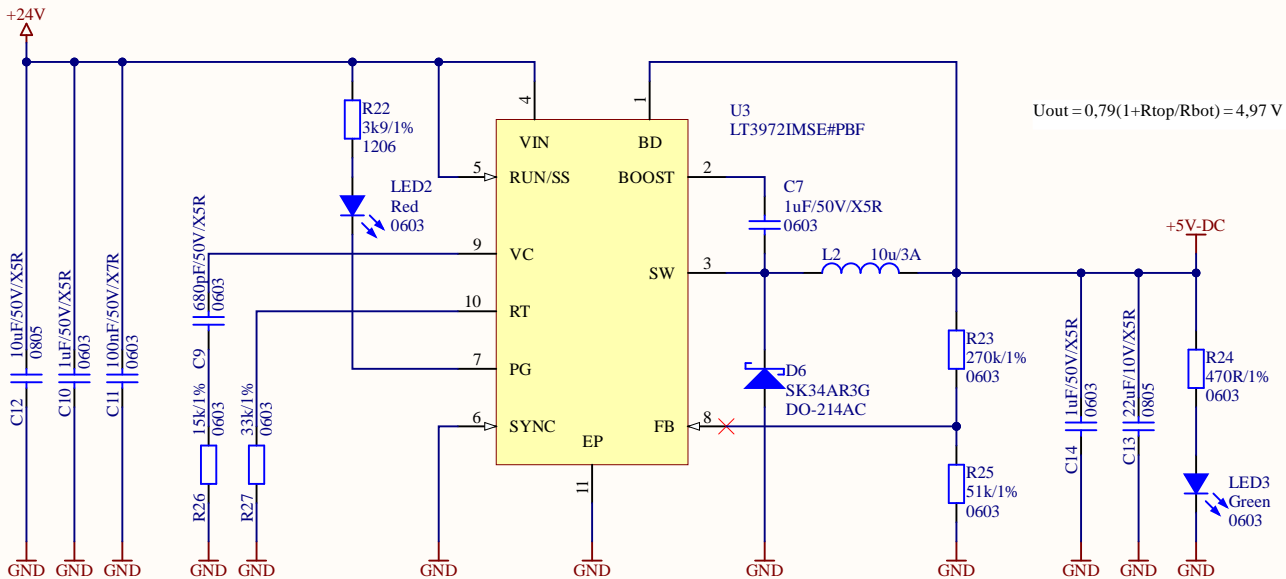


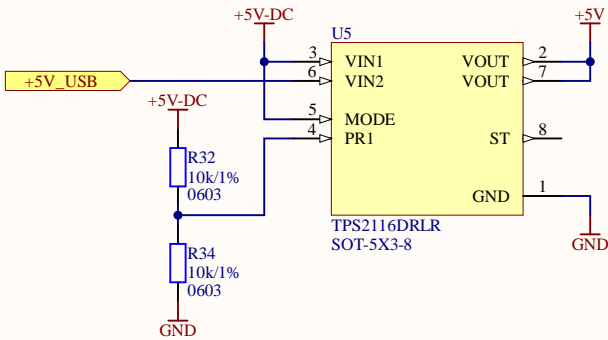
Main Power



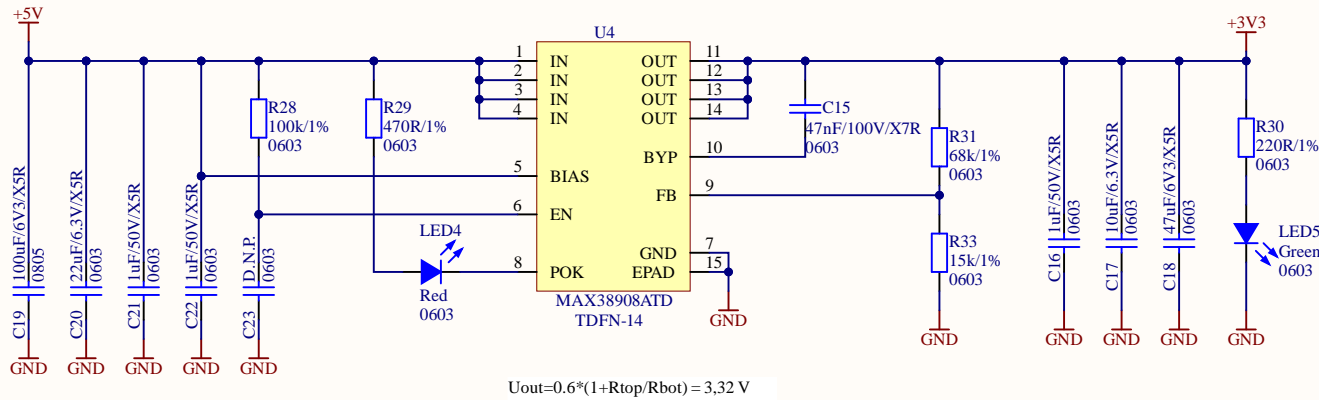
+5V Switching Regulator



Priority Power Switch

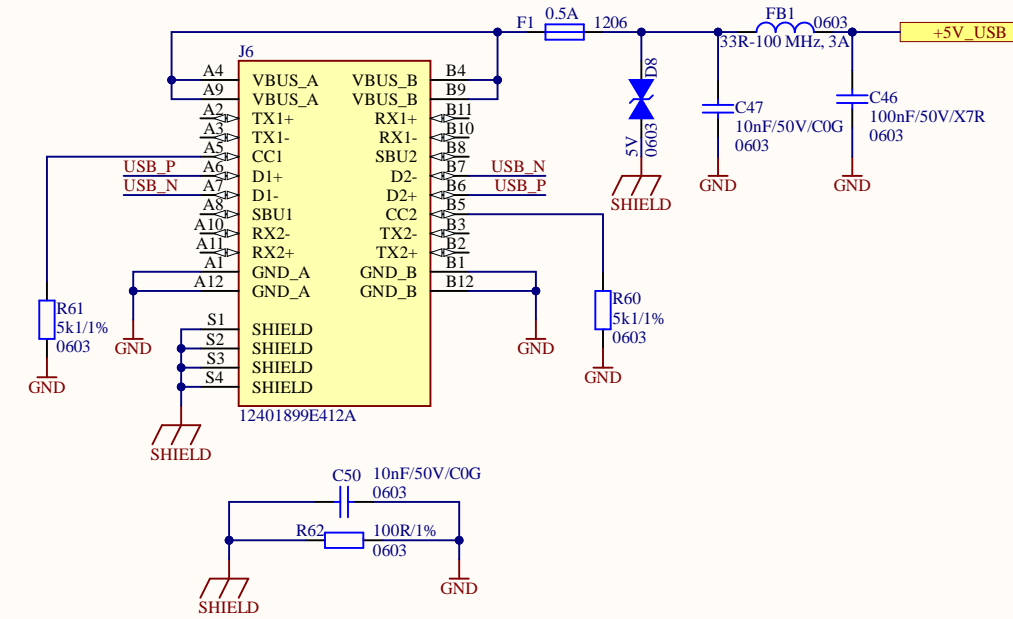
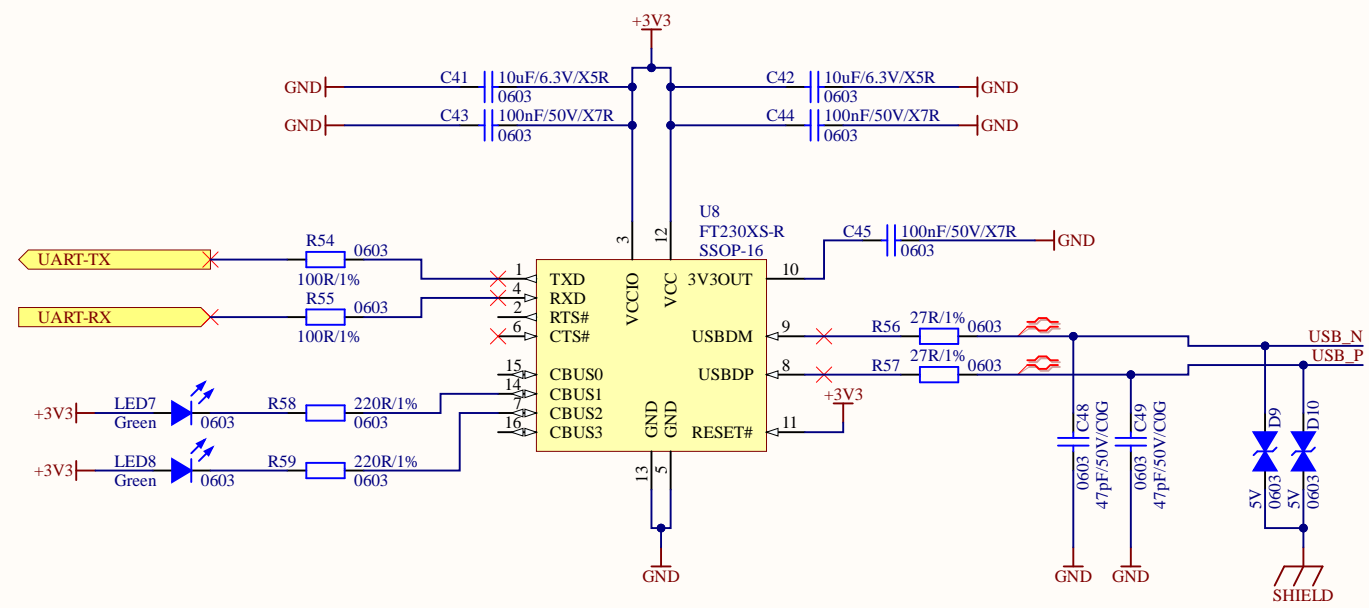


+3V3 Linear Regulator

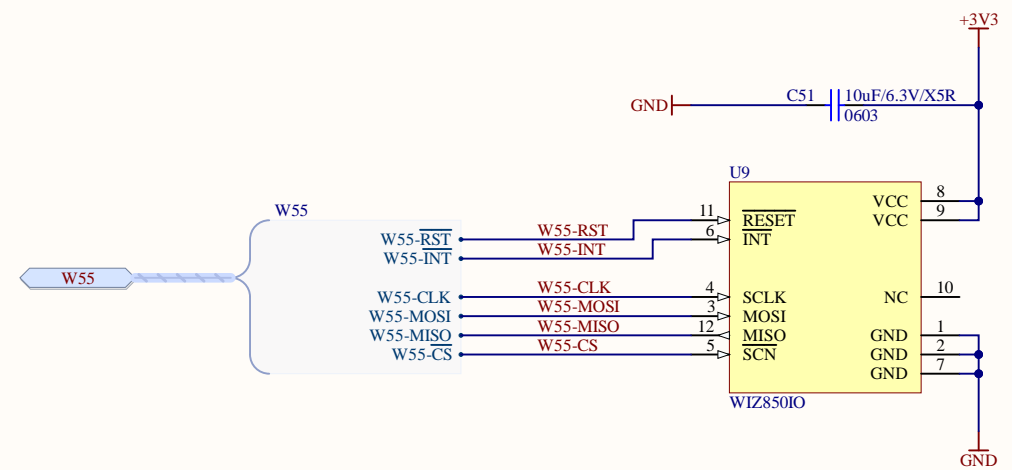


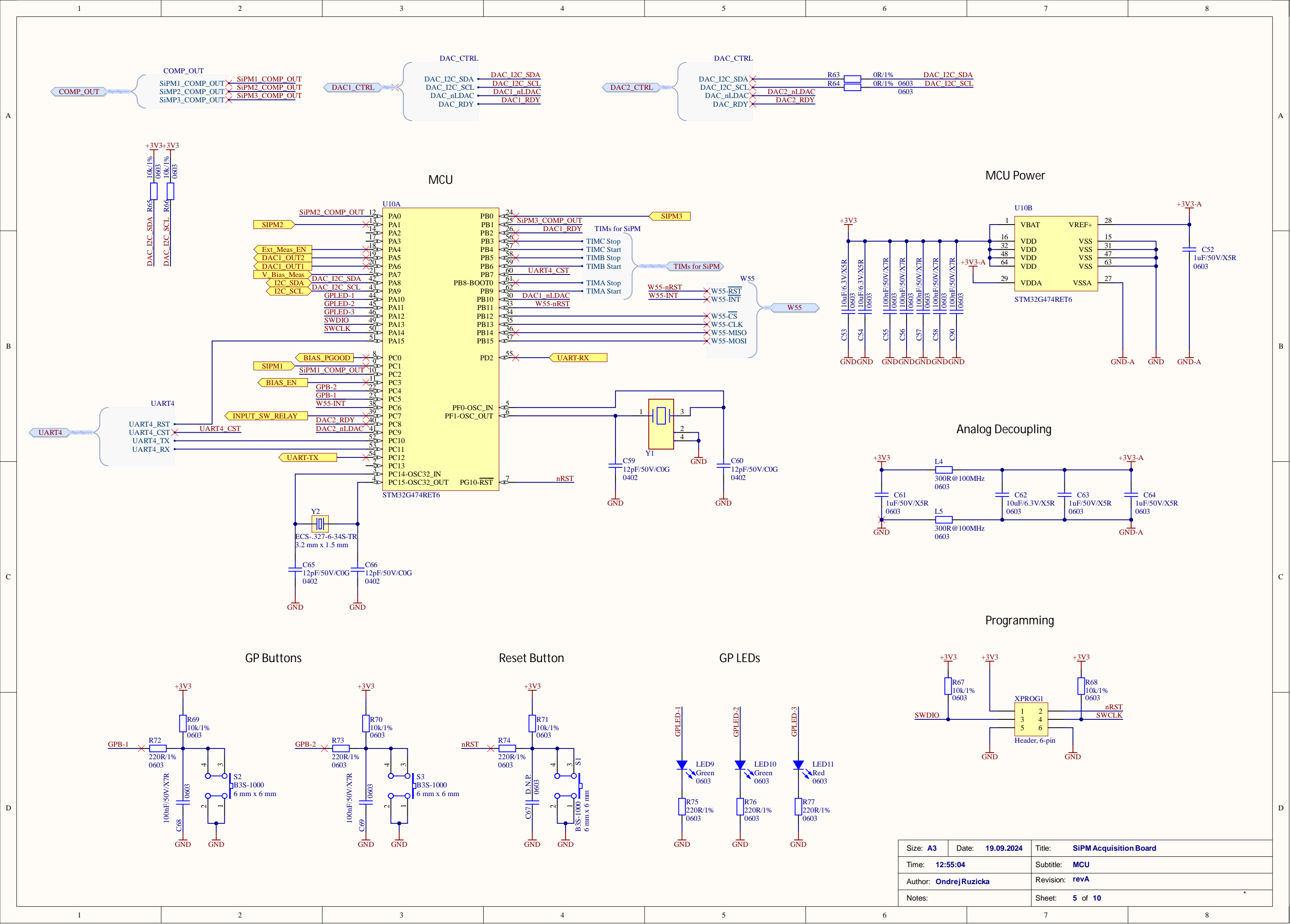
Size: A3	Date: 19.09.2024	Title: SiPM Acquisition Board
Time: 12:55:04	Subtitle: System Power	
Author: OndrejRuzicka	Revision: revA	
Notes:	Sheet: 2 of 10	

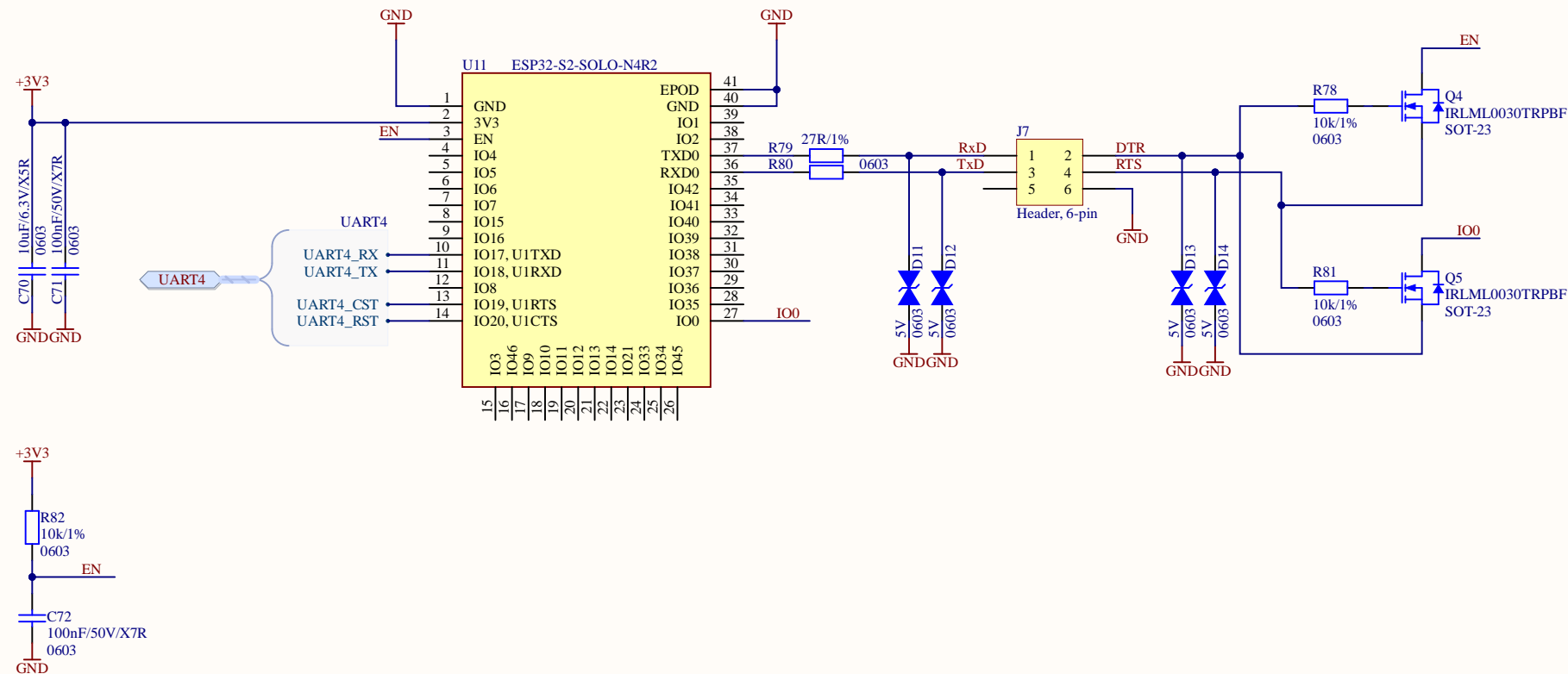
UART/USB FTDI



Ethernet Module

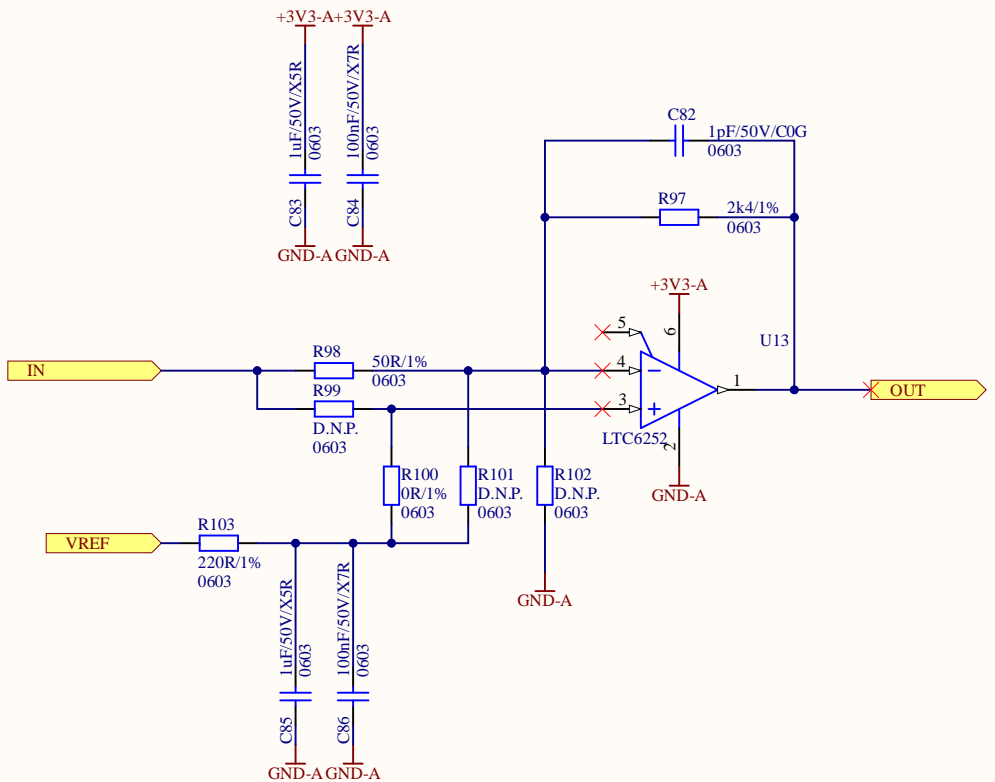






Size: A4	Date: 19.09.2024	Title: SiPM Acquisition Board
Time: 12:55:04	Subtitle: ESP Module	
Author: Patrik Kucera	Revision: revA	
Notes:	Sheet: 6 of 10 *	

Front-end Termination and Amplifier



Size: A4	Date: 19.09.2024	Title: SiPM Acquisition Board
Time: 12:55:04	Subtitle: Amplifier	
Author: Ondrej Urban	Revision: revA	
Notes:	Sheet: 8 of 10	*

A

A

B

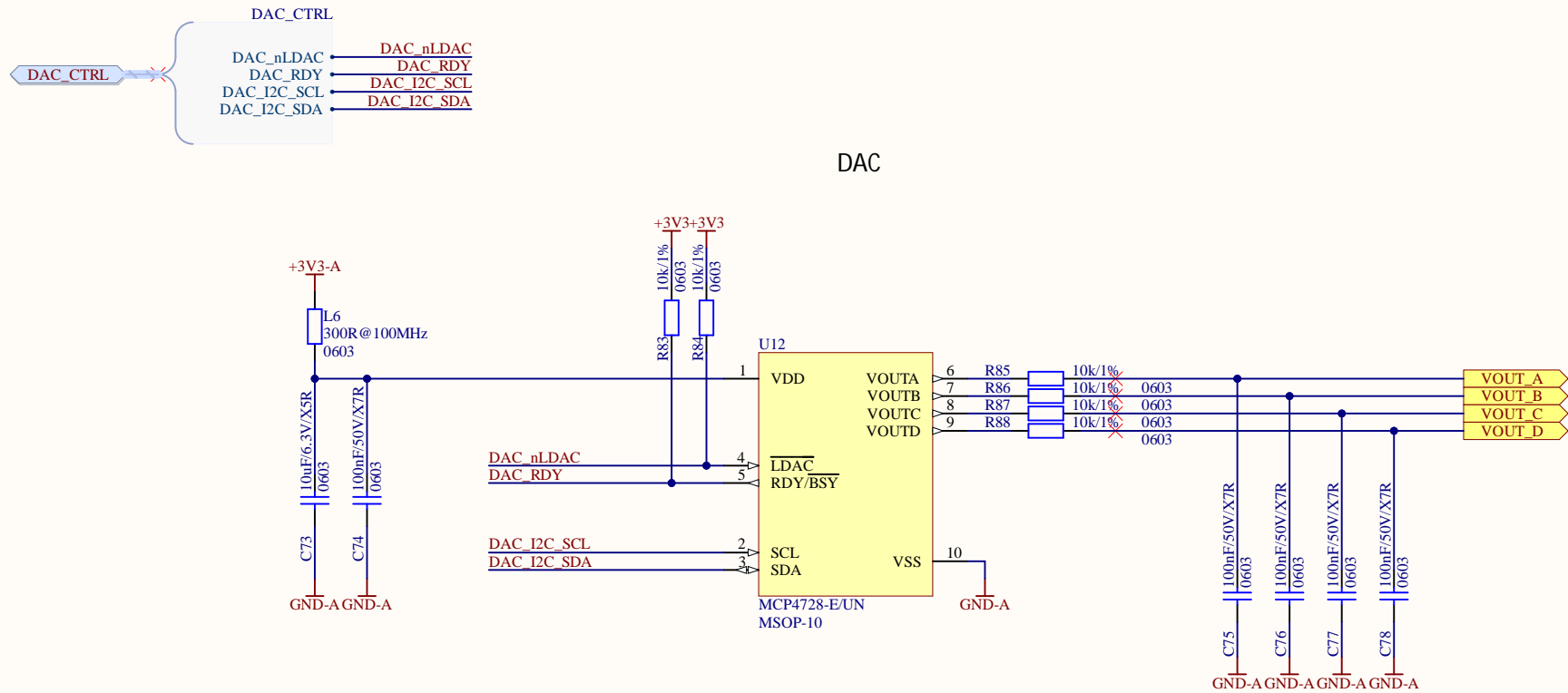
B

C

C

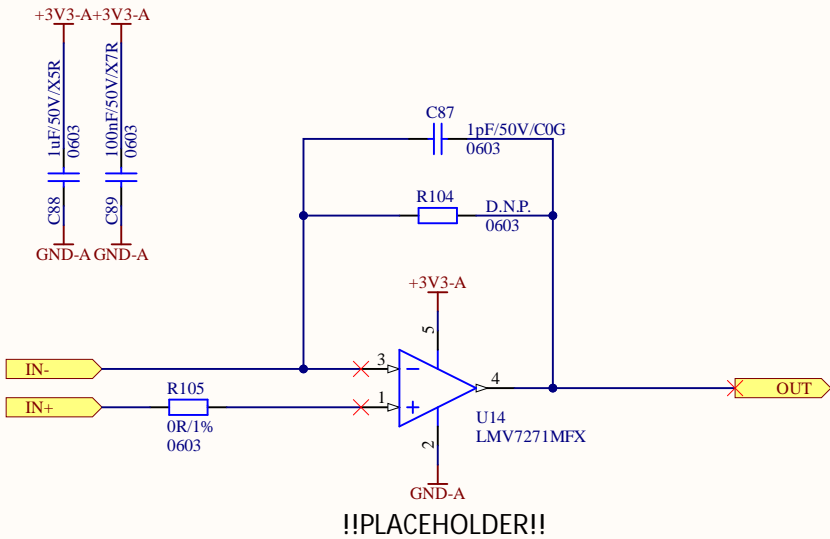
D

D



Size: A4	Date: 19.09.2024	Title: SiPM Acquisition Board
Time: 12:55:04	Subtitle: DAC	
Author: Ondrej Ruzicka	Revision: revA	
Notes:	Sheet: 9 of 10	

Fast External Comparator



Size: A4	Date: 19.09.2024	Title: SiPM Acquisition Board
Time: 12:55:05	Subtitle: External Comparator	
Author: OndrejRuzicka	Revision: revA	
Notes:	Sheet: 10 of 10	