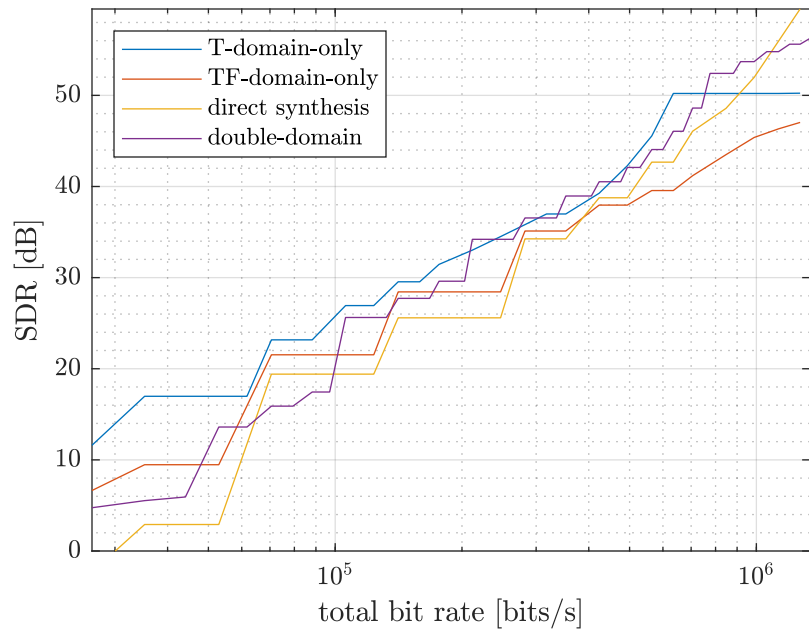


SDR on the whole signal

analysis model



synthesis model

