Onosen Ikhide

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PERSONAL STATEMENT

A highly motivated 3rd year Electronic Engineering student at UCD with strong problem-solving abilities, adaptability and a well-rounded skill set. Proven leader with experience in mentoring students, organizing society events, and excelling in team-based projects. Recognised for collaborative spirit, leadership abilities, and dedication to academic and extracurricular excellence.

EDUCATION

Bachelors of Electronic Engineering - University College Dublin (*UCD*) *2021 – 2025*

Cumulative GPA: 2:1 (Second Class Honours Expected)

Modules: Computer Science for Engineers II, Digital System Design, Digital Electronics, Mechanics, Electronic and Electrical Engineering, Circuit Theory, Multivariable Calculus II, Linear Algebra, Statistics and Probability, Solid State Devices, Electromagnetic Fields, Electrical Energy Systems, Communication Systems, French (level 4, 5 & 6).

Irish Leaving Certificate - St Joseph's College, Lucan *2014 – 2020*

532/625 points.

Awards and Recognitions

UCD Advantage Award *2024*

A recognition of involvement in UCD and the wider community, cultural engagement, and promotion of health and well-being through extracurriculars and volunteering.

Intel Women in Technology Scholar *2024*

Selected as a recipient of the prestigious scholarship by Intel Ireland, recognizing high-achieving and ambitious women in science and technology. Recognized for academic excellence and potential in technology including a grant and the program providing mentorship and professional development opportunities.

2nd Place, AI for Learning Hackathon, UCD *October 2023*

Collaborated with 3 peers to address a critical challenge the university was facing.
- Developed a comprehensive solution by prototyping an AI-powered lab simulator to allow students in STEM to prepare for their lab assessments.
-Prototyped a solution for the School of Computer Science enabling in-person coding exams to be held without the threat of students cheating using AI, ensuring integrity. The solution implemented a school VDI and proctoring software.
-The app prototype was created using UI design tools specifically Figma and Marvel.

Technical Skills

-Microsoft Package: Excel, Powerpoint, Word, Office, Sharepoint.
- Graphic Design, Website Design, UI design, Python, C++ & C Programming, R Programming, Verilog, MATLAB.
-Logisim, Tinkercad, LT Spice, Ardunio Programming, Citrix, Digital System Design, Electrical Schematic Design.

WORK EXPERIENCE

Intel, Leixlip Campus - Summer Engineering Intern, Electrical Team, Corporate Services *June 2024 - Present*

-Worked closely with cross-functional teams, including mechanical engineers, software developers, and project managers.
- Ensured safety with CoHE (Control of Hazardous Energy) procedures and LOTO (Lockout/Tagout) protocols.
- Assisted in commissioning switchgear and performed Functional Acceptance Tests on equipment.
- Worked on PLC (Programmable Logic Controller) automation to enhance system efficiency and reliability.

ESB International – Engineering Intern, 38 kV Electrical Substation Design Team *May 2023 – September 2023*

-Conducted on-site visits to ESB AIS and GIS substations, gaining practical insights into substation operations and design.
-Updated single-line diagrams and design schematics, maintaining precise technical records.
-Verified and corrected elementary schematics, identifying and rectifying errors.
-Delivered biweekly research presentations and organized team meetings.
-Strengthened proficiency in IT tools, including Citrix, Excel, PowerPoint, and SharePoint.

Clayton Hotel Liffey Valley - Restaurant Host *June 2021 – May 2024*
-Greeting and seating over 400 guests daily, managing wait times and reservations. Daily cash drop, filing revenue report.
-Received a 9/10 service rating from guests on TrustYou reviews. Upsell to customers and recommend suitable packages.

Volunteering

UCD Peer Mentor – *UCD School of Electrical and Electronic Engineering* *September 2023 – June 2024*
-Mentor of 10 first-year engineering students to aid them in their transition from second-level to third-level education.
-Provide the students with support, advice, and guidance and resources that will help them excel in engineering.

Cara Exchange student mentor *September 2022 – June 2023*
-I was the point of contact for 5 exchange students and help to make them feel welcome at UCD and in Ireland.
-Aided in the transition of exchange students, provide support, advice, and guidance during the students' exchange in Ireland.
-Co-ordinated with the other Cara Mentors to ensure the best possible welcome for new students.

UCD World Aid - Committee Member *(UCD's biggest charity Society)* *September 2022- June 2023*
-Recruiting Society members and organising fundraisers and events.
-Partnering with other charity Irish or international organisations.

Meals on Wheels - *Prepared ready meals for elderly customers.* *2021*

St. Vincent De Paul, Lucan - *Worked at the cash register and organised stock.* *2020*

Academic Projects & Activities

Robotics Project - UCD Electrical & Electronic Society *December 2022 – June 2023*
-Recreated the arcade game Dance-Dance Revolution with a team of Engineering and Computer Science students.
-Programmed graphics, gameplay and UI in Python; integrated hardware using Arduino and C.

Engineers Without Borders UK Globally Responsible Engineering *2023*
-Researched and presented the principles of global responsibility in relation to engineering, fostering inclusivity, and self-reflection for a better future.

C++ & C Language Projects *January 2021 – Present*
*- **Communication systems:** : Designed a link-layer protocol for file transfer between computers via serial ports.*
*- **Internet & TCP Protocol:** Developed a TCP-based client program for HTTP file transfers.*

Logisim Projects – *Digital Logic Circuit Simulator September 2022 – Present*
- Designed a digital clock system with user-controlled settings.
-Created a sequence detector for Mealy and Moore machines

Digital Design on FPGA on Nexys 4 circuit board *2023*
-Calculator: Designed a hexadecimal arithmetic calculator using Verilog; implemented on Nexys-4 board with keypad interface.
-Bicycle Light Controller: Developed a digital light control system with three modes; verified using testbenches and Vivado.Created a 7-segment display interface in Verilog; implemented and tested on FPGA.

ADDITIONAL INFORMATION

Certifications/Workshops
ECDL Certificate, CoHE training, Safe Pass Certificate, Manual Handling, Gaisce Bronze Medal, Engineers Without Borders Creativity and Design Workshop 2021, Mechanical Structures Workshop 2021.

Languages
English – Native, Hungarian – Native, French –Proficient, Irish – Elementary.

Interests
Photography, Modelling, Art and Design, Architecture, Languages, Basketball, Film and Literature, High Fashion, Travel.