

STPS2L40

Low drop power Schottky rectifier

Features

- Very small conduction losses
- Negligible switching losses
- Low forward voltage drop
- Surface mount miniature package
- Avalanche capability specified
- ECOPACK2[®] halogen-free component (SMAflat and SMBflat)

Description

Single chip Schottky rectifiers suited to Switched Mode Power Supplies and high frequency DC to DC converters.

Packaged in SMB, low profile SMB and low profile SMA, this device is especially intended for surface mounting and used in low voltage, high frequency inverters, free wheeling and polarity protection applications.

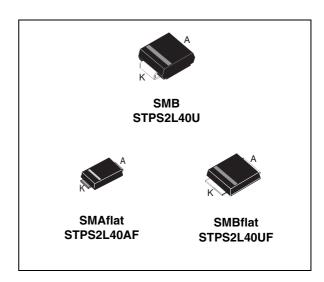


Table 1. Device summary

I _{F(AV)}	2 A
V _{RRM}	40 V
T _j (max)	150 °C
V _F (max)	0.34 V

Characteristics STPS2L40

Characteristics 1

Table 2. **Absolute ratings (limiting values)**

Symbol	P	Value	Unit		
V_{RRM}	Repetitive peak reverse voltage			40	V
		SMB	$T_L = 130 {}^{\circ}\text{C} \delta = 0.5$		
I _{F(AV)}	I _{F(AV)} Average forward current	SMBflat	$T_L = 140 {}^{\circ}\text{C} \delta = 0.5$	2	Α
, ,		SMAflat	$T_L = 130 {}^{\circ}\text{C} \delta = 0.5$		
I _{FSM}	Surge non repetitive forward current $t_p = 10 \text{ ms sinusoidal}$			75	Α
P _{ARM}	Repetitive peak avalanche power $t_p = 1 \mu s$ $T_j = 25 °C$			2200	W
T _{stg}	Storage temperature range			-65 to + 150	°C
T _j	Operating junction temperature (1)			150	°C

^{1.} $\frac{dPtot}{dTj} < \frac{1}{Rth(j-a)}$ condition to avoid thermal runaway for a diode on its own heatsink

Table 3. Thermal resistances

Symbol	Parameter		Value	Unit
		SMB	20	
R _{th (j-l)}	Junction to lead	SMBflat	10	°C/W
		SMAflat	20	

Table 4. Static electrical characteristics

Symbol	Tests conditions			Min.	Тур.	Max.	Unit
	I _R ⁽¹⁾ Reverse leakage current	T _j = 25 °C	V _R = 40 V			220	μΑ
I _R ⁽¹⁾		T _j = 100 °C				20	mA
		T _j = 125 °C			38	80	mA
		T _j = 25 °C	I _F = 1 A			0.39	V
		T _j = 125 °C			0.25	0.28	
V _F ⁽¹⁾	V _F ⁽¹⁾ Forward voltage drop	$T_j = 25 ^{\circ}C$	I _F = 2 A			0.43	
V _F ···· Forward voilage drop	T _j = 125 °C	IF – Z A		0.31	0.34		
		T _j = 25 °C	I _E = 4 A			0.5	V
		T _j = 125 °C	IF – 4 A		0.39	0.45	

^{1.} Pulse test: t_p = 380 μ s, δ < 2

To evaluate the conduction losses use the following equation: P = 0.22 x $I_{F(AV)}$ + 0.06 $I_{F}^{2}_{(RMS)}$

$$P = 0.22 \times I_{F(AV)} + 0.06 I_{F^2(RMS)}$$

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STPS2L40 Characteristics

Figure 1. Average forward power dissipation Figure 2. Average forward current versus versus average forward current ambient temperature (δ = 0.5) SMB

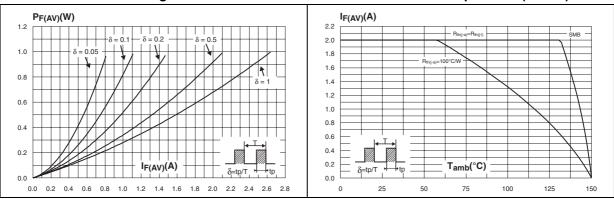


Figure 3. Average forward current versus ambient temperature (δ = 0.5) SMBflat

Figure 4. Average forward current versus ambient temperature (δ = 0.5) SMAflat

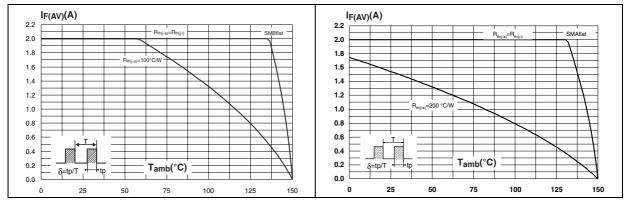
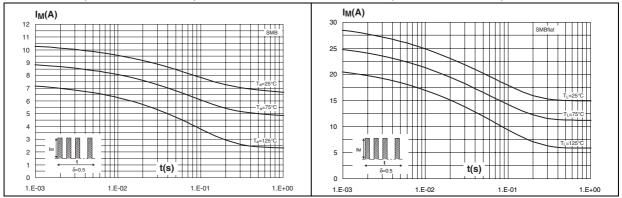


Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values) SMB

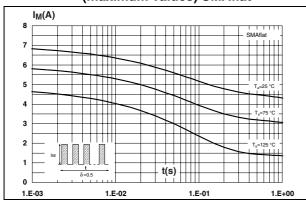
Figure 6. Non repetitive surge peak forward current versus overload duration (maximum values) SMBflat



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Figure 7. Non repetitive surge peak forward current versus overload duration (maximum values) SMAflat

Figure 8. Normalized avalanche power derating versus pulse duration



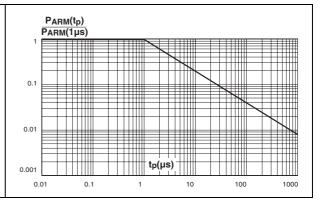
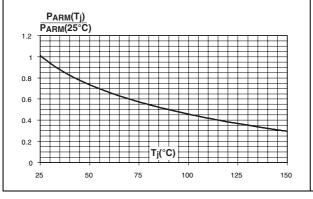


Figure 9. Normalized avalanche power derating versus junction temperature

Figure 10. Relative variation of thermal impedance junction to ambient versus pulse duration - SMB



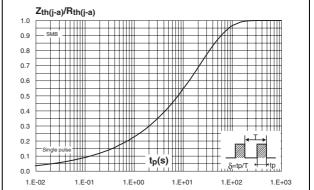
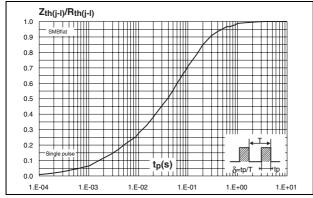
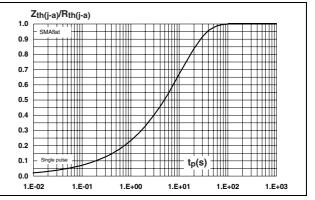


Figure 11. Relative variation of thermal impedance junction to lead versus pulse duration - SMBflat

Figure 12. Relative variation of thermal impedance junction to ambient versus pulse duration - SMAflat



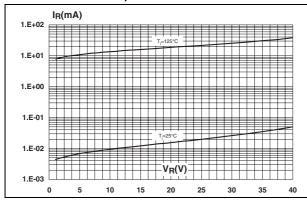


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Figure 13. Reverse leakage current versus reverse voltage applied (typical values)

Figure 14. Junction capacitance versus reverse voltage applied (typical values)



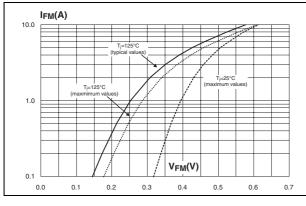
1000 F-1MHz
Vosc-30m/sssT,=25°C

VR(V)

10 10 100

Figure 15. Forward voltage drop versus forward current (high level)

Figure 16. Forward voltage drop versus forward current (low level)



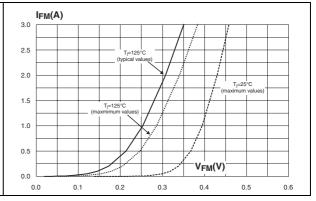
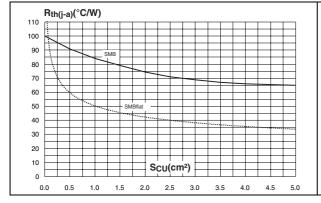
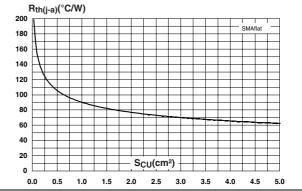


Figure 17. Thermal resistance junction to ambient versus copper surface under each lead, SMB, SMBflat (epoxy printed board FR4, copper thickness = 35 μm)

Figure 18. Thermal resistance junction to ambient versus copper surface under each lead, SMAflat (epoxy printed board FR4, copper thickness = 35 µm)





Package Information STPS2L40

2 Package Information

Epoxy meets UL94,V0

In order to meet environmental requirements, ST offers these devices in ECOPACK[®] packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at www.st.com.

Table 5. SMB dimensions

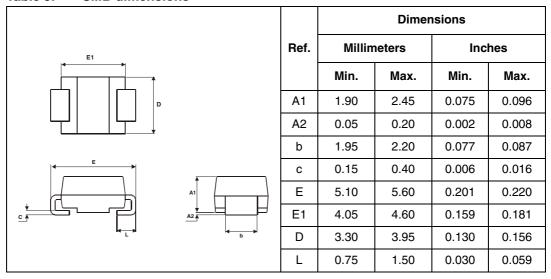
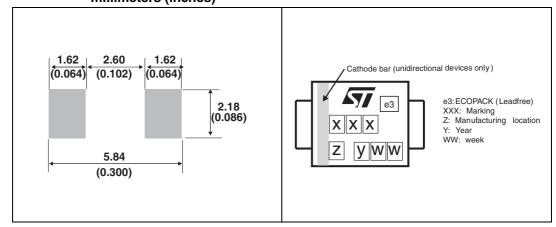
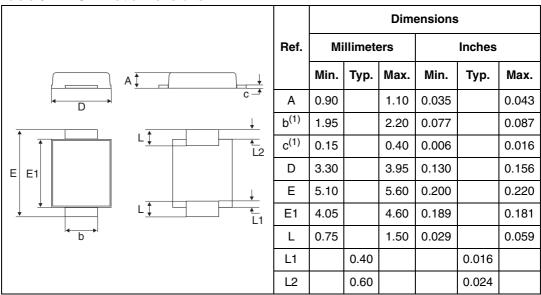


Figure 19. SMB footprint dimensions in Figure 20. Marking information millimeters (inches)



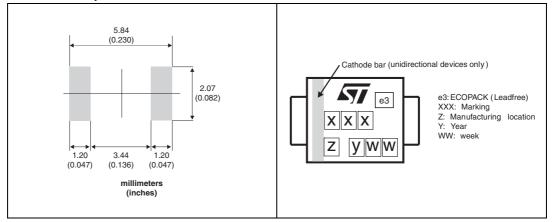
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Table 6. SMBflat dimensions



^{1.} Applies to plated leads

Figure 21. SMBflat footprint dimensions Figure 22. Marking information optimized for SMBflat⁽¹⁾



1. SMB footprint may also be used.

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Table 7. SMAflat dimensions

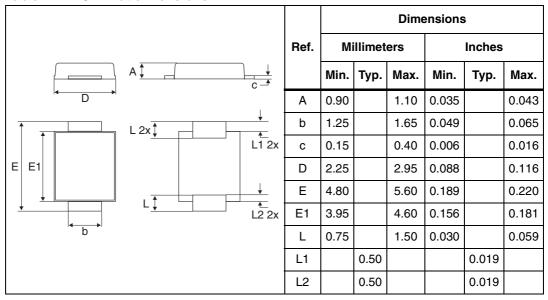
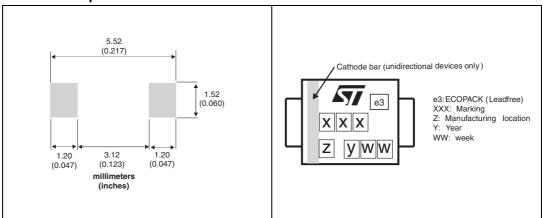


Figure 23. SMAflat footprint dimensions Figure 24. Marking information optimized for SMAflat⁽¹⁾



1. SMA footprint may also be used.

STPS2L40 Ordering Information

3 Ordering Information

Table 8. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
STPS2L40U	GD4	SMB	0.107 g	2500	Tape and reel
STPS2L40UF	FGD4	SMBflat	0.50 g	5000	Tape and reel
STPS2L40AF	F2L4	SMAflat	0.35 g	10000	Tape and reel

4 Revision history

Table 9. Document revision history

Date	Revision	Description of changes
Jul-2003	2A	Last update.
31-Jan-2007	3	Reformatted to current standard. Added ECOPACK statement. Added SMBflat package.
18-Sep-2008	4	Reformatted to current standard. Updated ECOPACK statement. Added SMAflat package.

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