## Computer Architecture (../index.html) /

## **Exercise 6: Design of Control Unit**

This time we will create a control unit.

The mips processor created in this exercise supports the instructions listed in the table (assemble.html). The mips processor in the textbook does not support immediate instructions (addi, andi, ori, slti instructions), so you need to make additional modifications to the data path, ALU control block, and state transition diagram. Keep this in mind, please create a control unit according to the following procedure.

- 1. Creation of ALU control unit (ALUcont.html)
- 2. Creation of main control unit (CU.html)
- 3. Complete the data path (imm.html)