

Exercise 7 : Overall circuit test

For this time, perform the whole circuit test and make corrections if there is an error in the operation.

1. In order to verify whether each instruction is working correctly, let's run the assembler program called test1.s (test1.s) on my MIPS processor. Since there is a testfixture file corresponding to this assembler program below, please simulate the entire circuit using this. If the circuit is correct, the same value as the expected value should be output.

Testfixture.new for each instruction test (testfixture.new1)

The simulation results (expected value and computed result) are output to a text file called simout.tmp in the run directory (Cadence6/cell_name_run1), so please look at more etc.

2. For each of the three exercise assignments 1-1, 1-2, 1-3 and exercise 2, please convert your assembler program into a testfixture file and check that your MIPS processor will get the correct results.

How to convert assembler program to testfixture file (asm2fixture.html)

Hint

Operations that can be used for debugging. (debug.html)